

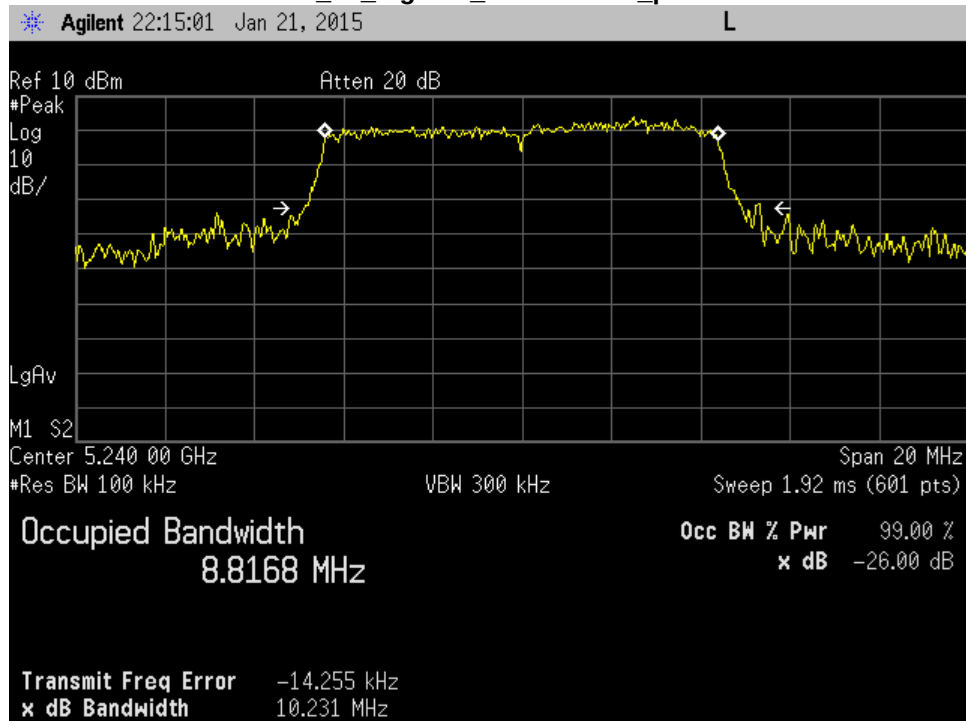


Annex D

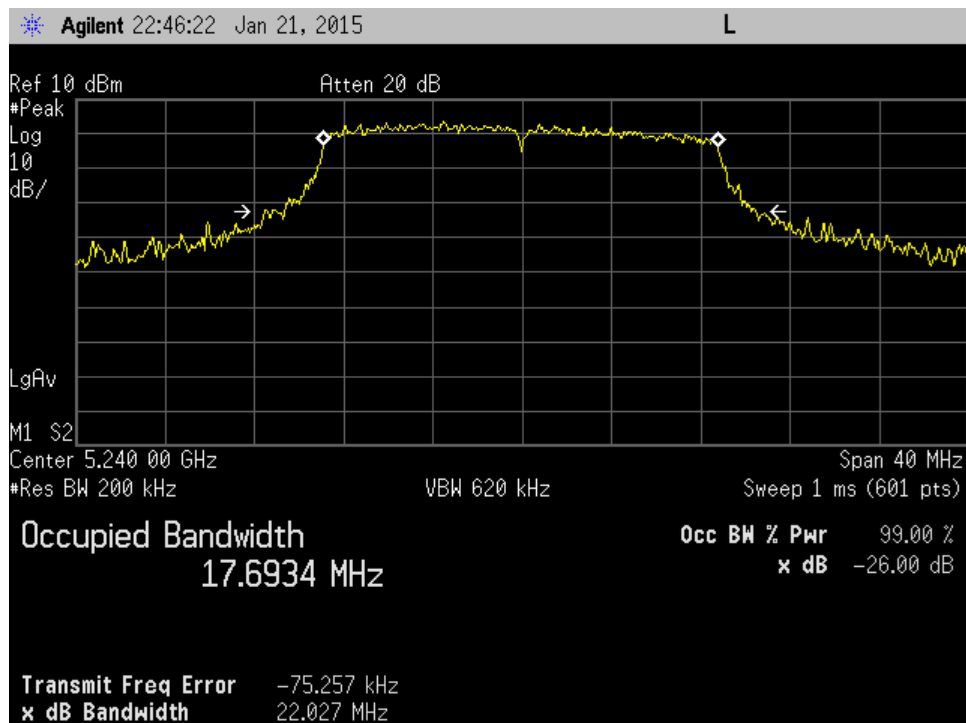
Occupied Bandwidth



OCC BW_J1_high ch_bw=10 MHz _p14a0028

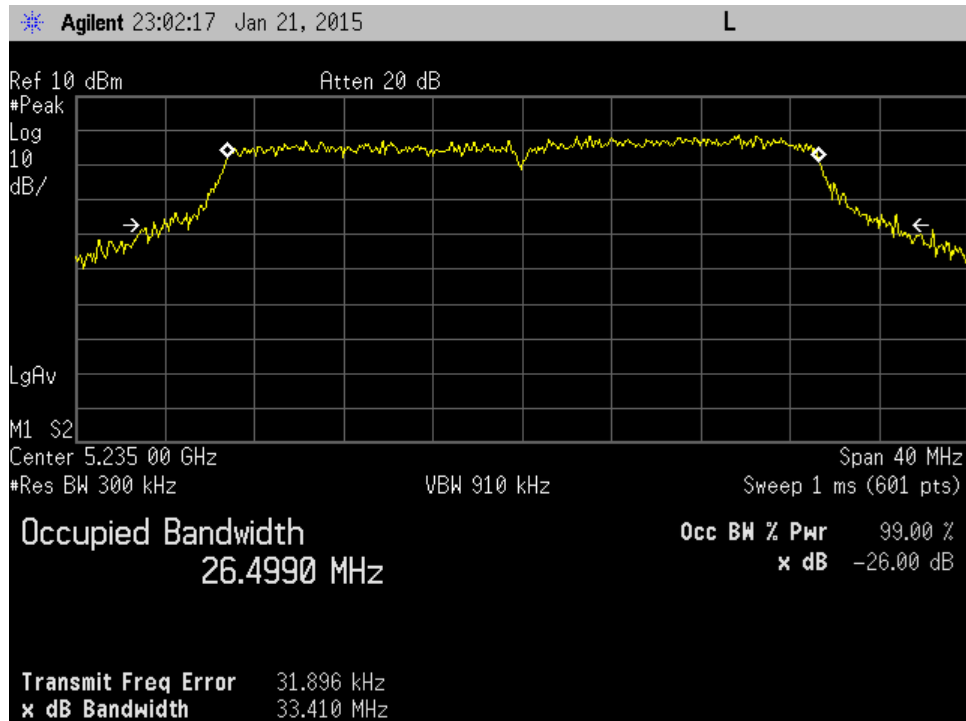


OCC BW_J1_high ch_bw=20 MHz _p14a0028

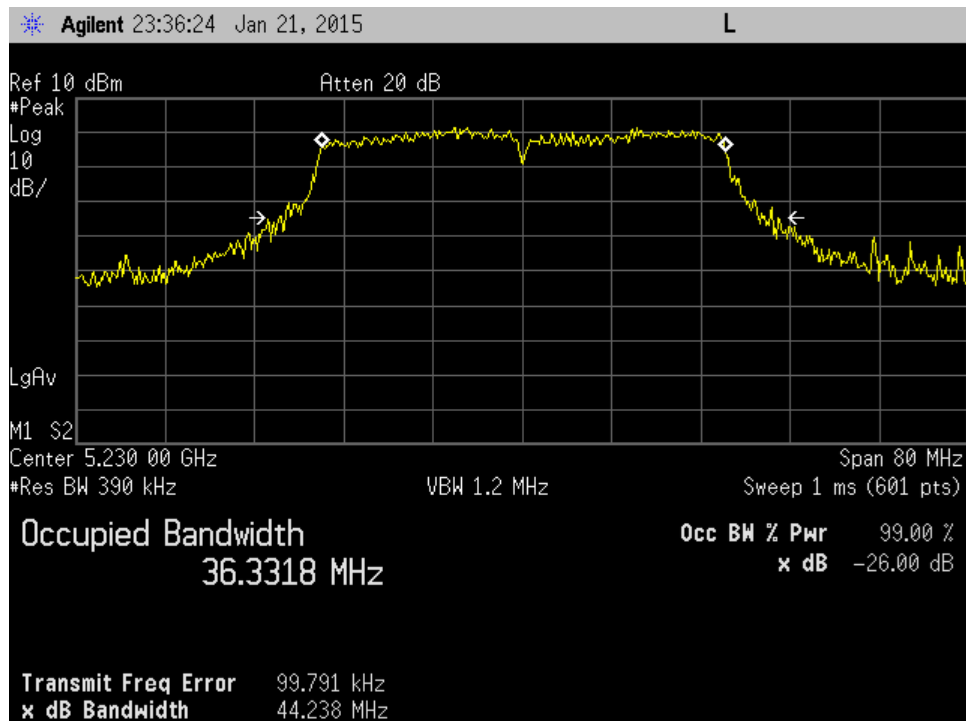




OCC BW_J1_high ch_bw=30 MHz _p14a0028

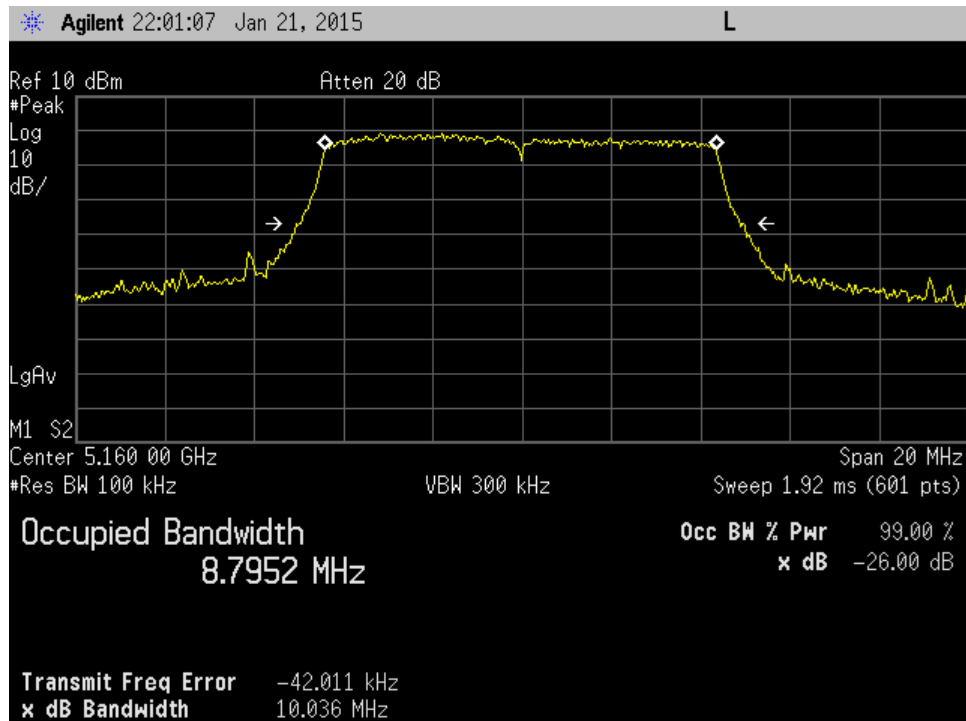


OCC BW_J1_high ch_bw=40 MHz _p14a0028

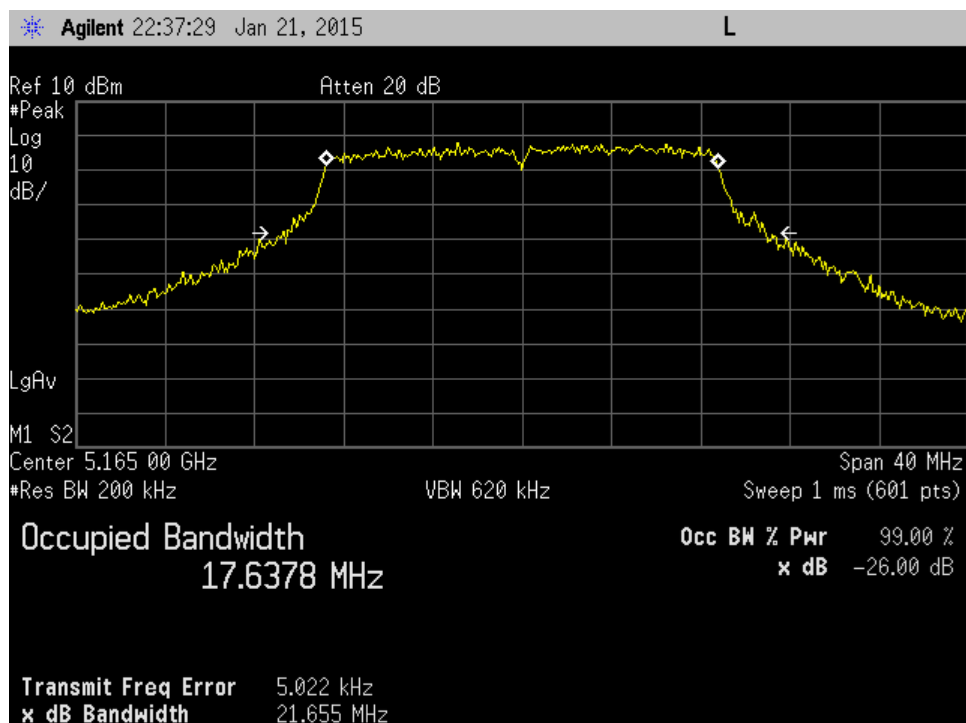




OCC BW_J1_low ch_bw=10 MHz _p14a0028

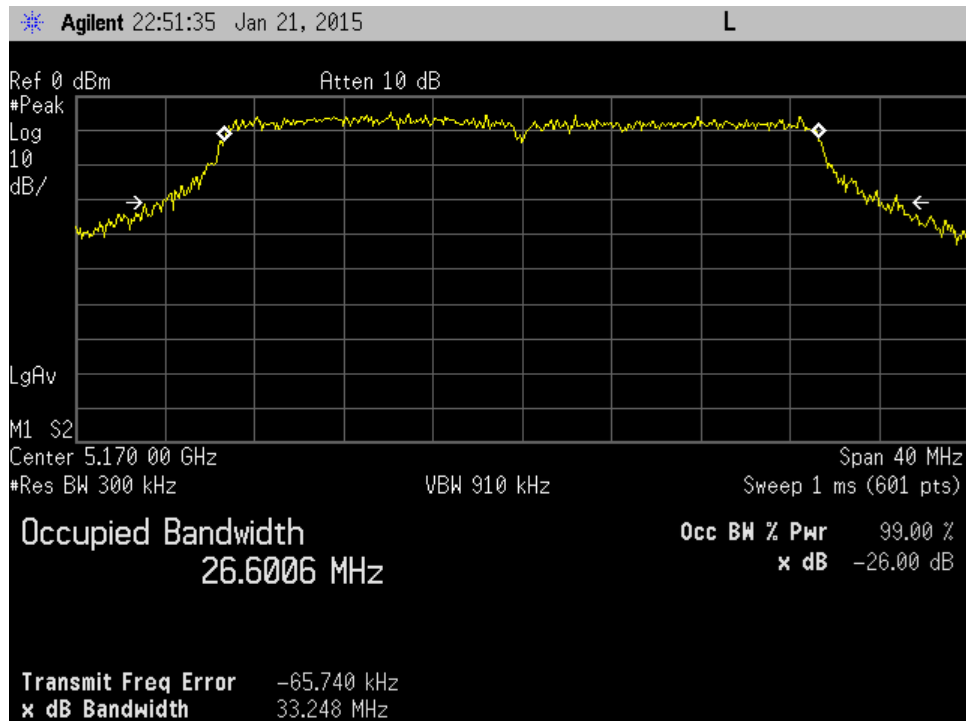


OCC BW_J1_low ch_bw=20 MHz _p14a0028

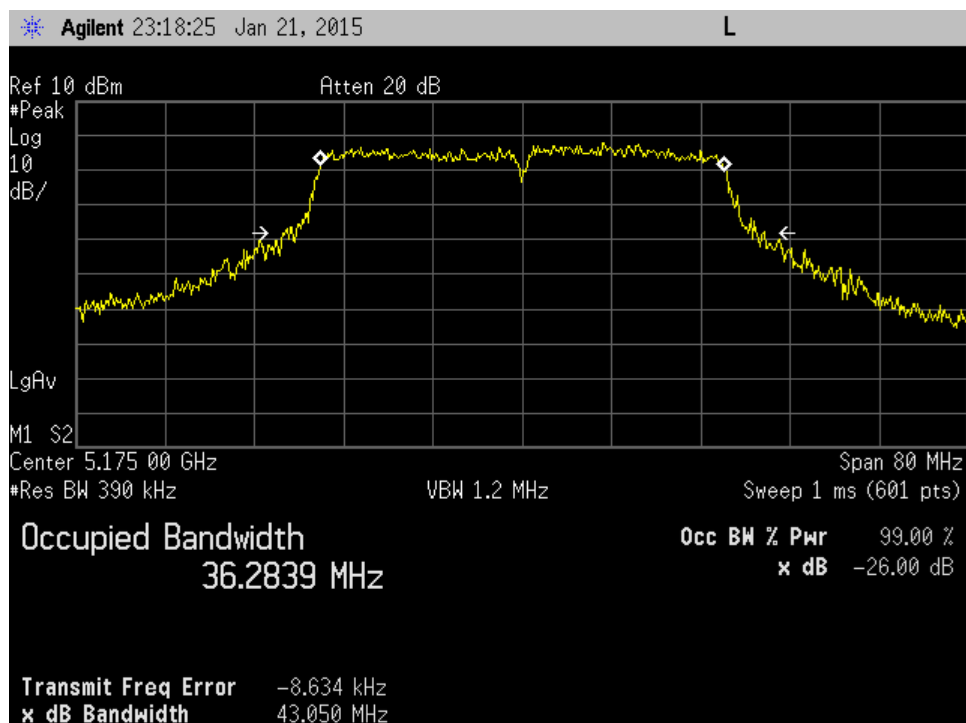




OCC BW_J1_low ch_bw=30 MHz _p14a0028

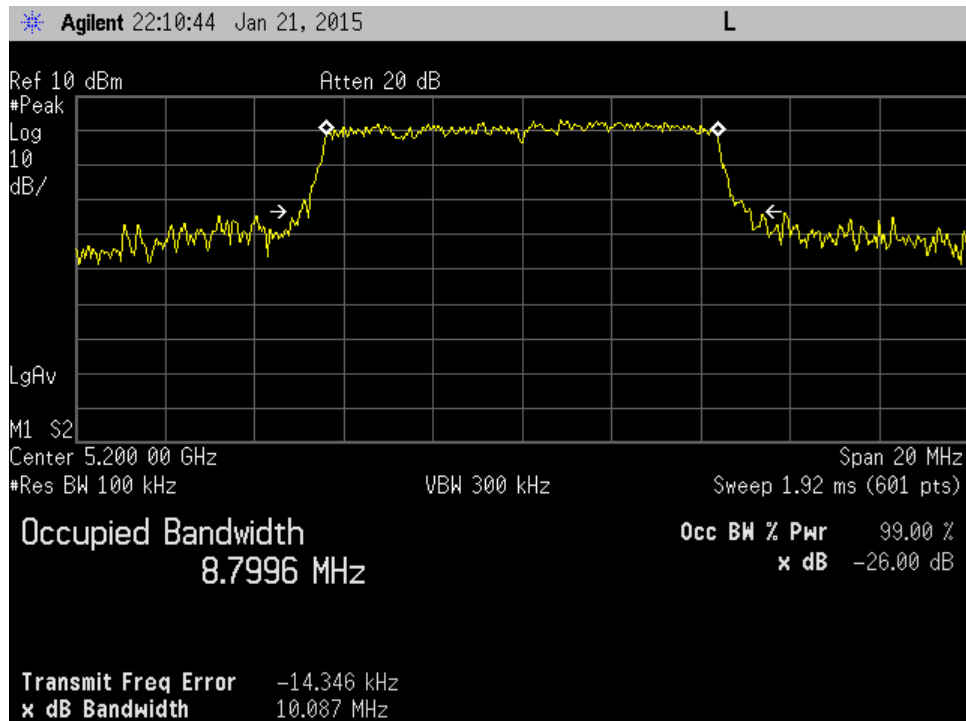


OCC BW_J1_low ch_bw=40 MHz _p14a0028

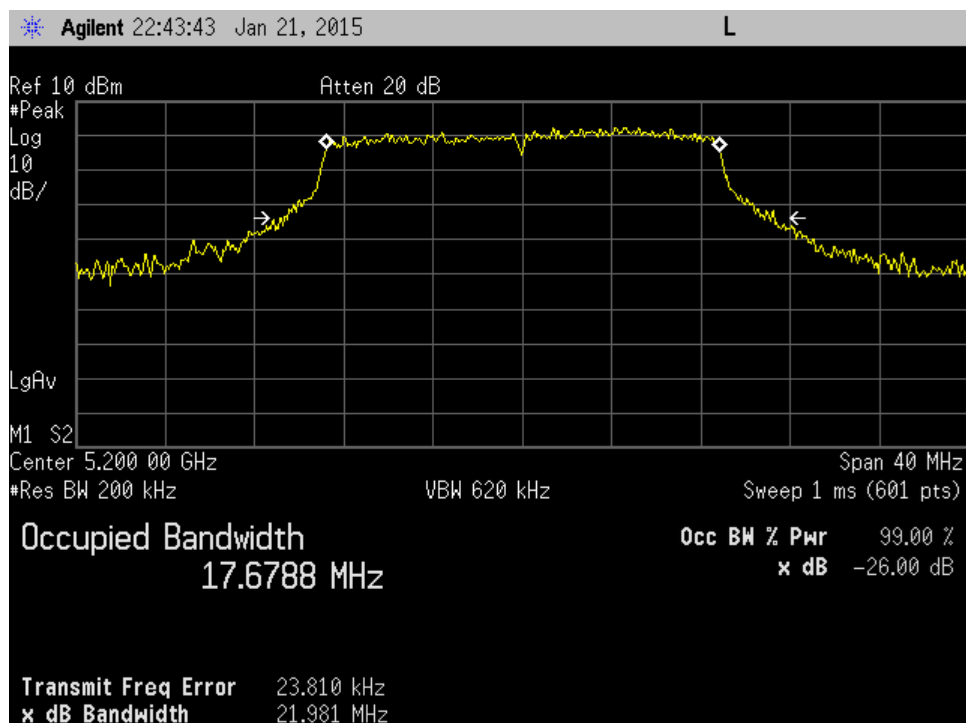




OCC BW_J1_mid ch_bw=10 MHz _p14a0028

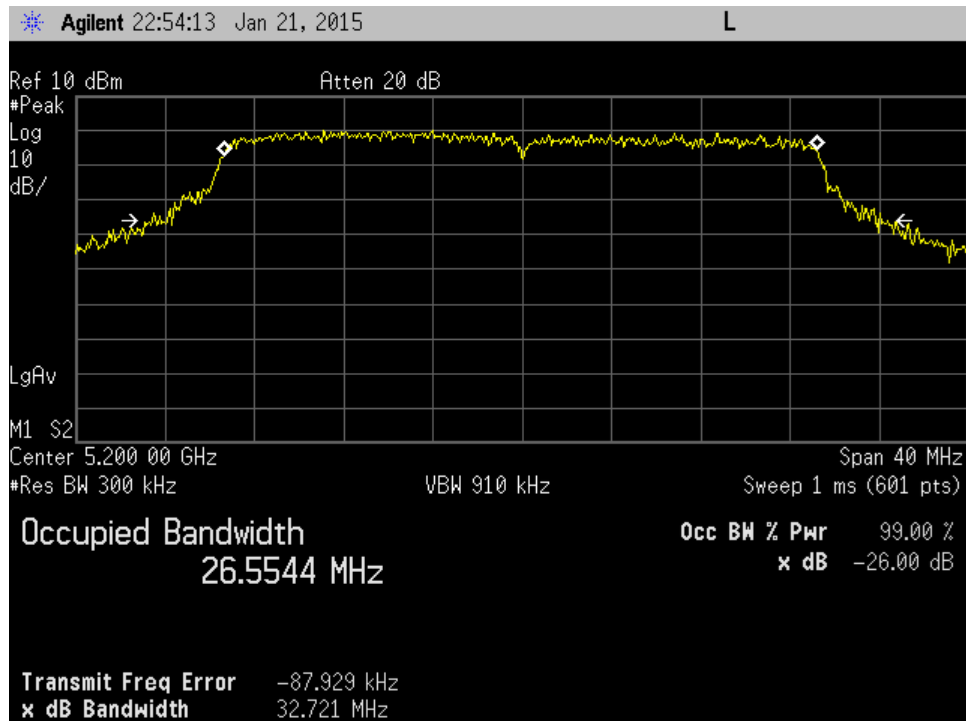


OCC BW_J1_mid ch_bw=20 MHz _p14a0028

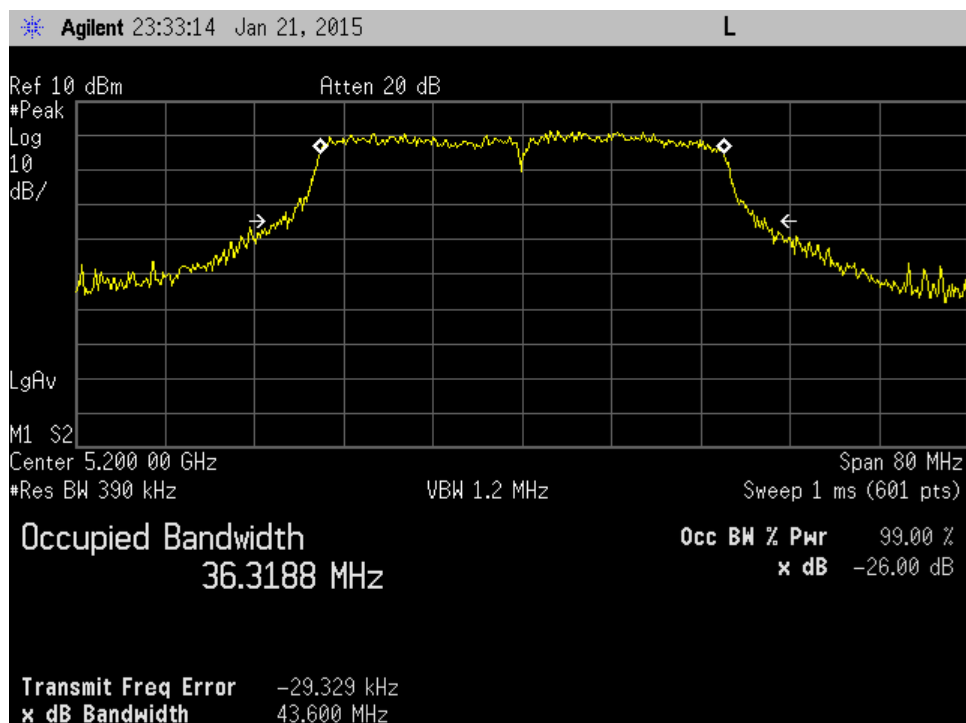




OCC BW_J1_mid ch_bw=30 MHz _p14a0028

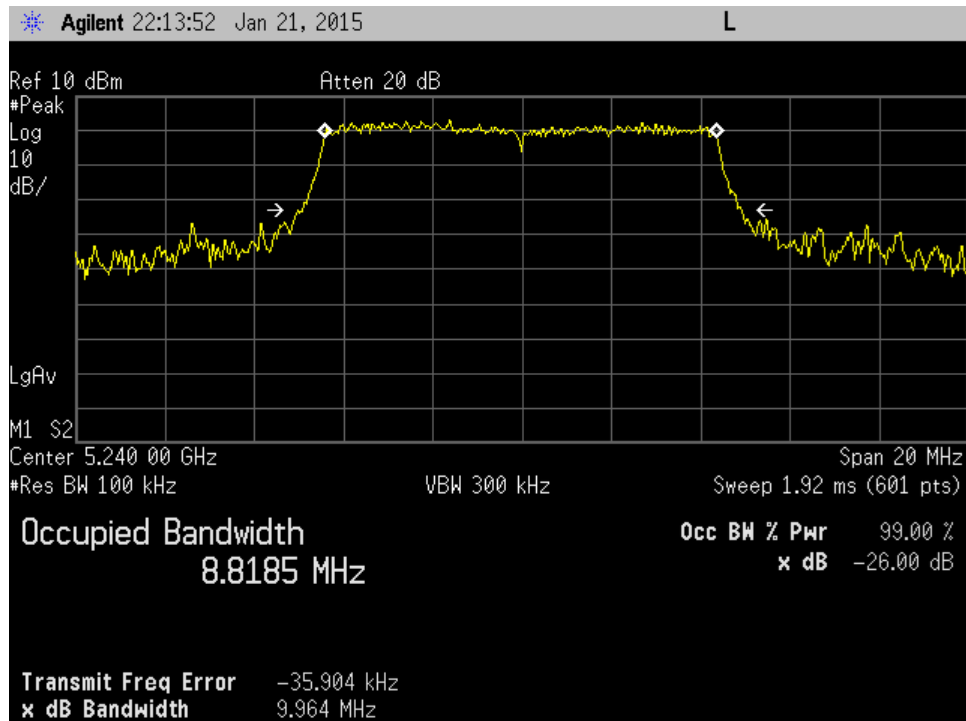


OCC BW_J1_mid ch_bw=40 MHz _p14a0028

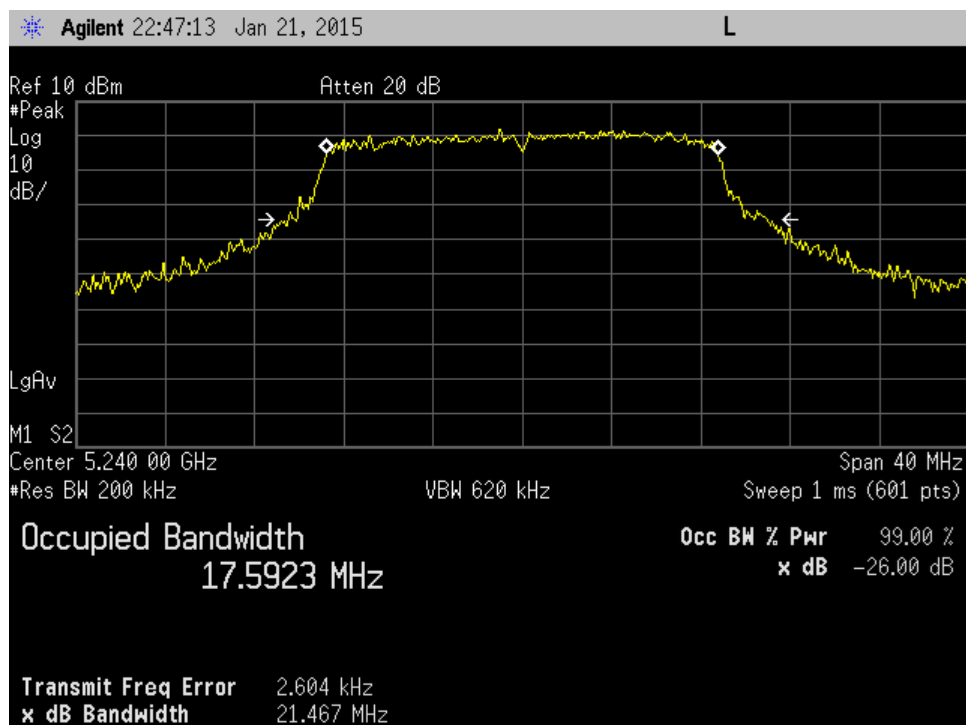




OCC BW_J3_high ch_bw=10 MHz _p14a0028

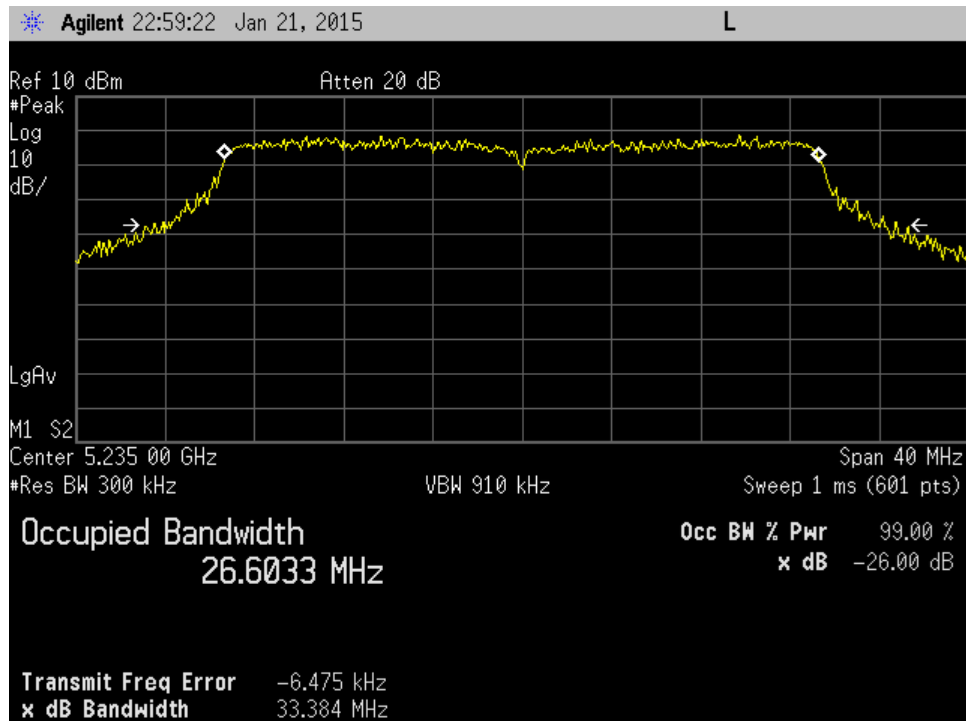


OCC BW_J3_high ch_bw=20 MHz _p14a0028

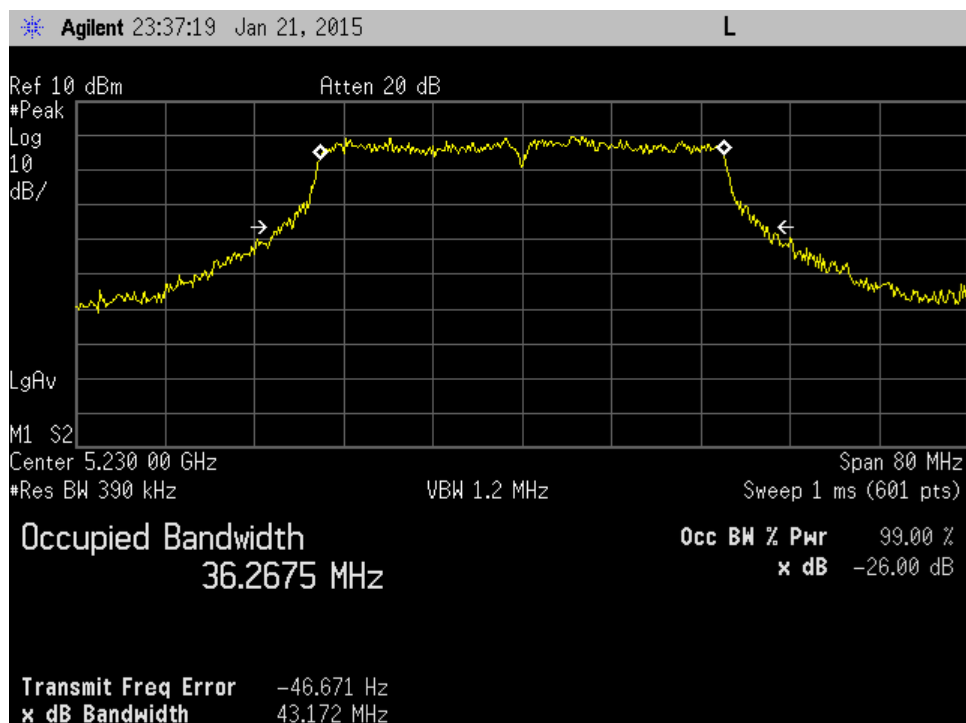




OCC BW_J3_high ch_bw=30 MHz _p14a0028

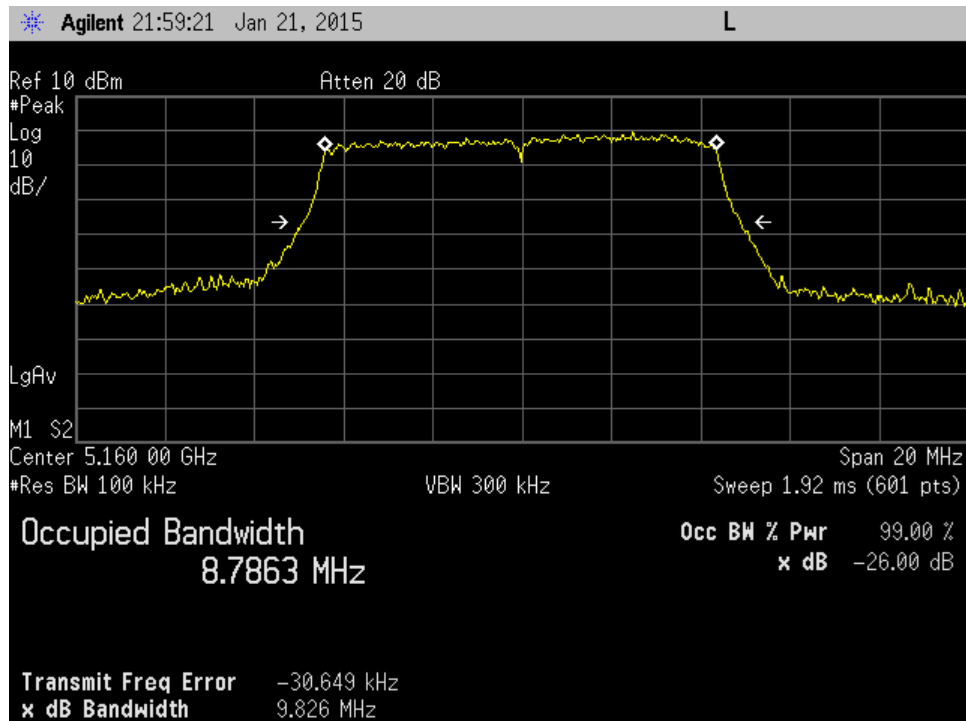


OCC BW_J3_high ch_bw=40 MHz _p14a0028

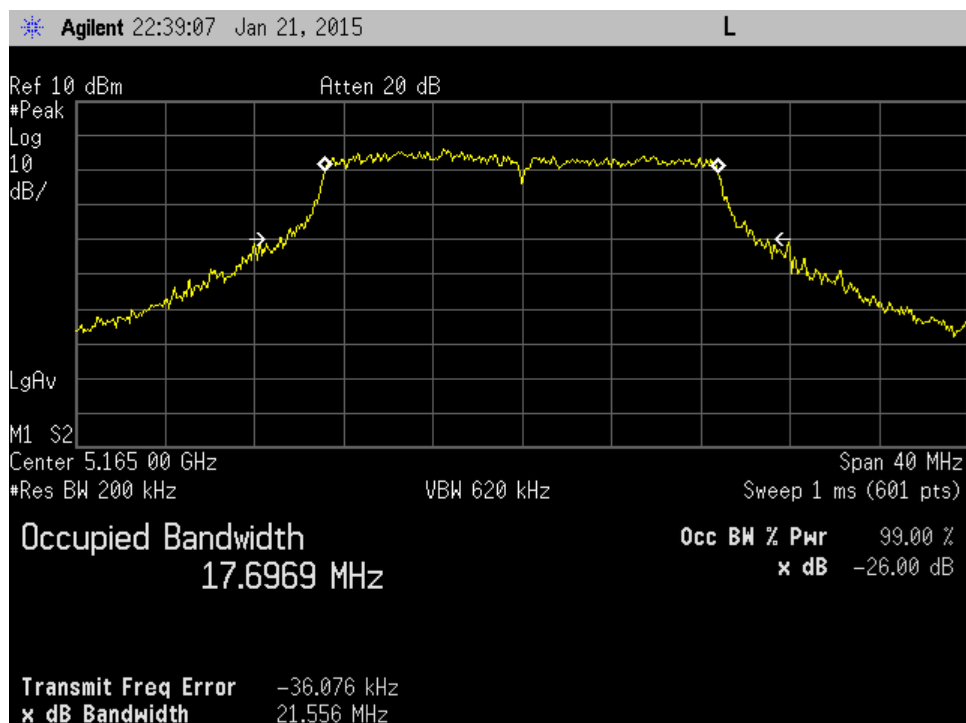




OCC BW_J3_low ch_bw=10 MHz _p14a0028

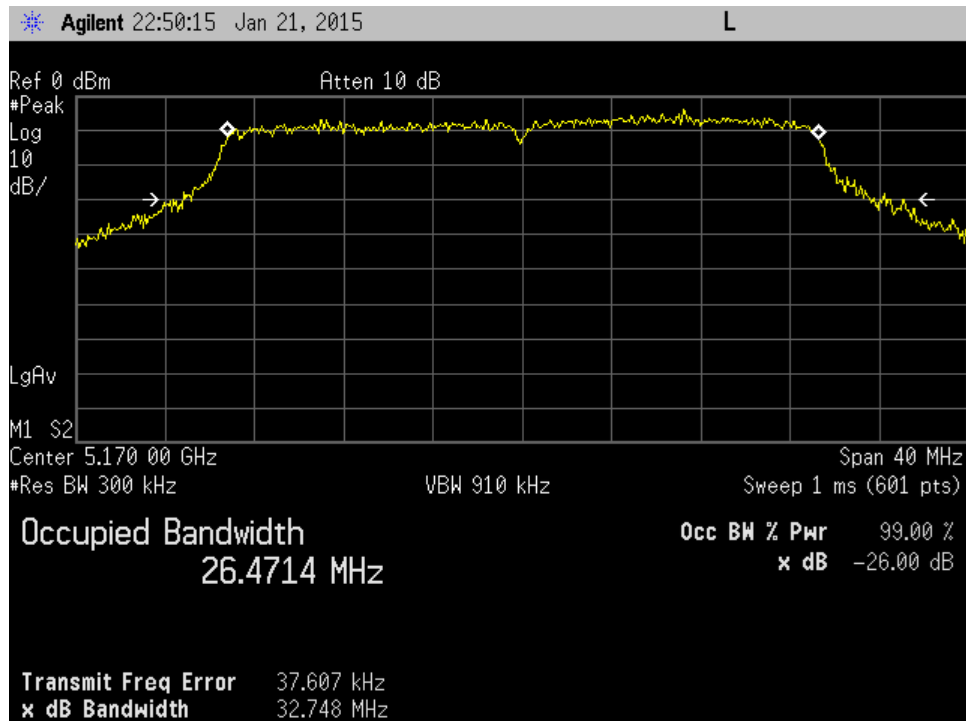


OCC BW_J3_low ch_bw=20 MHz _p14a0028

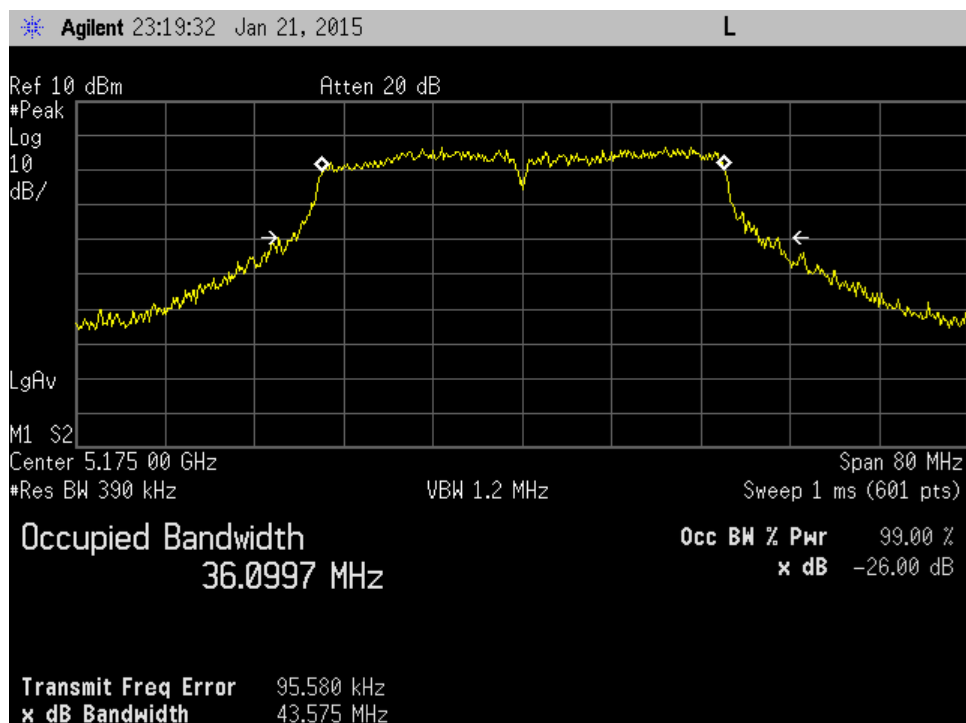




OCC BW_J3_low ch_bw=30 MHz _p14a0028

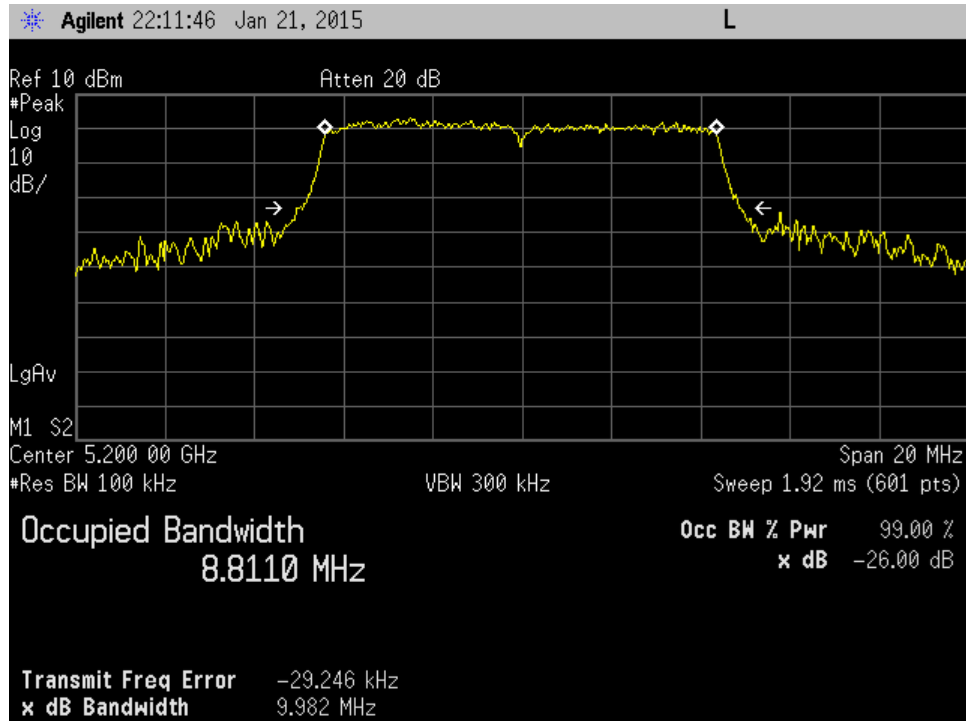


OCC BW_J3_low ch_bw=40 MHz _p14a0028

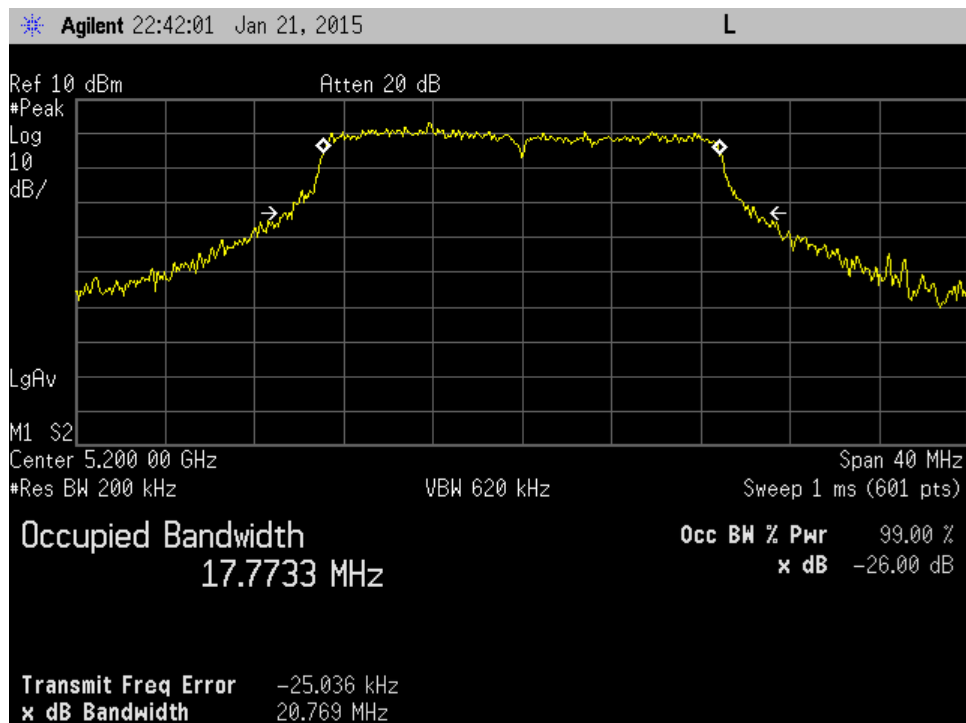




OCC BW_J3_mid ch_bw=10 MHz _p14a0028

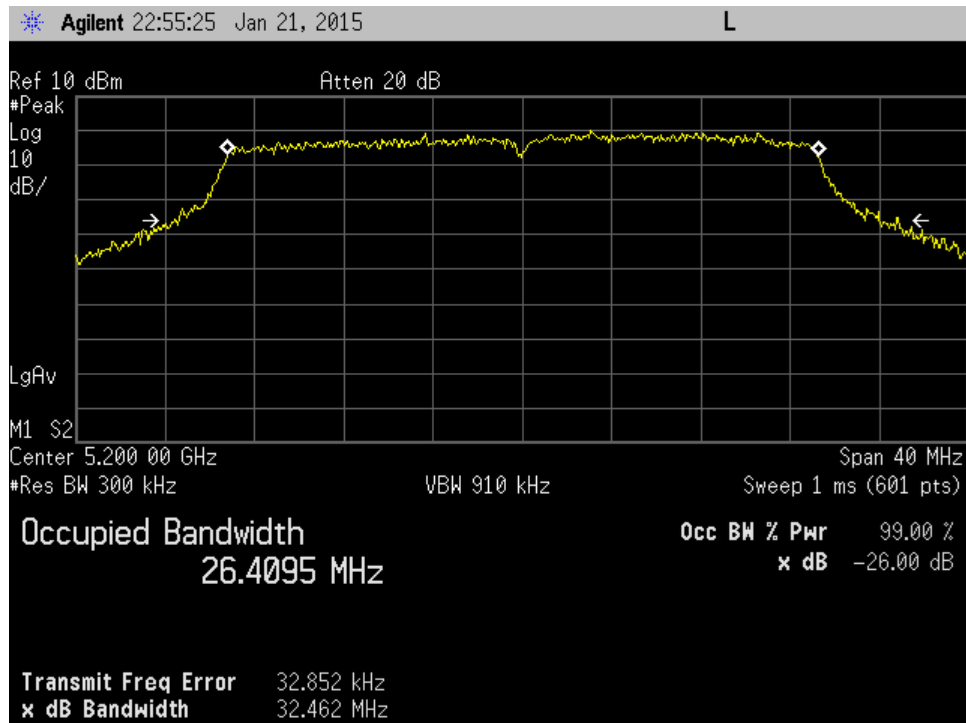


OCC BW_J3_mid ch_bw=20 MHz _p14a0028





OCC BW_J3_mid ch_bw=30 MHz _p14a0028



OCC BW_J3_mid ch_bw=40 MHz _p14a0028

