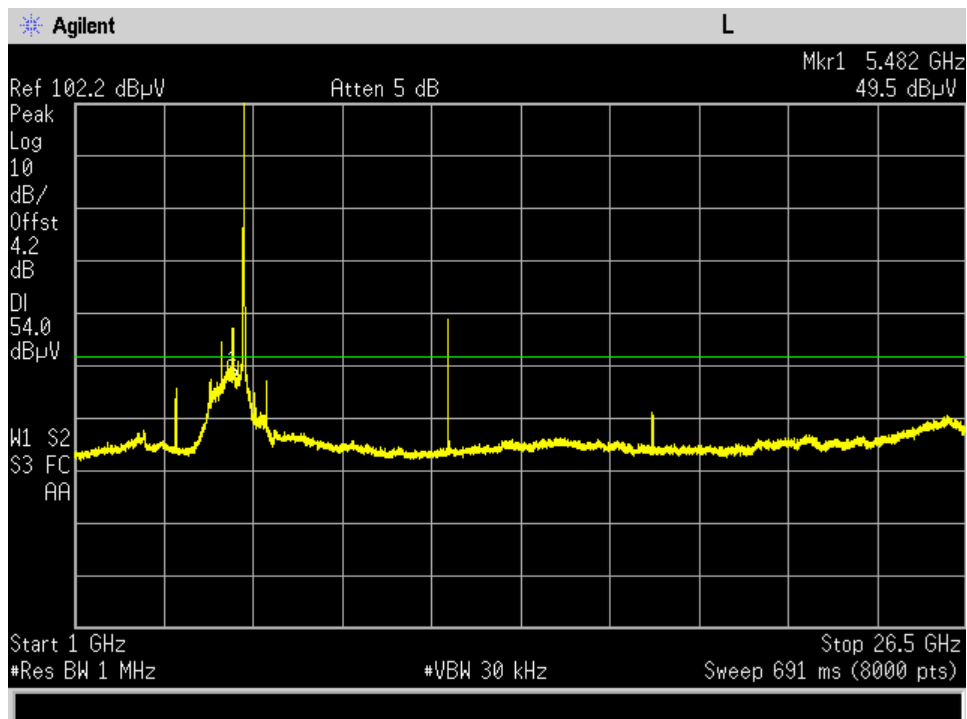


Annex A

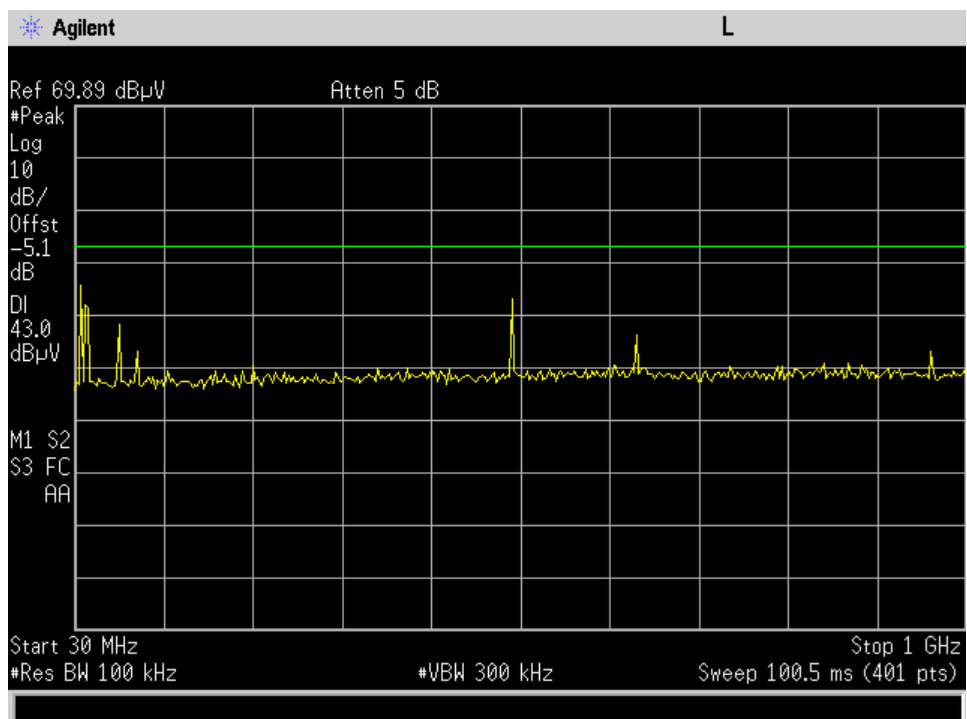
Undesirable Emissions Conducted



high ch port 1 10MHz BW 1-26.5G

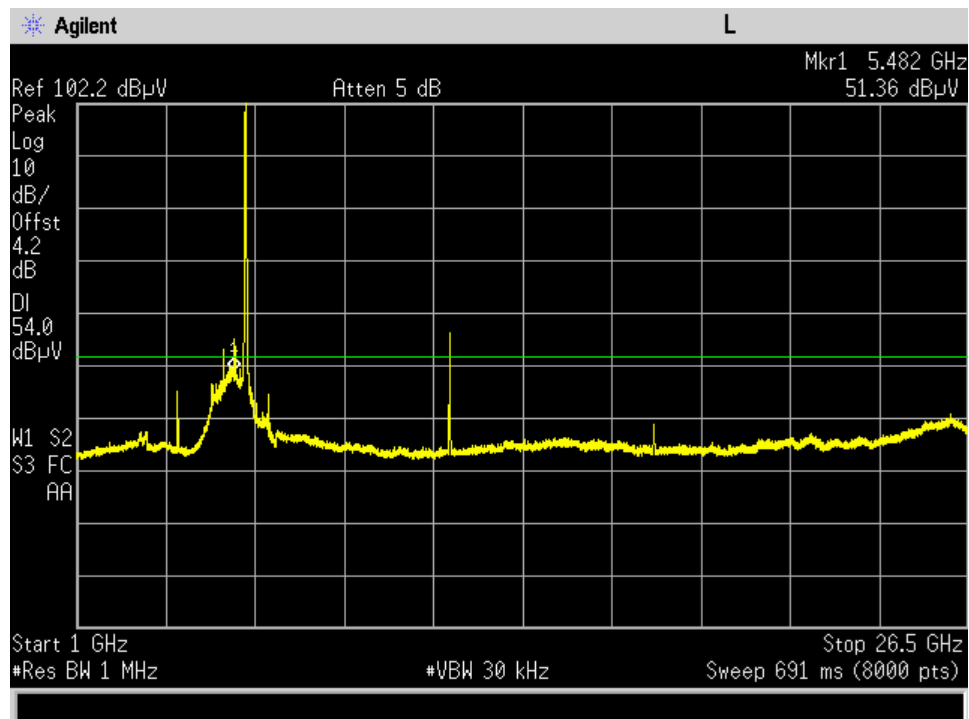


high ch port 1 10MHz BW 30-1000MHz

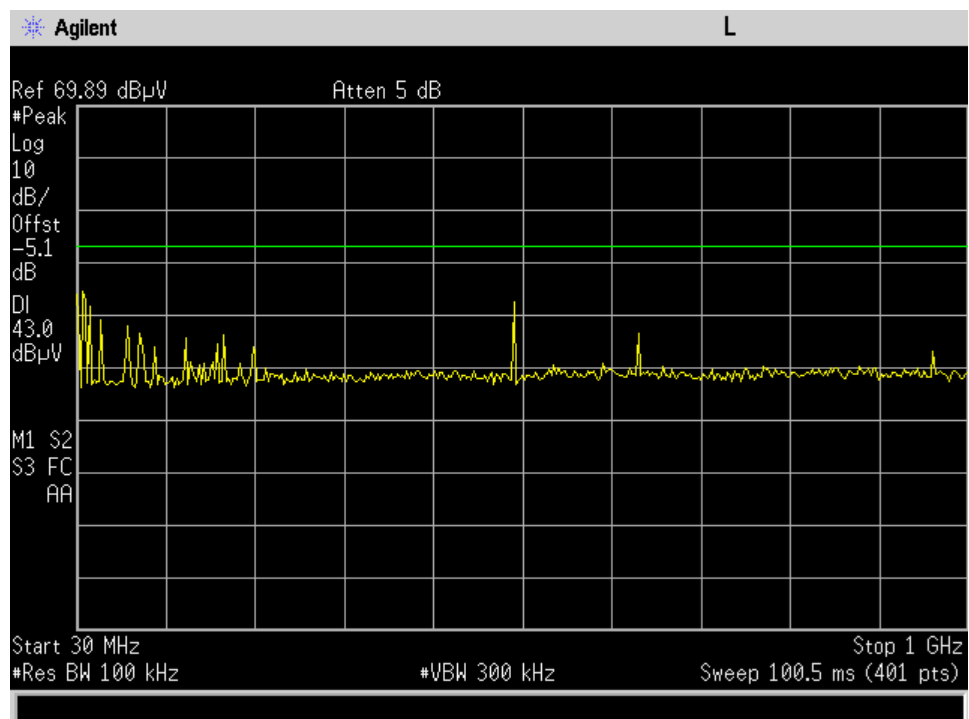




high ch port 1 20MHz BW 1-26.5G

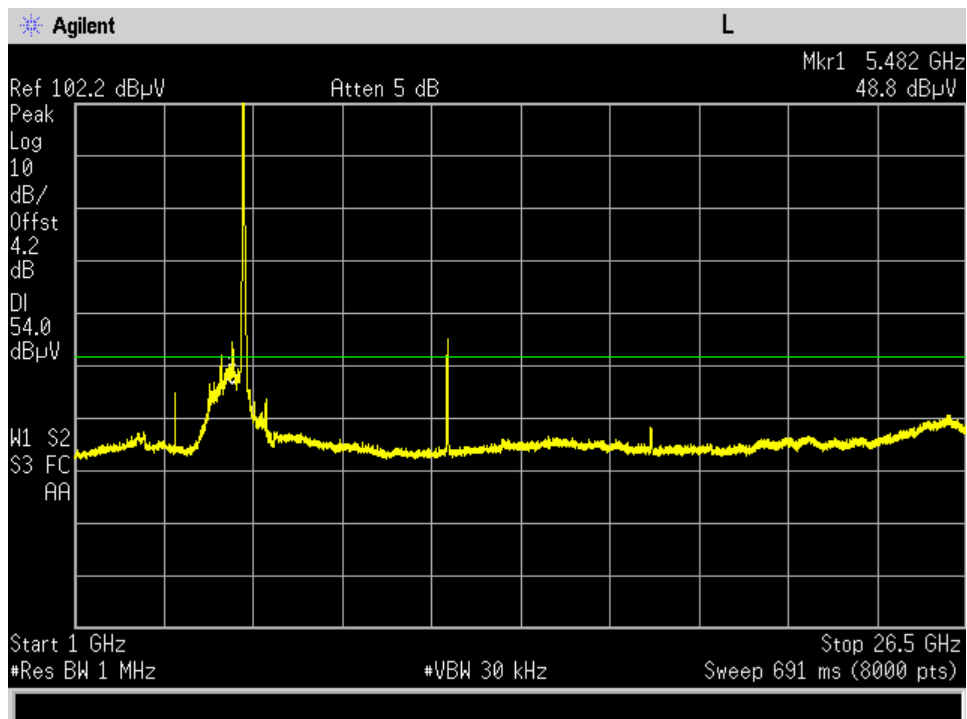


high ch port 1 20MHz BW 30-1000MHz

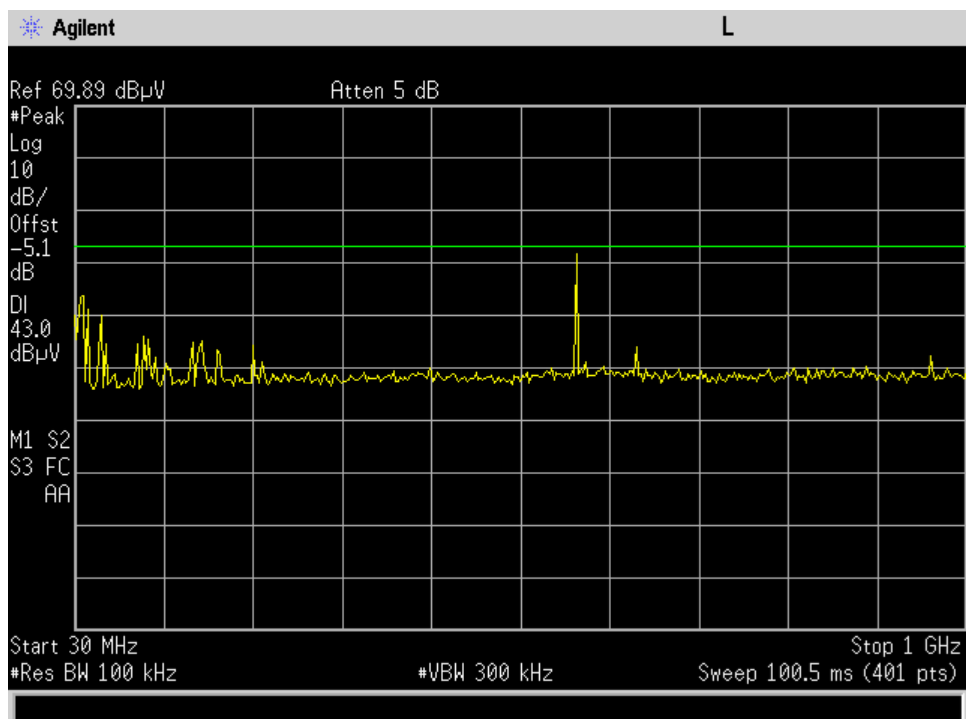




high ch port 1 30MHz BW 1-26.5G

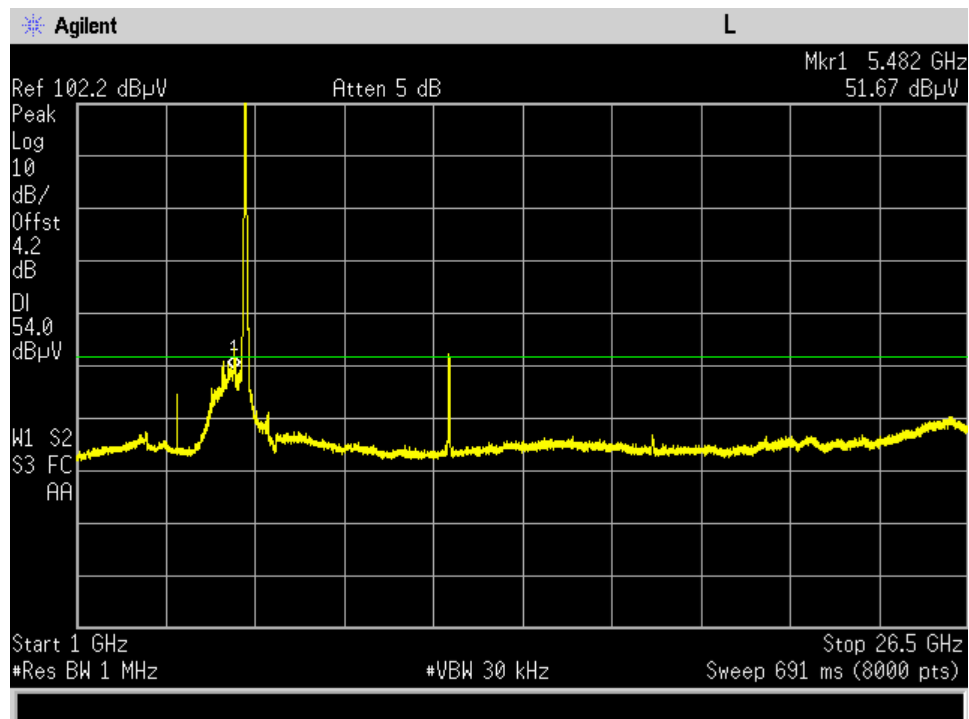


high ch port 1 30MHz BW 30-1000MHz

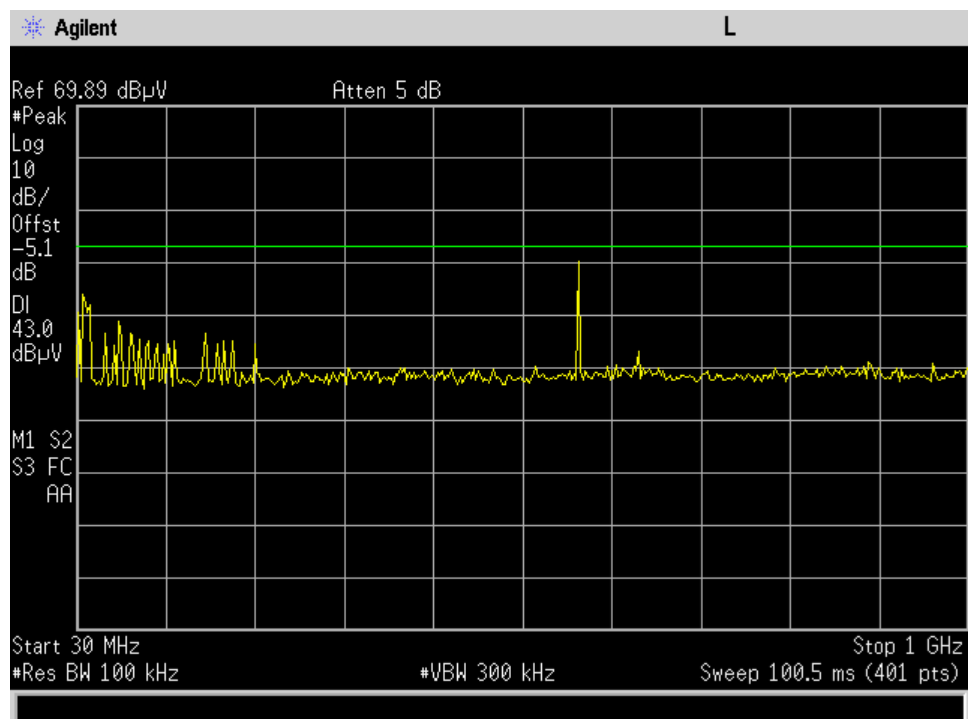




high ch port 1 40MHz BW 1-26.5G

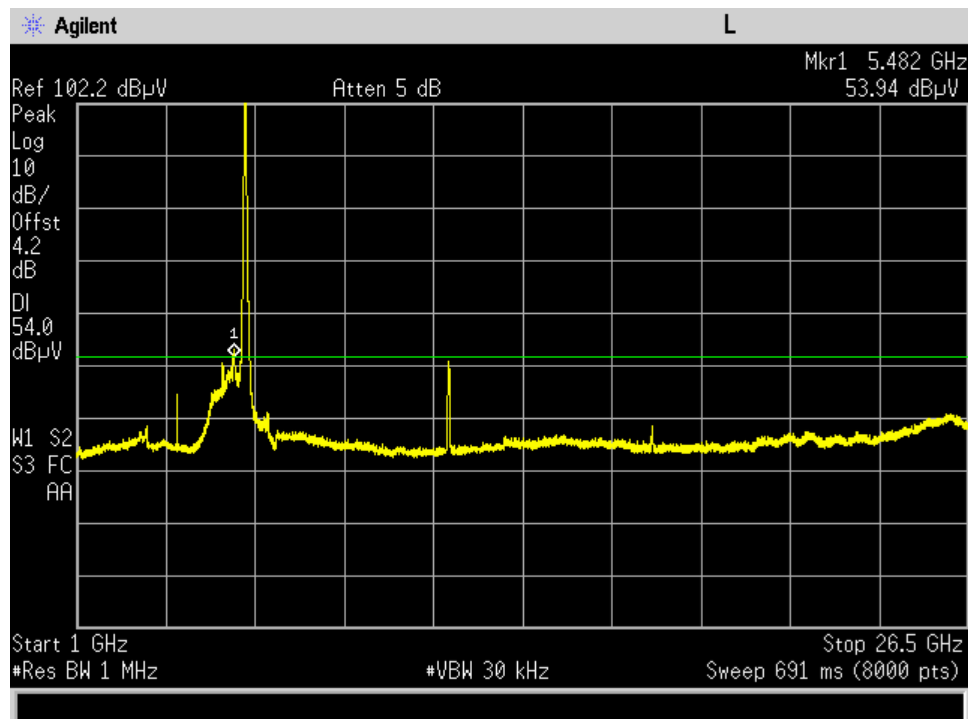


high ch port 1 40MHz BW 30-1000MHz

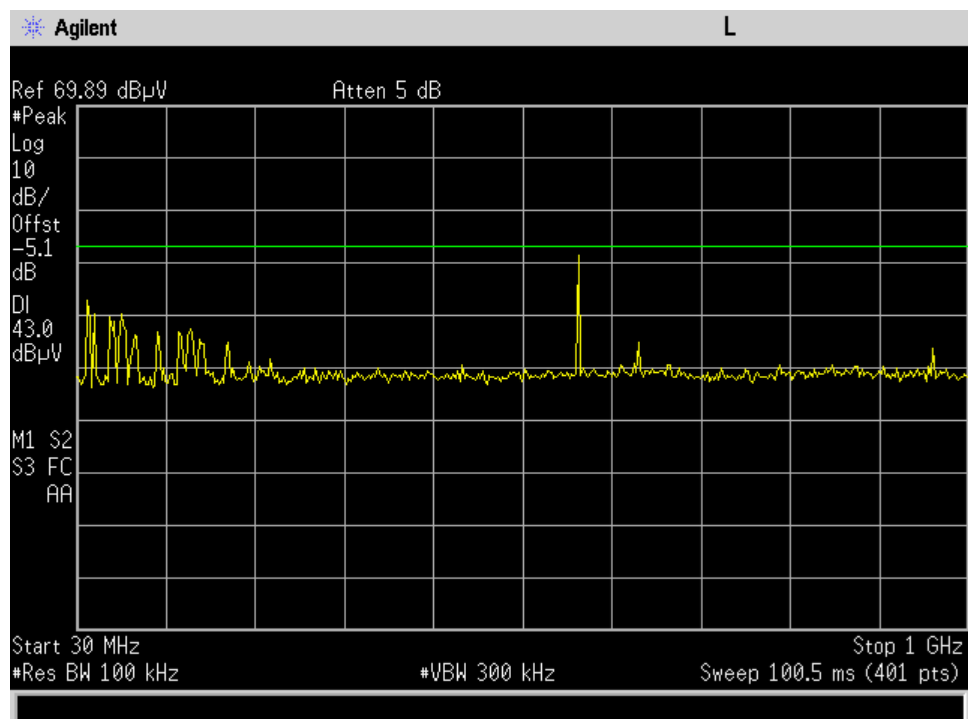




high ch port 1 50MHz BW 1-26.5G

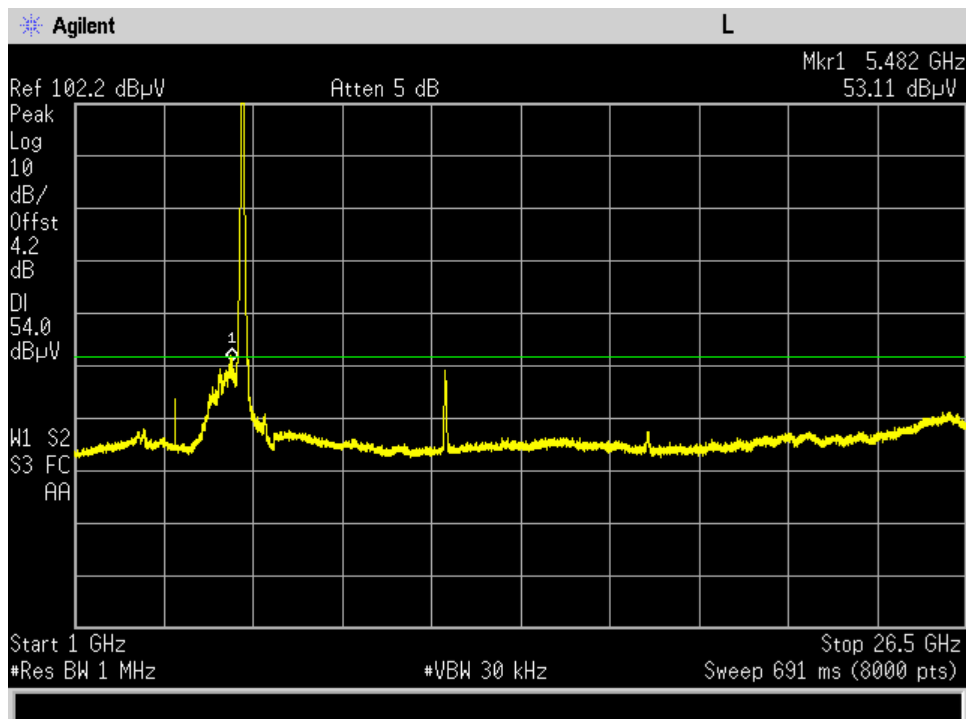


high ch port 1 50MHz BW 30-1000MHz

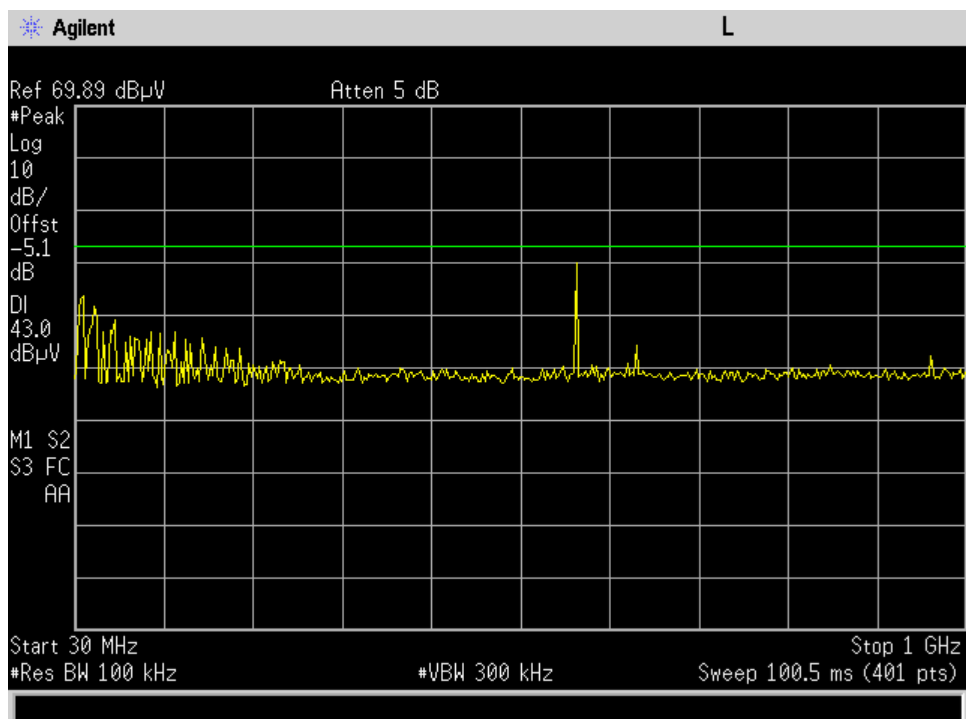




high ch port 1 60MHz BW 1-26.5G

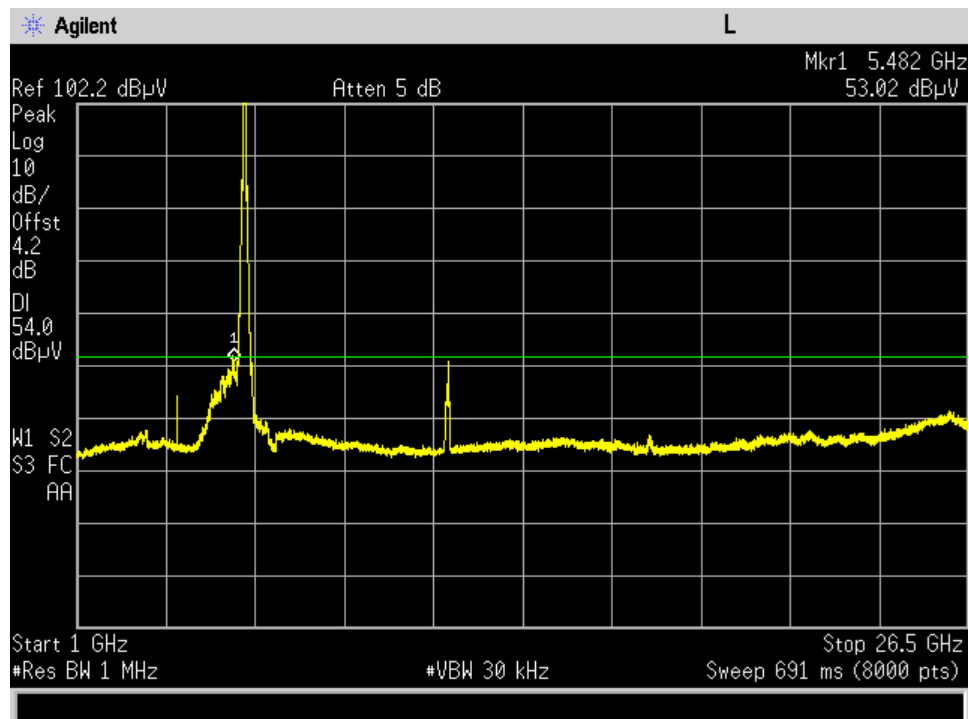


high ch port 1 60MHz BW 30-1000MHz

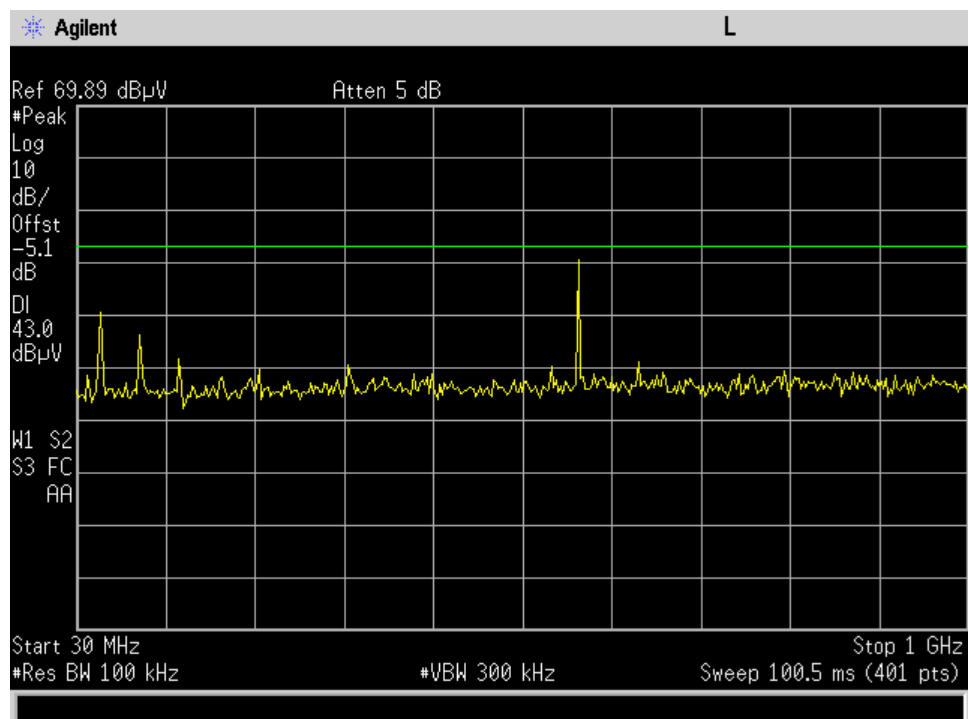




high ch port 1 80MHz BW 1-26.5G

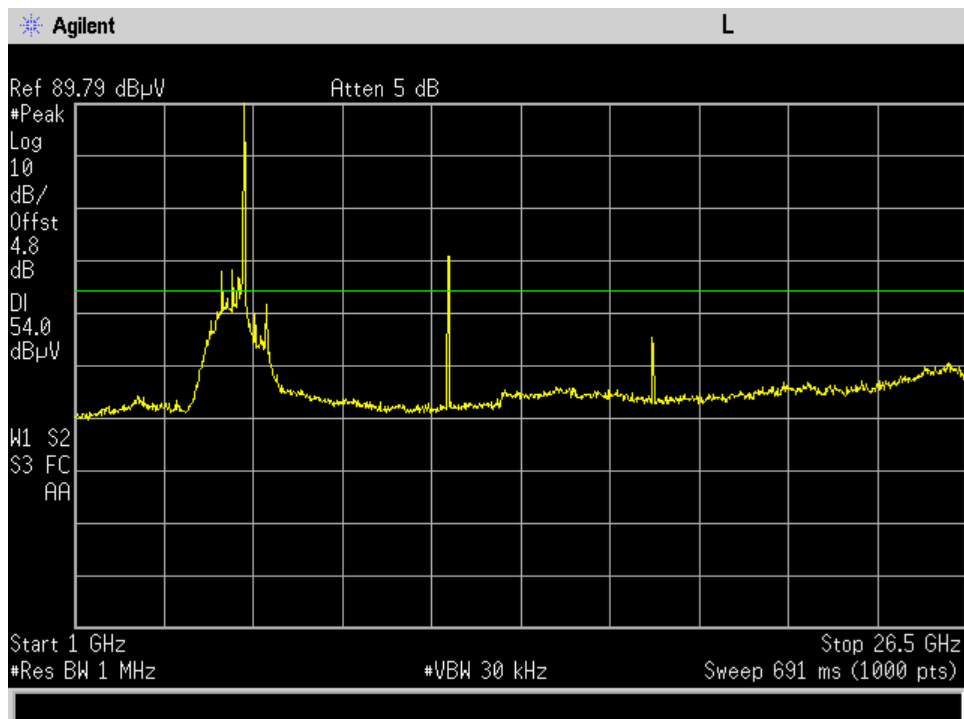


high ch port 1 80MHz BW 30-1000MHz

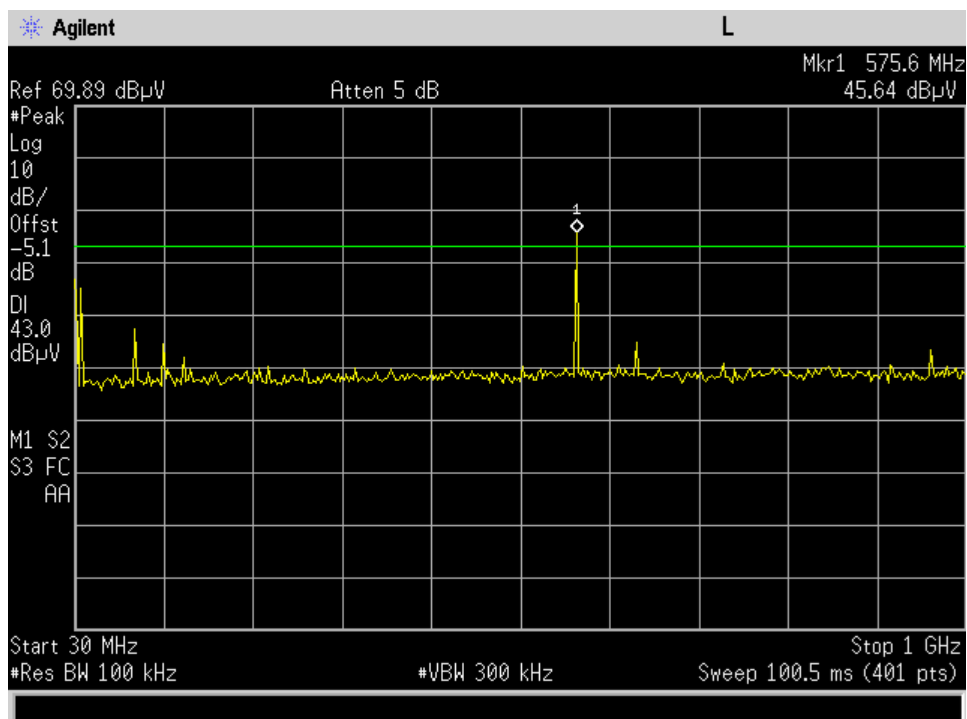




high ch port 2 10MHz BW 1-26.5G

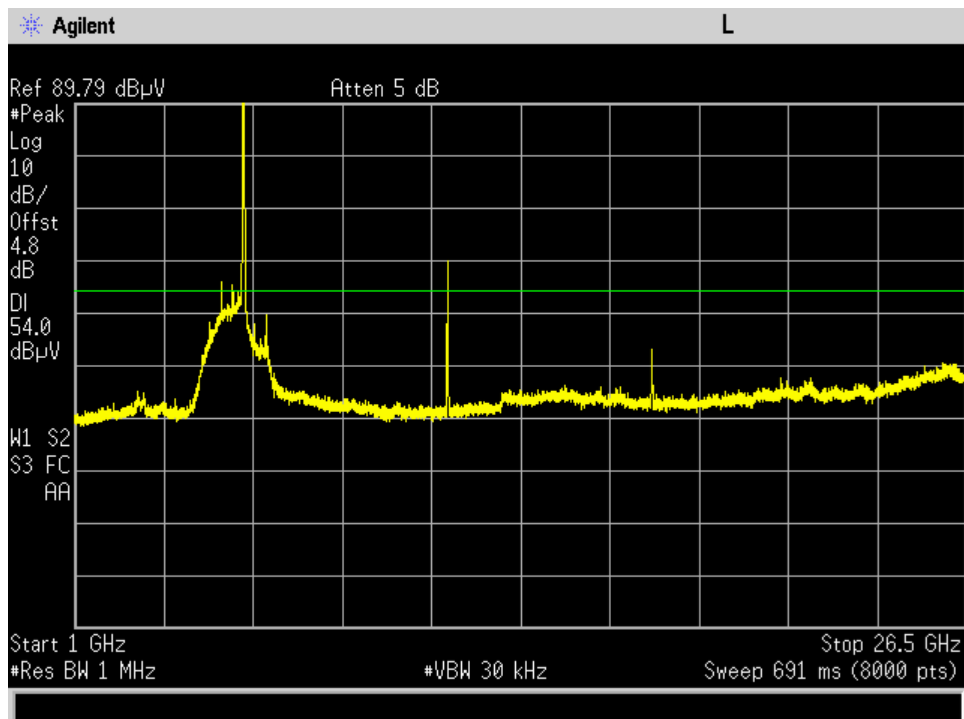


high ch port 2 10MHz BW 30-1000MHz

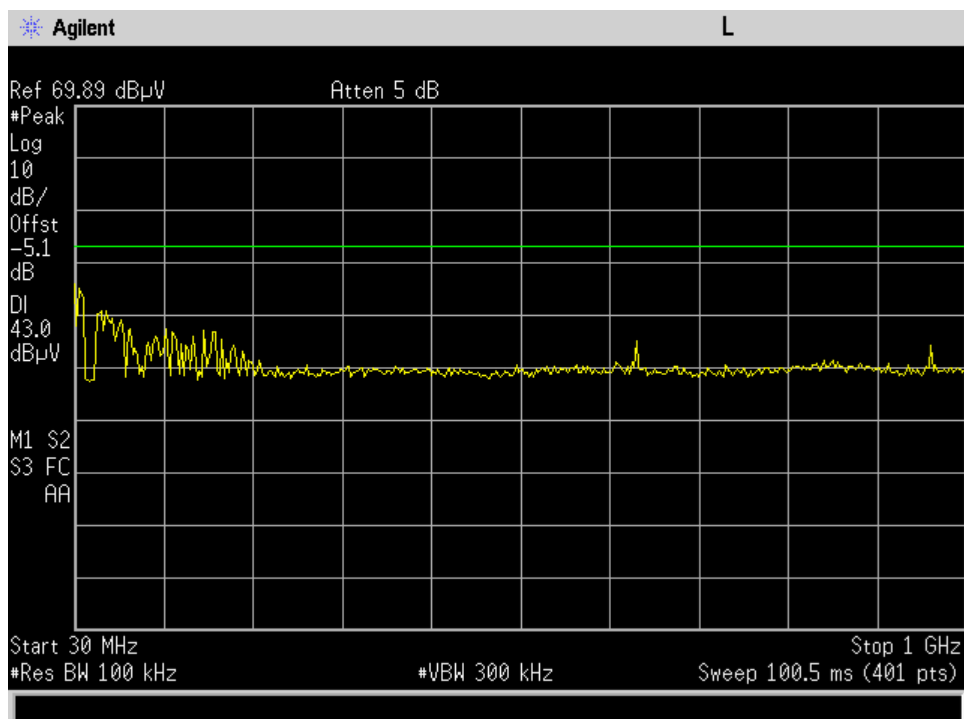




high ch port 2 20MHz BW 1-26.5G

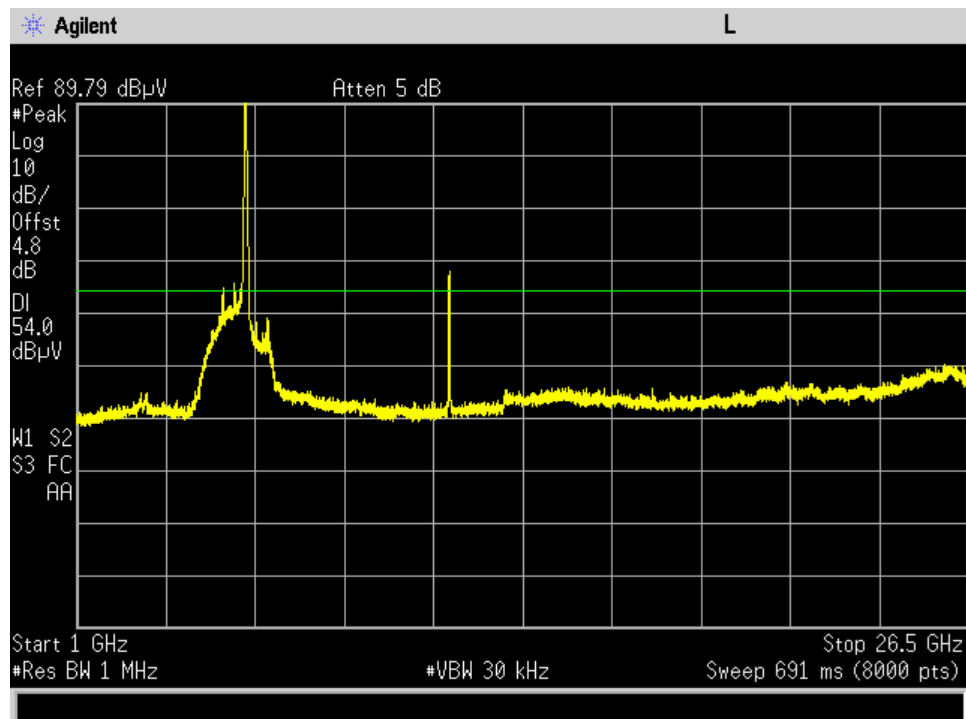


high ch port 2 20MHz BW 30-1000MHz

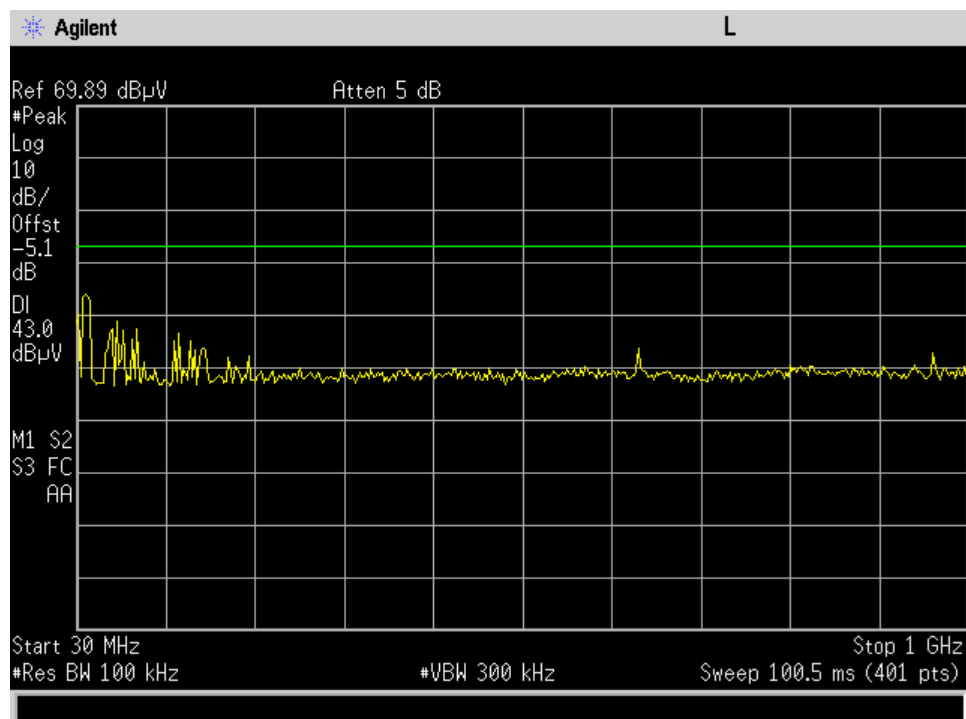




high ch port 2 30MHz BW 1-26.5G

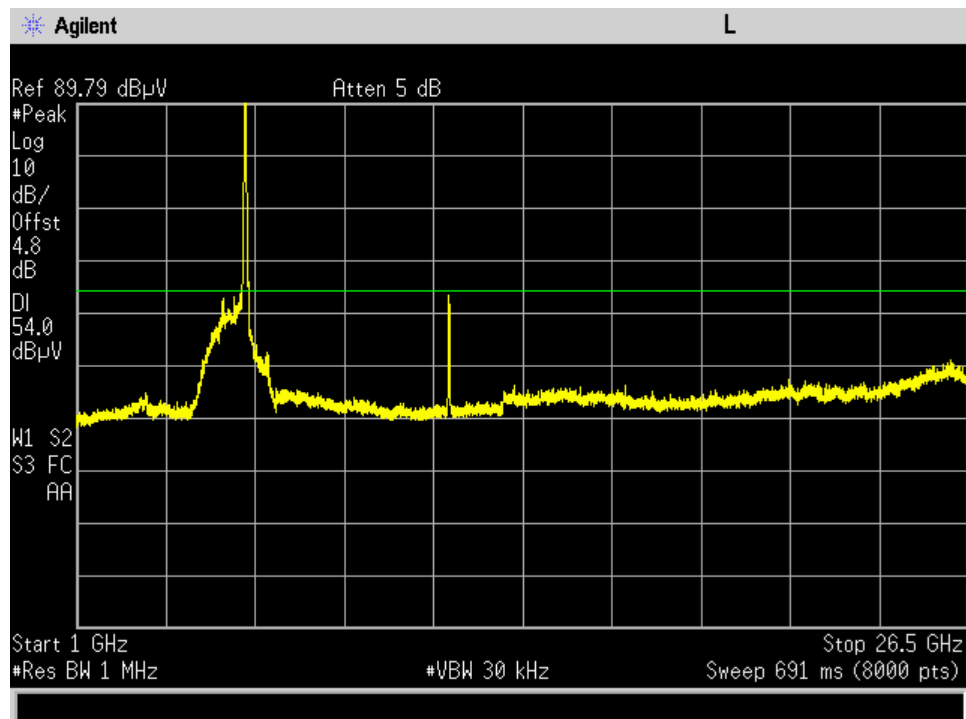


high ch port 2 30MHz BW 30-1000MHz

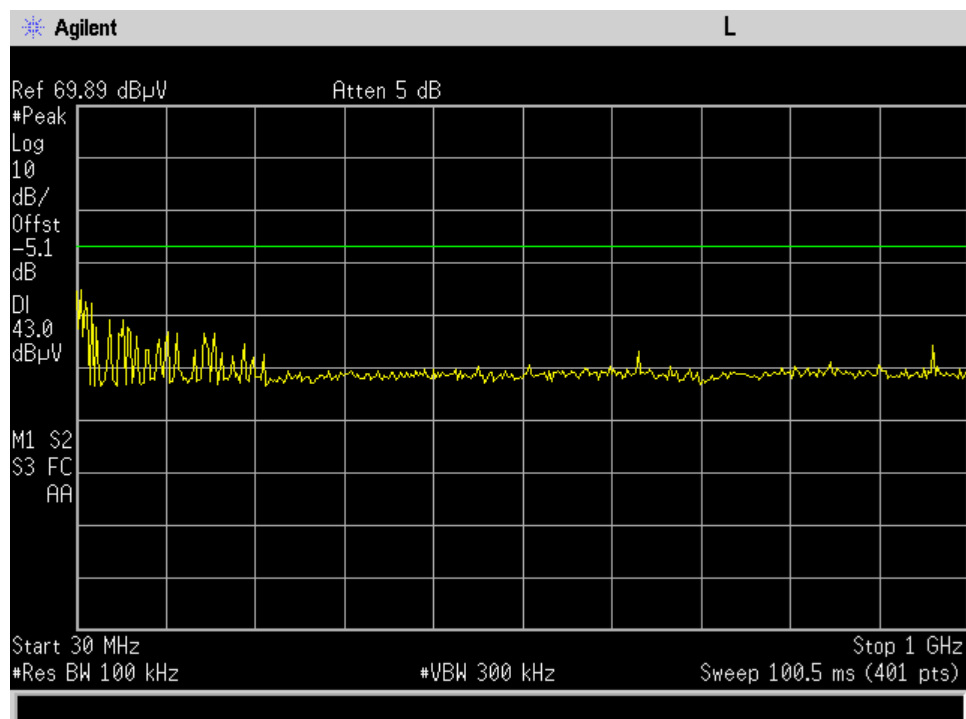




high ch port 2 40MHz BW 1-26.5G

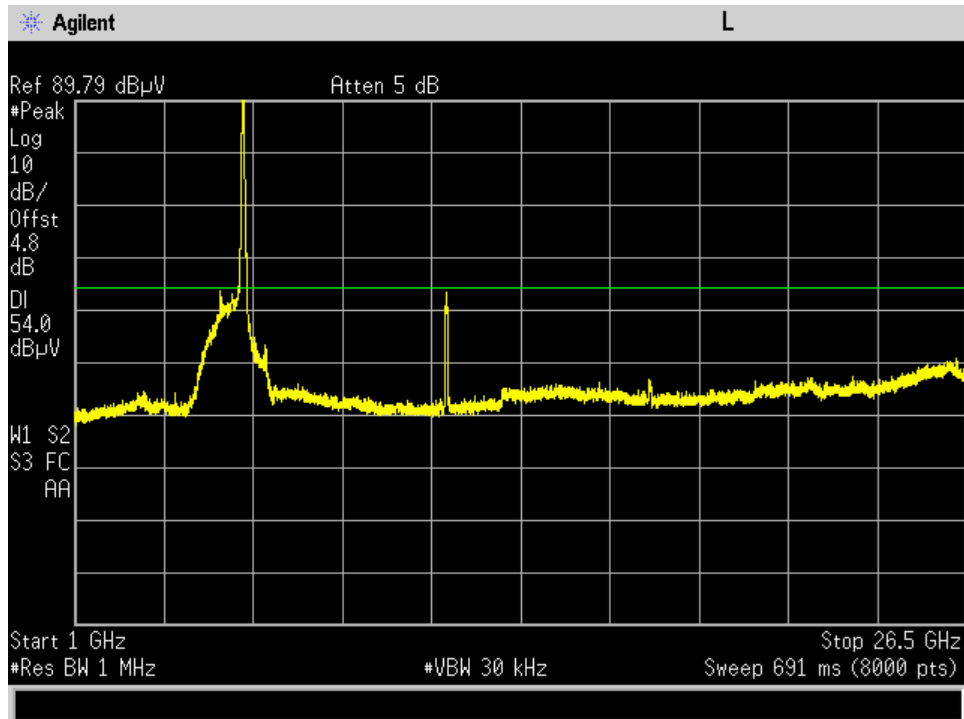


high ch port 2 40MHz BW 30-1000MHz

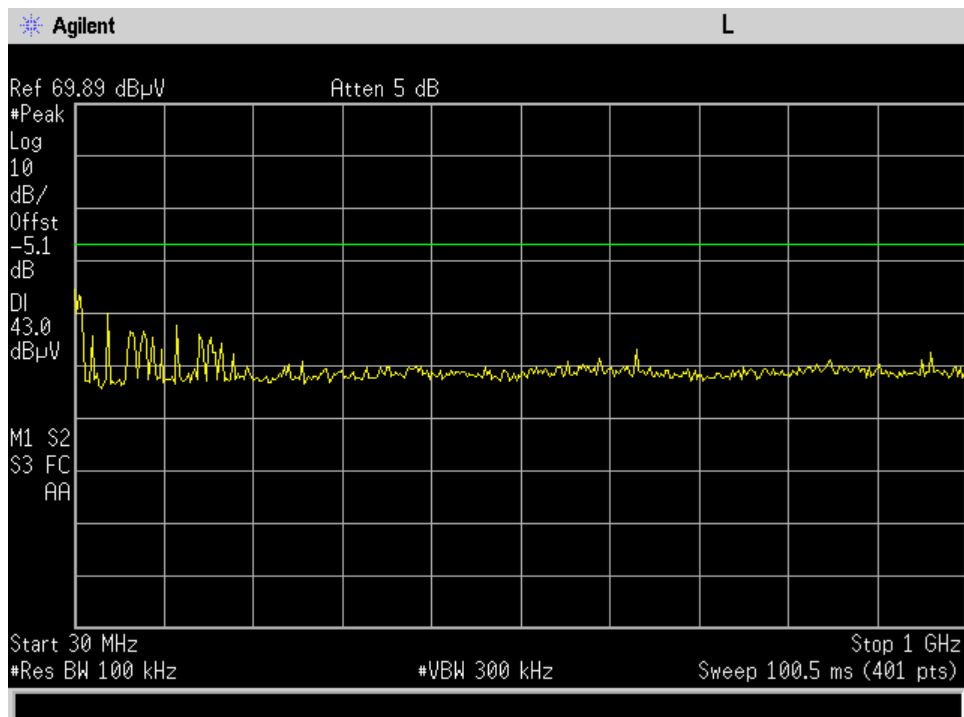




high ch port 2 50MHz BW 1-26.5G

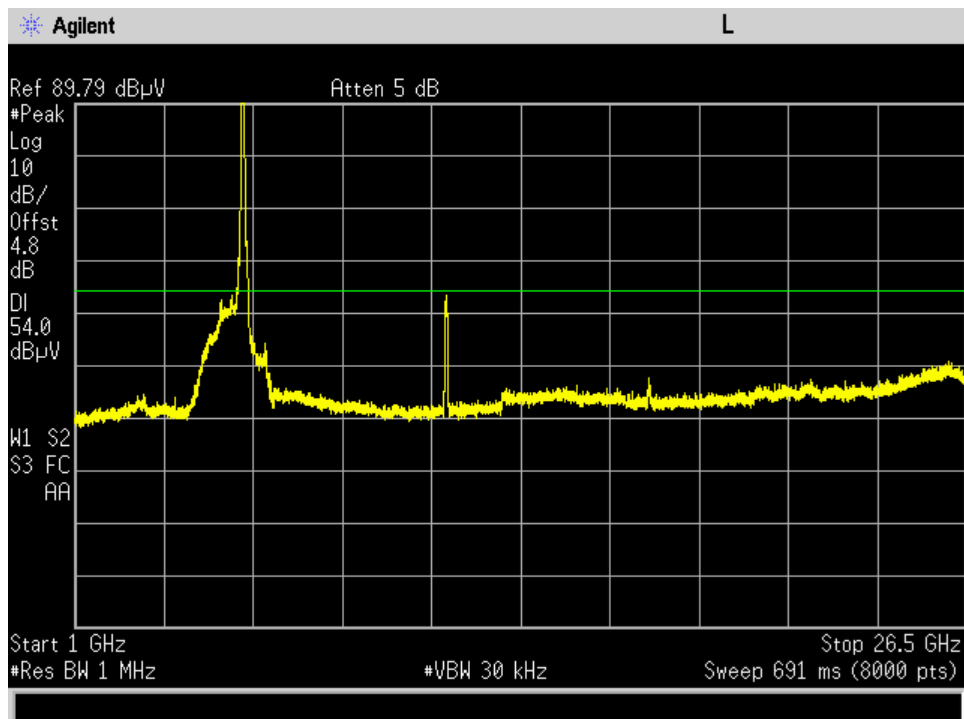


high ch port 2 50MHz BW 30-1000MHz

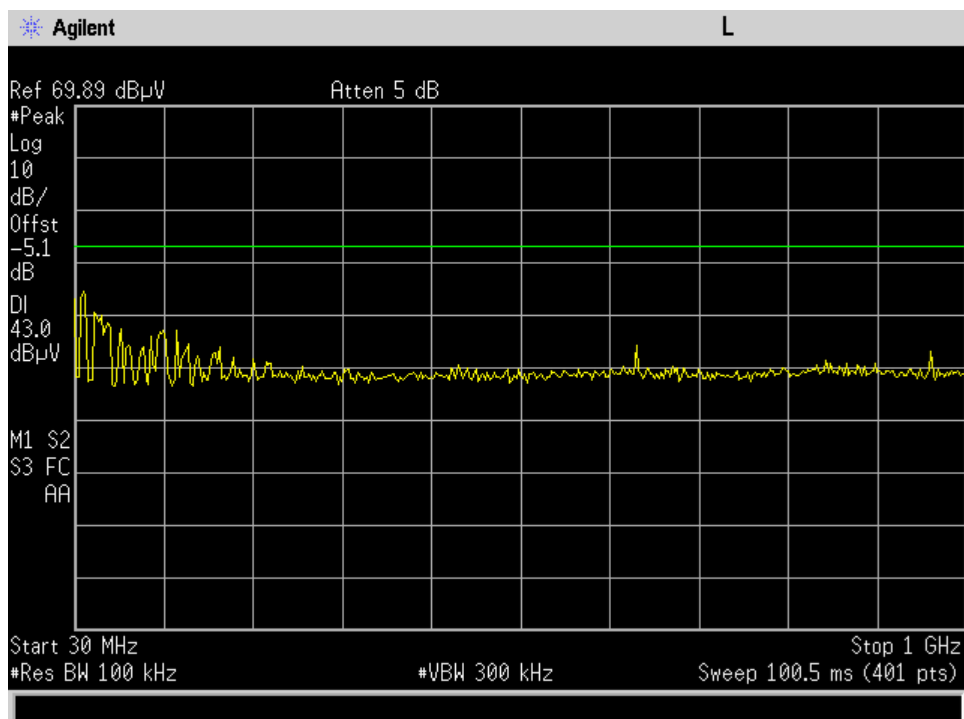




high ch port 2 60MHz BW 1-26.5G

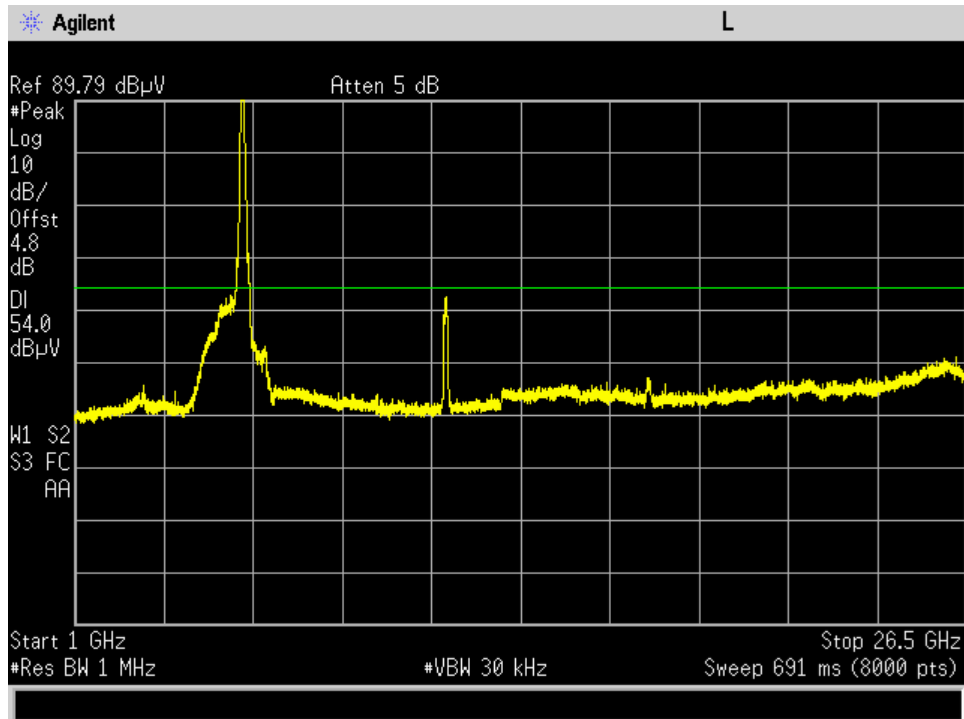


high ch port 2 60MHz BW 30-1000MHz

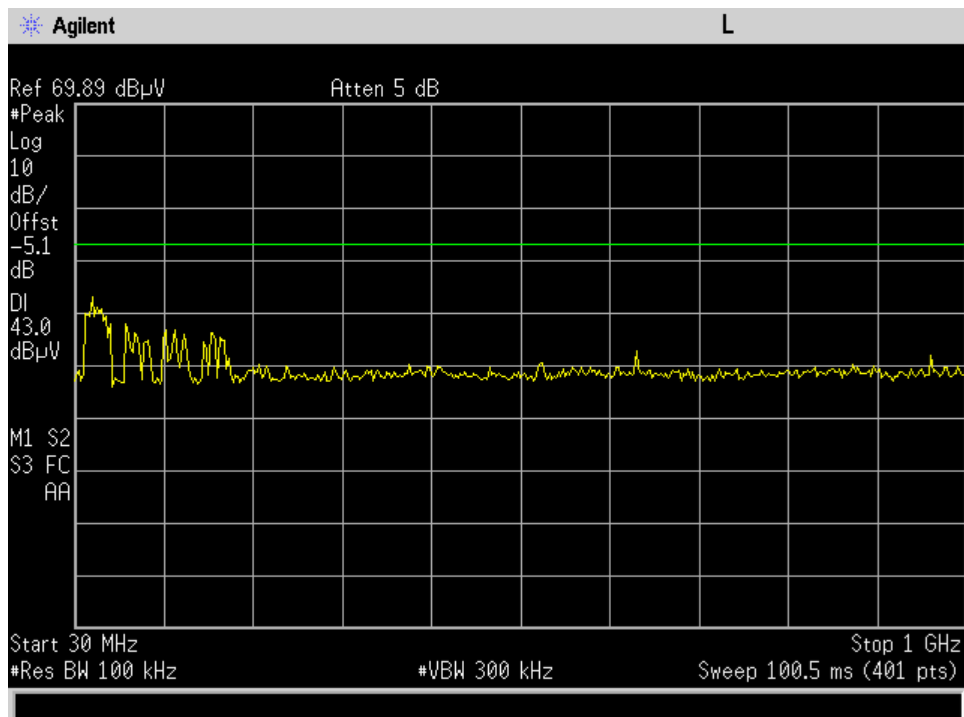




high ch port 2 80MHz BW 1-26.5G

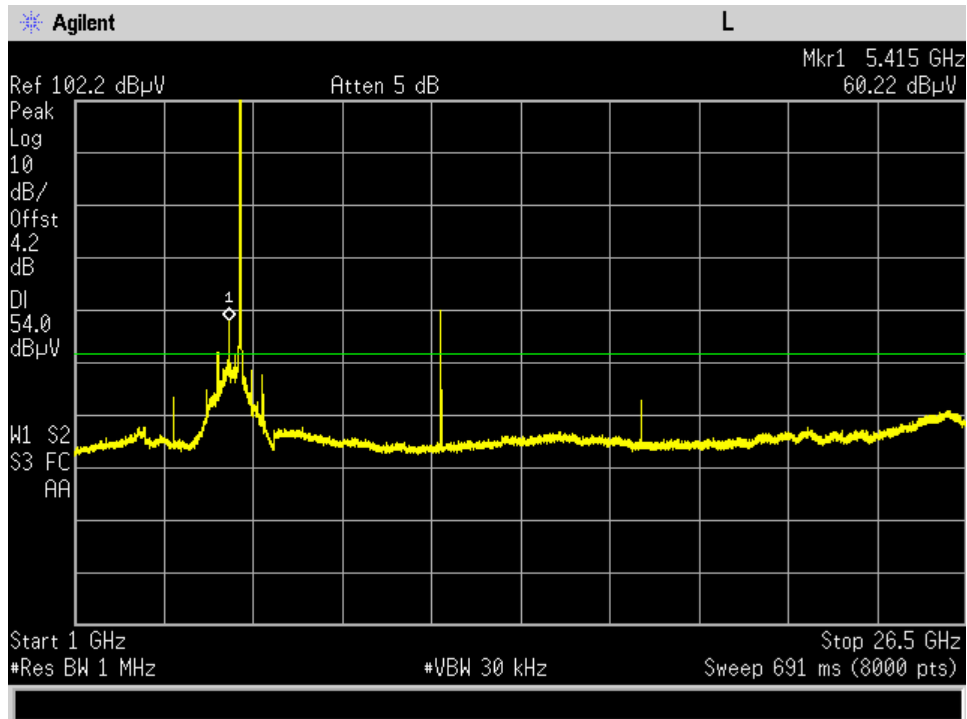


high ch port 2 80MHz BW 30-1000MHz

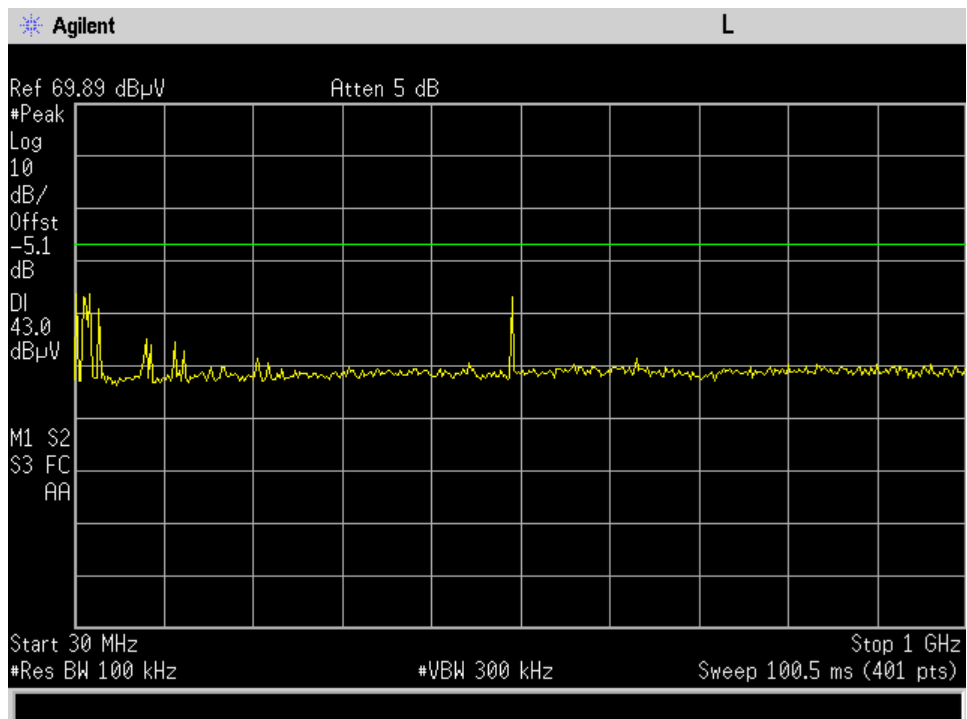




low ch port 1 10MHz BW 1-26.5G

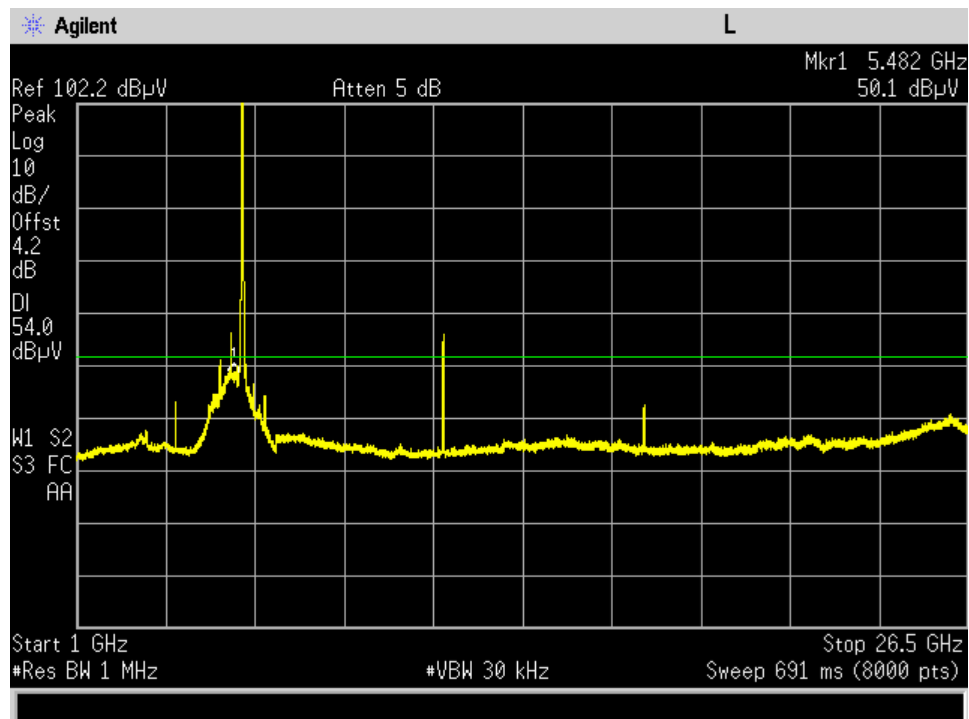


low ch port 1 10MHz BW 30-1000MHz

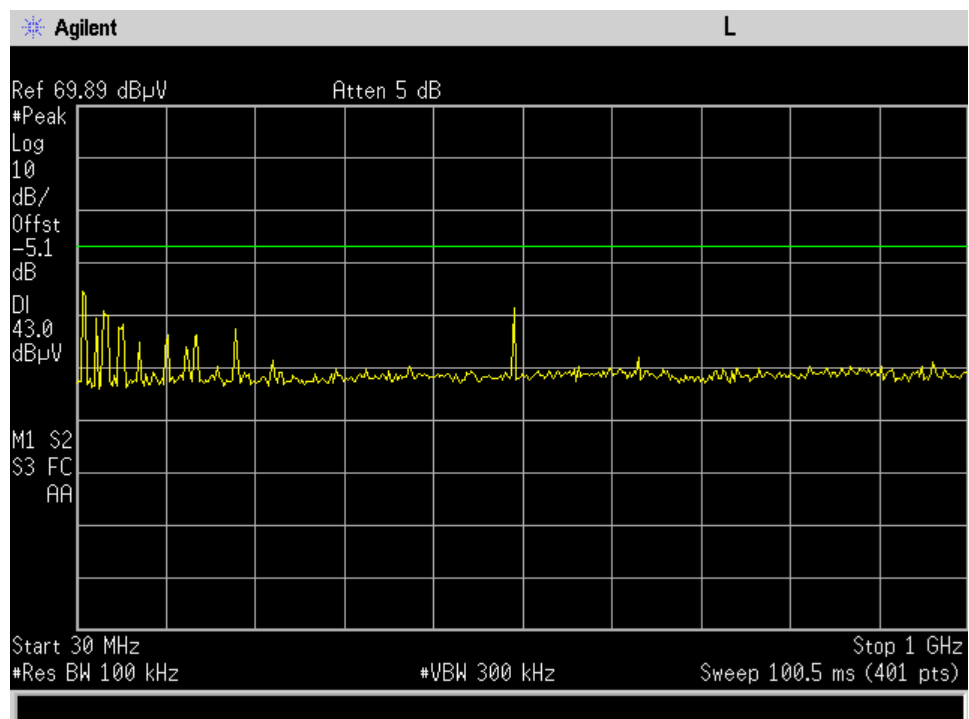




low ch port 1 20MHz BW 1-26.5G

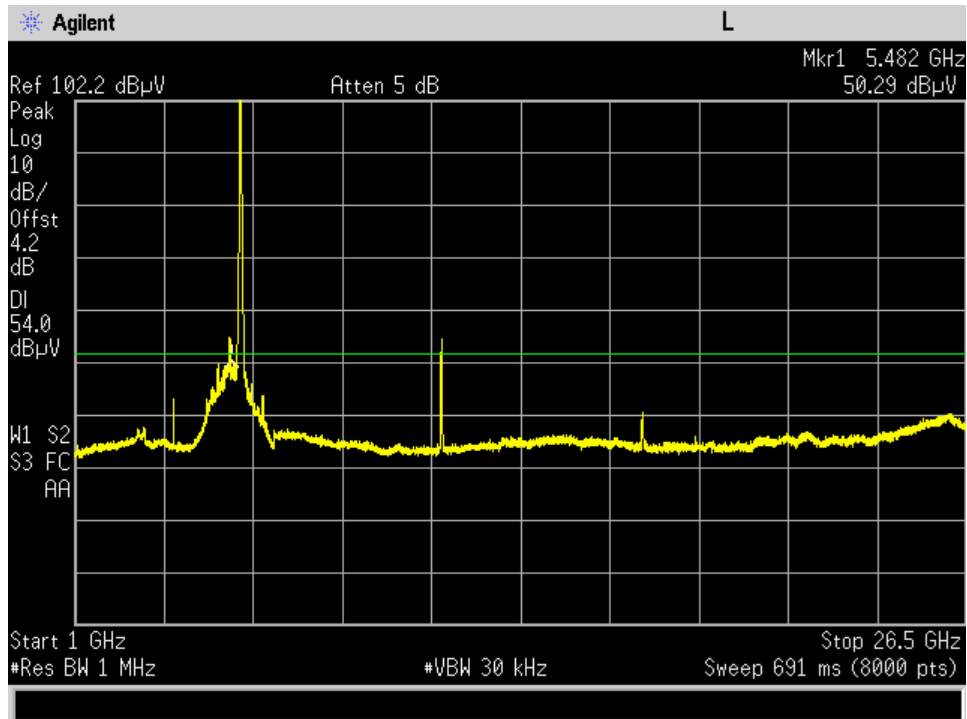


low ch port 1 20MHz BW 30-1000MHz

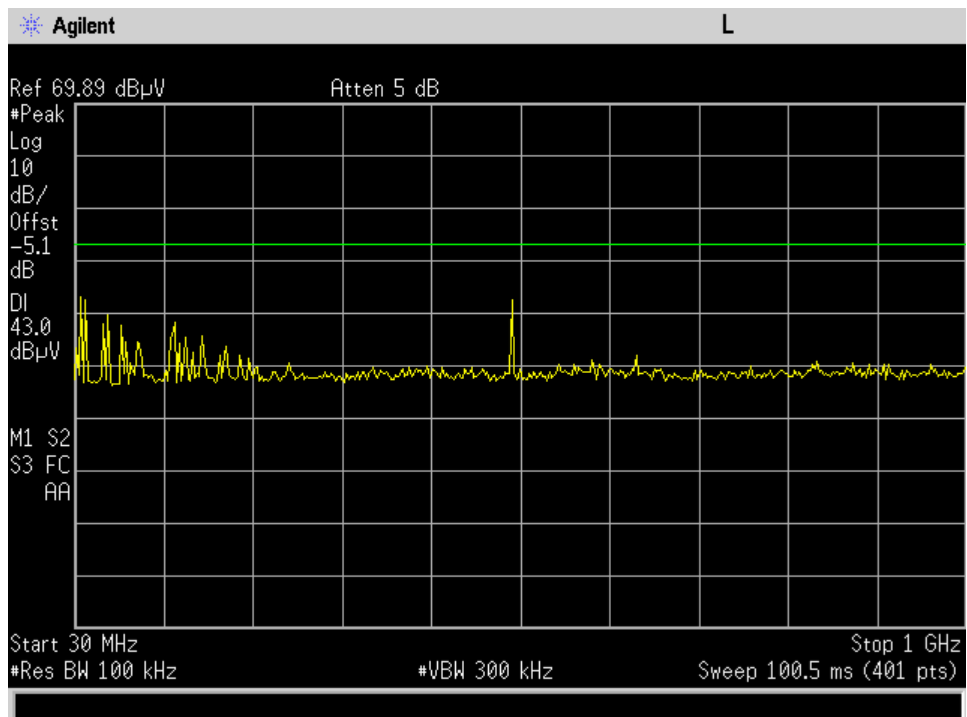




low ch port 1 30MHz BW 1-26.5G

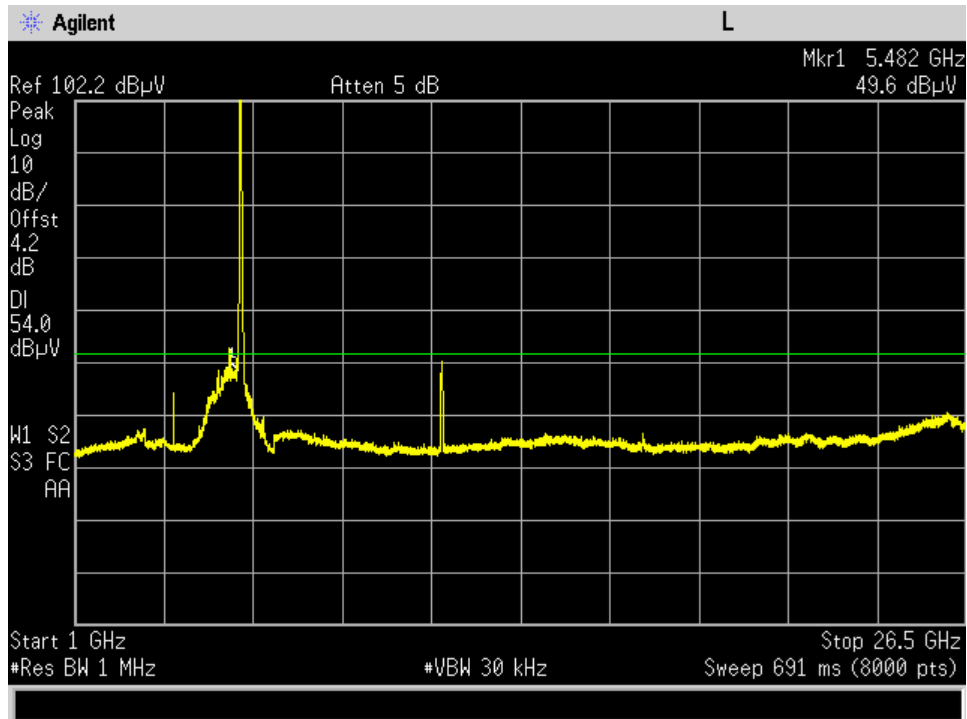


low ch port 1 30MHz BW 30-1000MHz

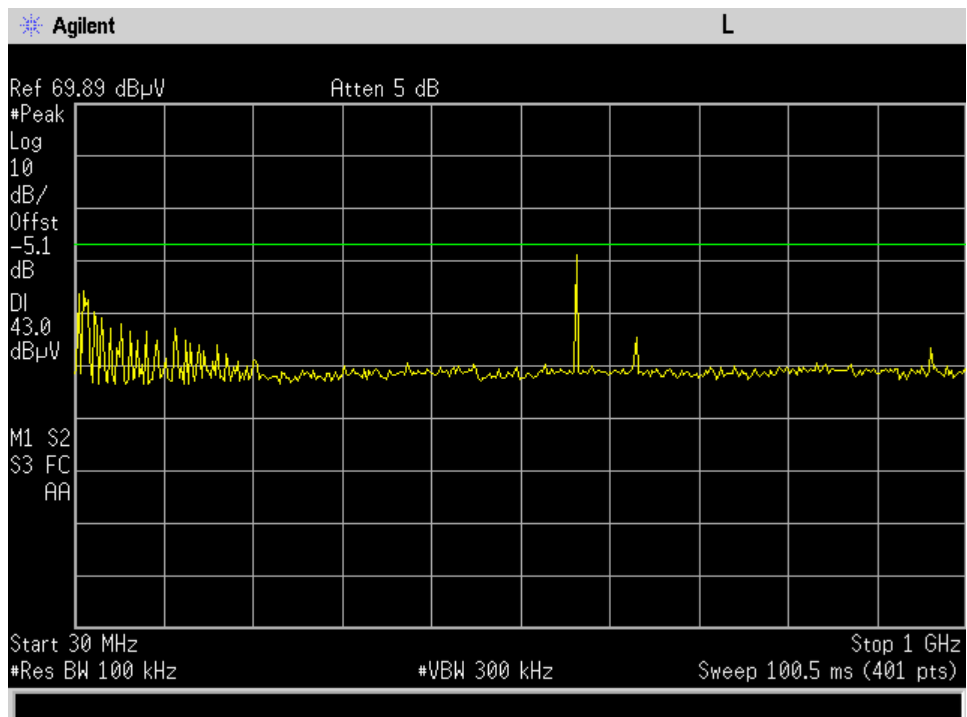




low ch port 1 40MHz BW 1-26.5G

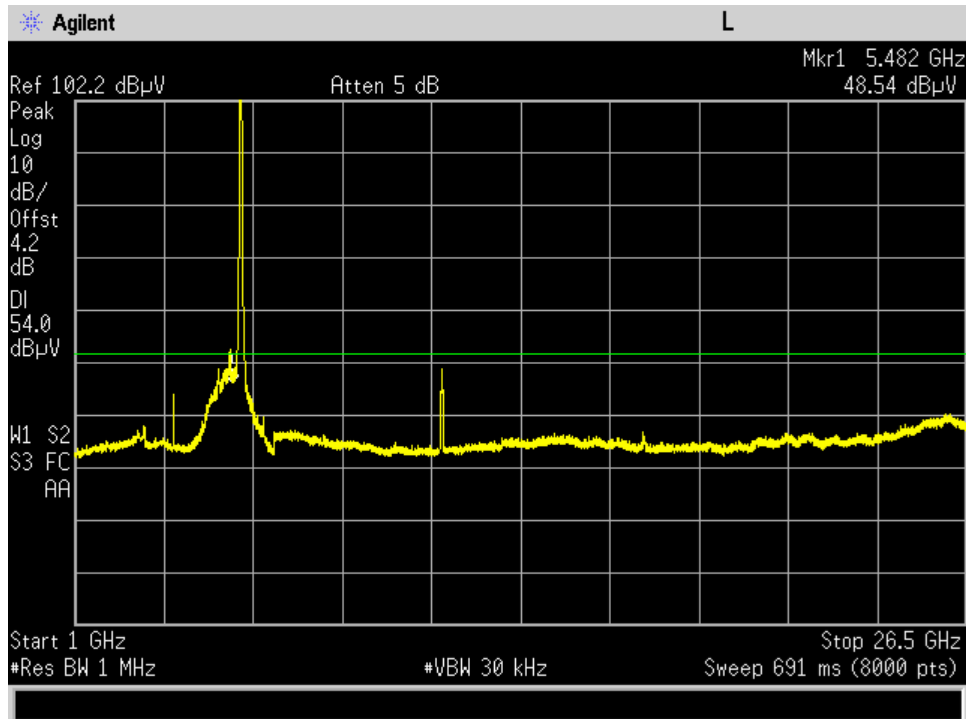


low ch port 1 40MHz BW 30-1000MHz

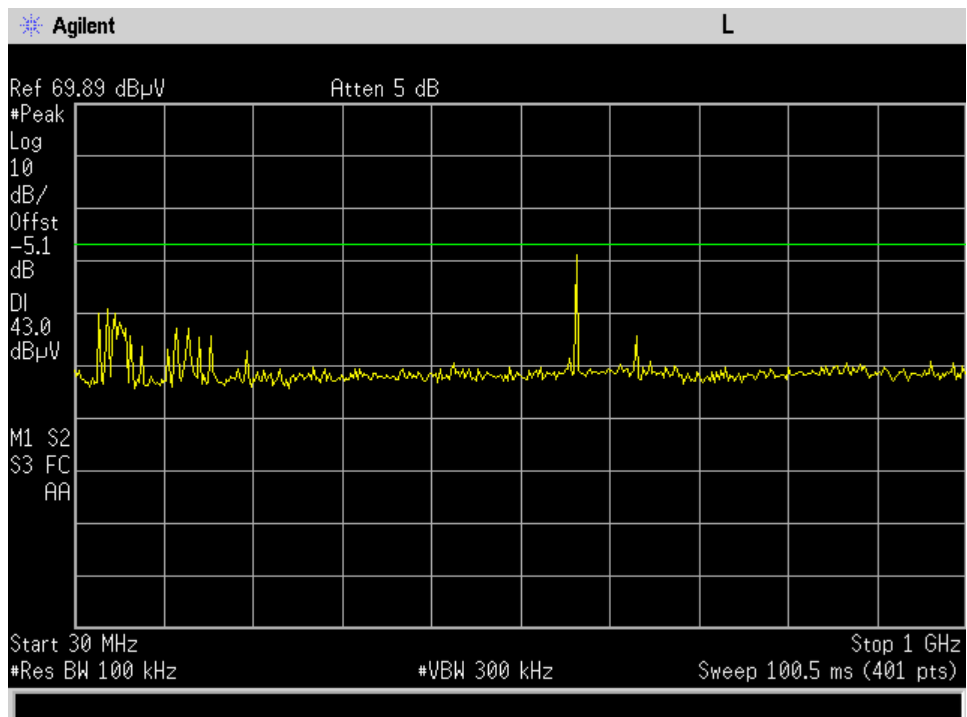




low ch port 1 50MHz BW 1-26.5G

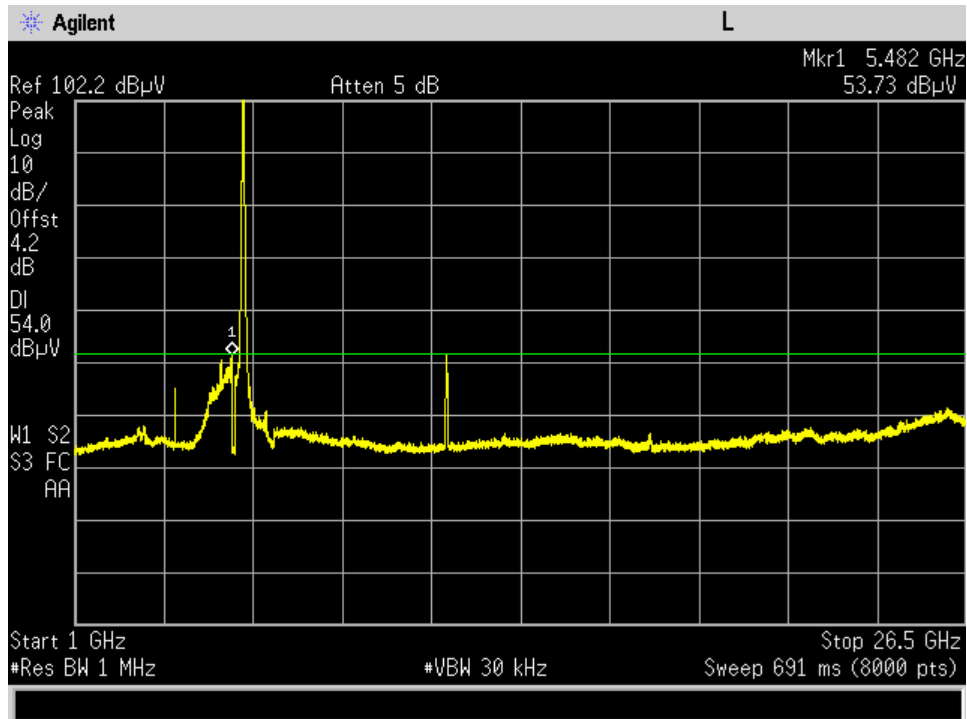


low ch port 1 50MHz BW 30-1000MHz

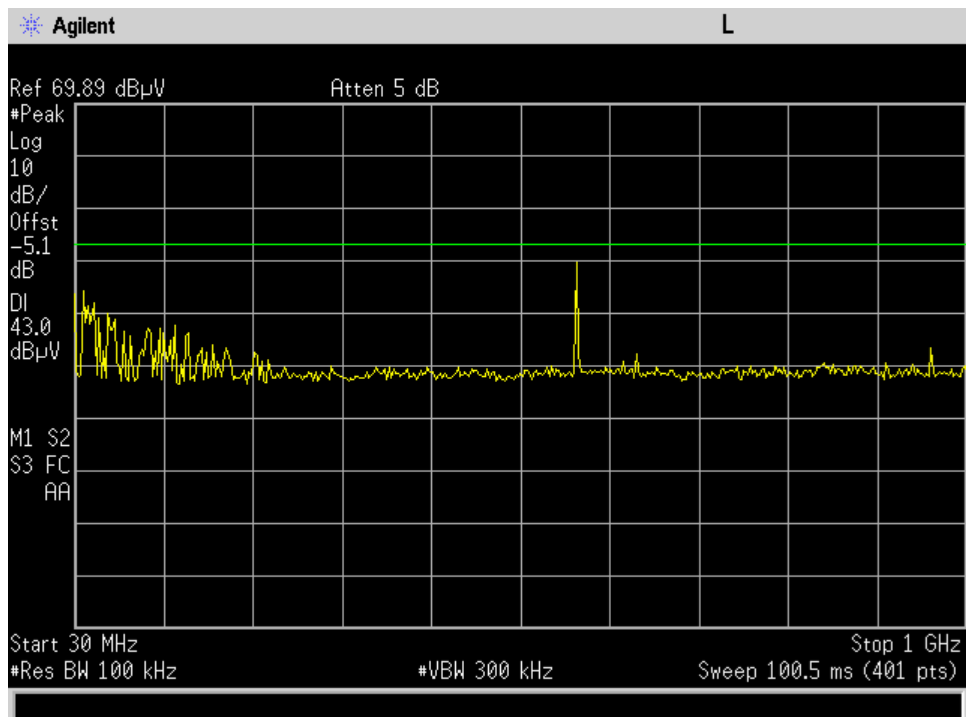




low ch port 1 60MHz BW 1-26.5G

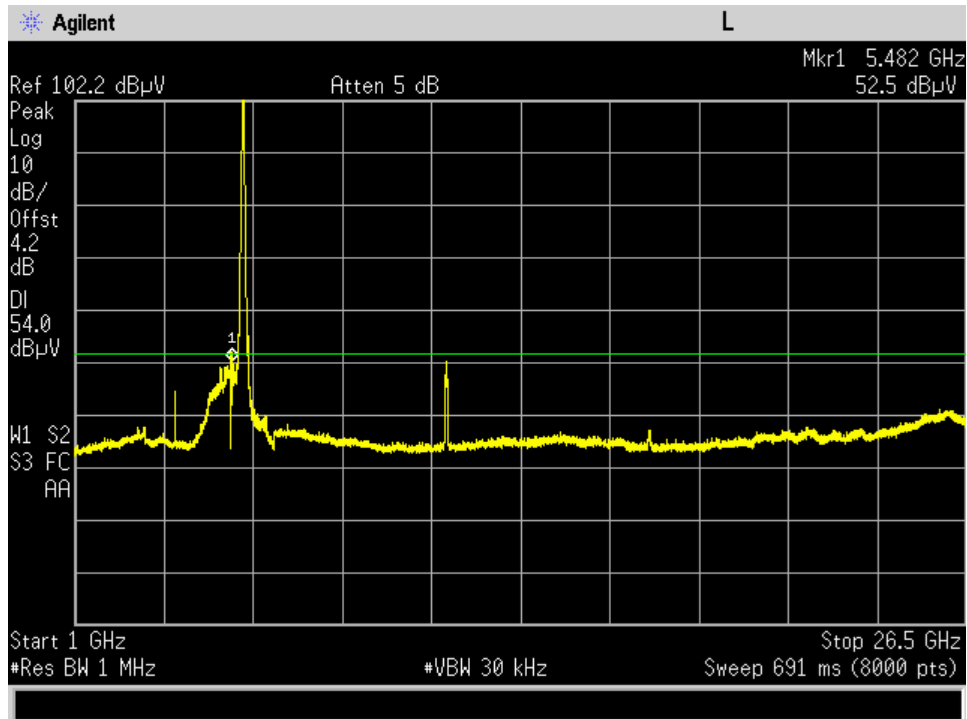


low ch port 1 60MHz BW 30-1000MHz

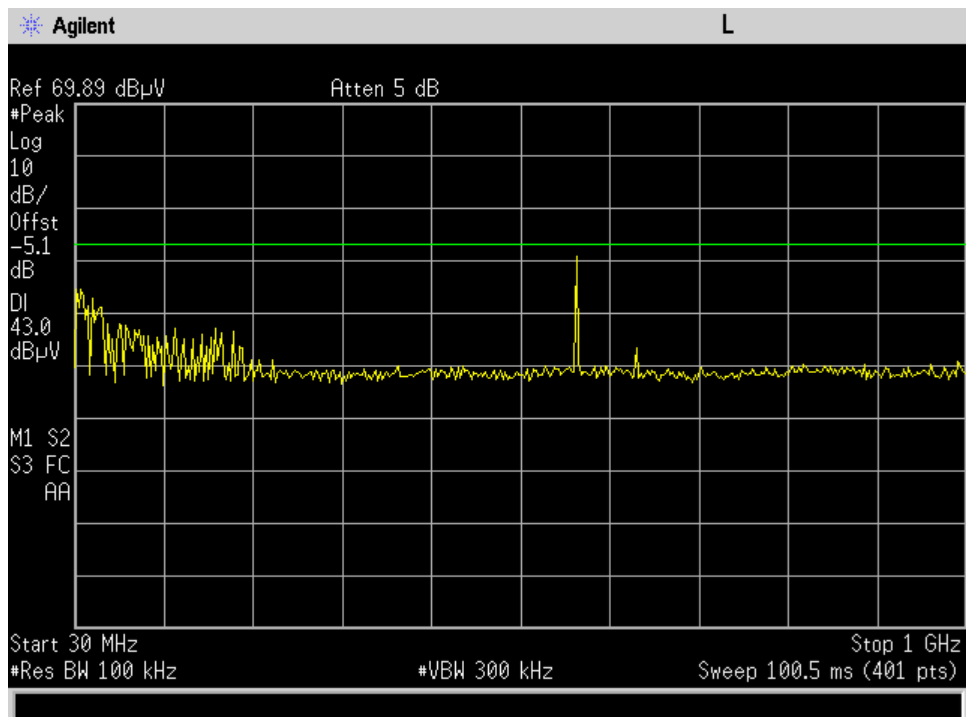




low ch port 1 80MHz BW 1-26.5G

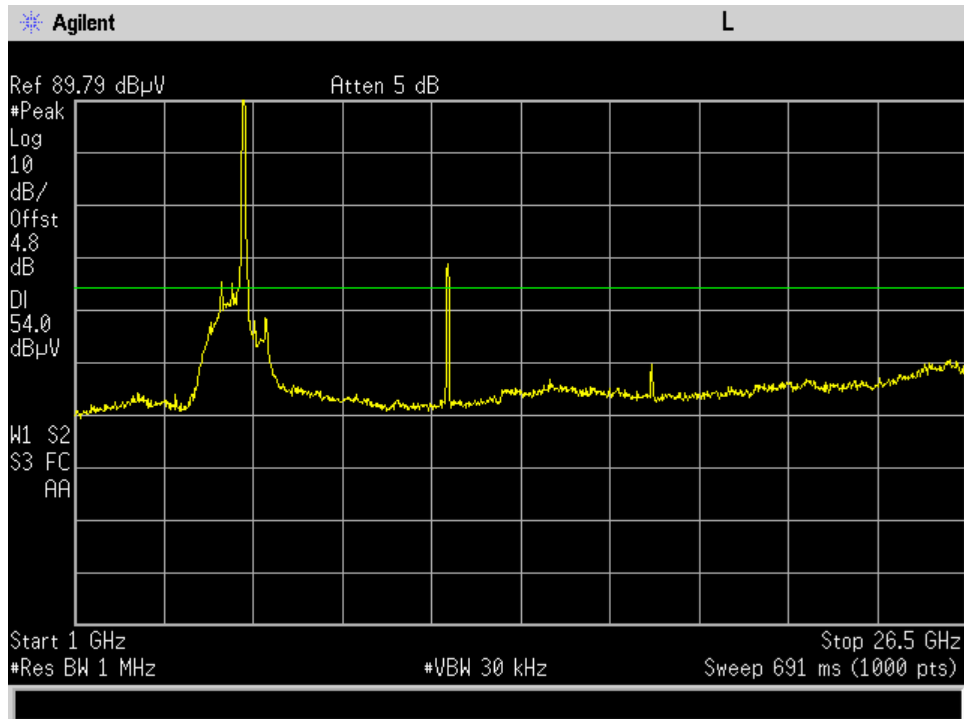


low ch port 1 80MHz BW 30-1000MHz

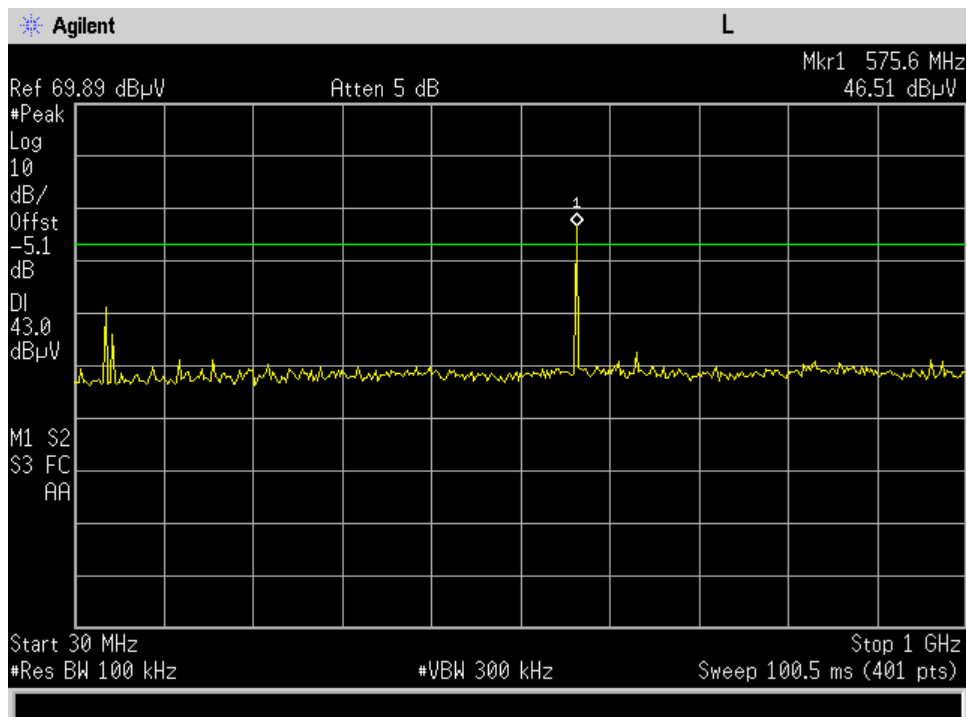




low ch port 2 10MHz BW 1-26.5G

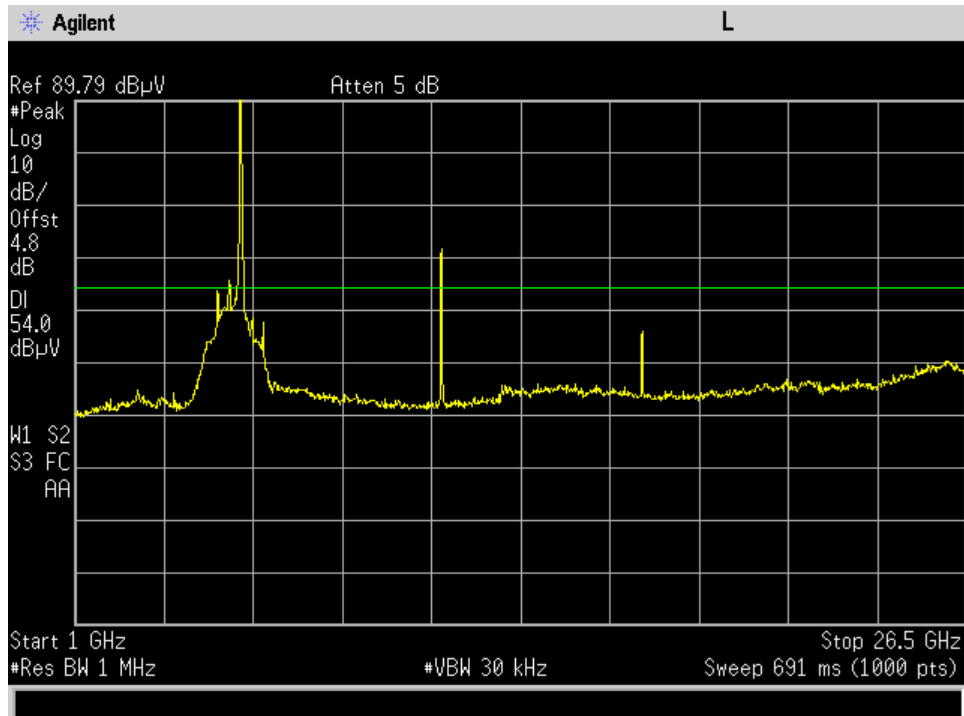


low ch port 2 10MHz BW 30-1000MHz

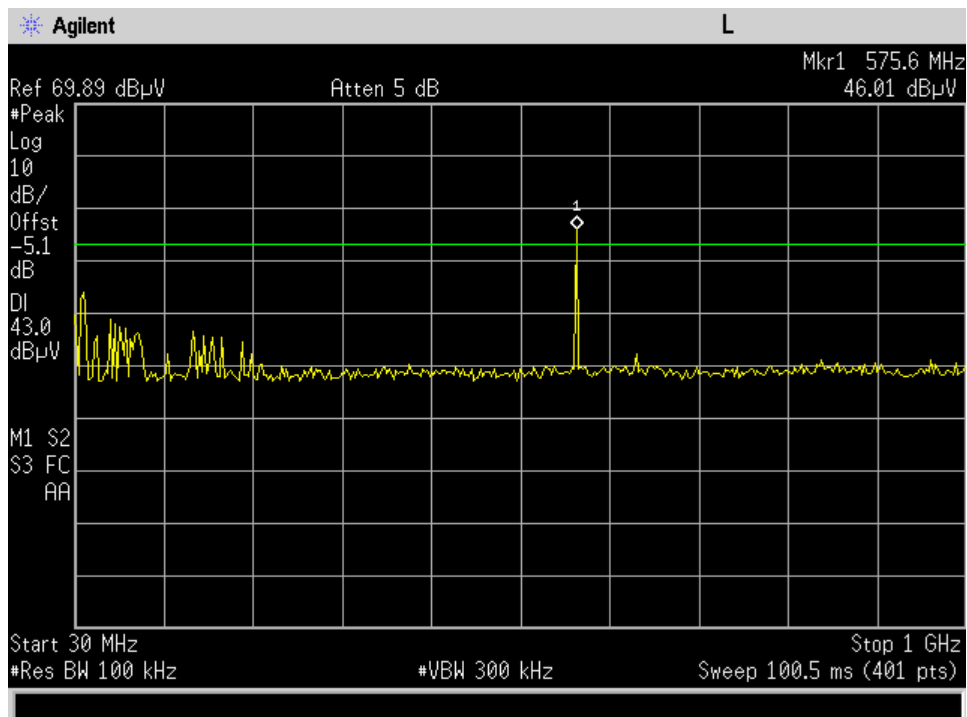




low ch port 2 20MHz BW 1-26.5G

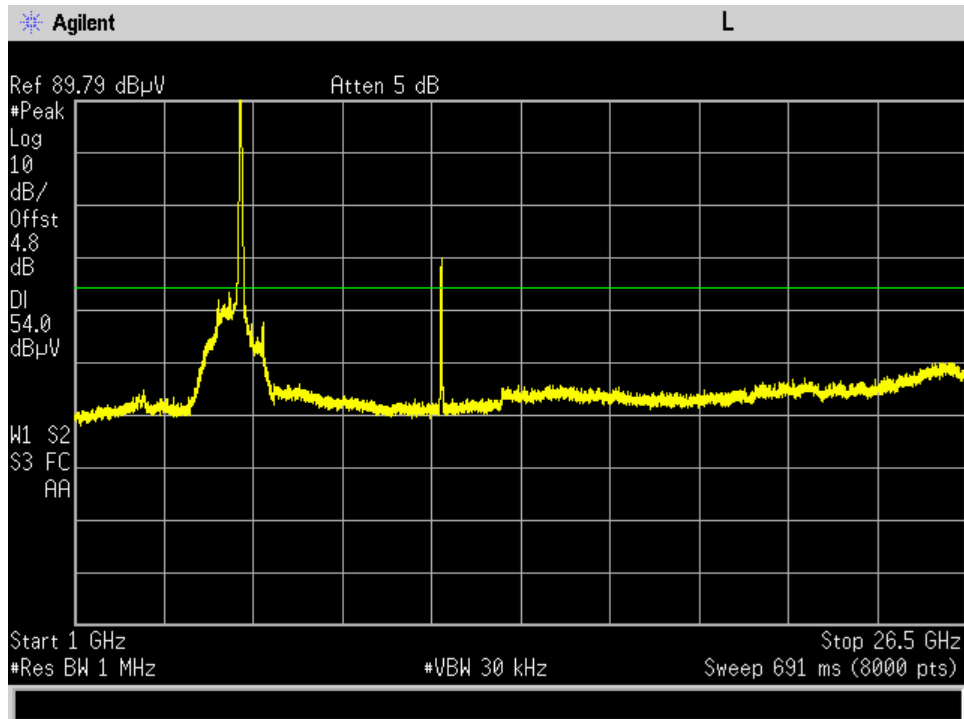


low ch port 2 20MHz BW 30-1000MHz

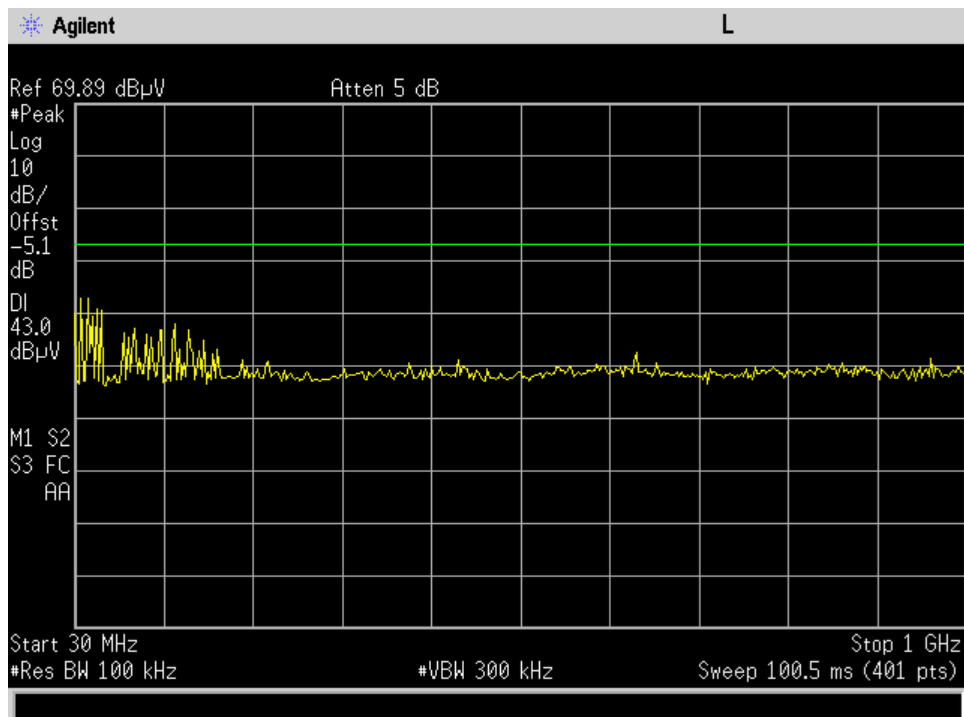




low ch port 2 30MHz BW 1-26.5G

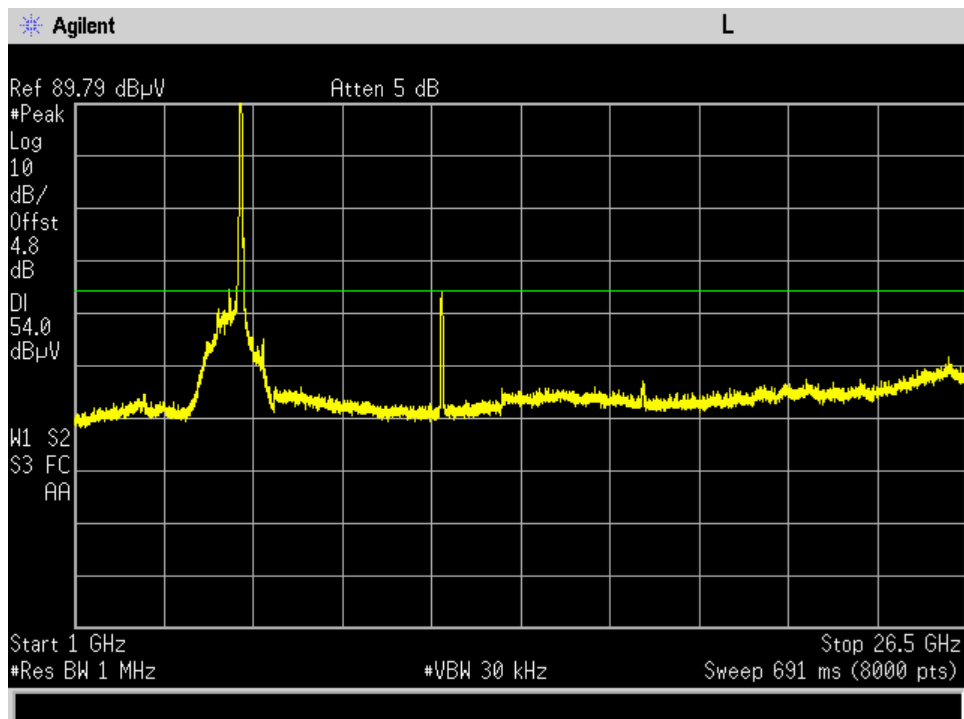


low ch port 2 30MHz BW 30-1000MHz

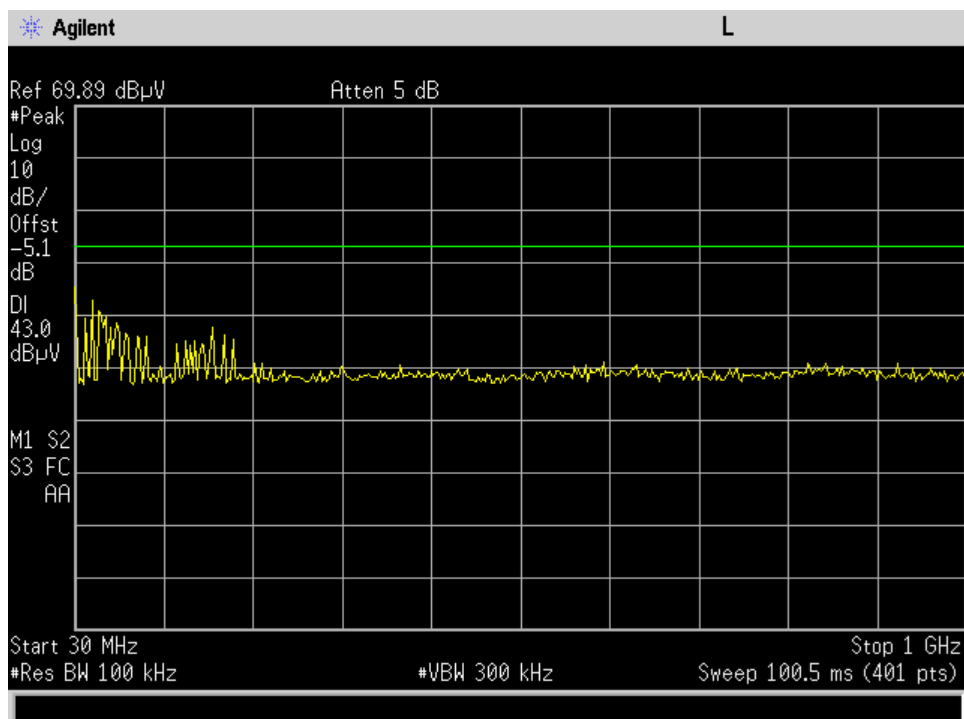




low ch port 2 40MHz BW 1-26.5G

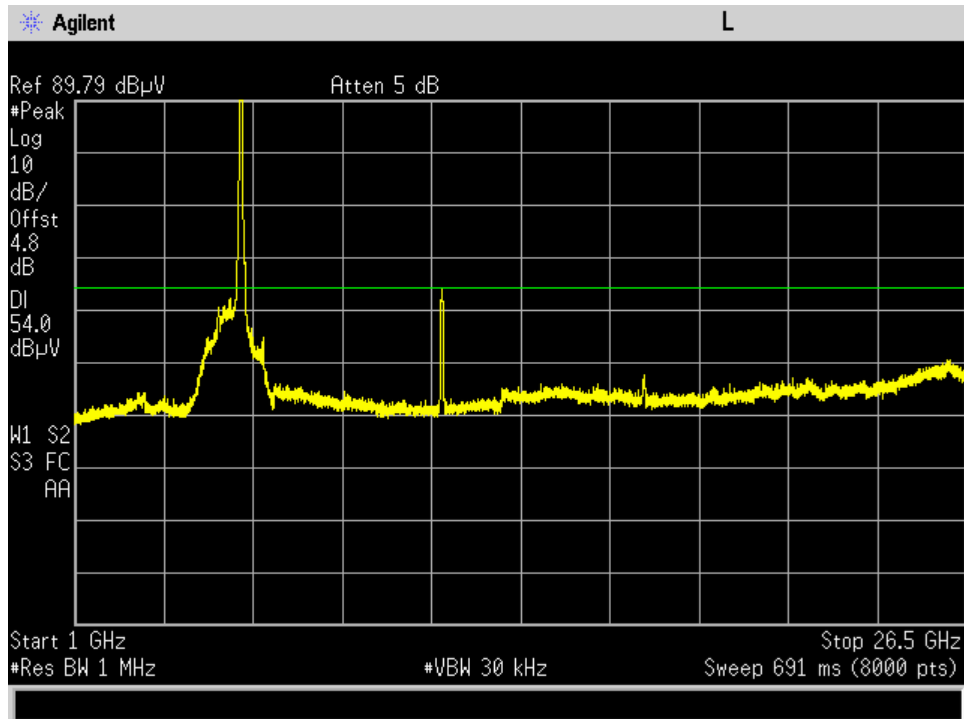


low ch port 2 40MHz BW 30-1000MHz

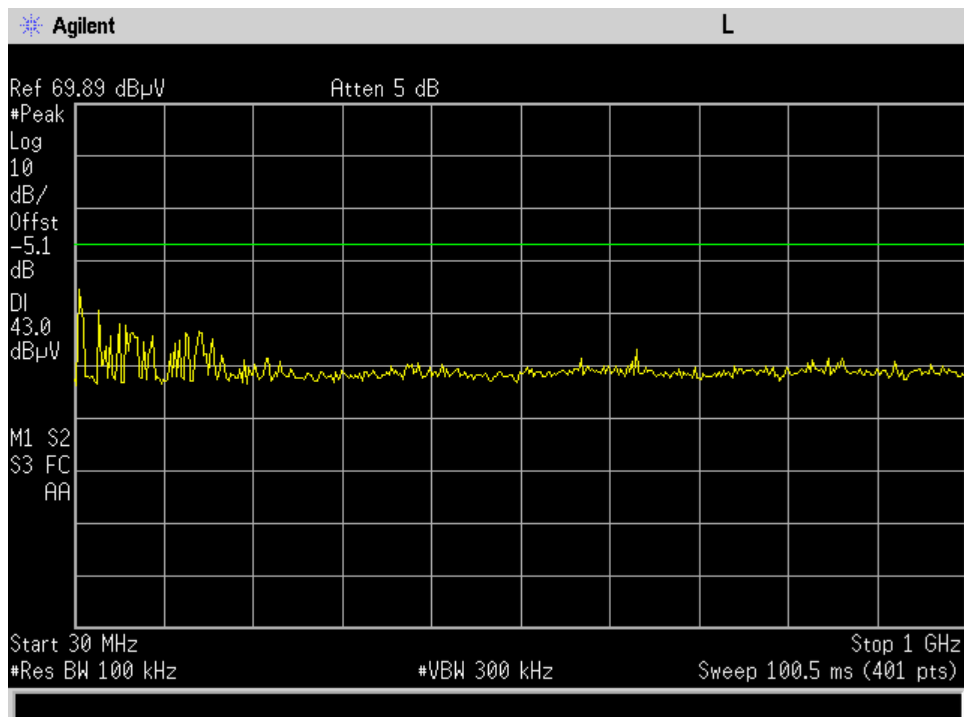




low ch port 2 50MHz BW 1-26.5G

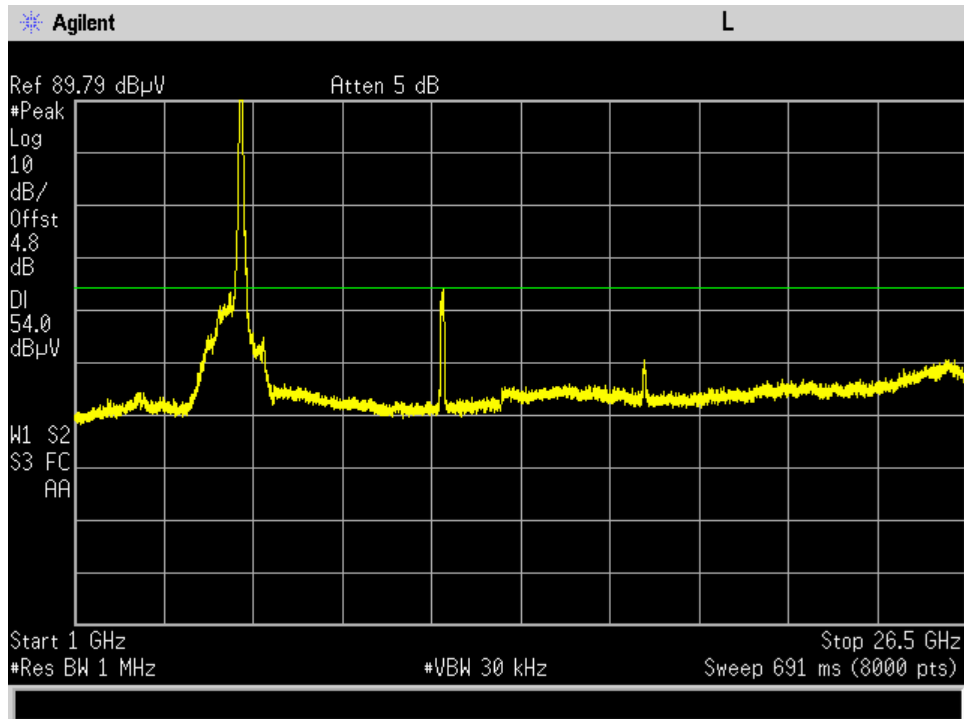


low ch port 2 50MHz BW 30-1000MHz

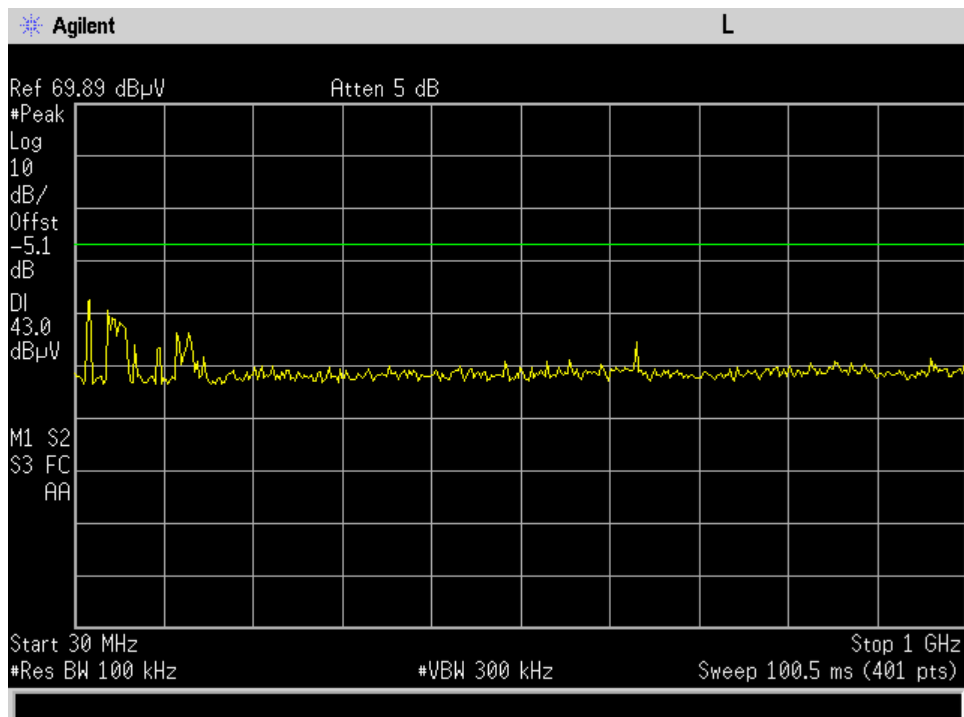




low ch port 2 60MHz BW 1-26.5G

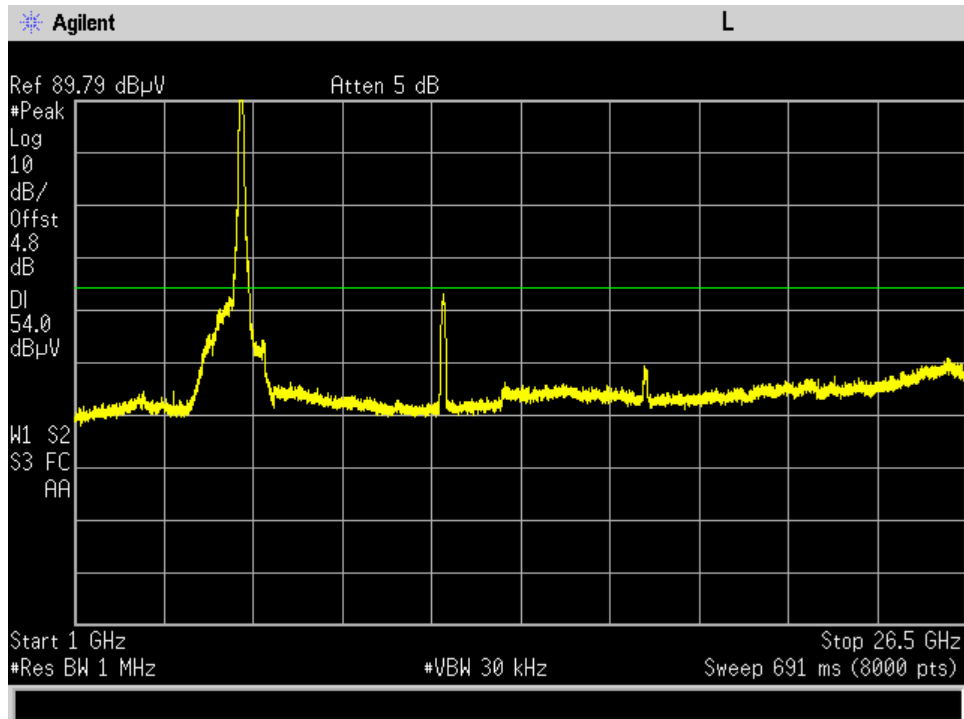


low ch port 2 60MHz BW 30-1000MHz

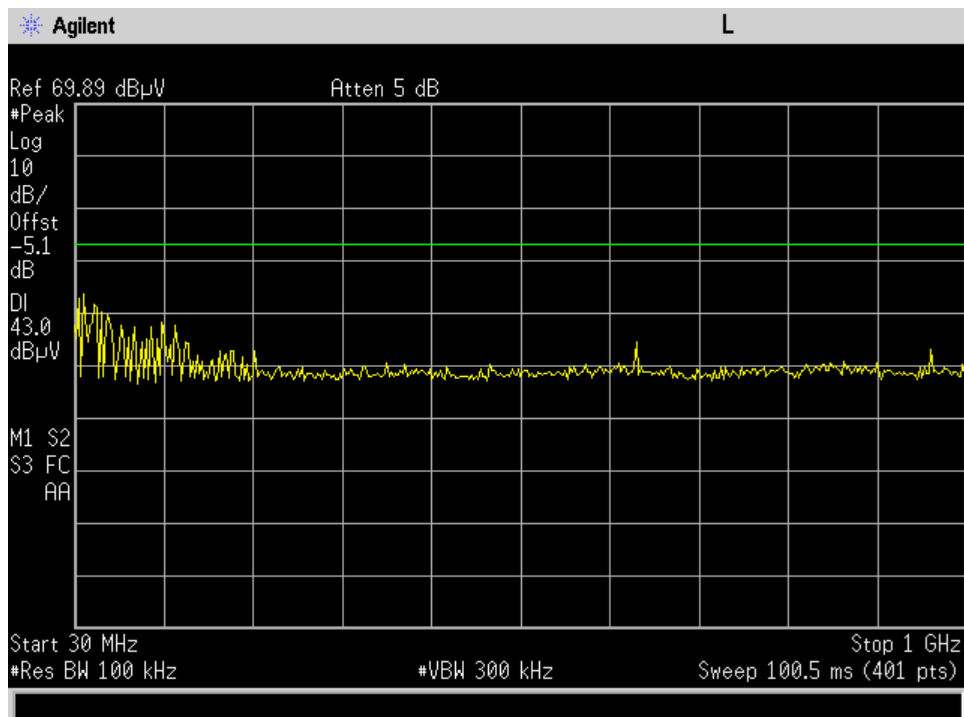




low ch port 2 80MHz BW 1-26.5G

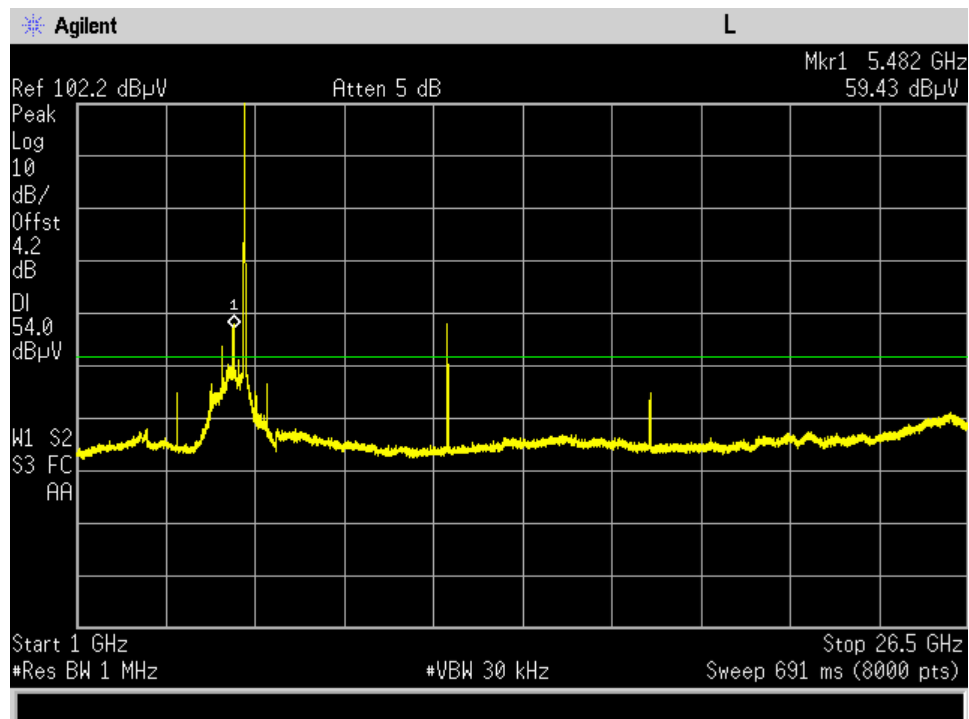


low ch port 2 80MHz BW 30-1000MHz

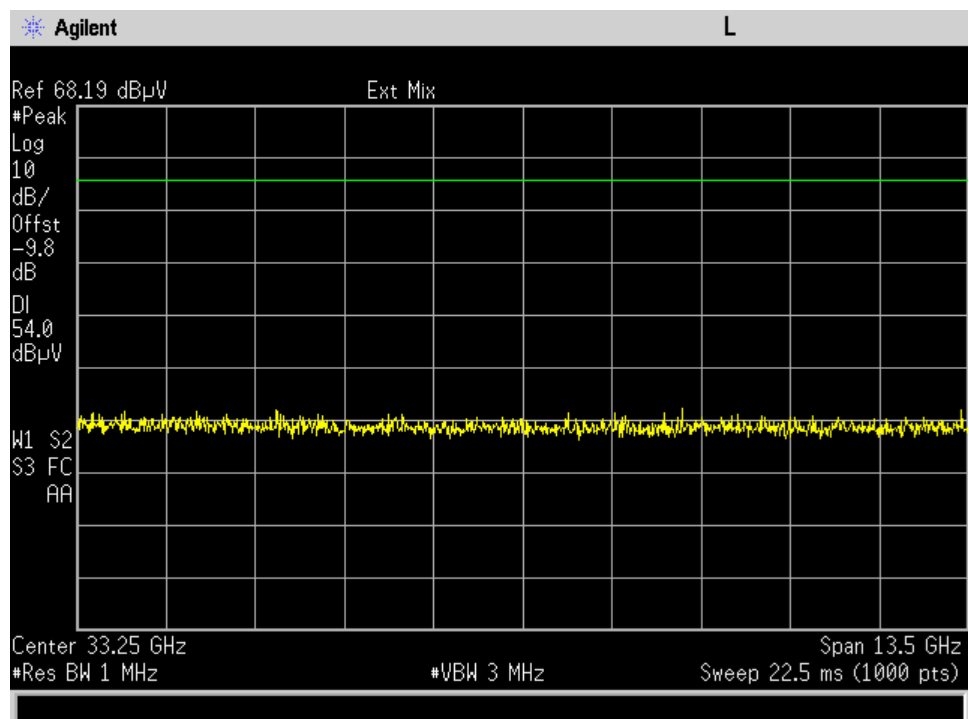




mid ch port 1 10MHz BW 1-26.5G

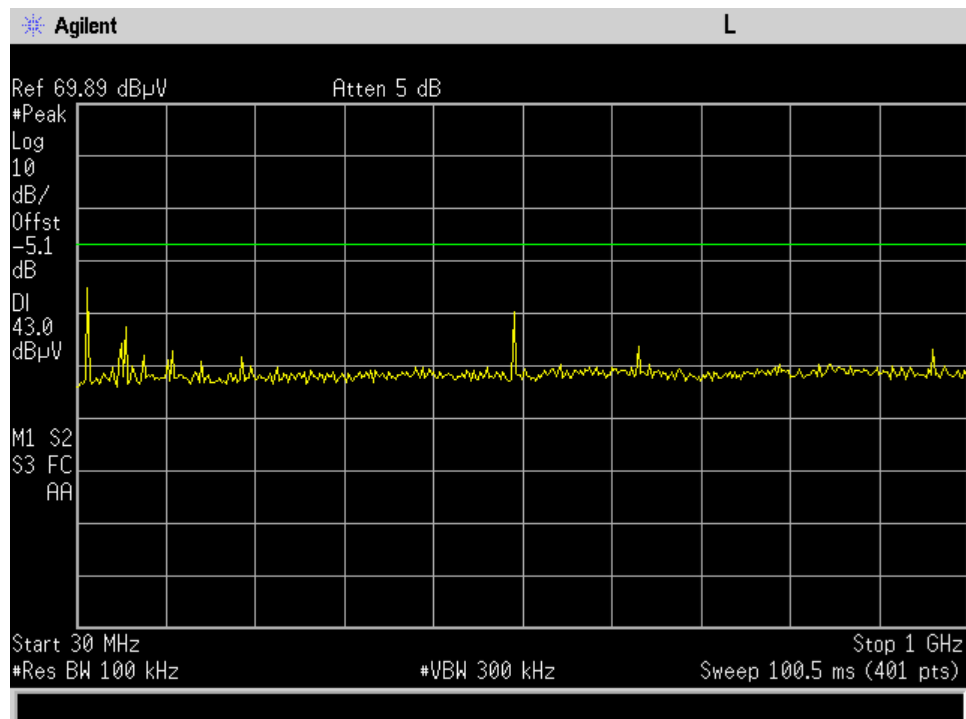


mid ch port 1 10MHz BW 26.5-40GHz

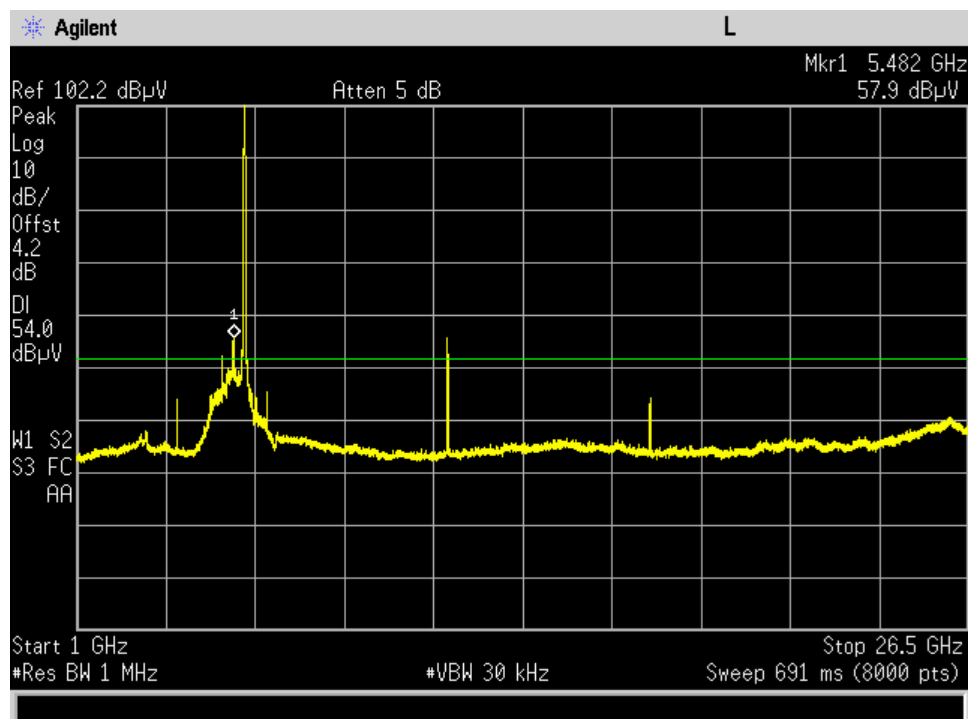




mid ch port 1 10MHz BW 30-1000MHz

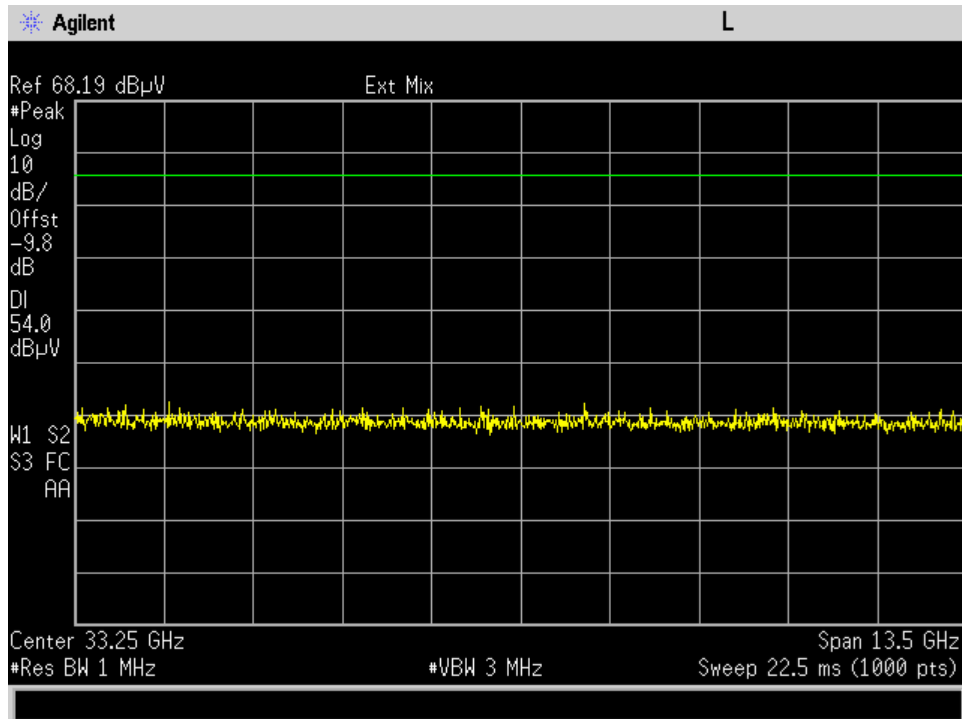


mid ch port 1 20MHz BW 1-26.5G

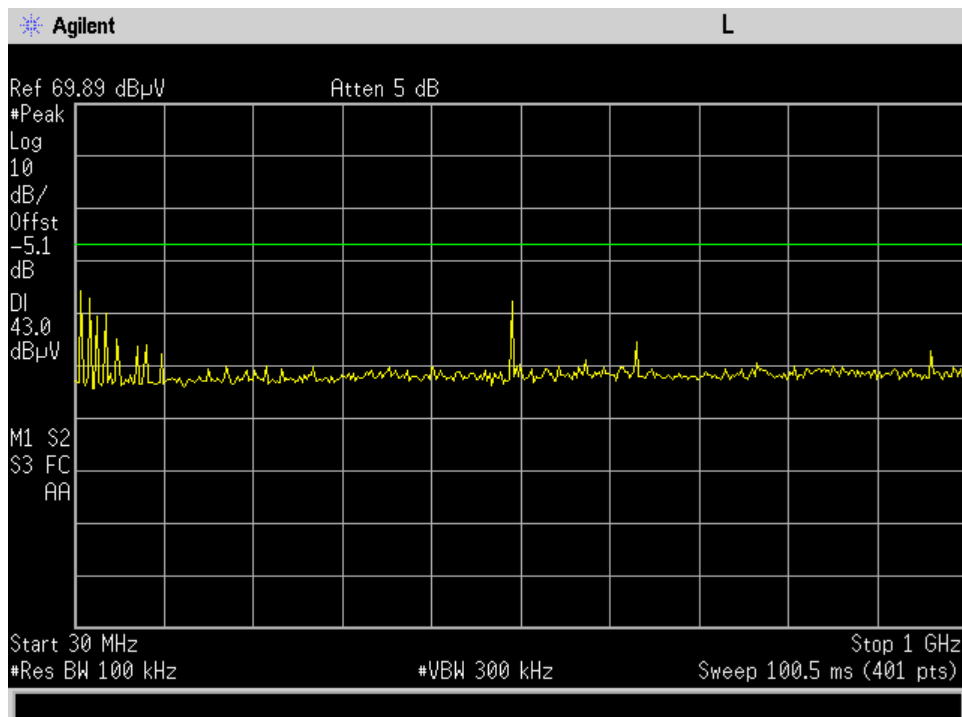




mid ch port 1 20MHz BW 26.5-40GHz

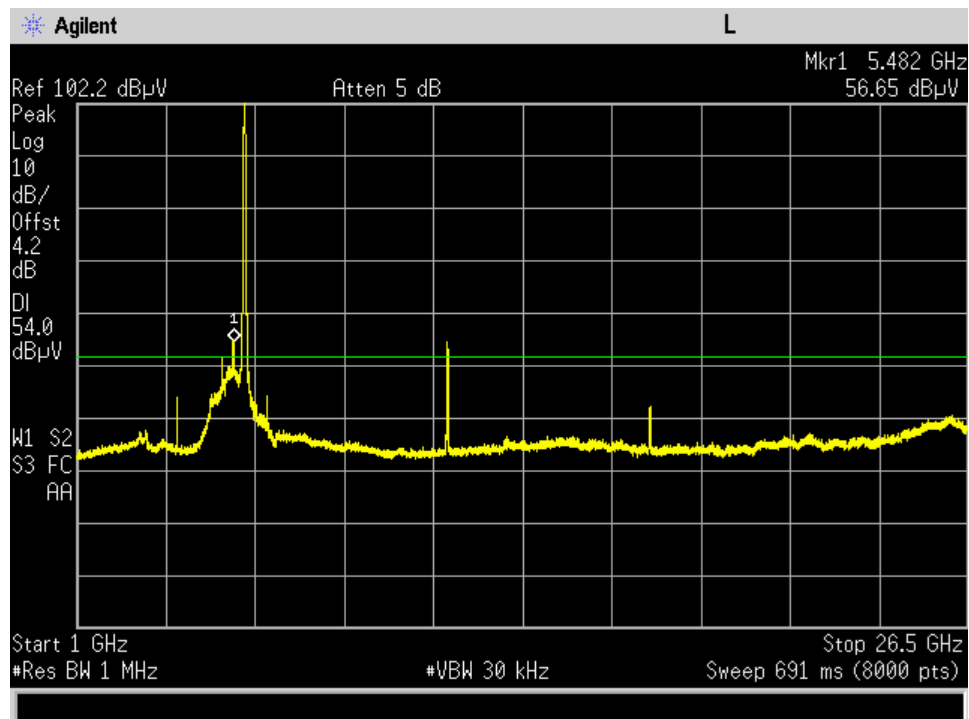


mid ch port 1 20MHz BW 30-1000MHz

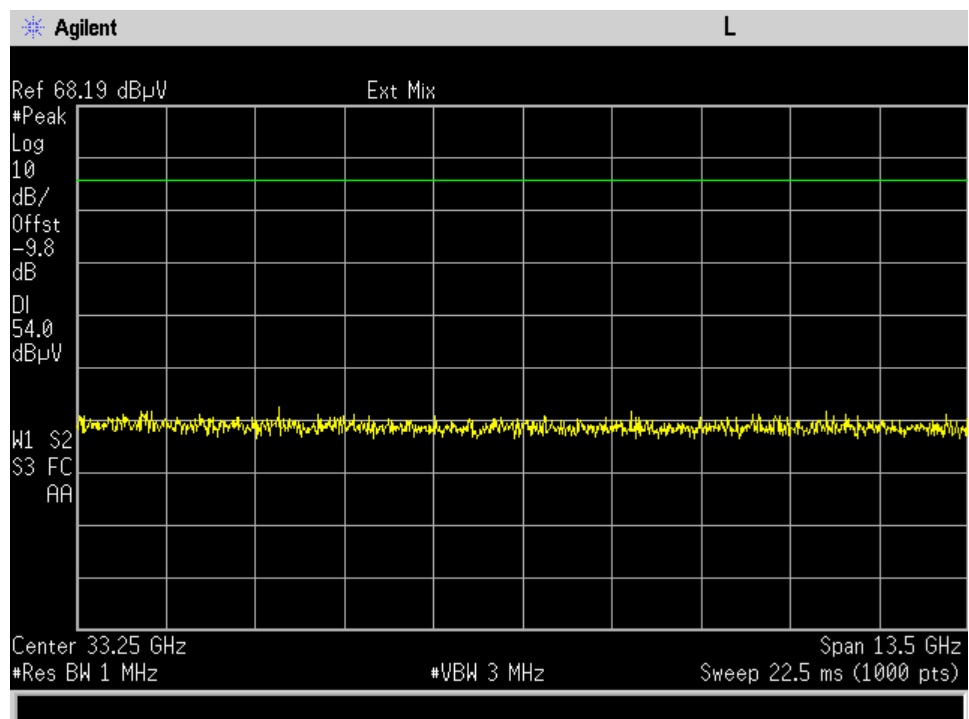




mid ch port 1 30MHz BW 1-26.5G

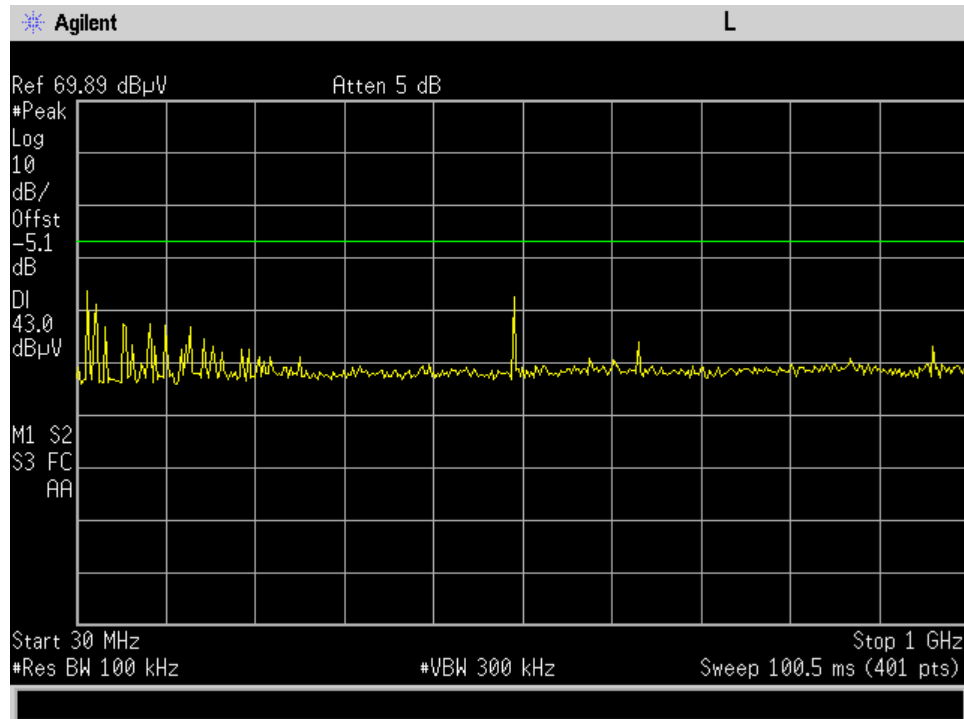


mid ch port 1 30MHz BW 26.5-40GHz

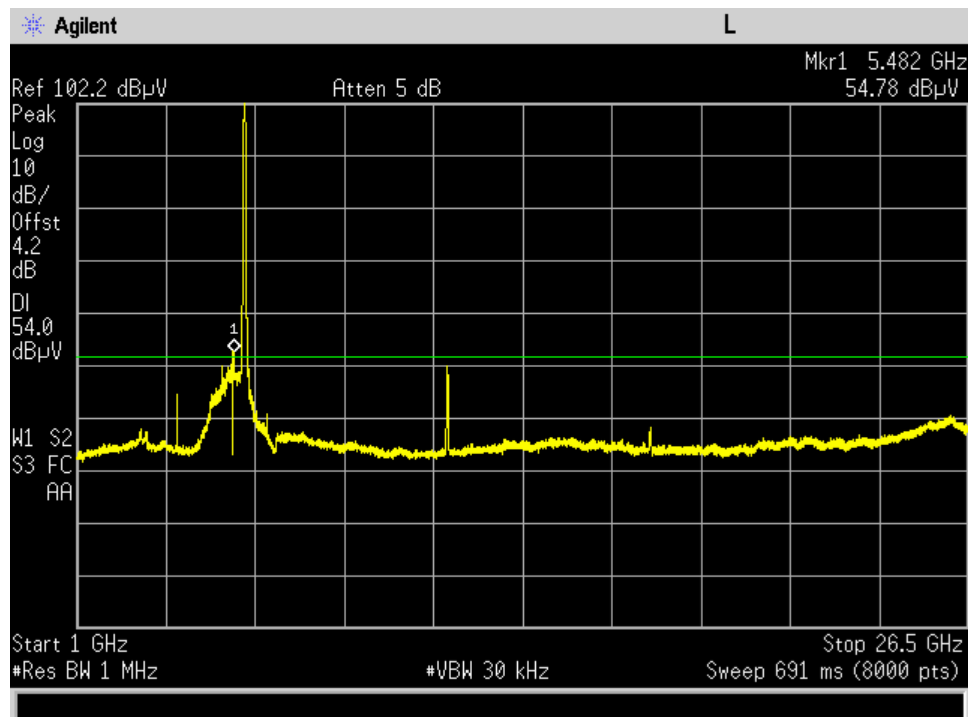




mid ch port 1 30MHz BW 30-1000MHz

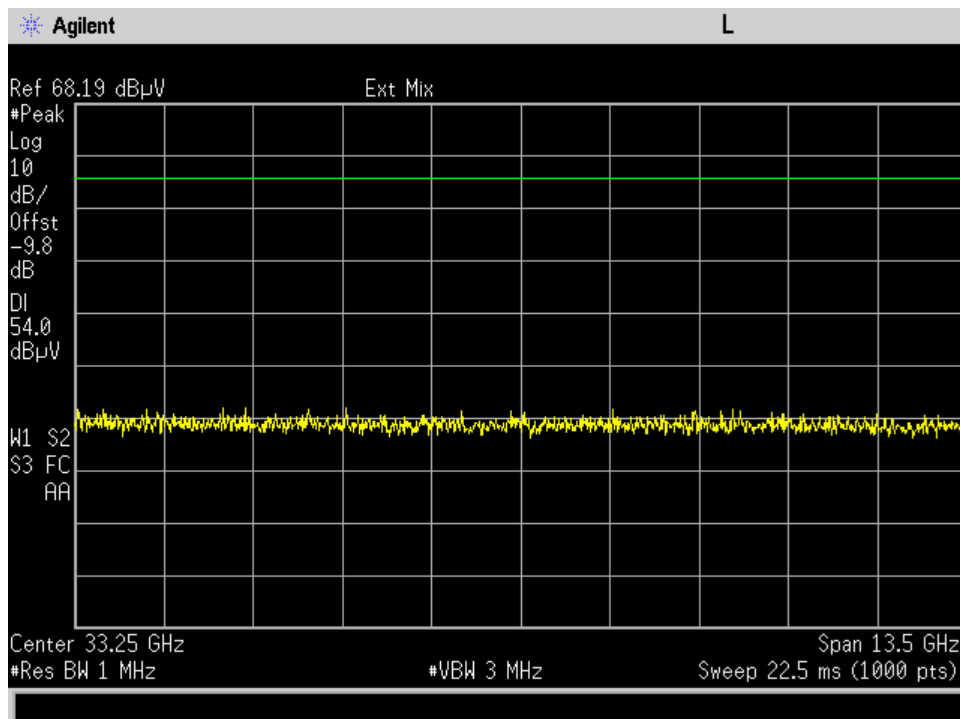


mid ch port 1 40MHz BW 1-26.5G

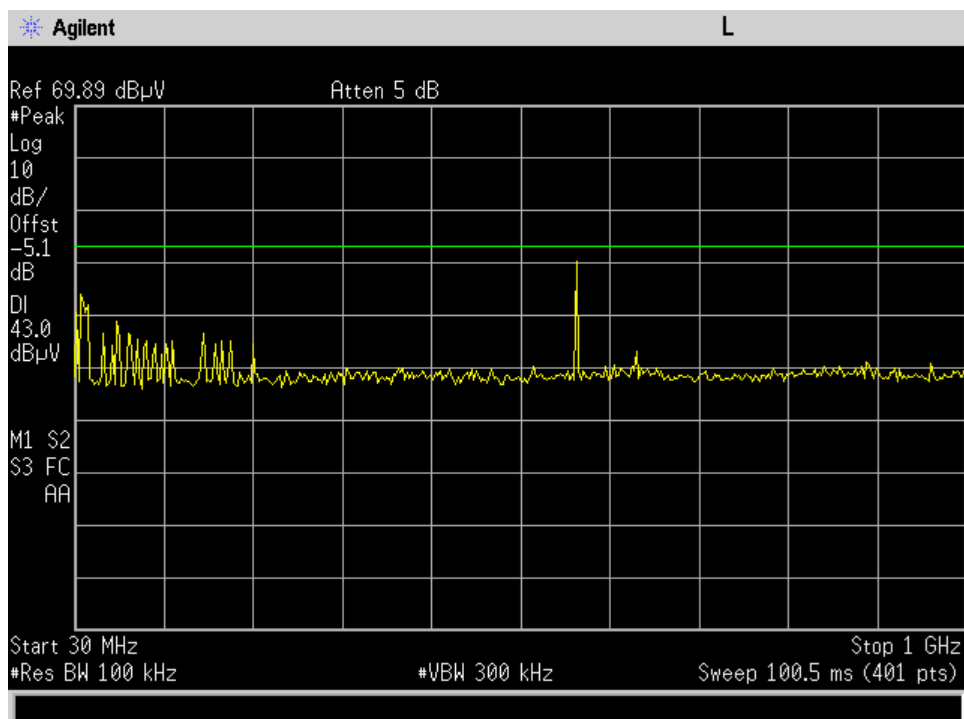




mid ch port 1 40MHz BW 26.5-40GHz

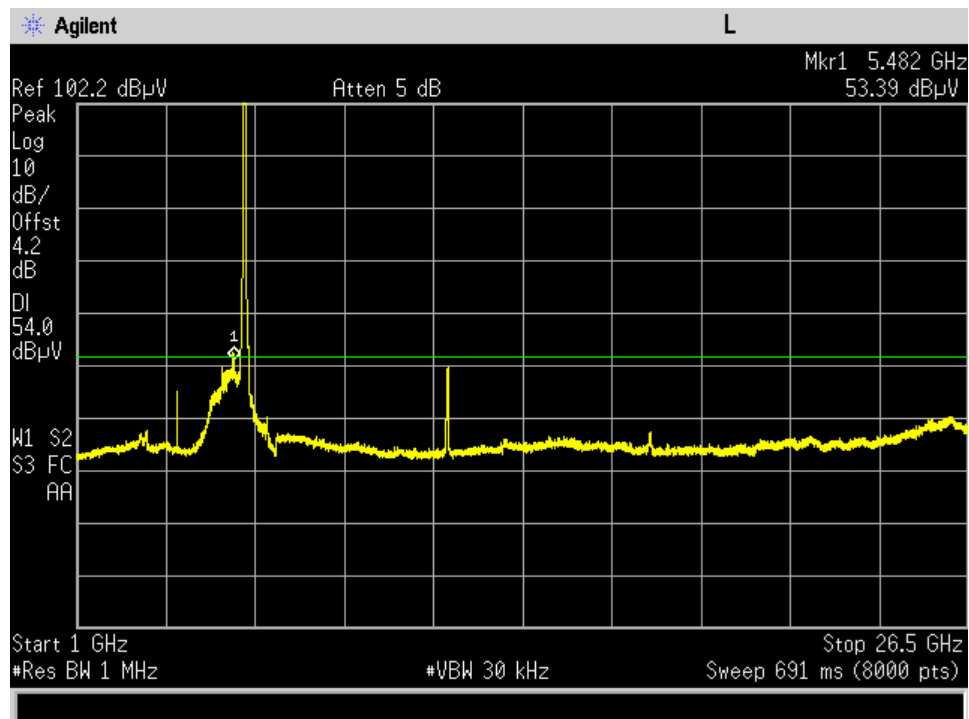


mid ch port 1 40MHz BW 30-1000MHz

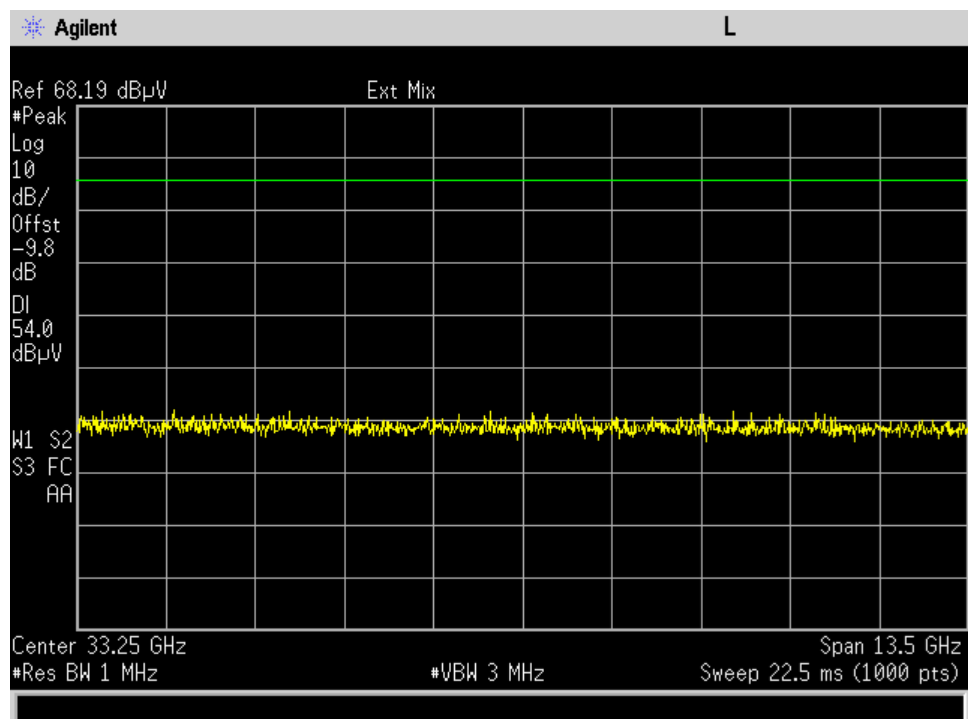




mid ch port 1 50MHz BW 1-26.5G

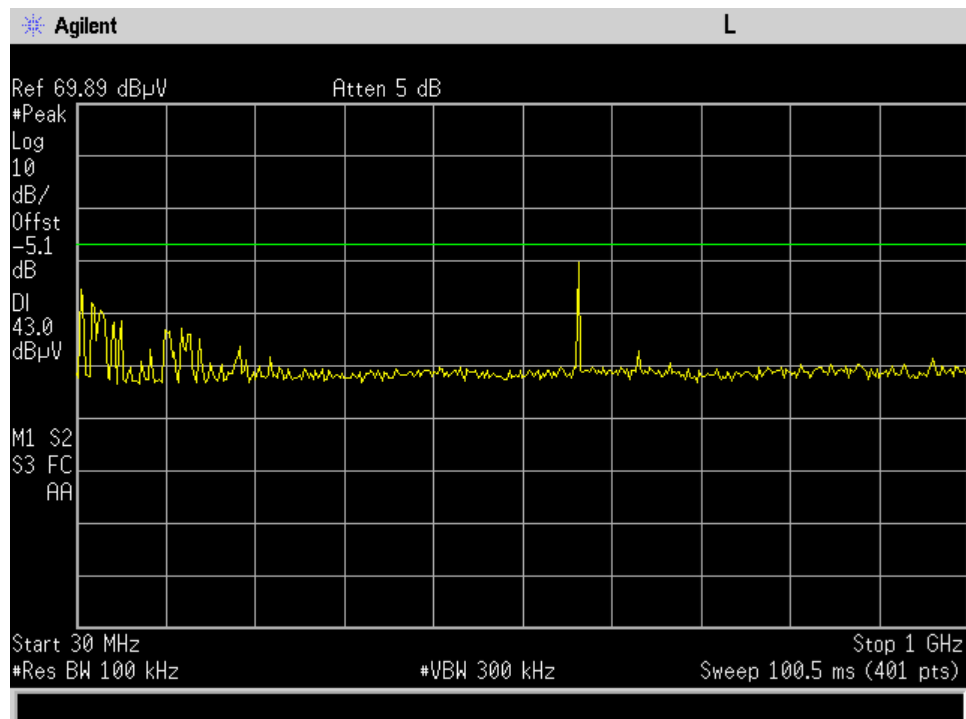


mid ch port 1 50MHz BW 26.5-40GHz

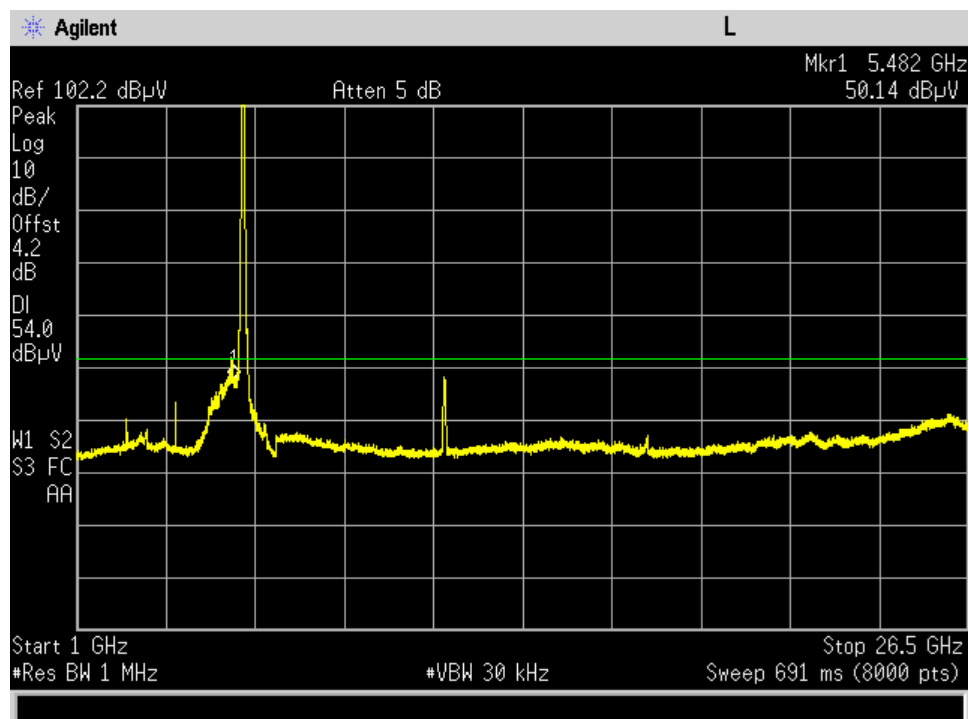




mid ch port 1 50MHz BW 30-1000MHz

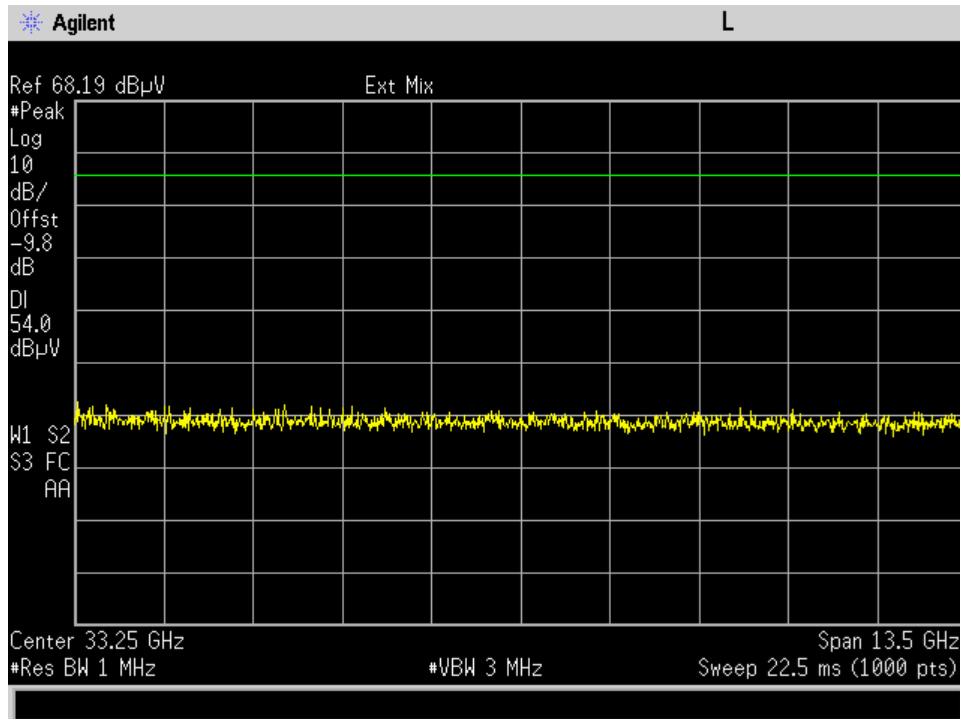


mid ch port 1 60MHz BW 1-26.5G

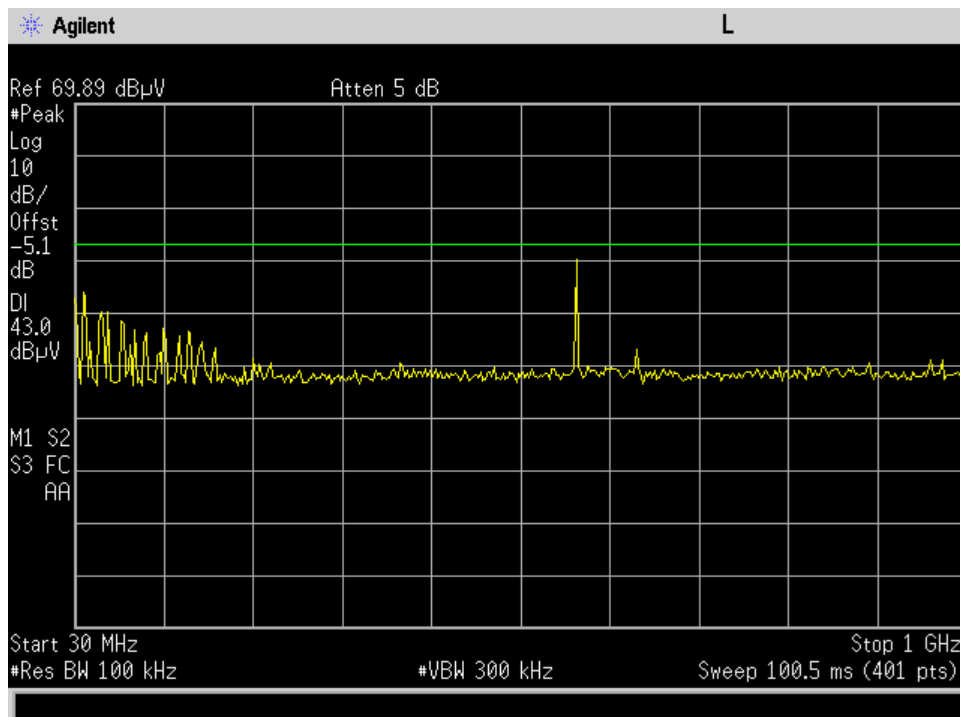




mid ch port 1 60MHz BW 26.5-40GHz

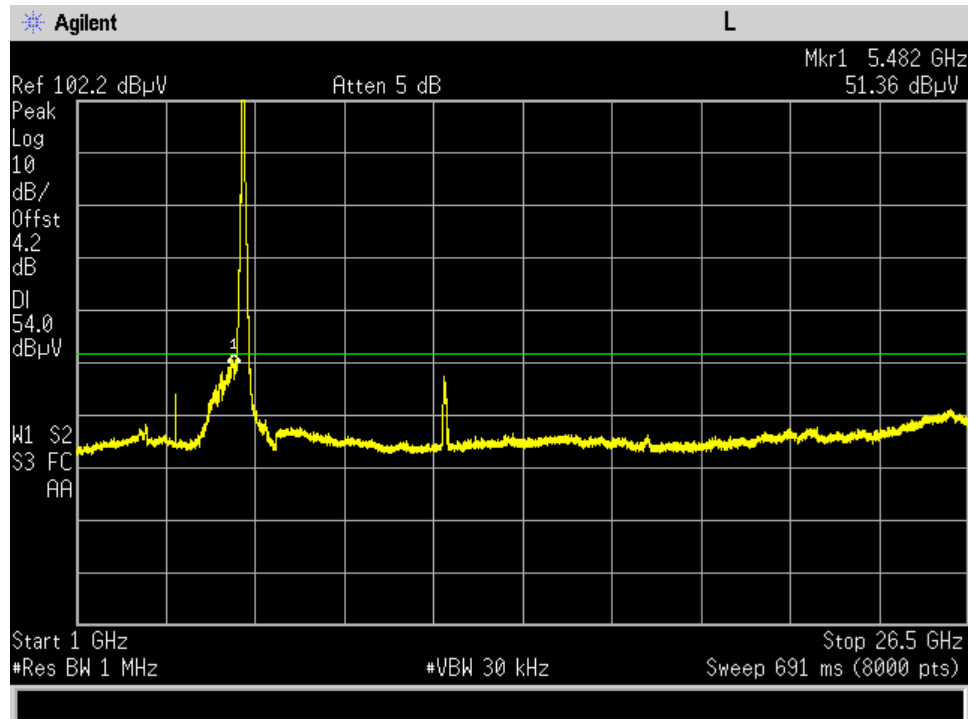


mid ch port 1 60MHz BW 30-1000MHz

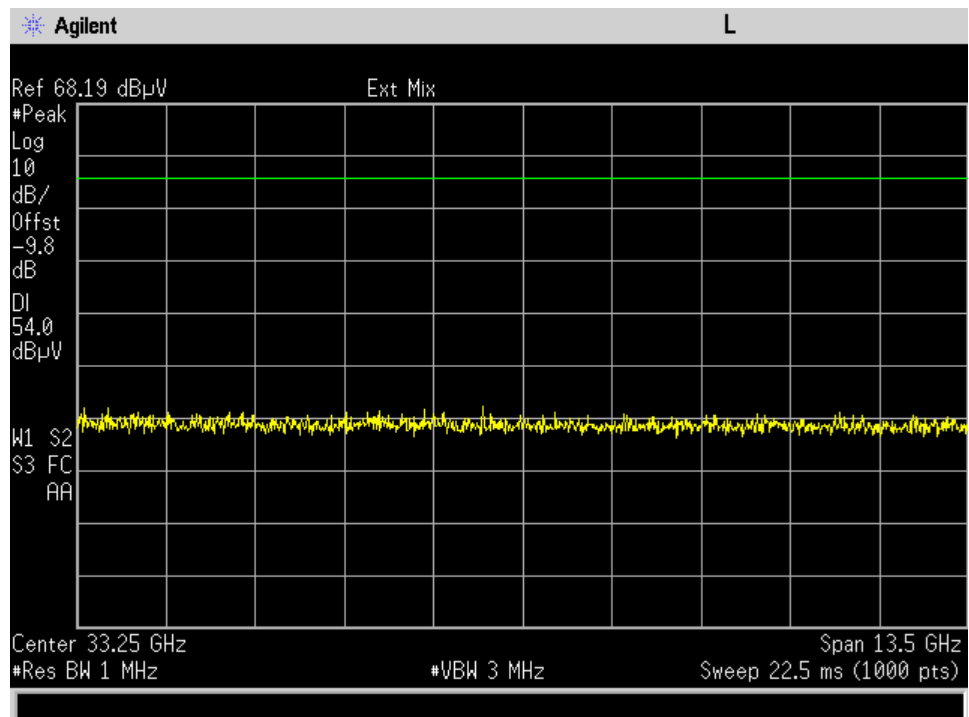




mid ch port 1 80MHz BW 1-26.5G

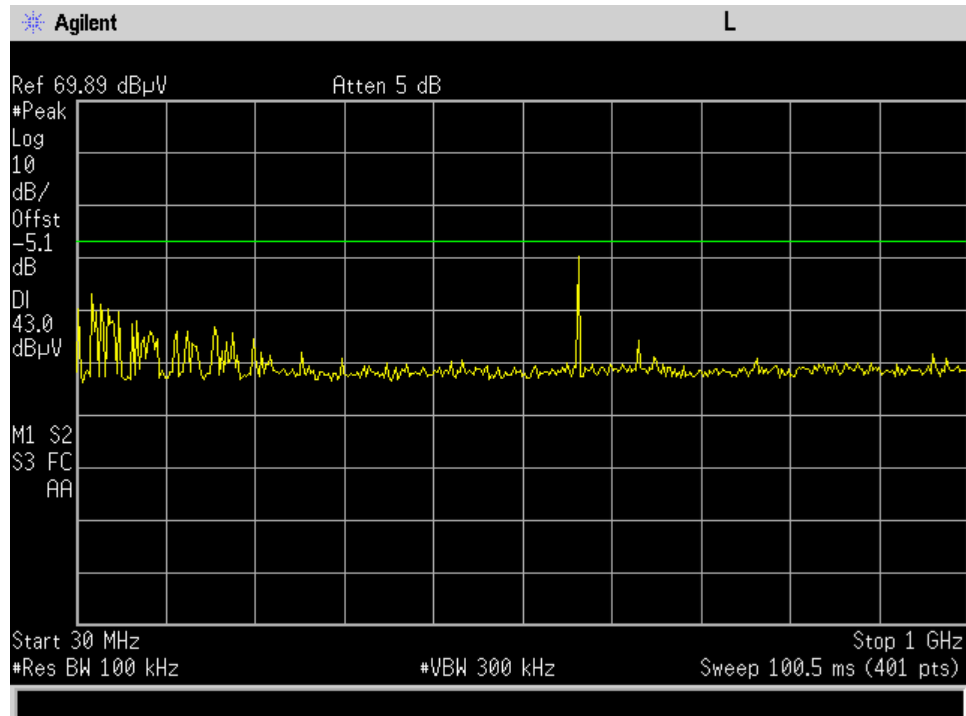


mid ch port 1 80MHz BW 26.5-40GHz

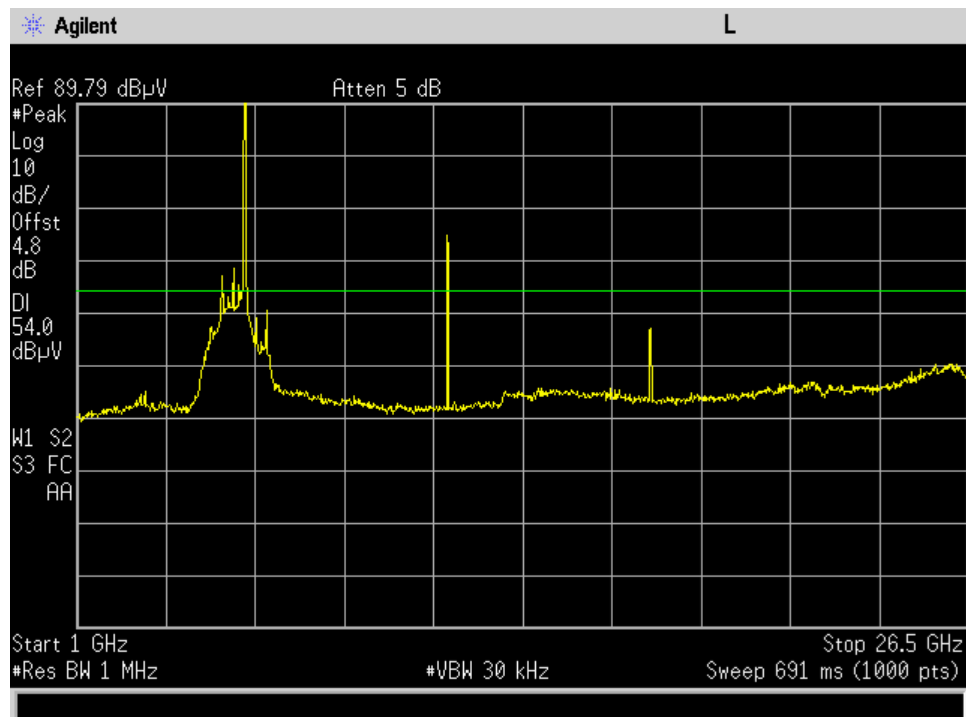




mid ch port 1 80MHz BW 30-1000MHz

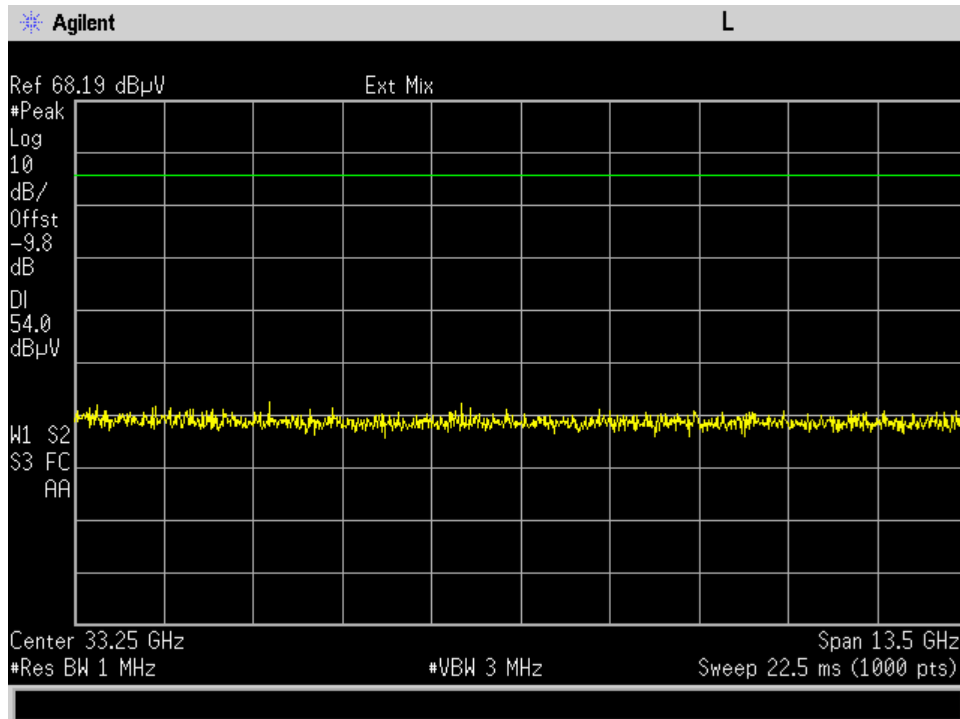


mid ch port 2 10MHz BW 1-26.5G

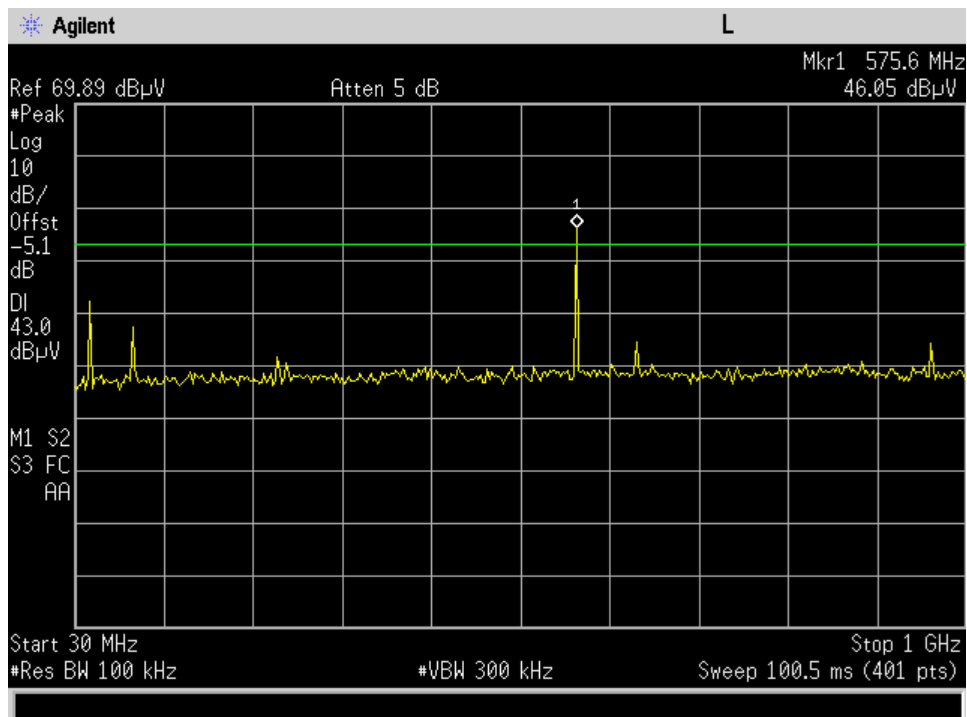




mid ch port 2 10MHz BW 26.5-40GHz

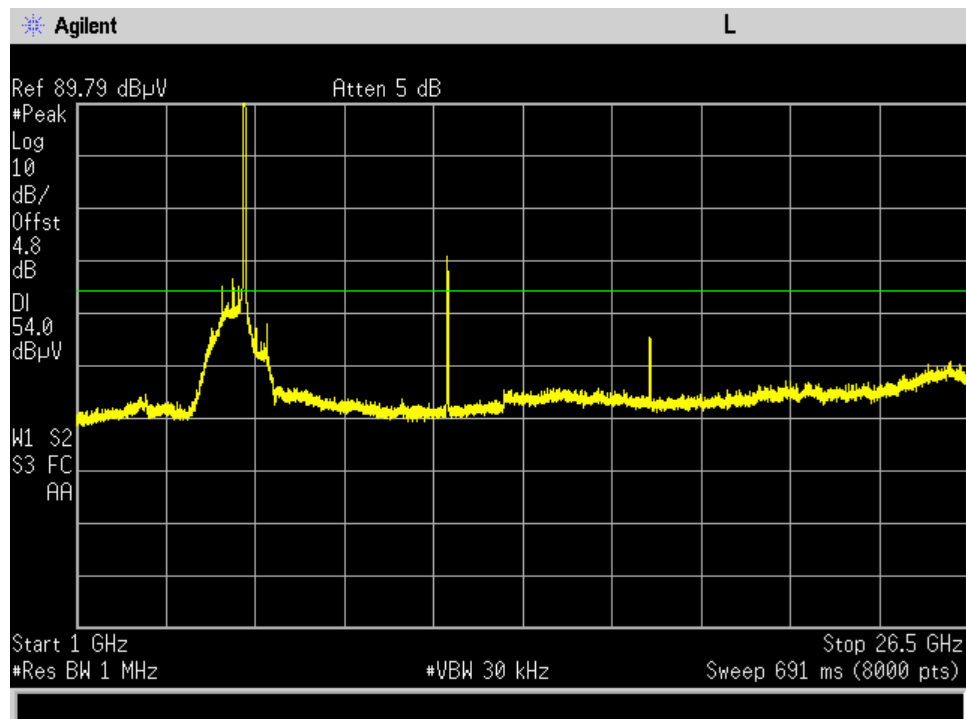


mid ch port 2 10MHz BW 30-1000MHz

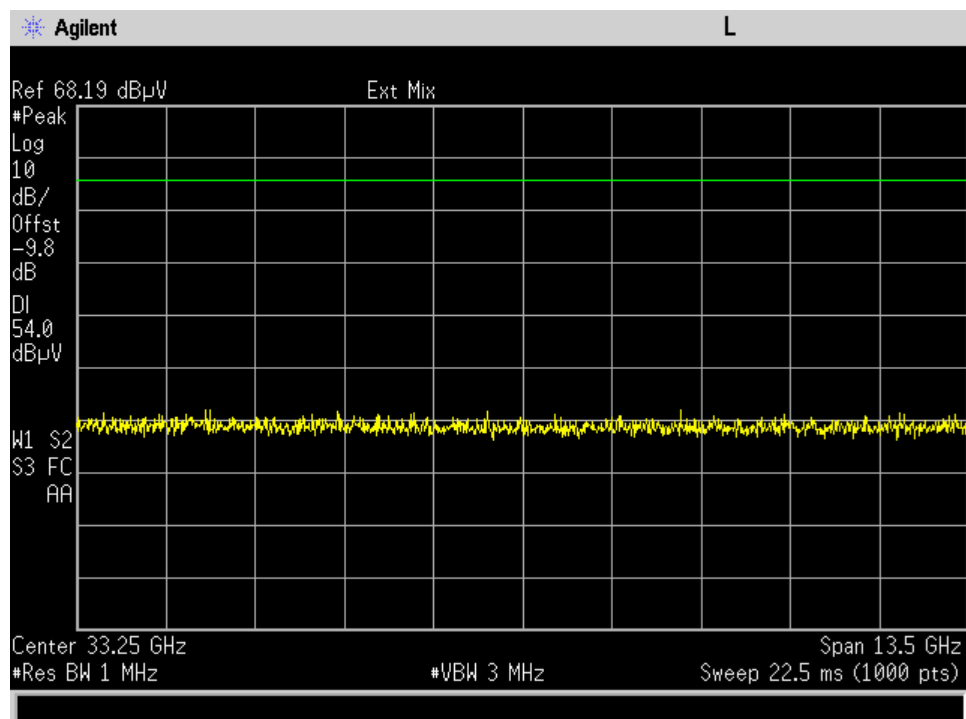




mid ch port 2 20MHz BW 1-26.5G

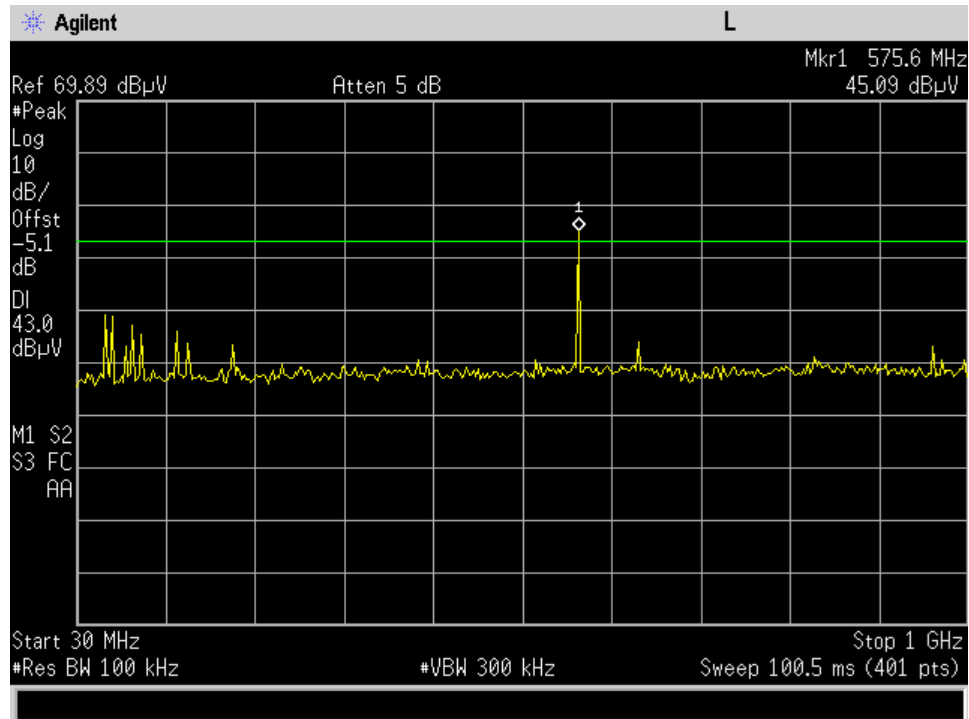


mid ch port 2 20MHz BW 26.5-40GHz

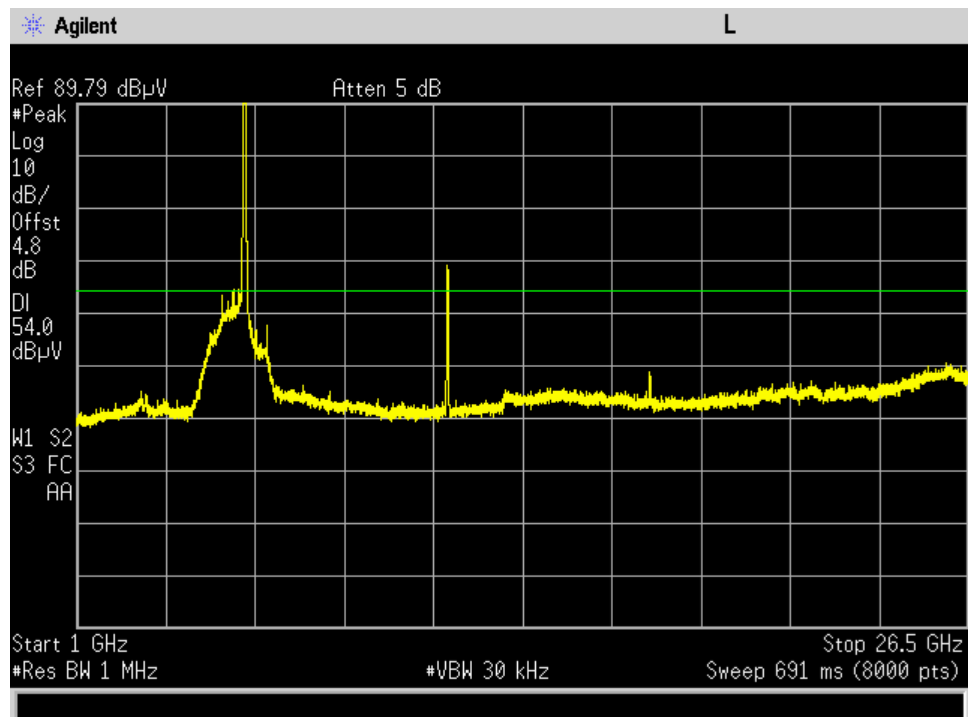




mid ch port 2 20MHz BW 30-1000MHz

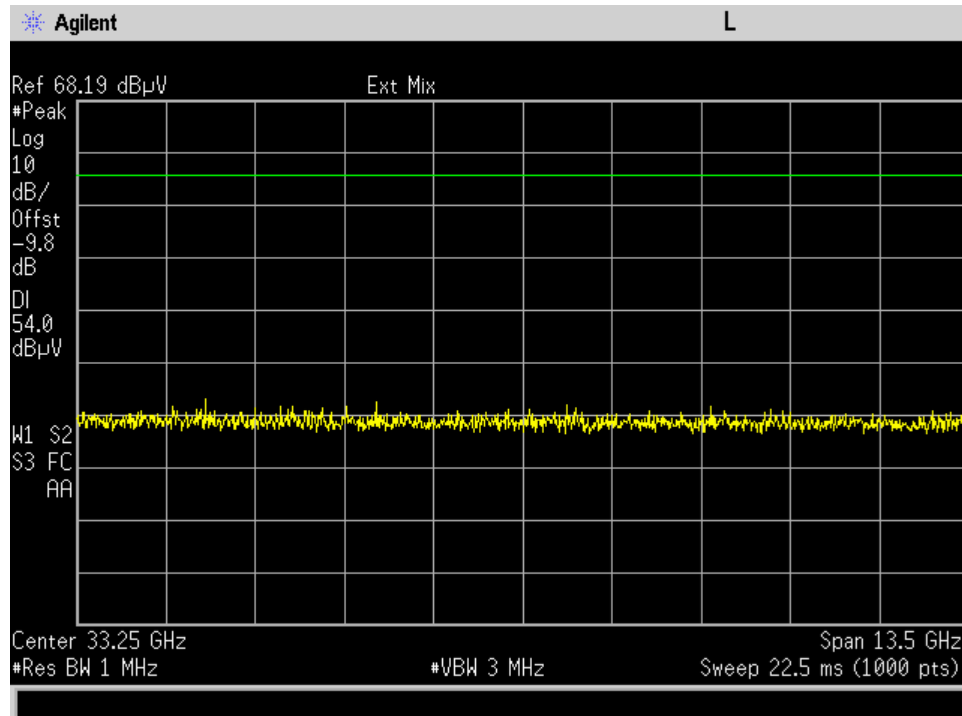


mid ch port 2 30MHz BW 1-26.5G

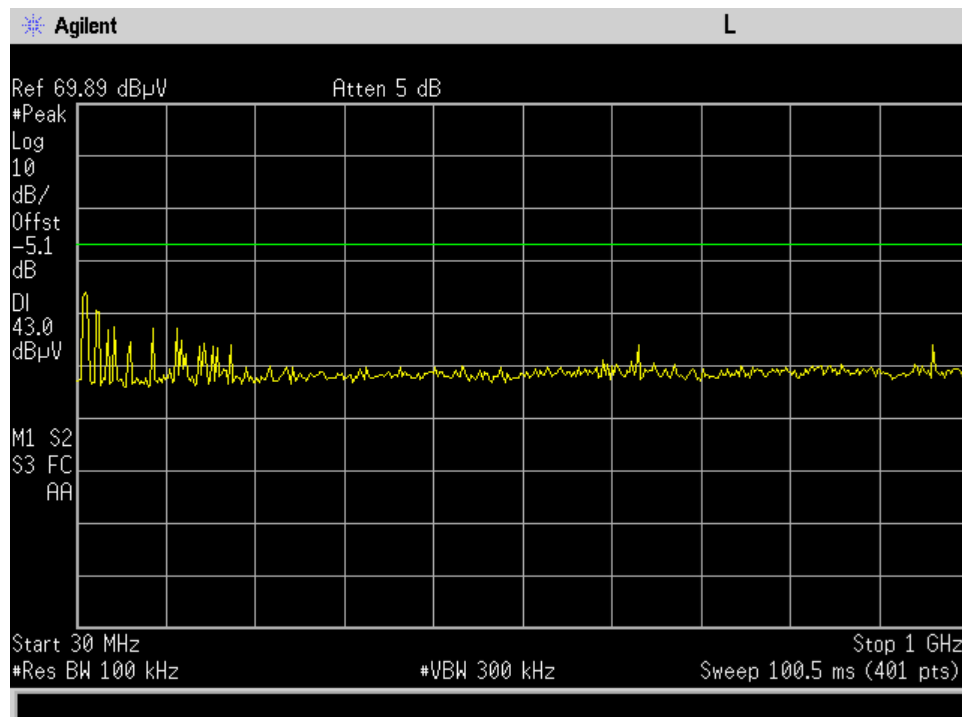




mid ch port 2 30MHz BW 26.5-40GHz

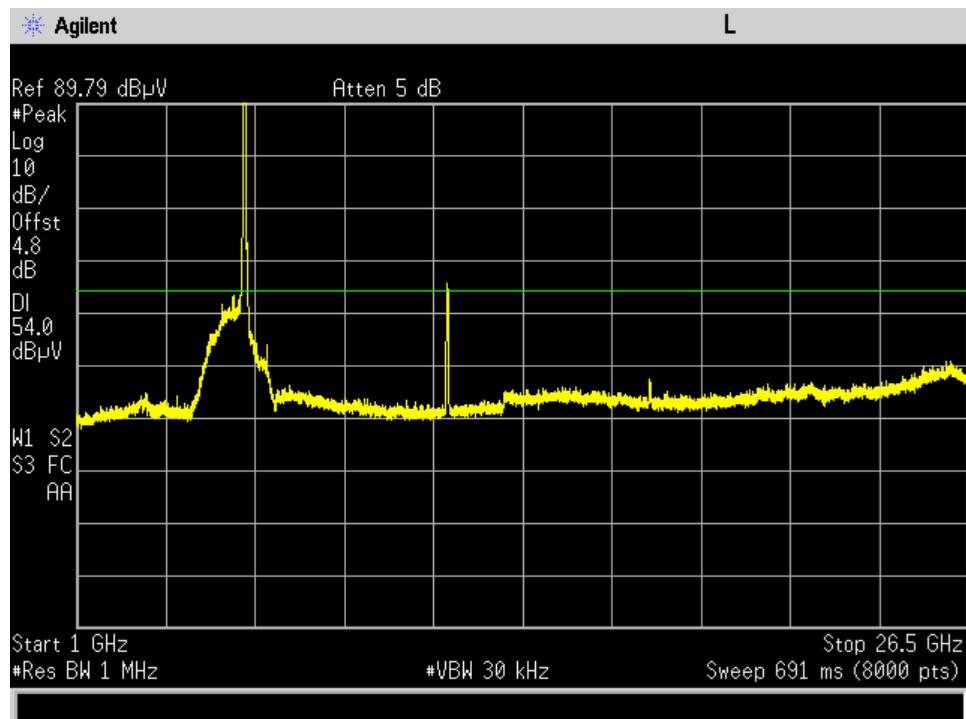


mid ch port 2 30MHz BW 30-1000MHz

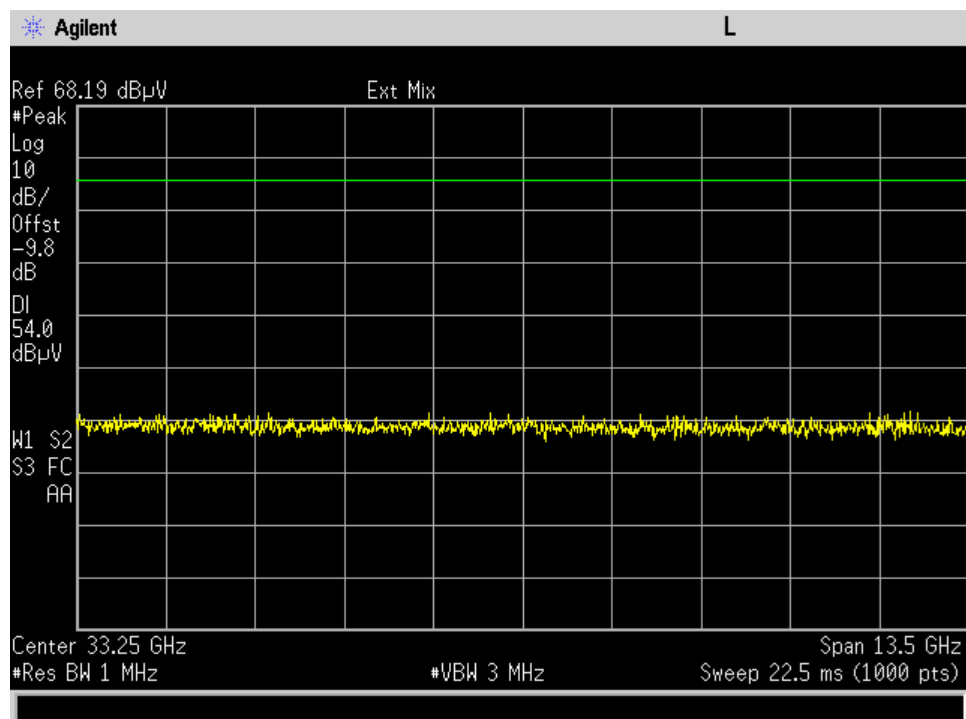




mid ch port 2 40MHz BW 1-26.5G

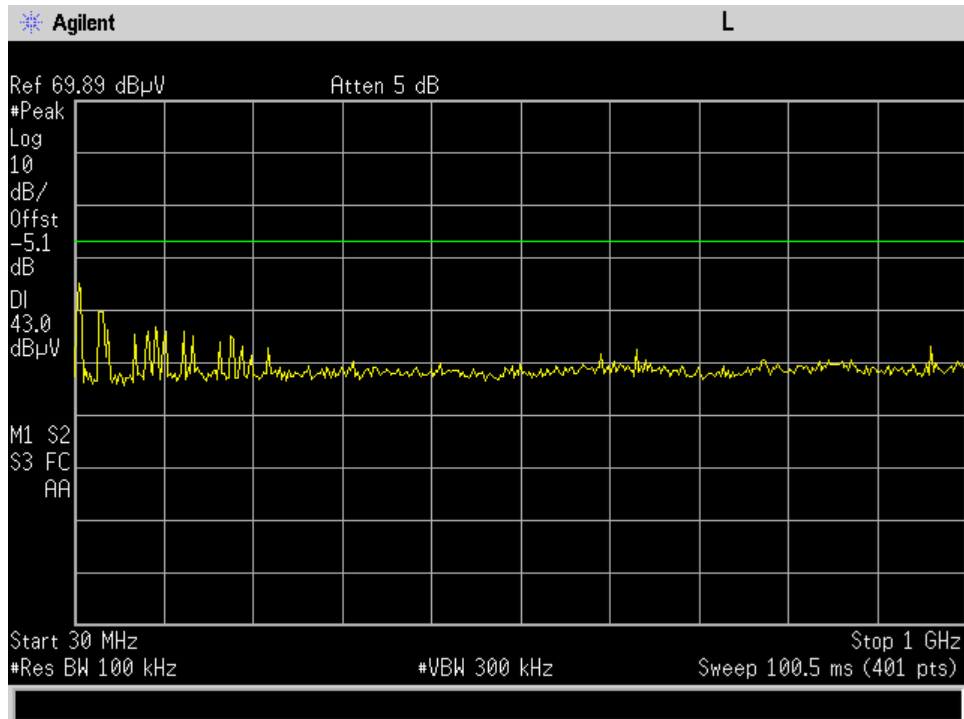


mid ch port 2 40MHz BW 26.5-40GHz

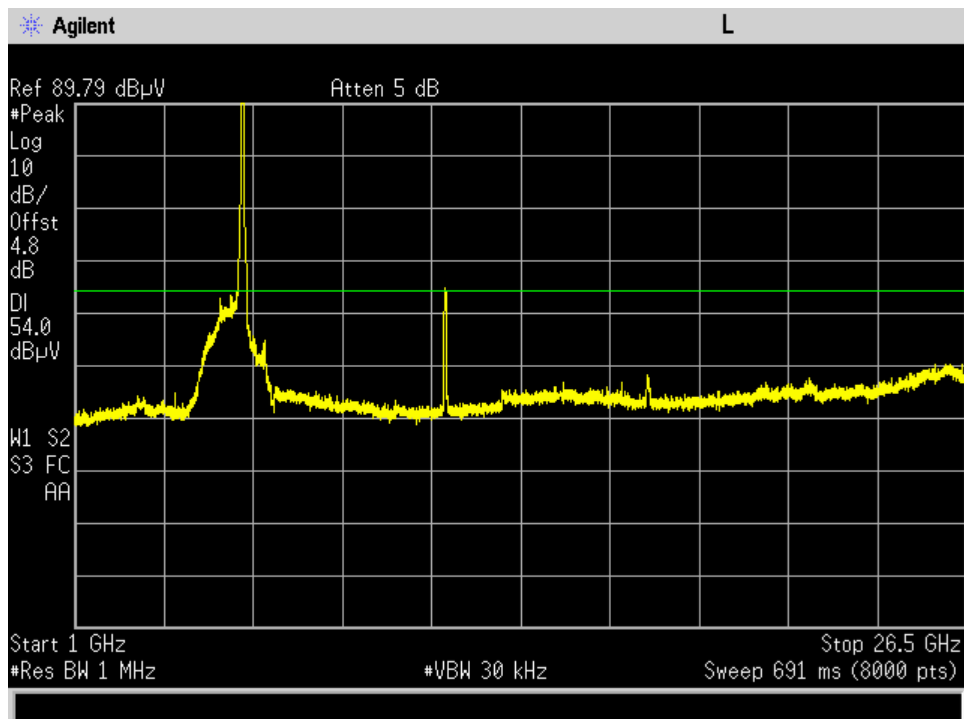




mid ch port 2 40MHz BW 30-1000MHz

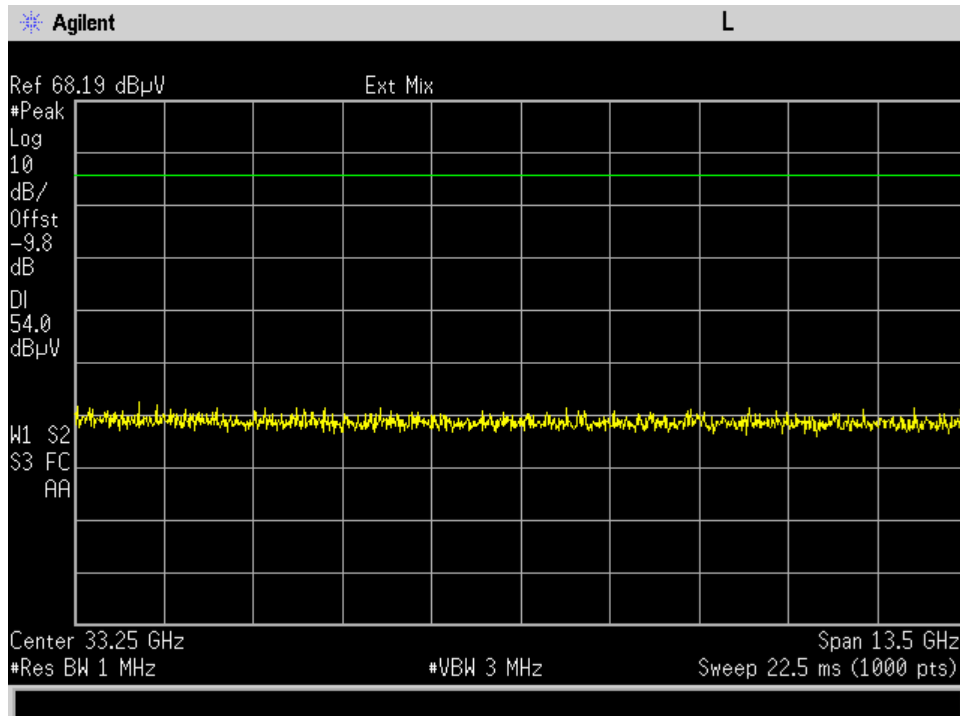


mid ch port 2 50MHz BW 1-26.5G

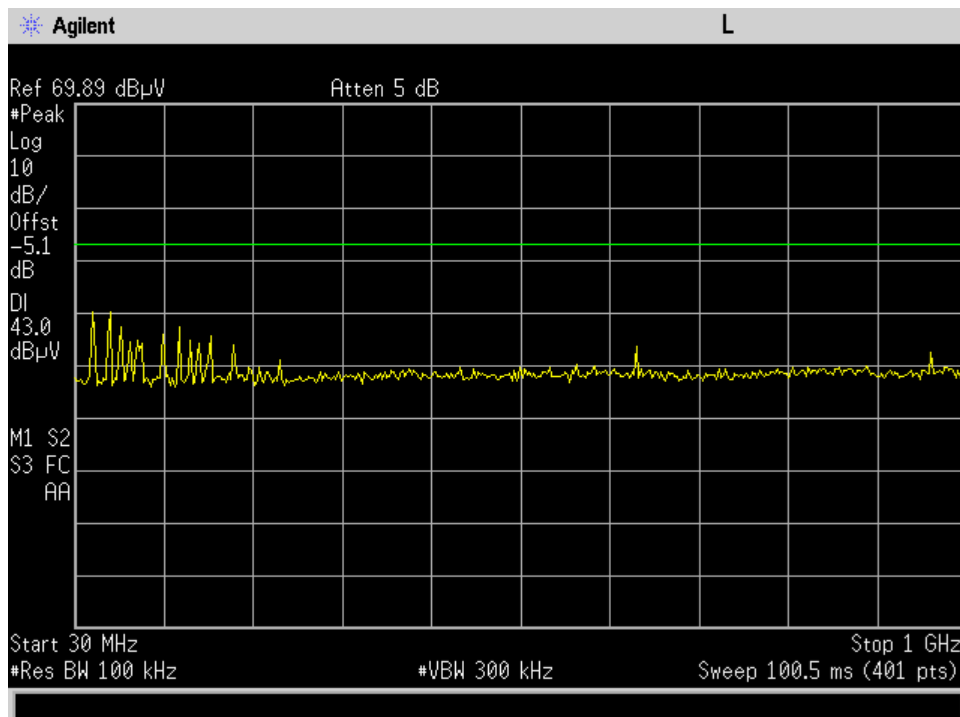




mid ch port 2 50MHz BW 26.5-40GHz

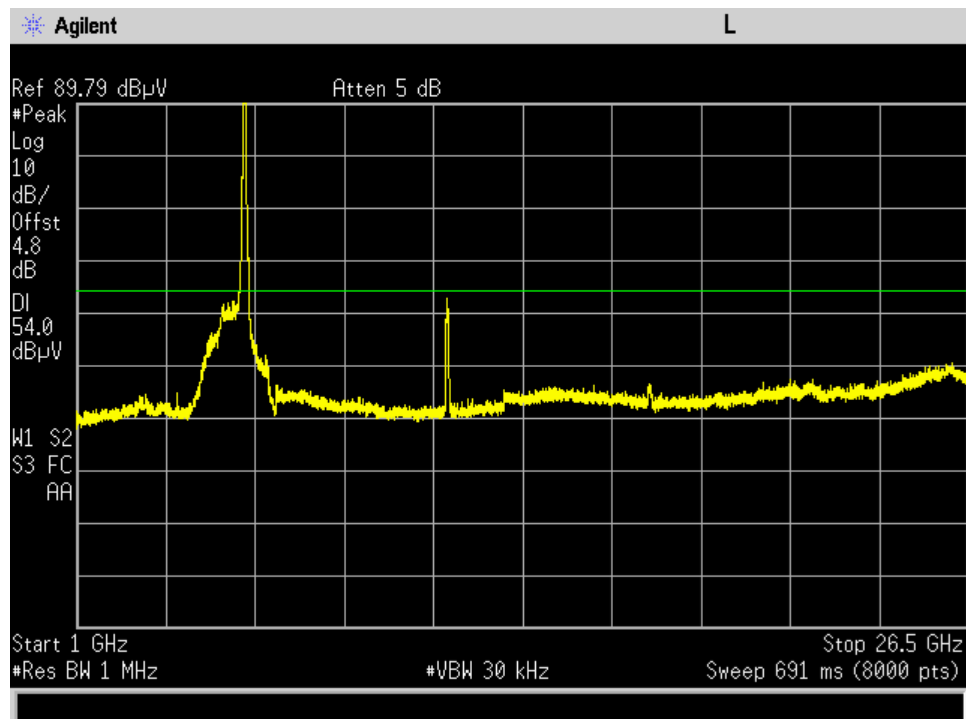


mid ch port 2 50MHz BW 30-1000MHz

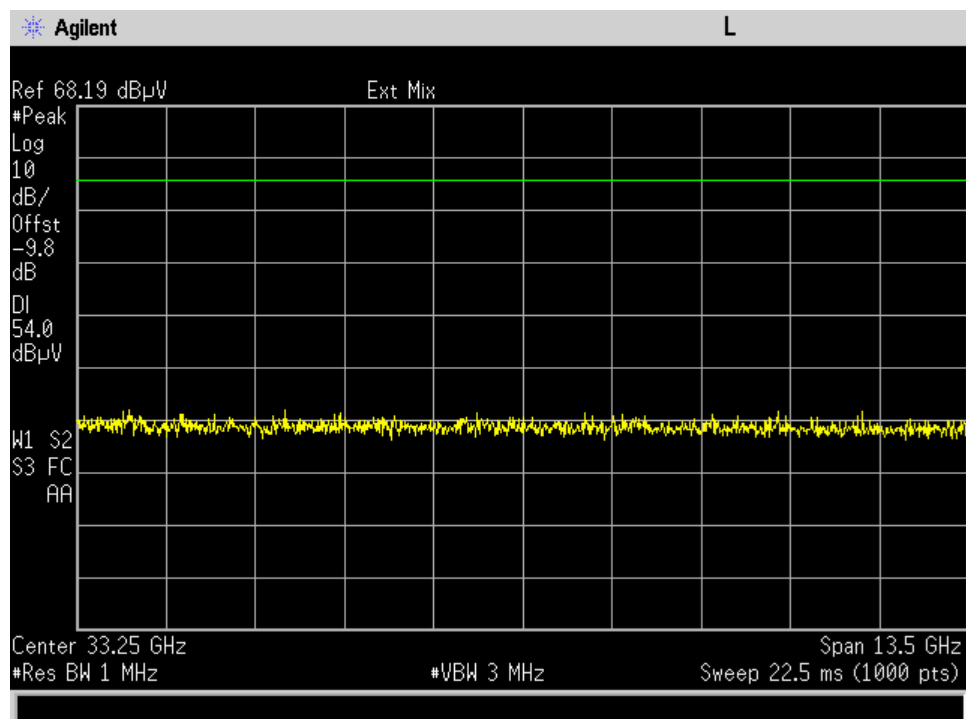




mid ch port 2 60MHz BW 1-26.5G

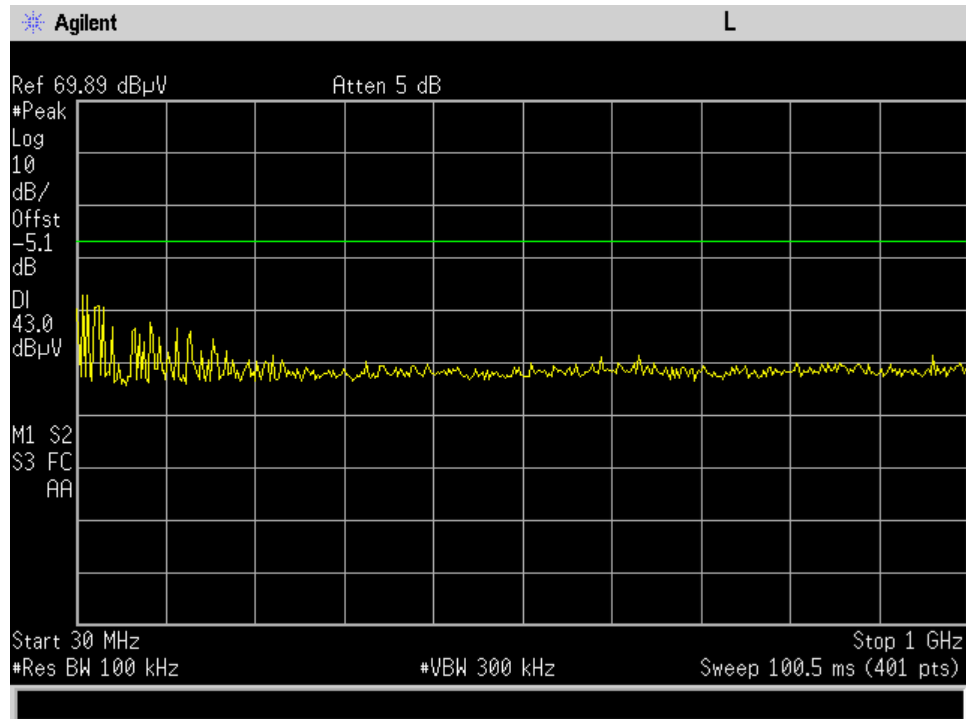


mid ch port 2 60MHz BW 26.5-40GHz

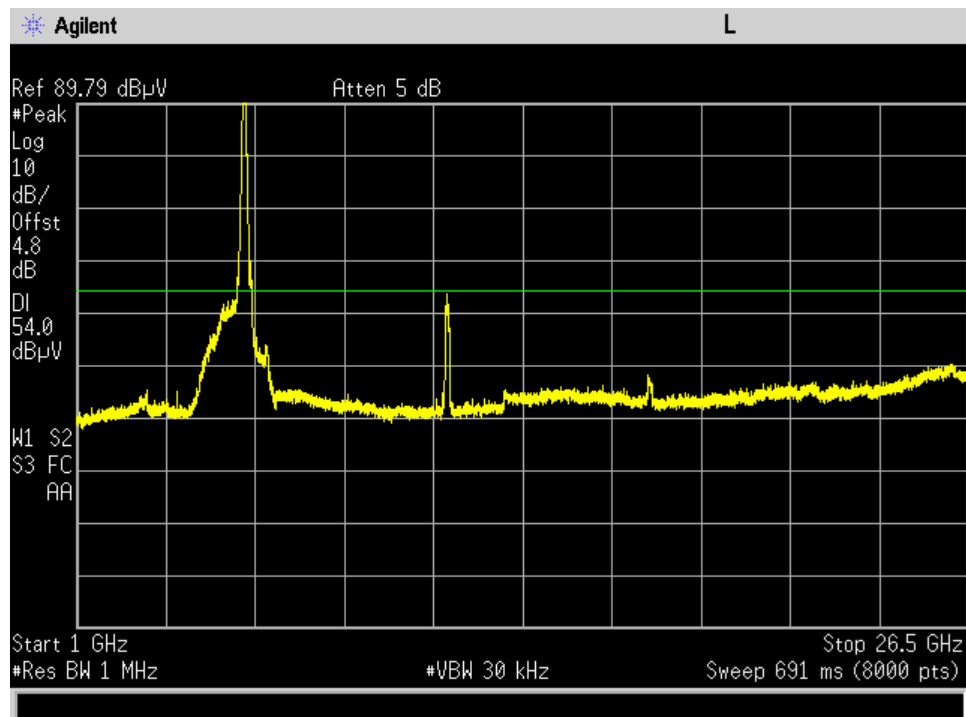




mid ch port 2 60MHz BW 30-1000MHz

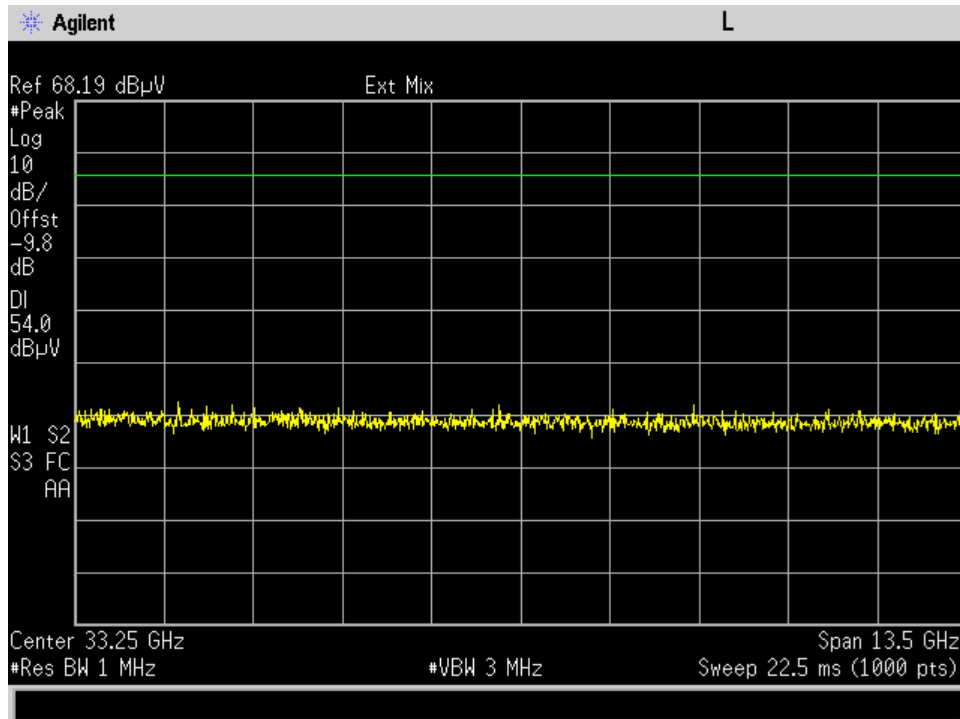


mid ch port 2 80MHz BW 1-26.5G

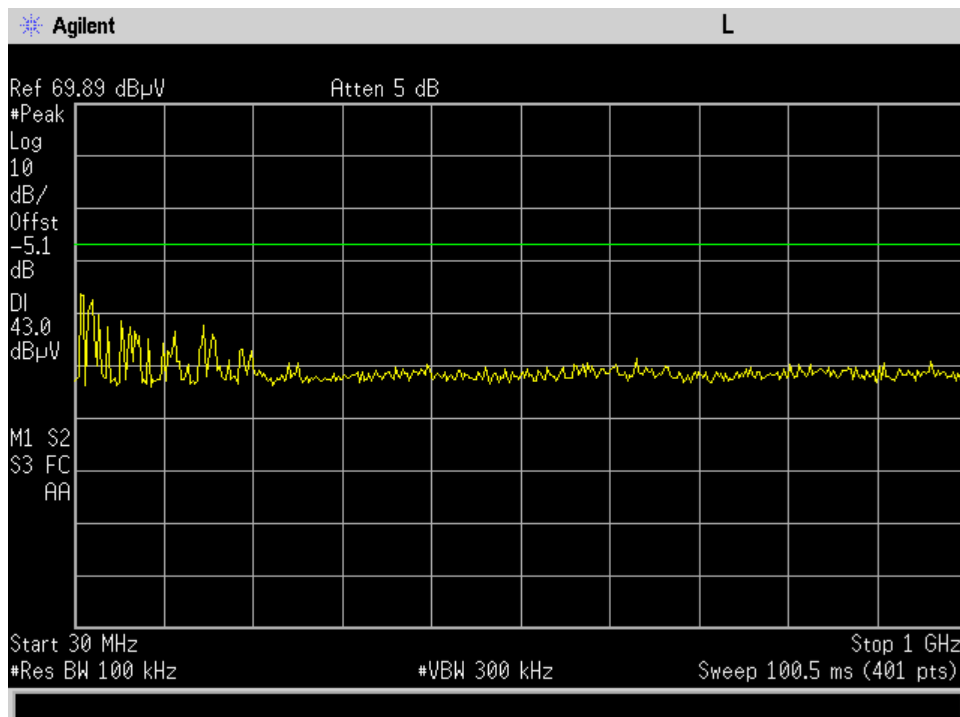




mid ch port 2 80MHz BW 26.5-40GHz

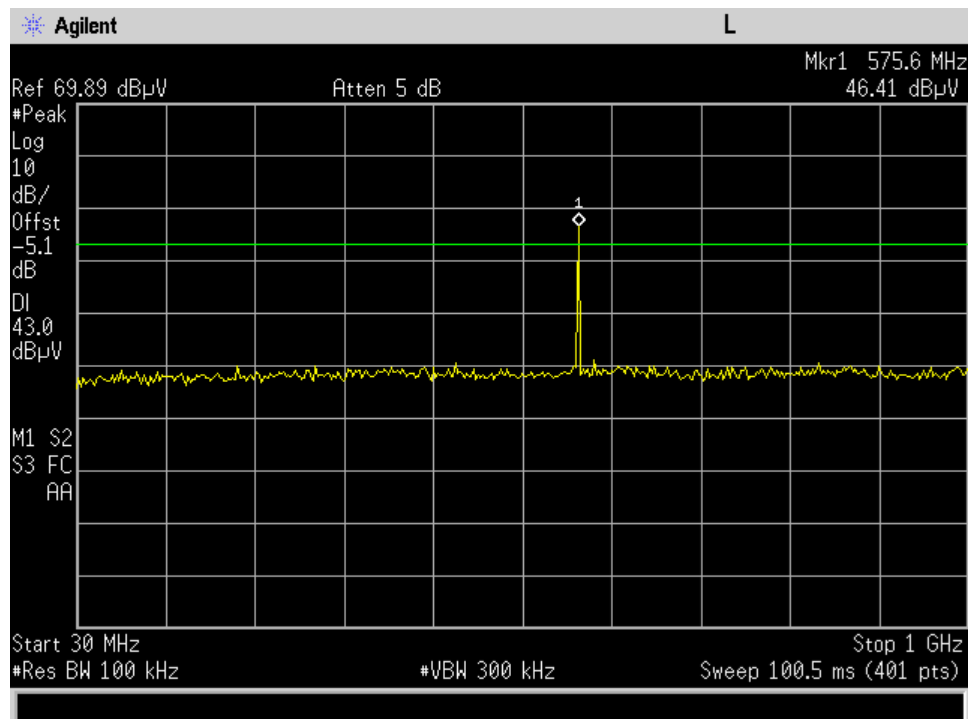


mid ch port 2 80MHz BW 30-1000MHz

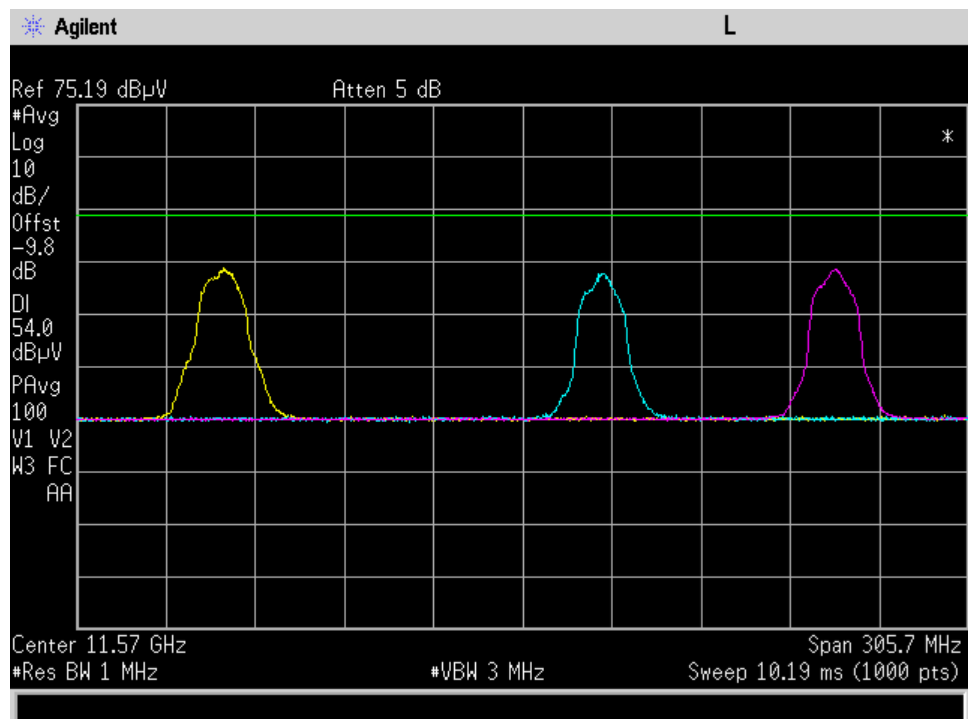




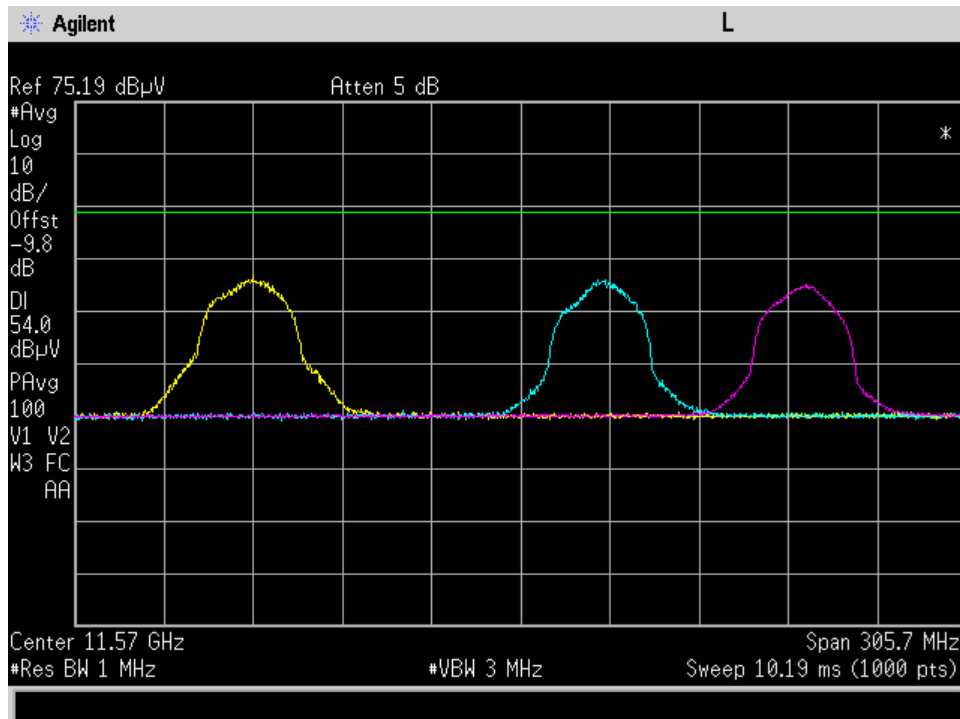
TX off port 2 30-1000MHz



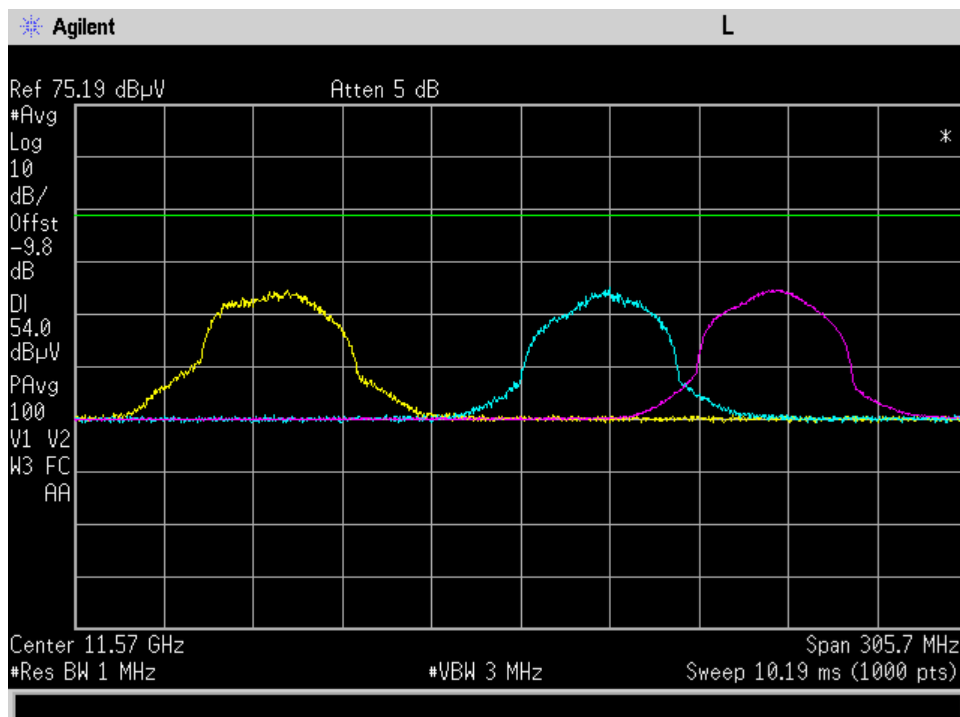
2nd harmonic port 1 10MHz low mid and high ch



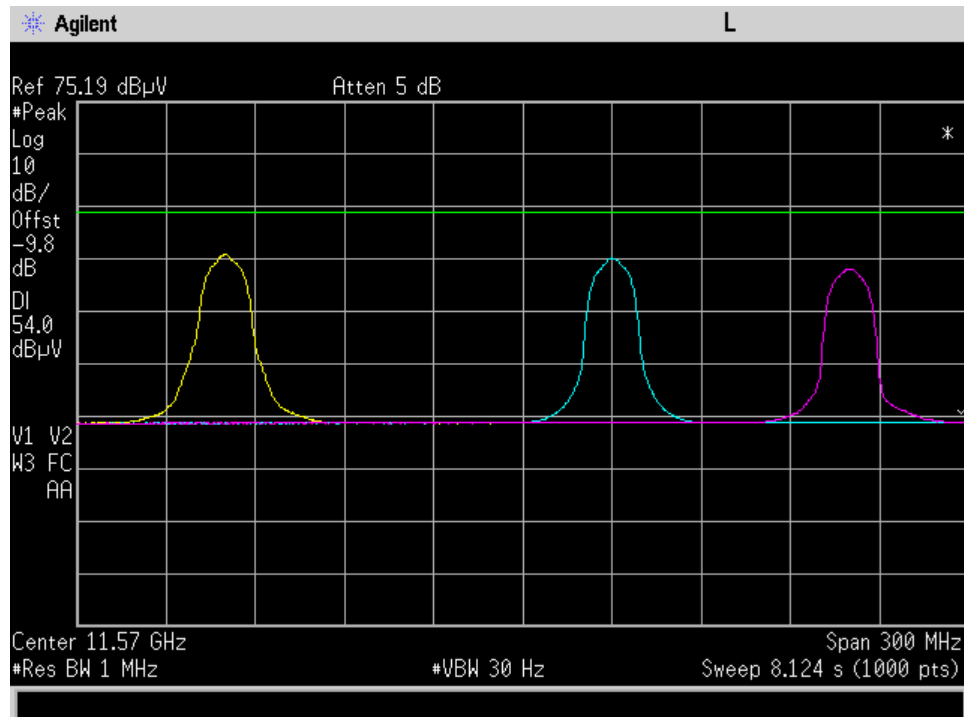
2nd harmonic port 1 20MHz low mid and high ch



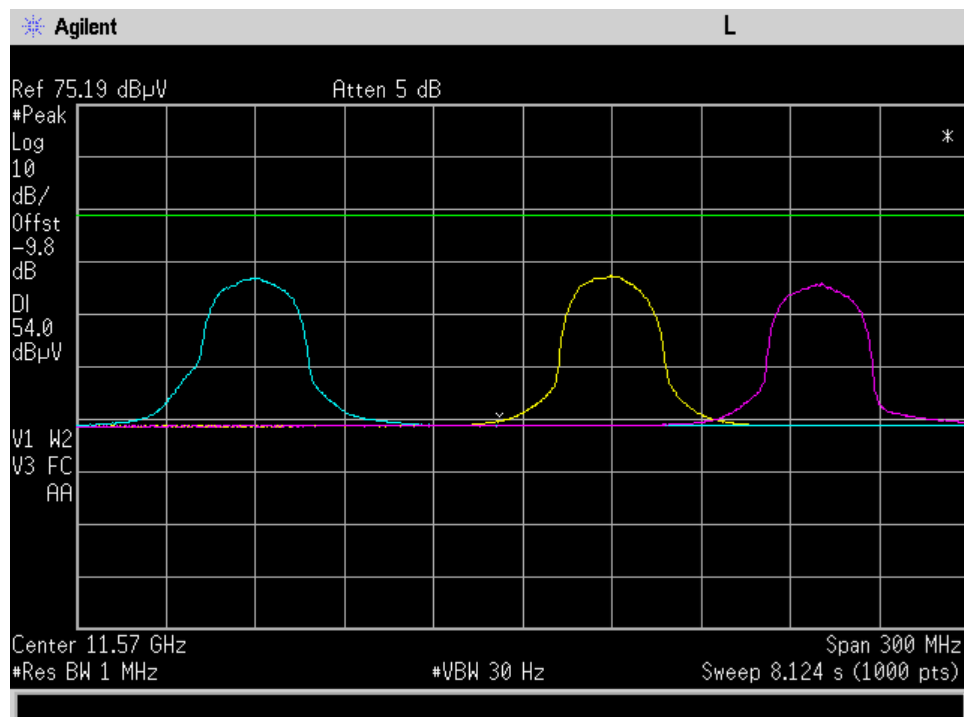
2nd harmonic port 1 30MHz low mid and high ch



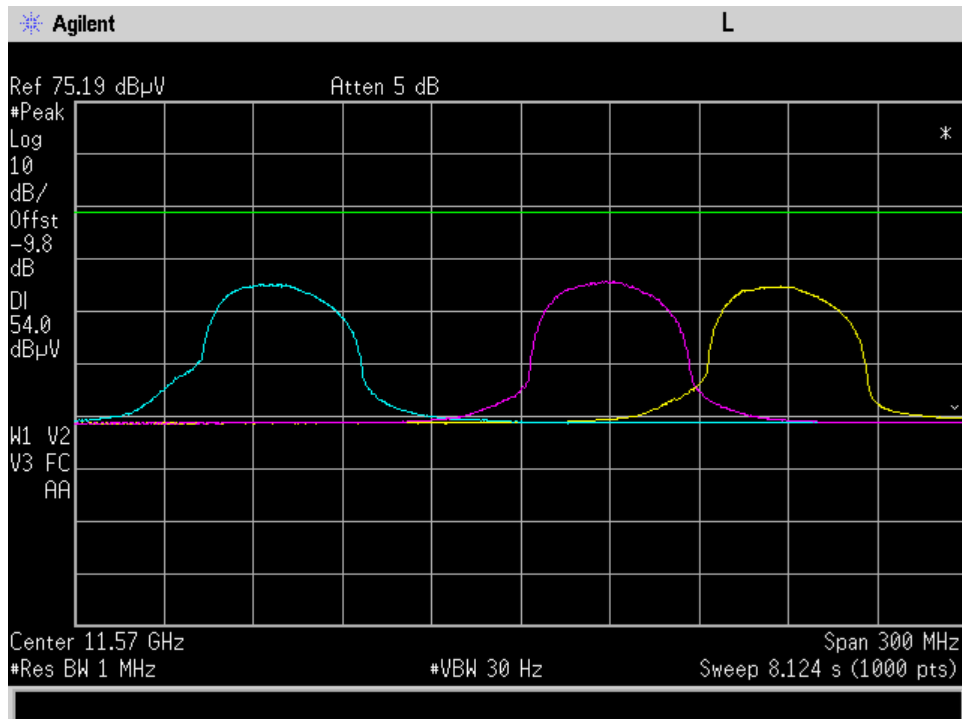
2nd harmonic port 2 10MHz low mid and high ch



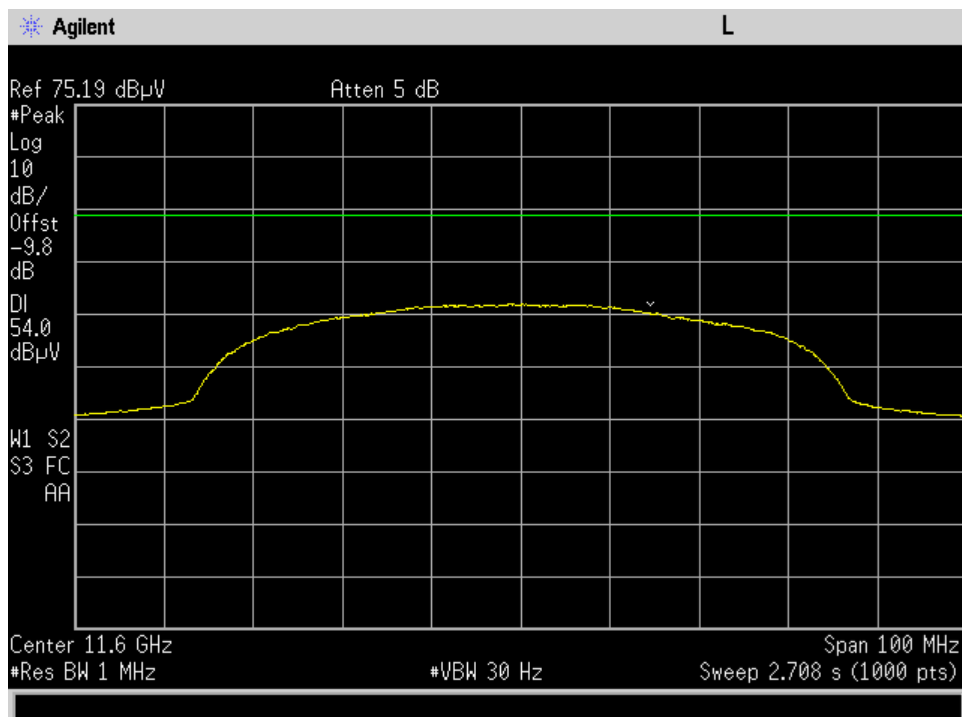
2nd harmonic port 2 20MHz low mid and high ch



2nd harmonic port 2 30MHz low mid and high ch



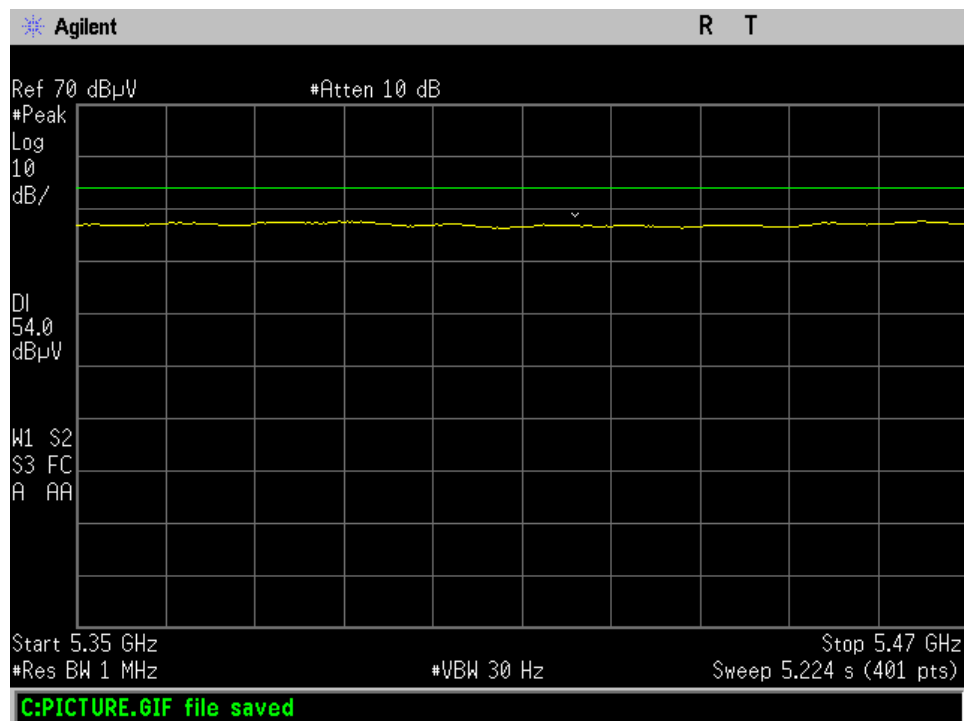
2nd harmonic port 2 40MHz mid ch



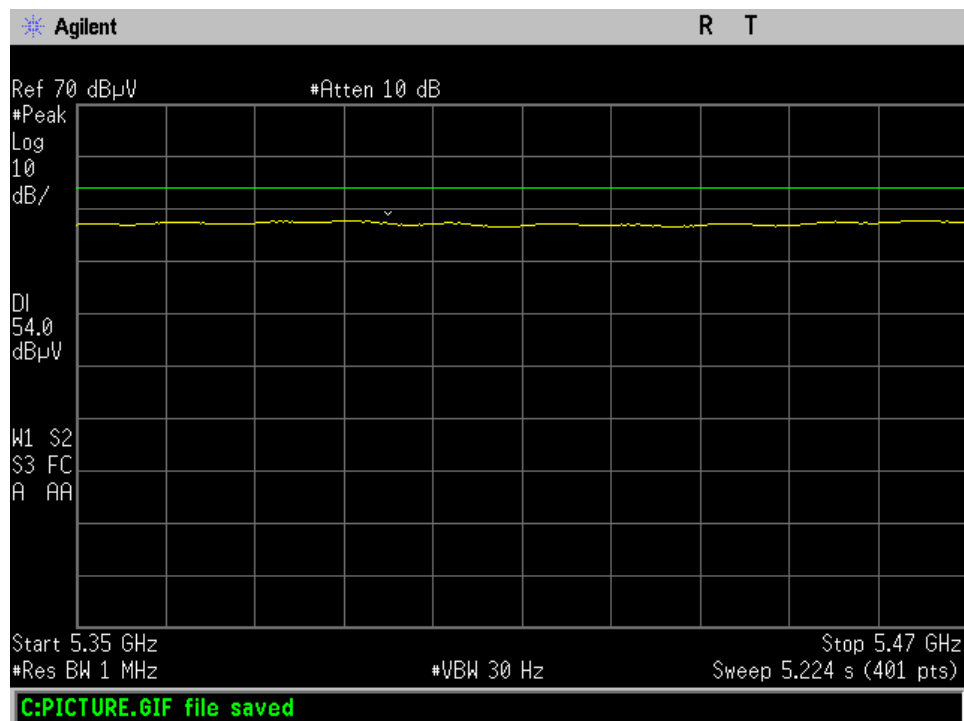


Radiated Restricted Band

high ch 10MHz BW 5350-5470MHz

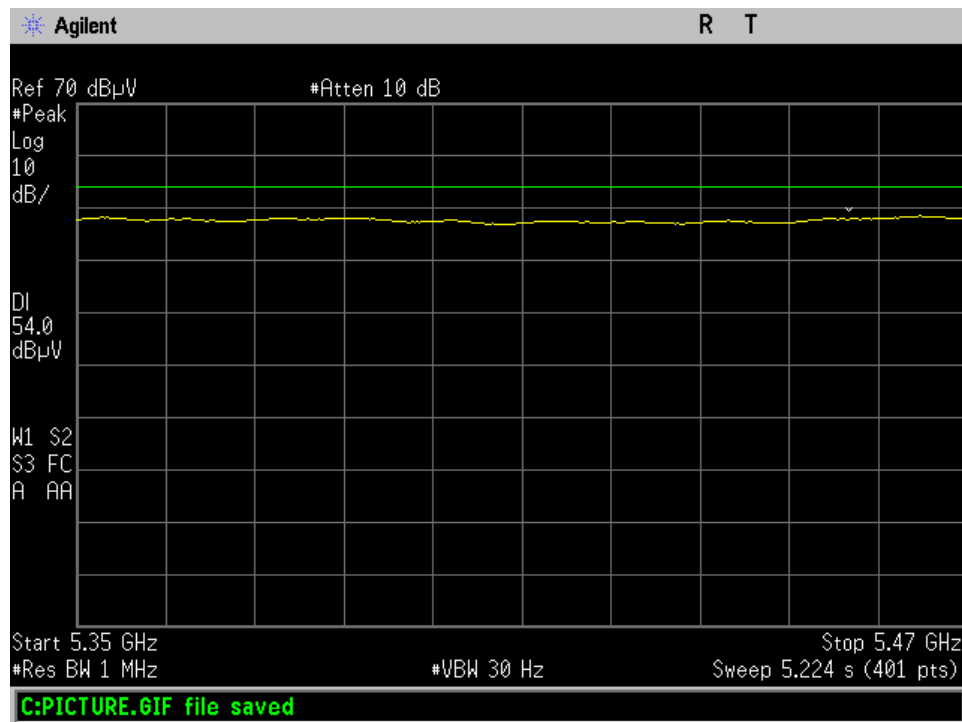


high ch 20MHz BW 5350-5470MHz

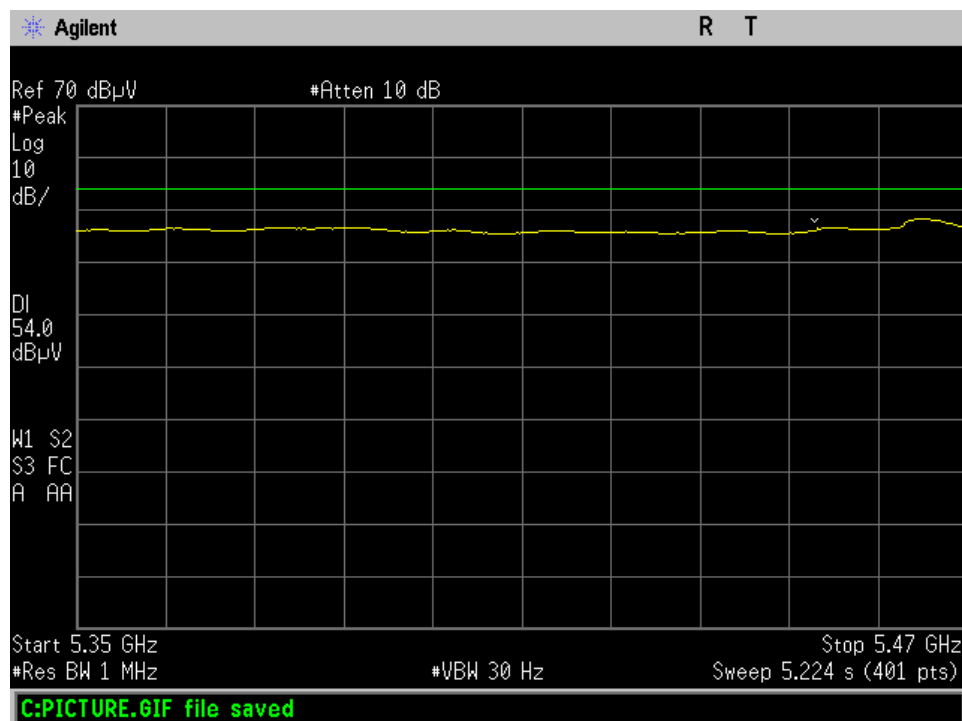




high ch 30MHz BW 5350-5470MHz

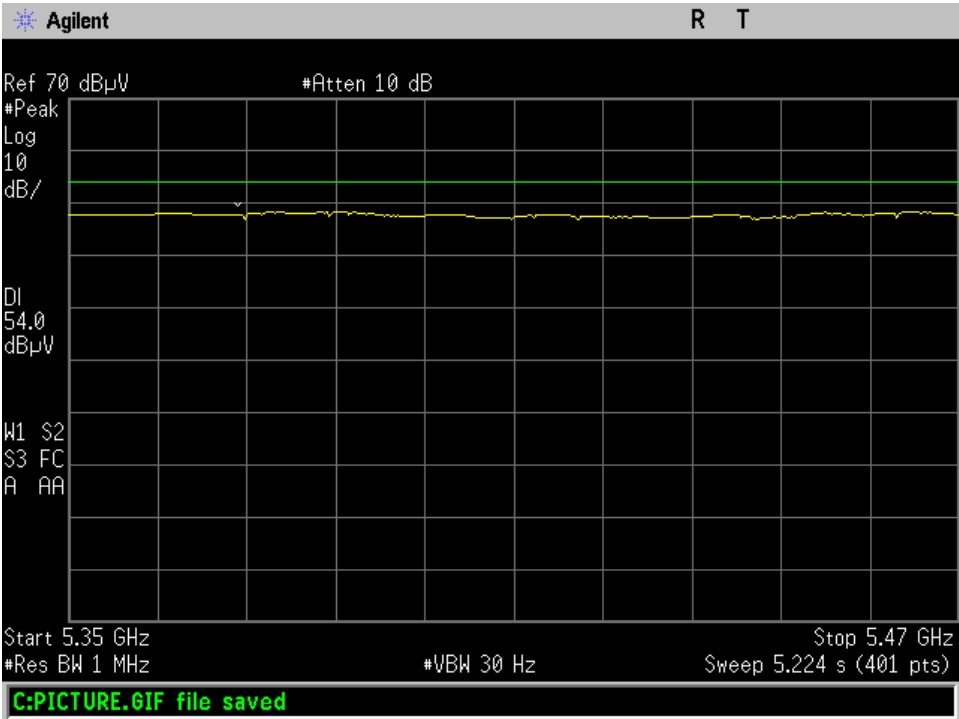


high ch 40MHz BW 5350-5470MHz

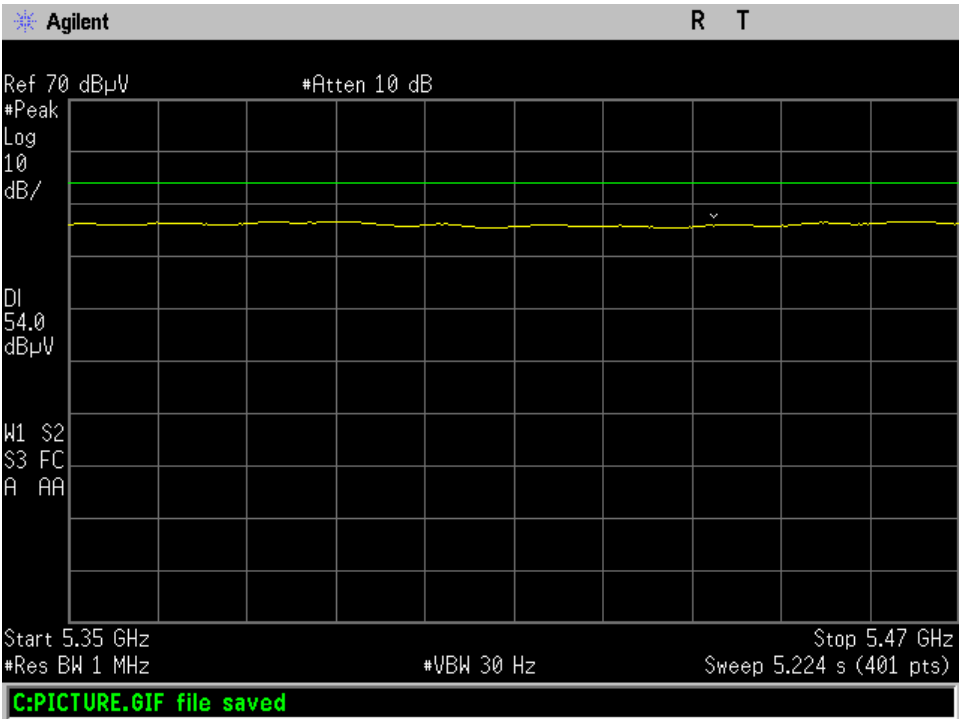




high ch 50MHz BW 5350-5470MHz

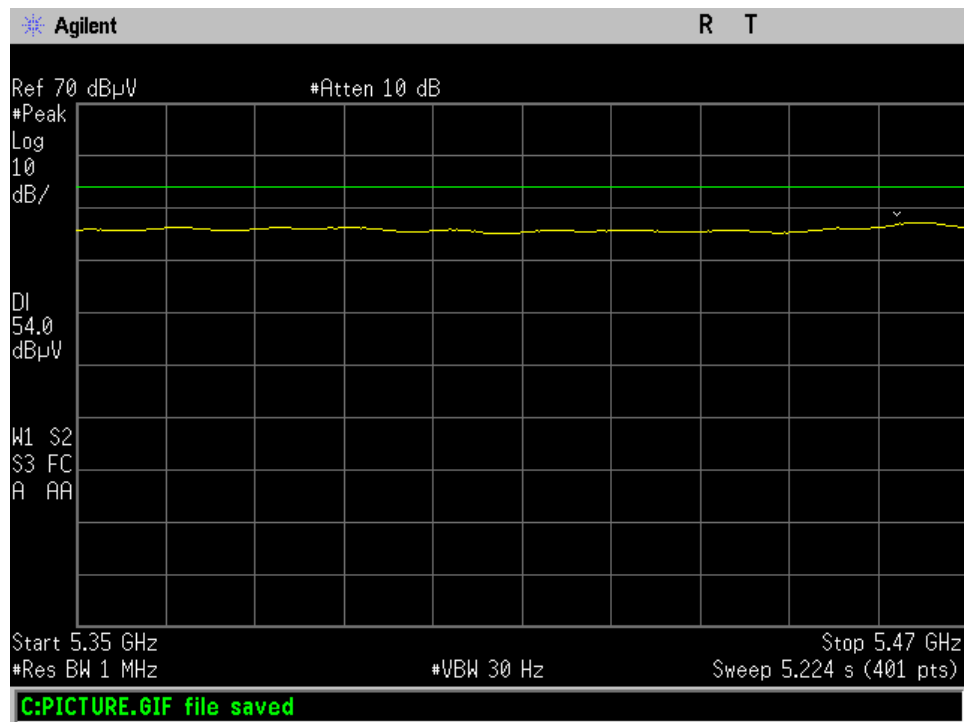


high ch 60MHz BW 5350-5470MHz

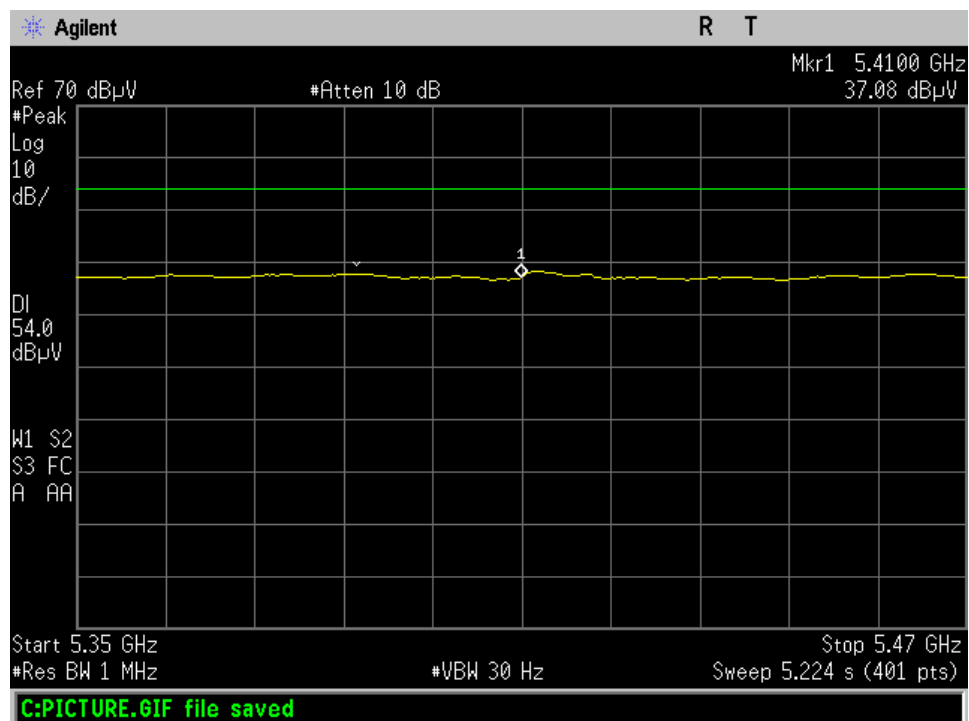




high ch 80MHz BW 5350-5470MHz

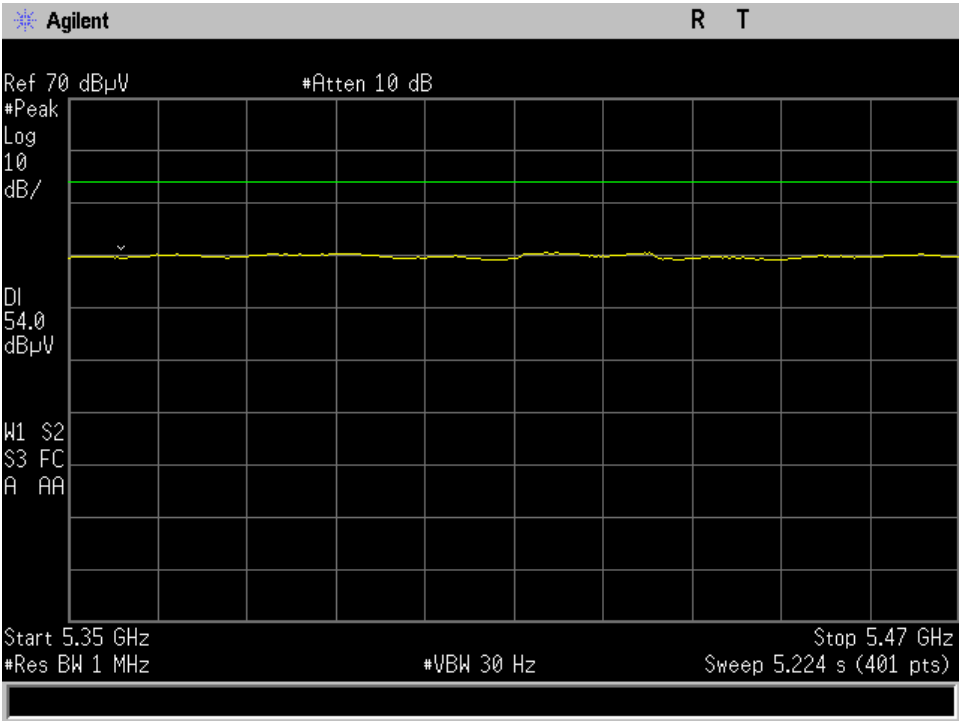


low ch 10MHz BW 5350-5470MHz

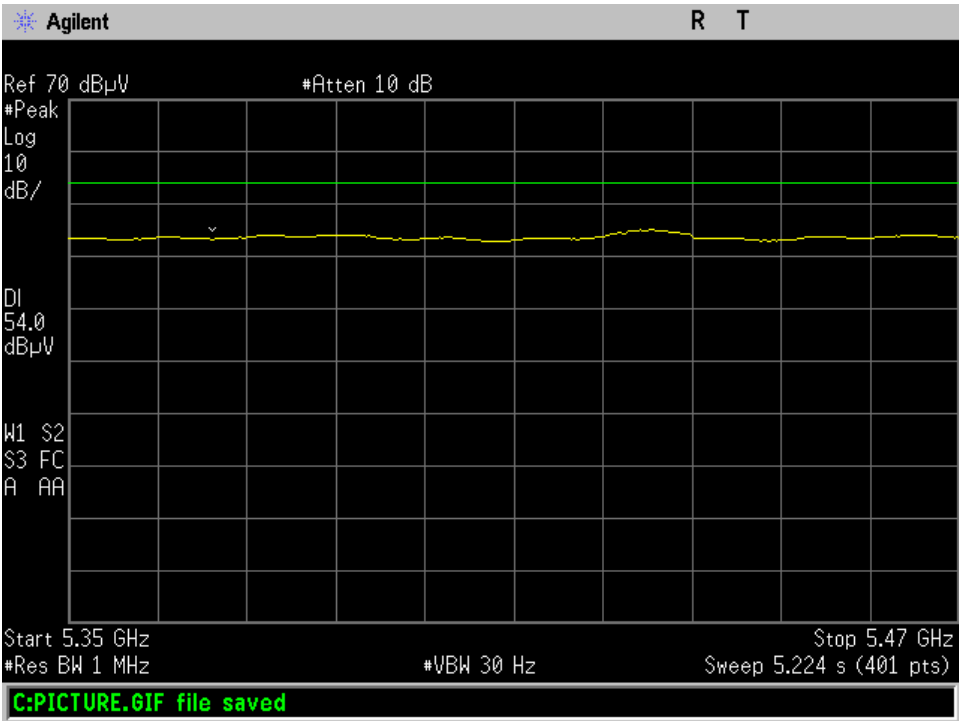




low ch 20MHz BW 5350-5470MHz

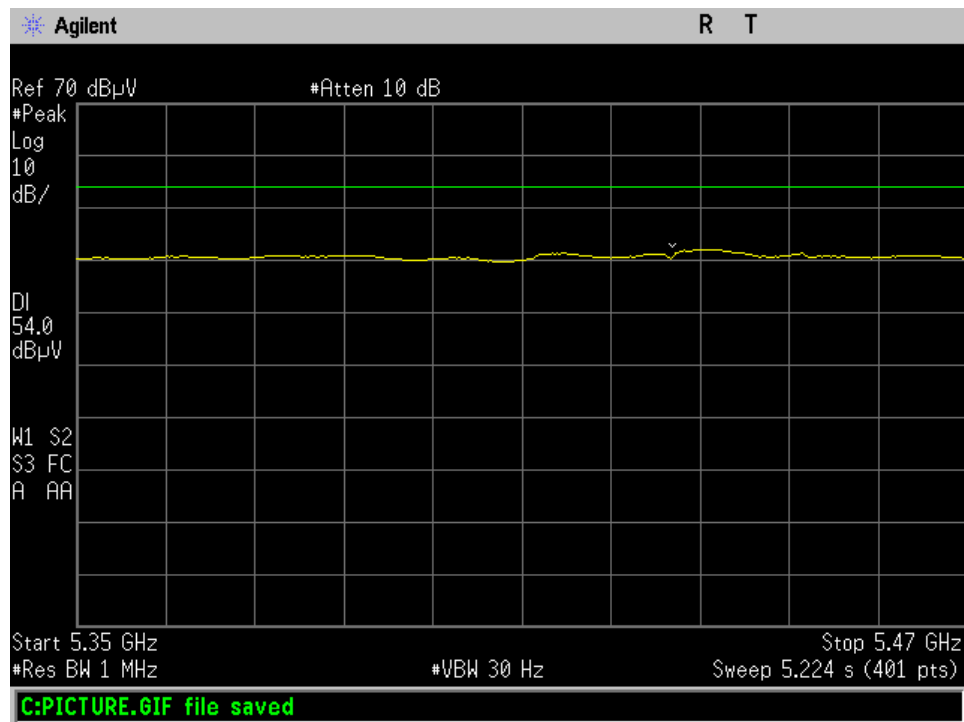


low ch 30MHz BW 5350-5470MHz

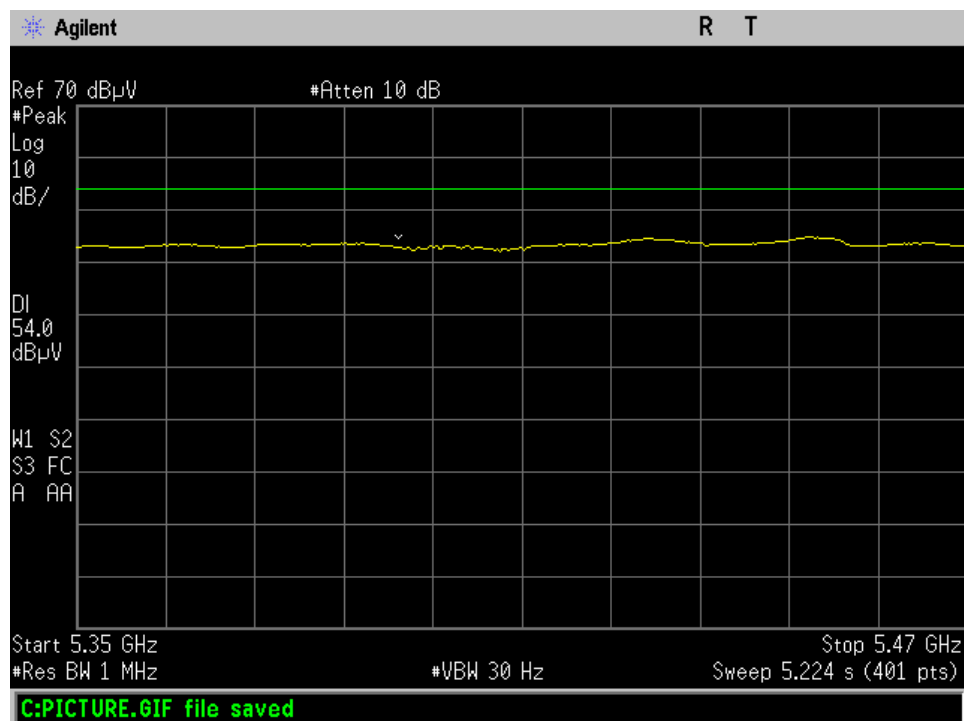




low ch 40MHz BW 5350-5470MHz

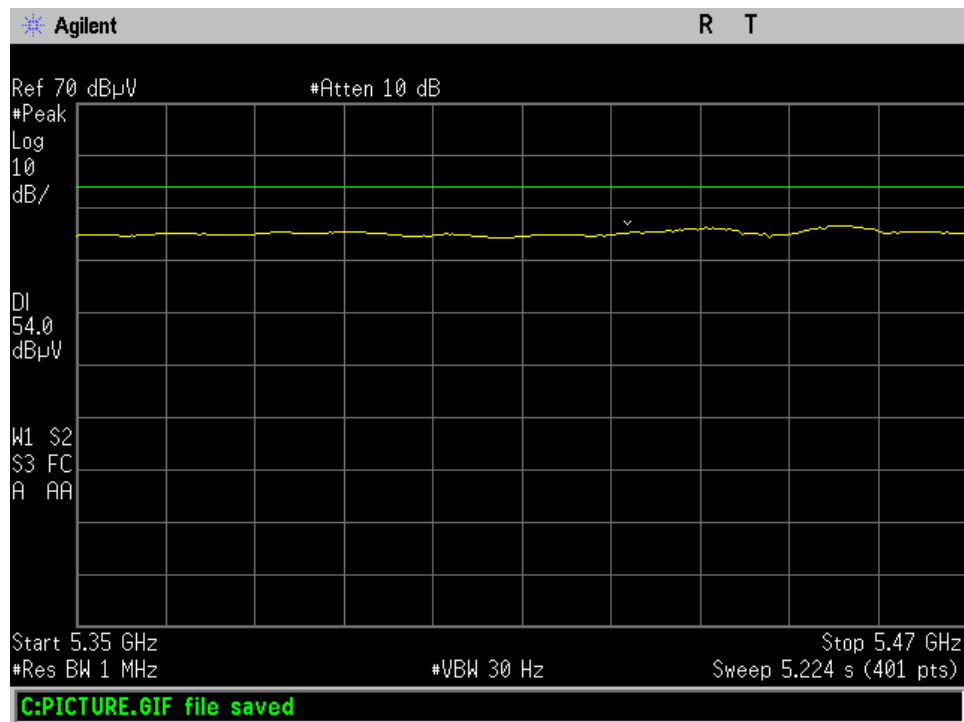


low ch 50MHz BW 5350-5470MHz

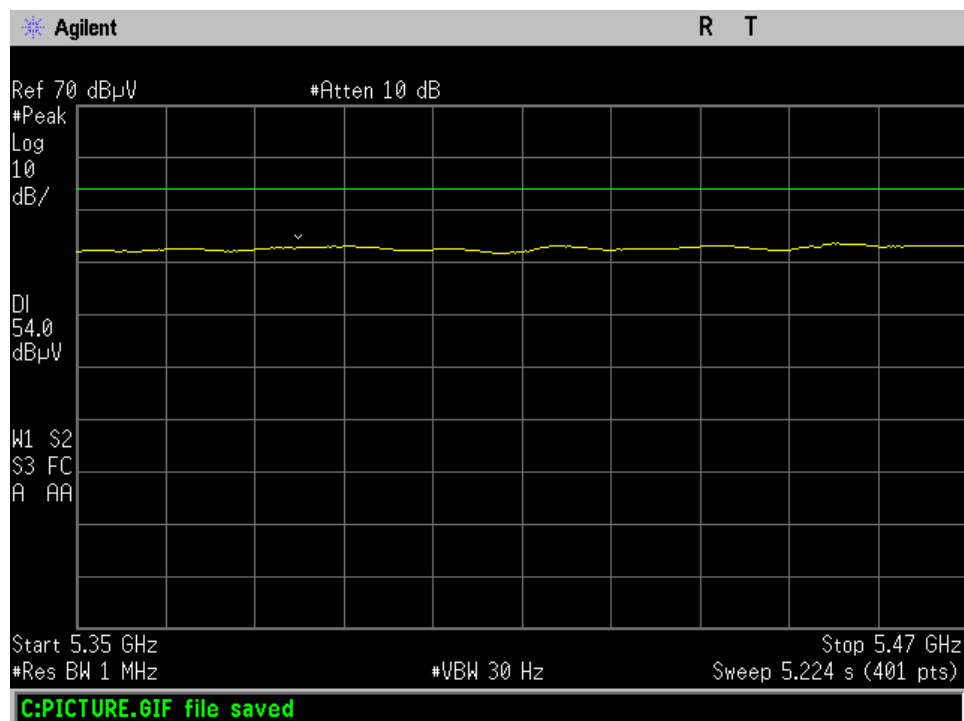




low ch 60MHz BW 5350-5470MHz

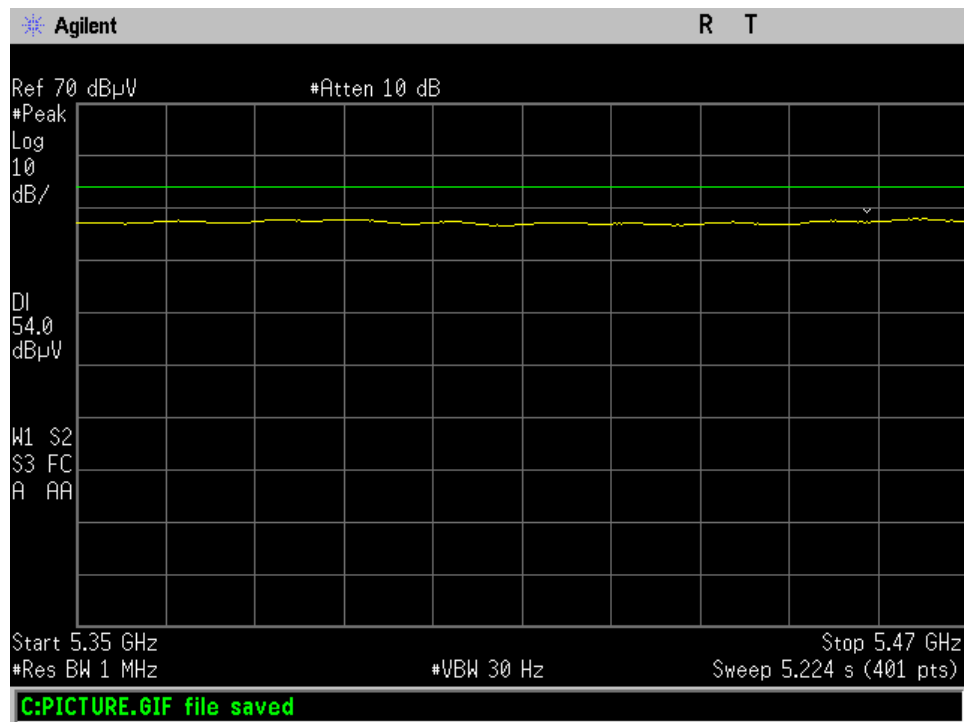


low ch 80MHz BW 5350-5470MHz

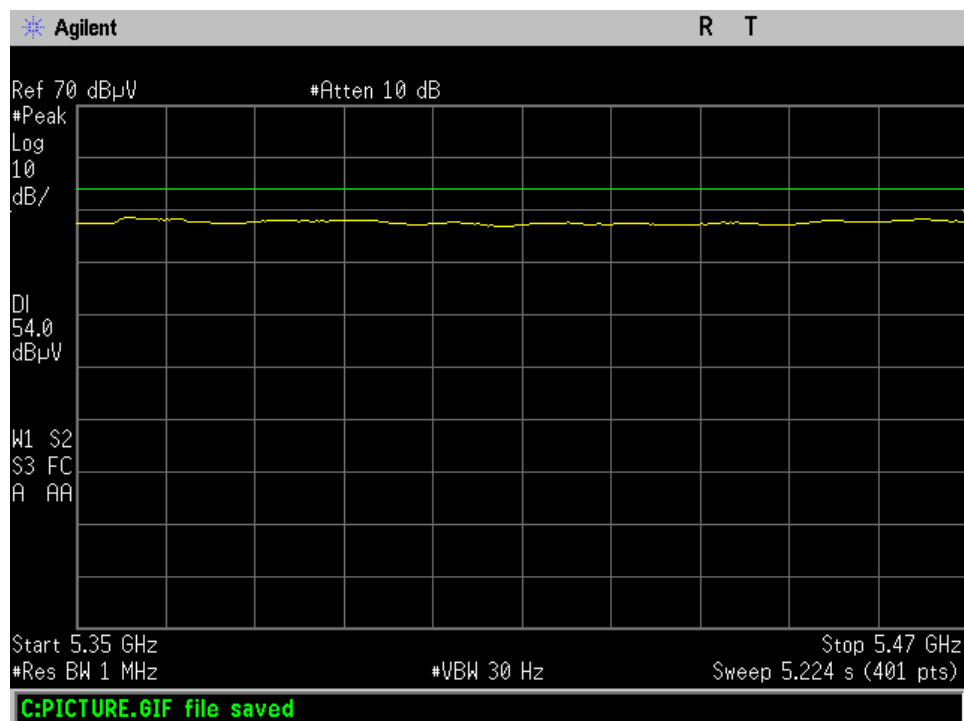




mid ch 10MHz BW 5350-5470MHz

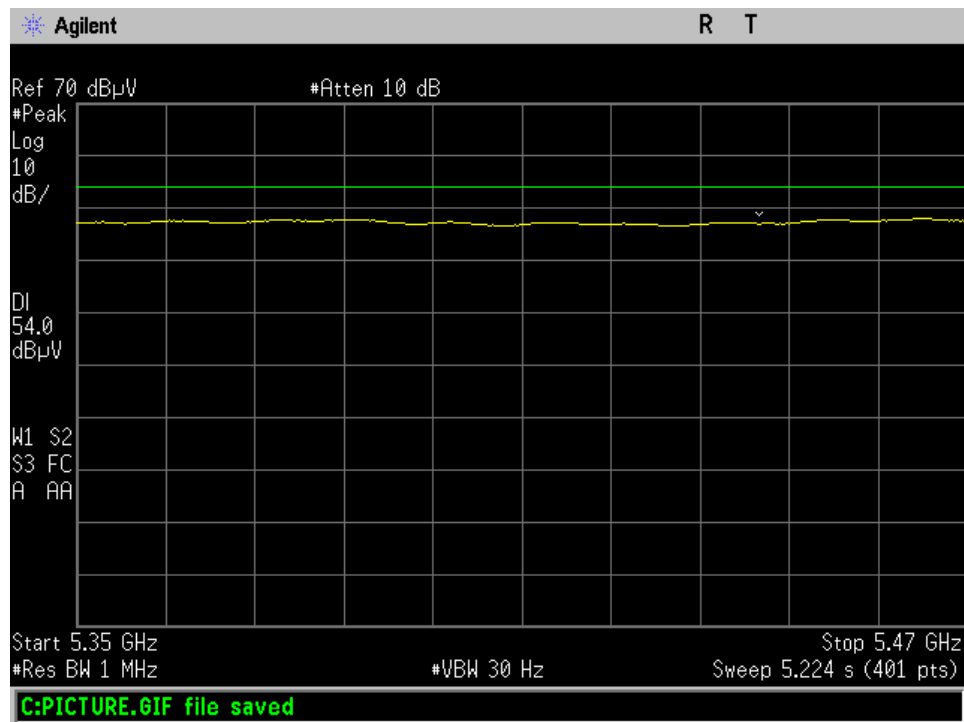


mid ch 20MHz BW 5350-5470MHz

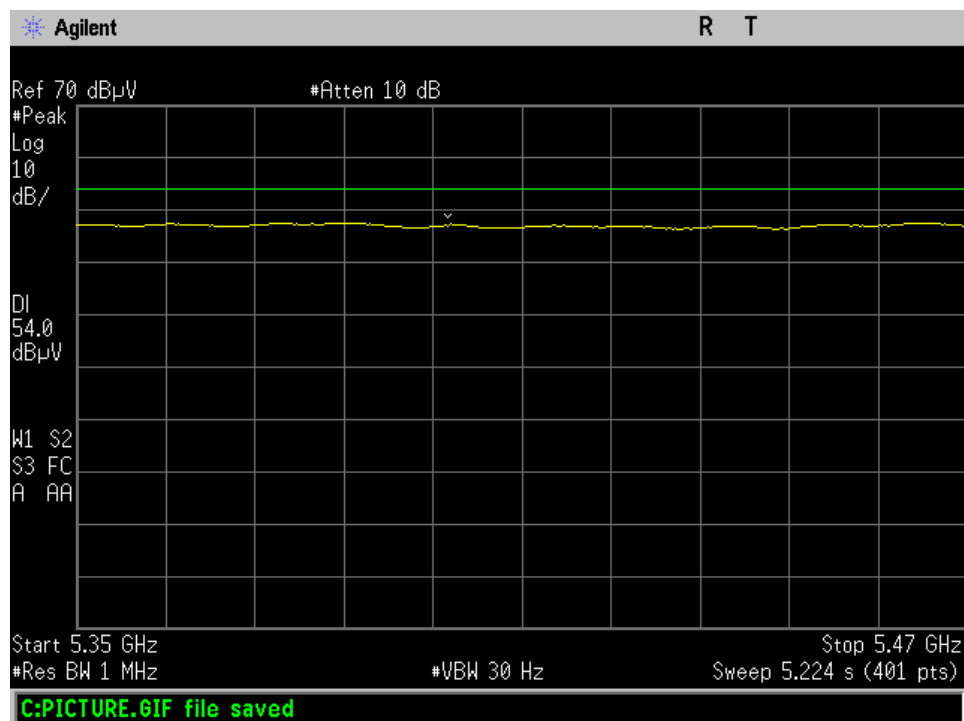




mid ch 30MHz BW 5350-5470MHz

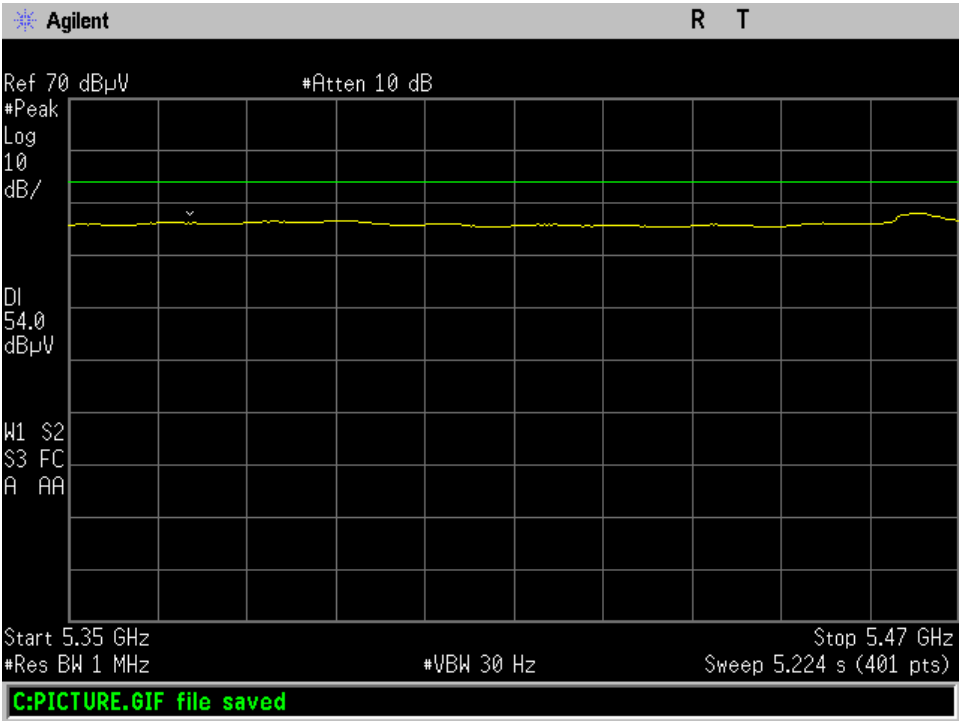


mid ch 40MHz BW 5350-5470MHz

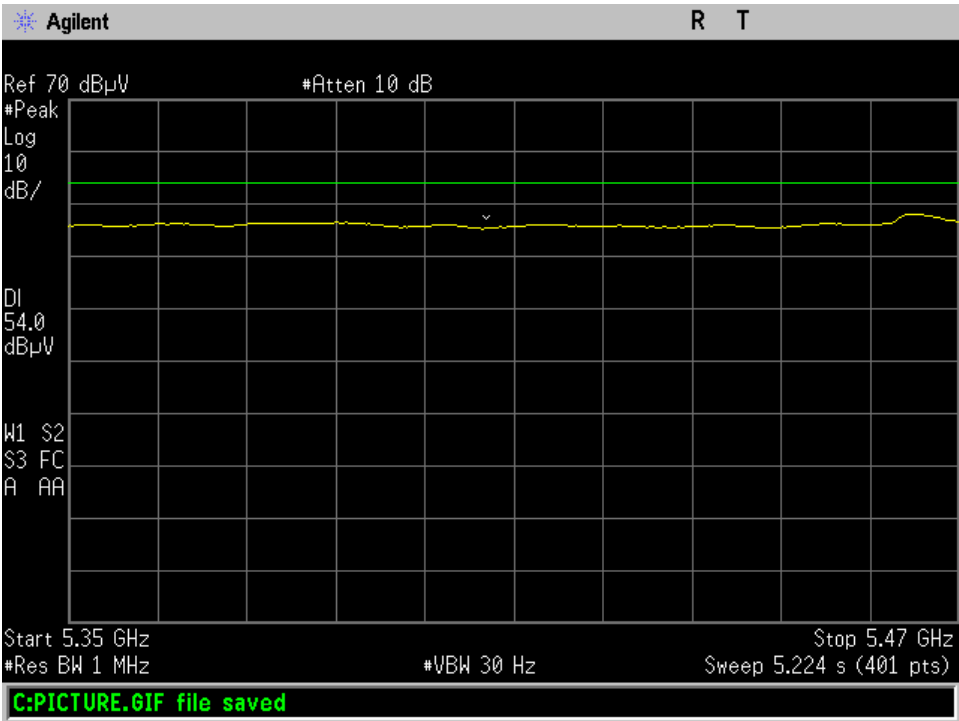




mid ch 50MHz BW 5350-5470MHz

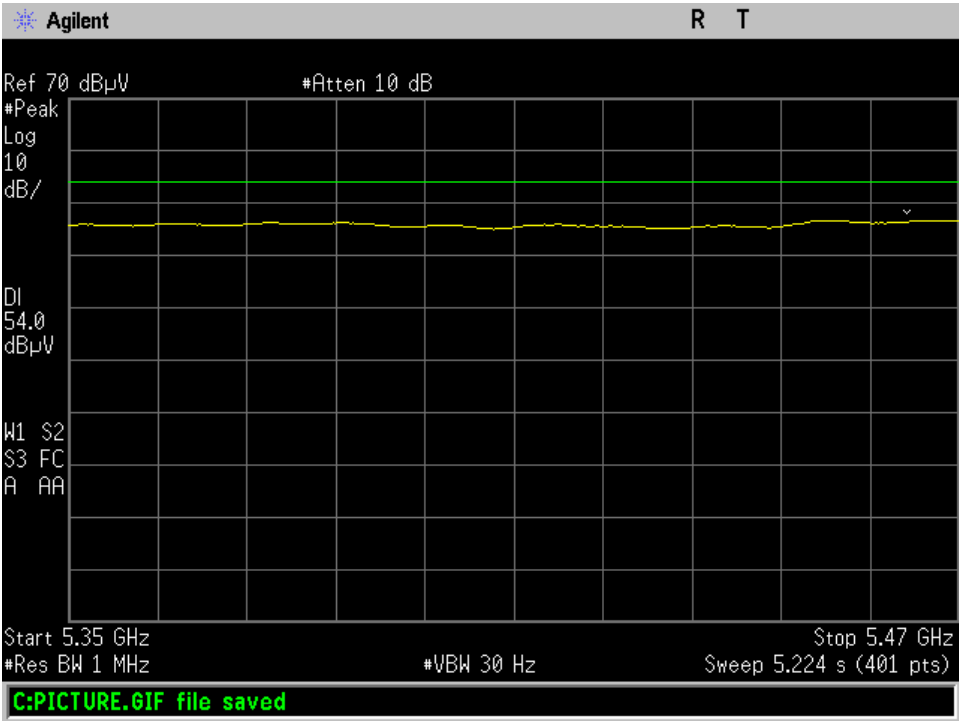


mid ch 60MHz BW 5350-5470MHz



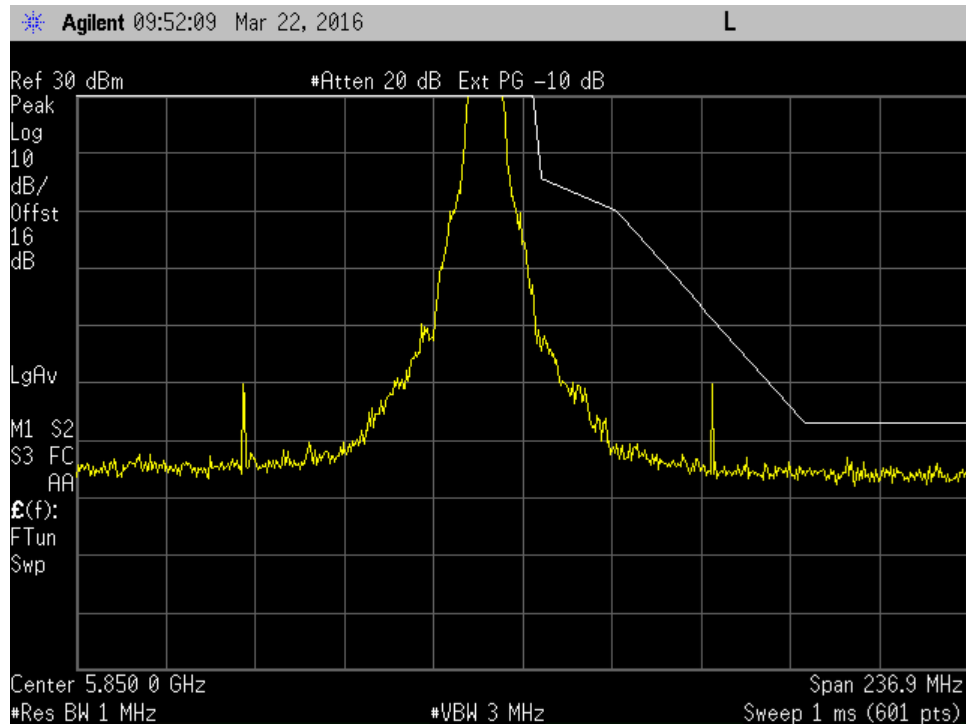


mid ch 80MHz BW 5350-5470MHz

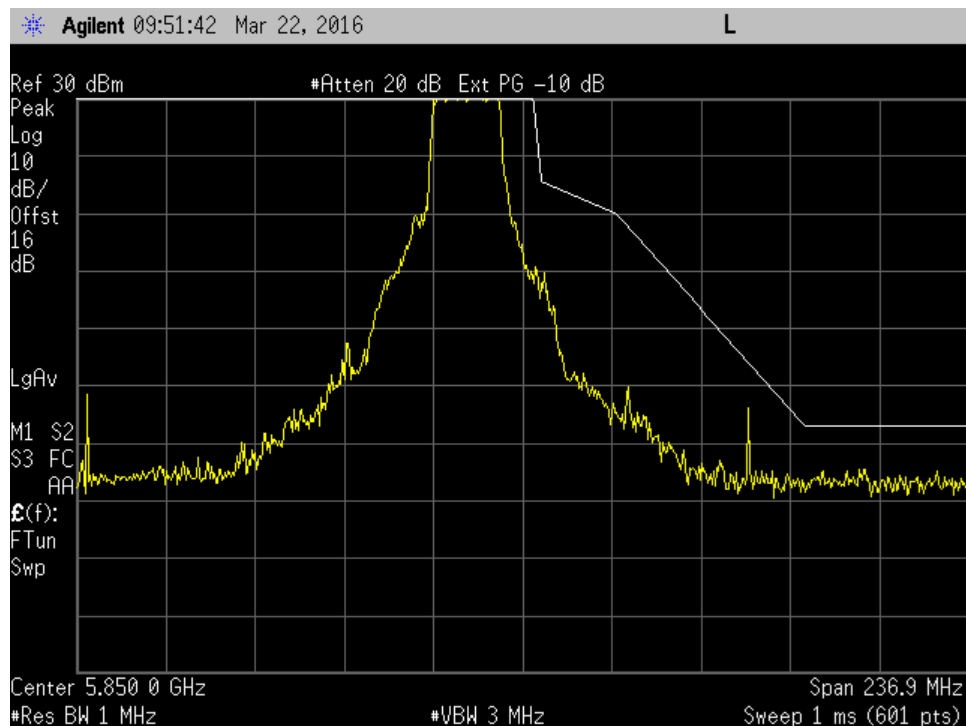




high ch port 1 10MHz BW

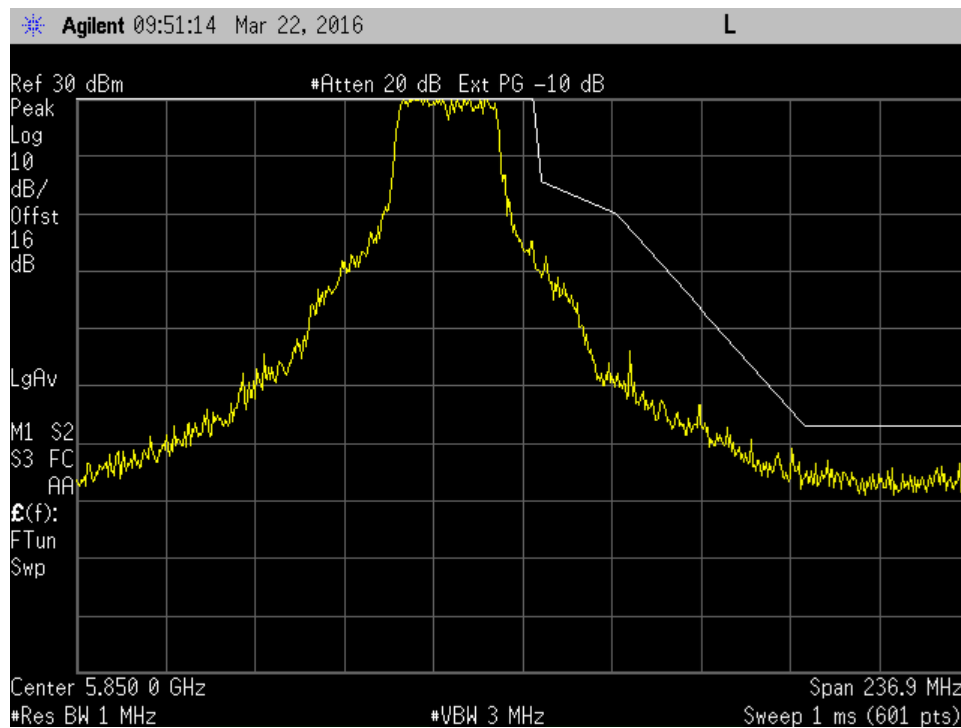


high ch port 1 20MHz BW

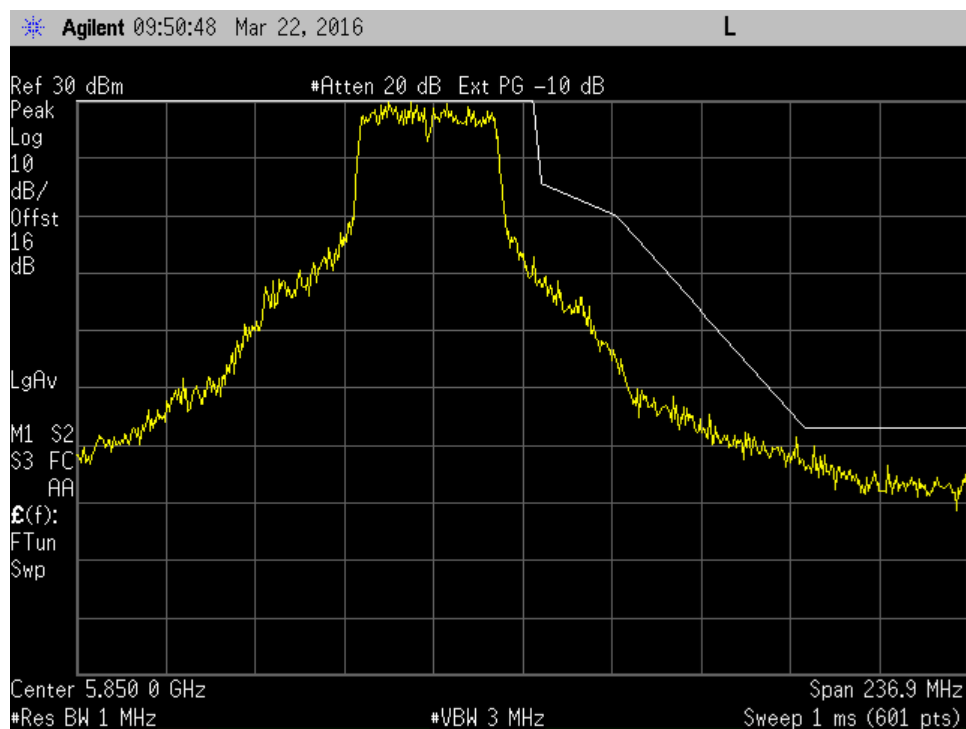




high ch port 1 30MHz BW

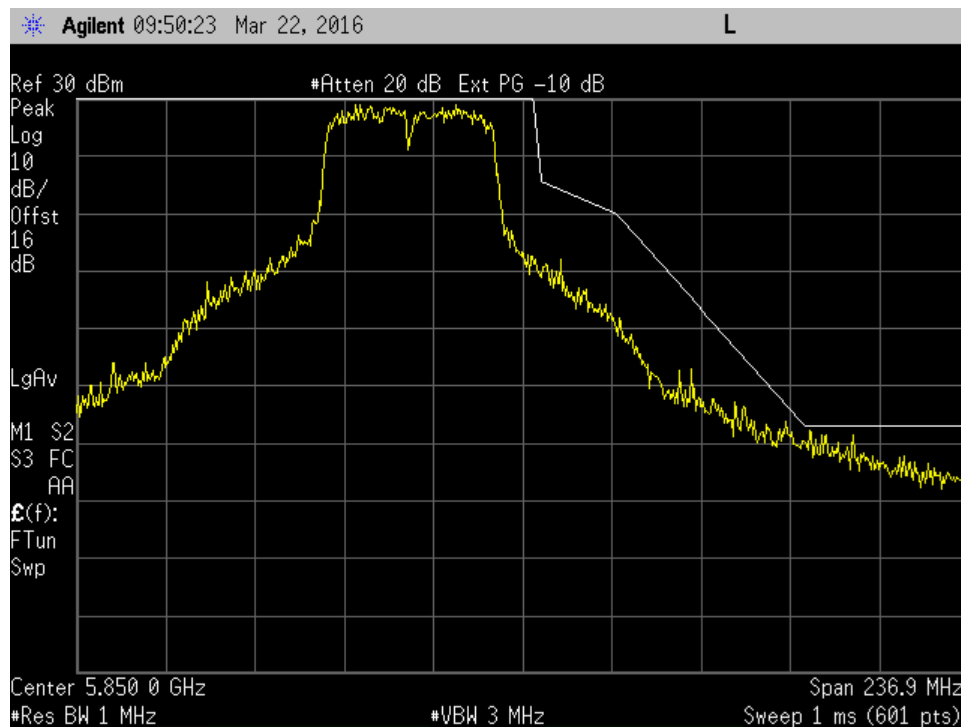


high ch port 1 40MHz BW

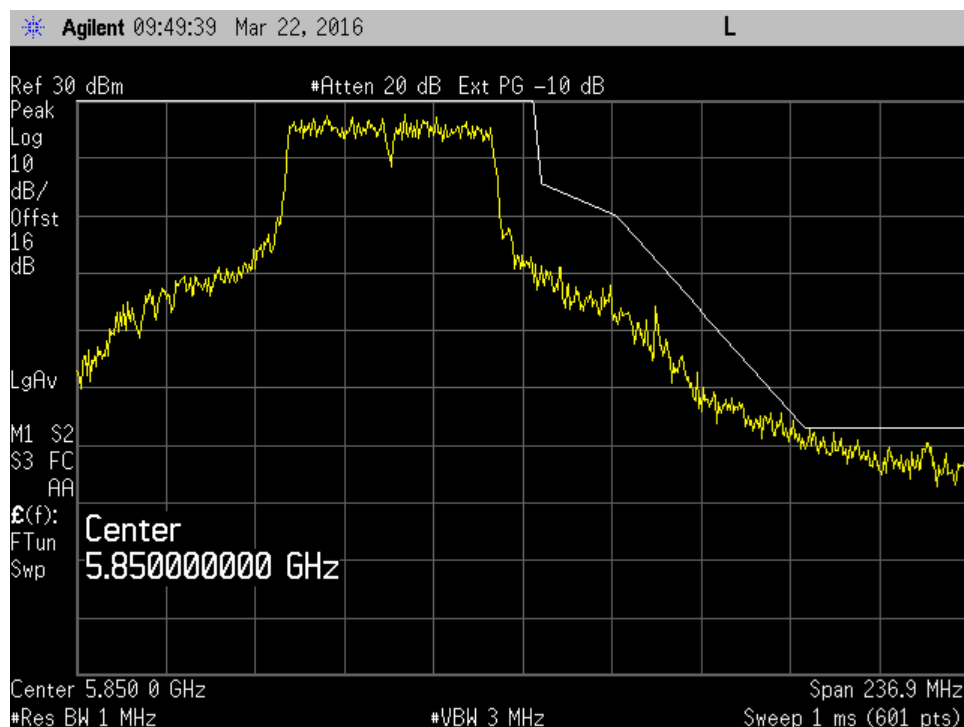




high ch port 1 50MHz BW

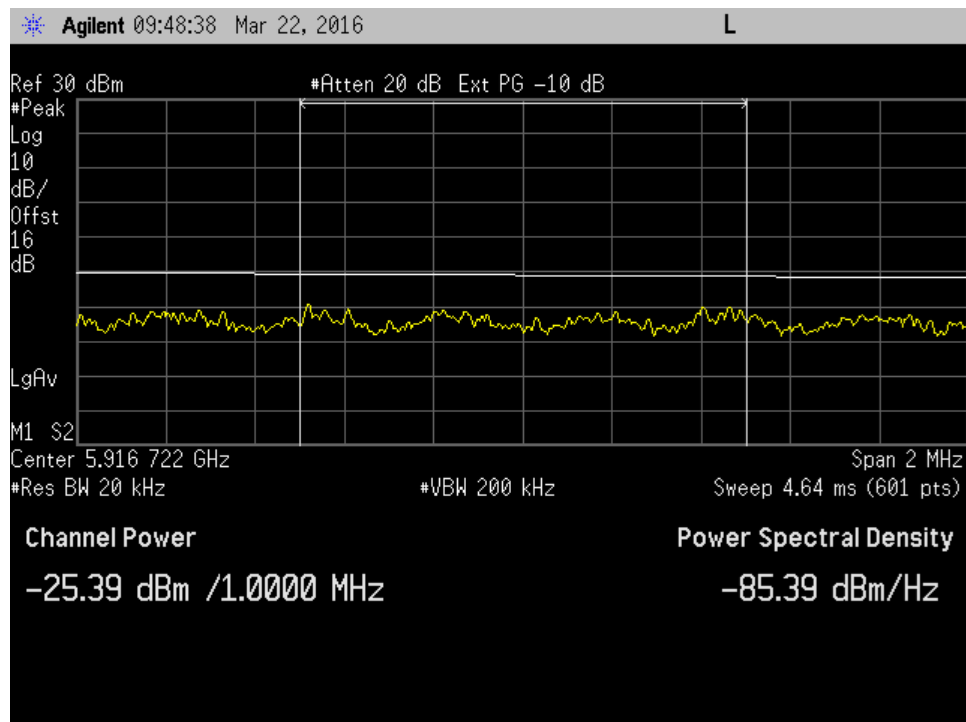


high ch port 1 60MHz BW

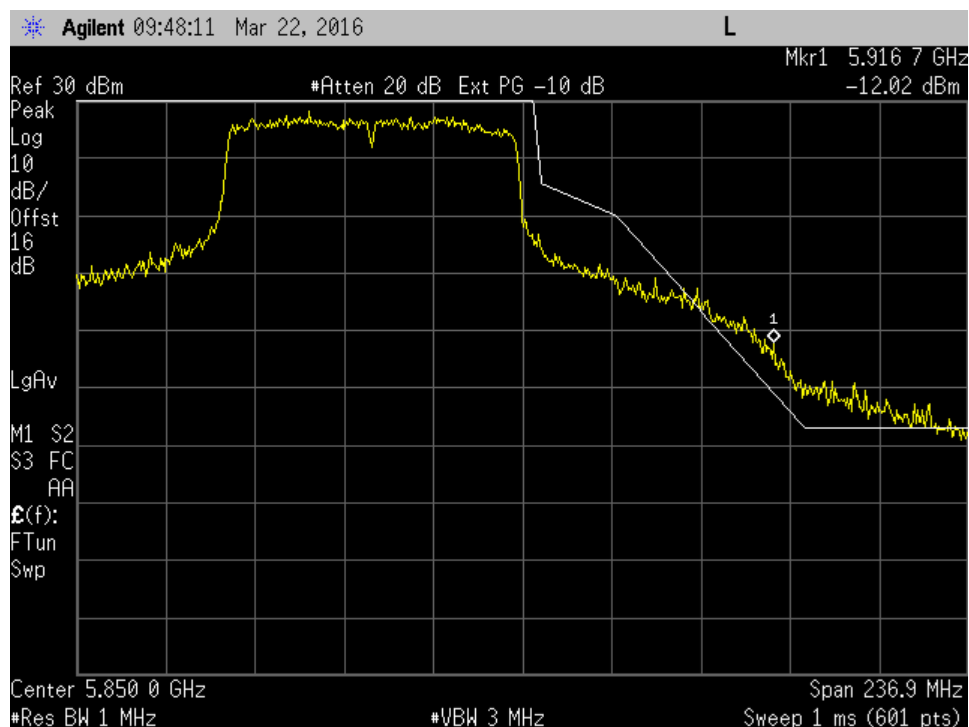




high ch port 1 80MHz BW channel integration

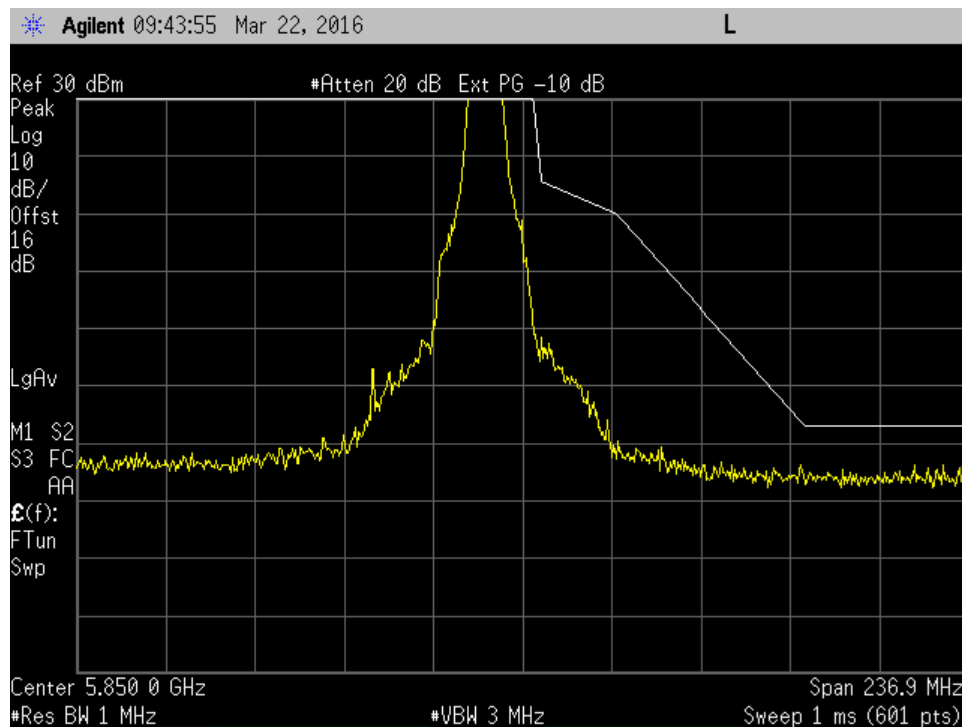


high ch port 1 80MHz BW

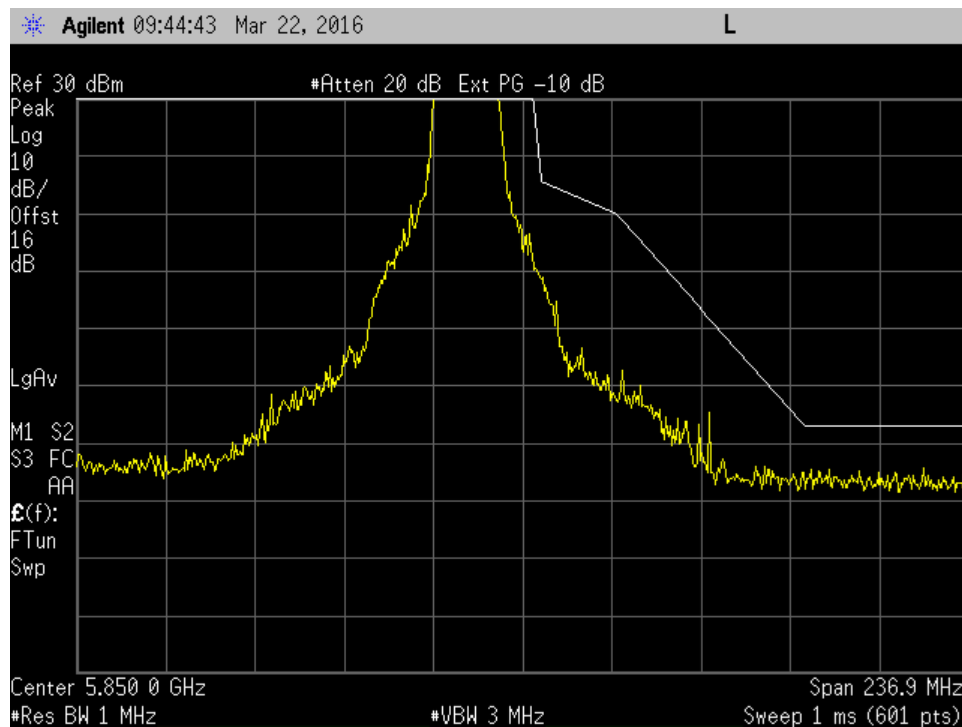




high ch port 2 10MHz BW

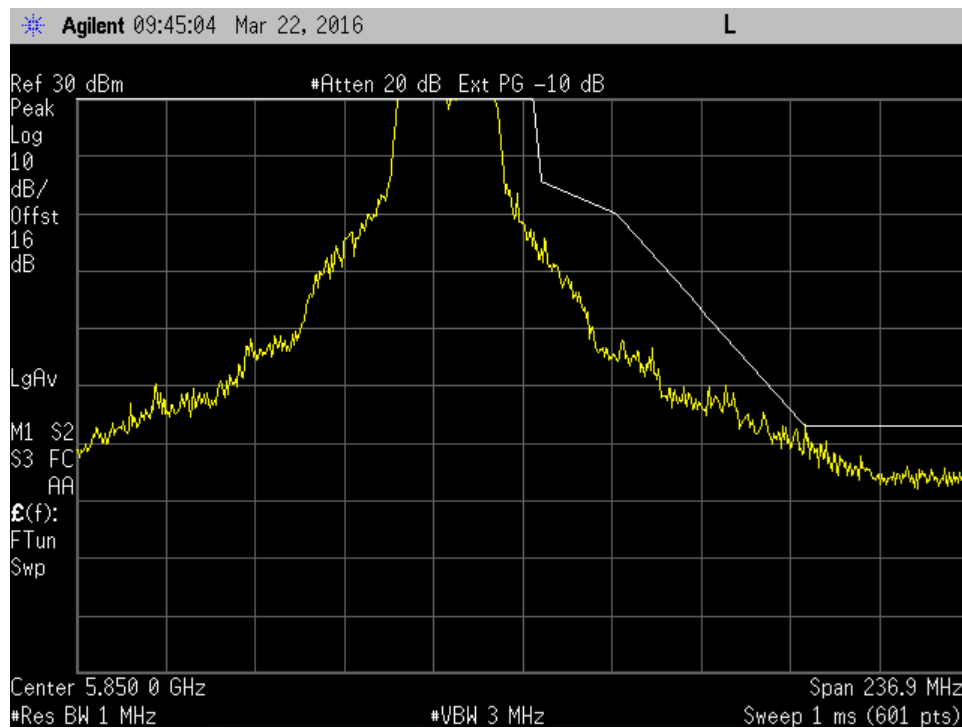


high ch port 2 20MHz BW

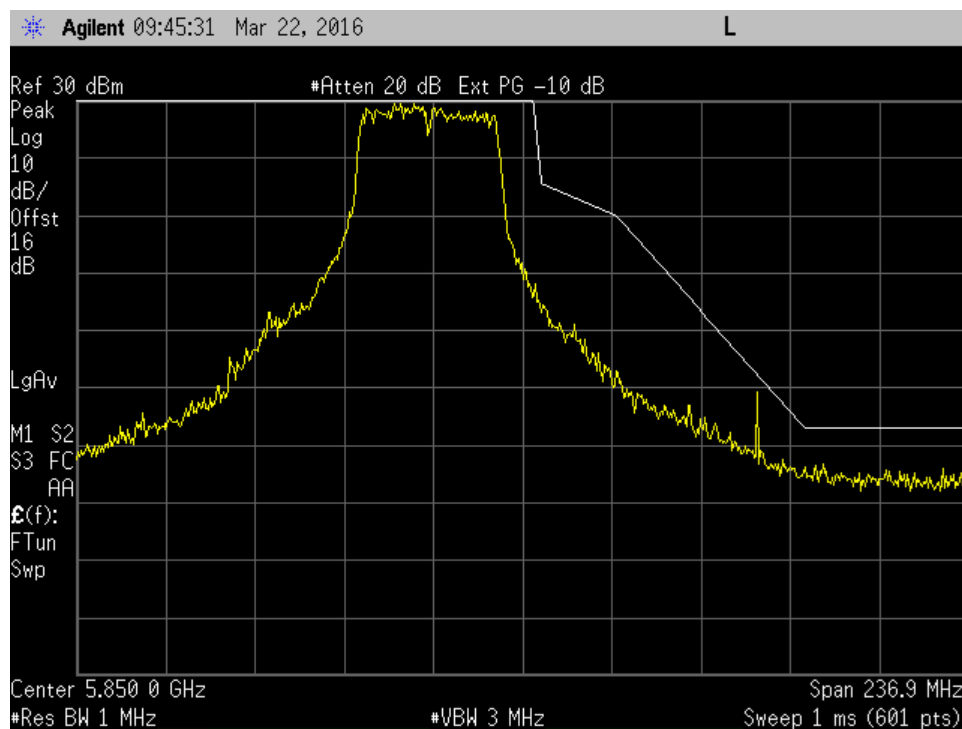




high ch port 2 30MHz BW

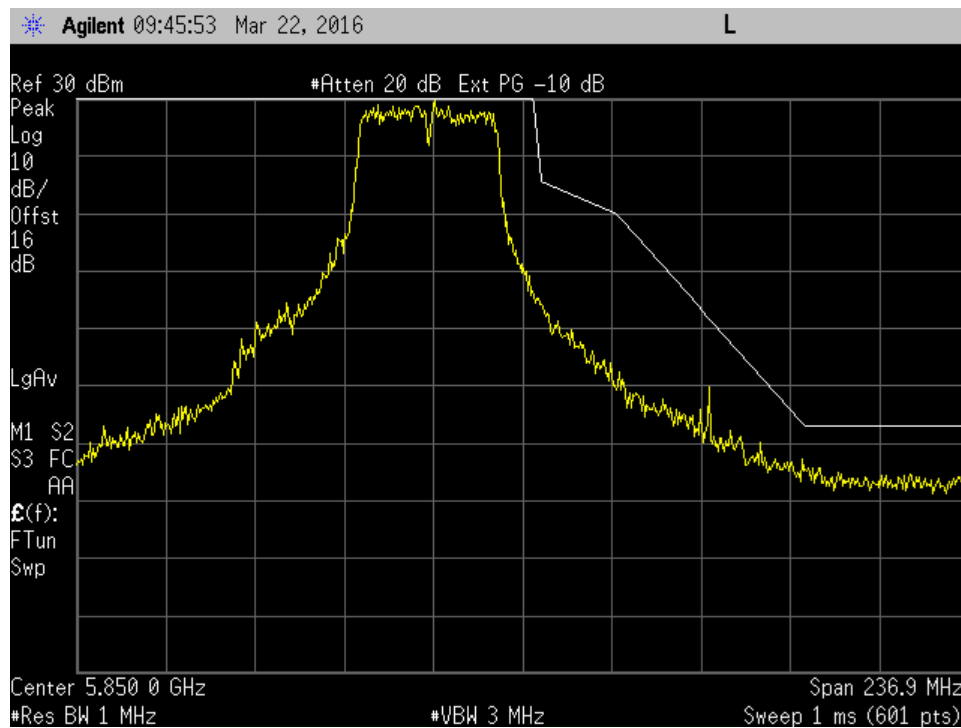


high ch port 2 40MHz BW

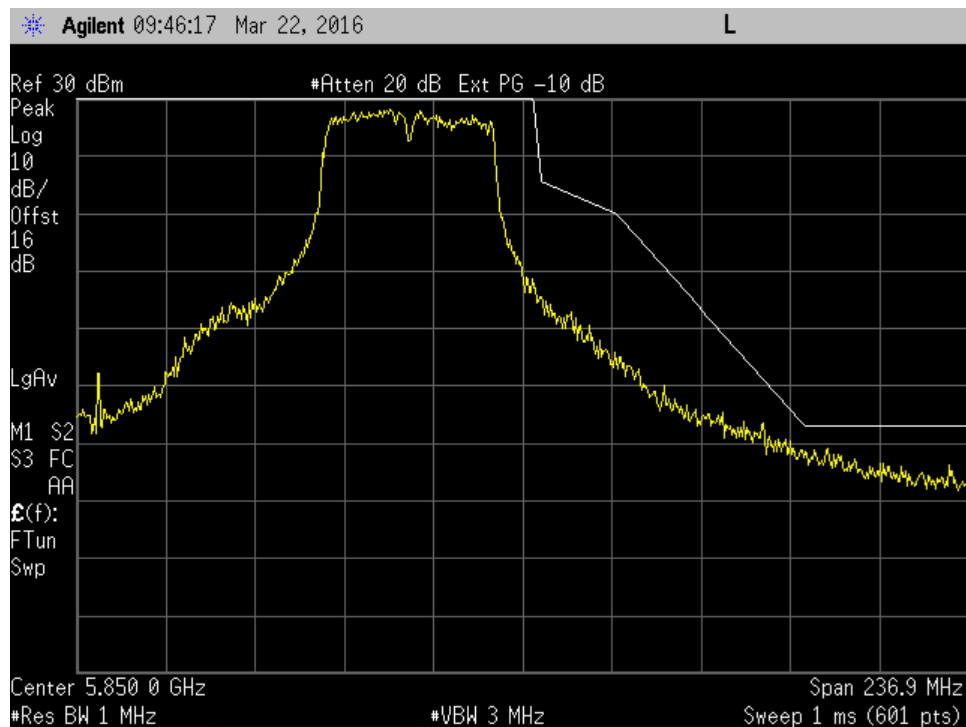




high ch port 2 50MHz BW

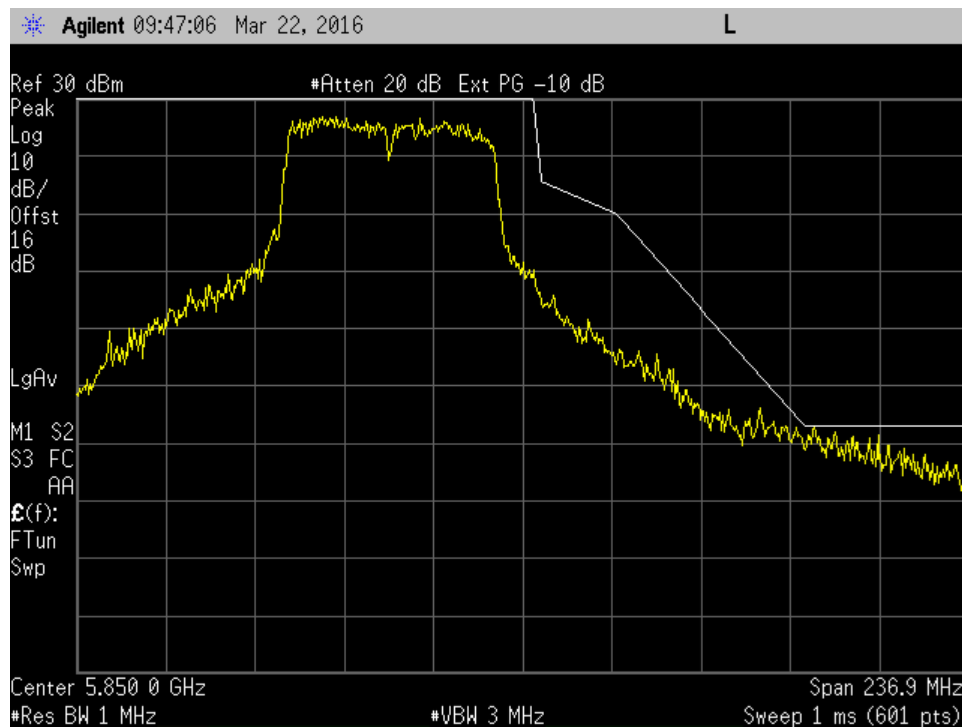


high ch port 2 60MHz BW

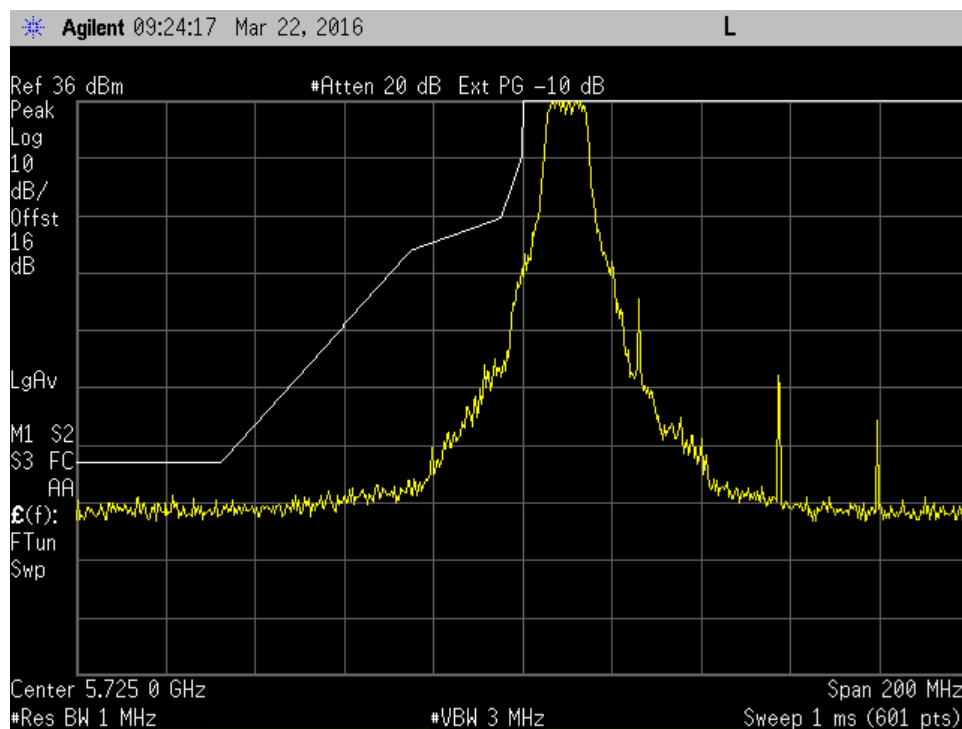




high ch port 2 80MHz BW

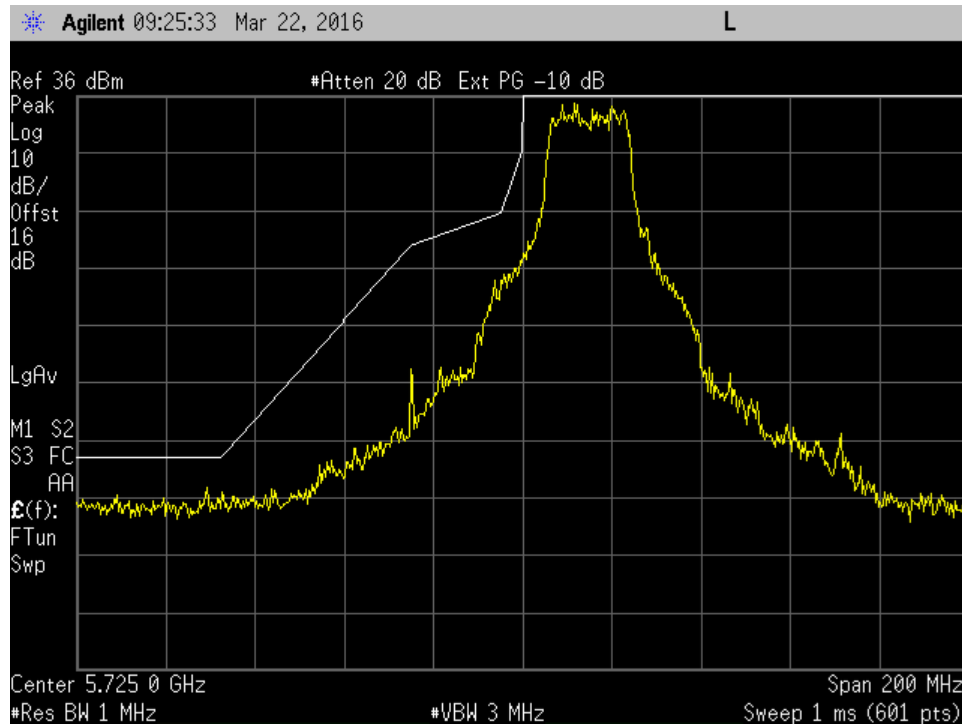


Low ch port 1 10MHz BW

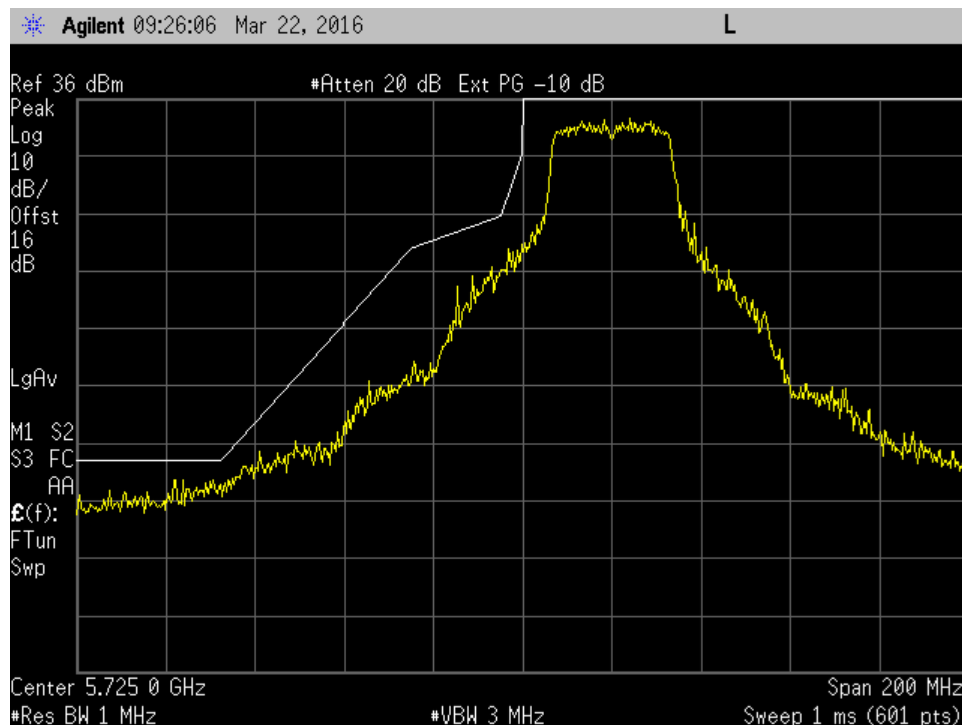




Low ch port 1 20MHz BW

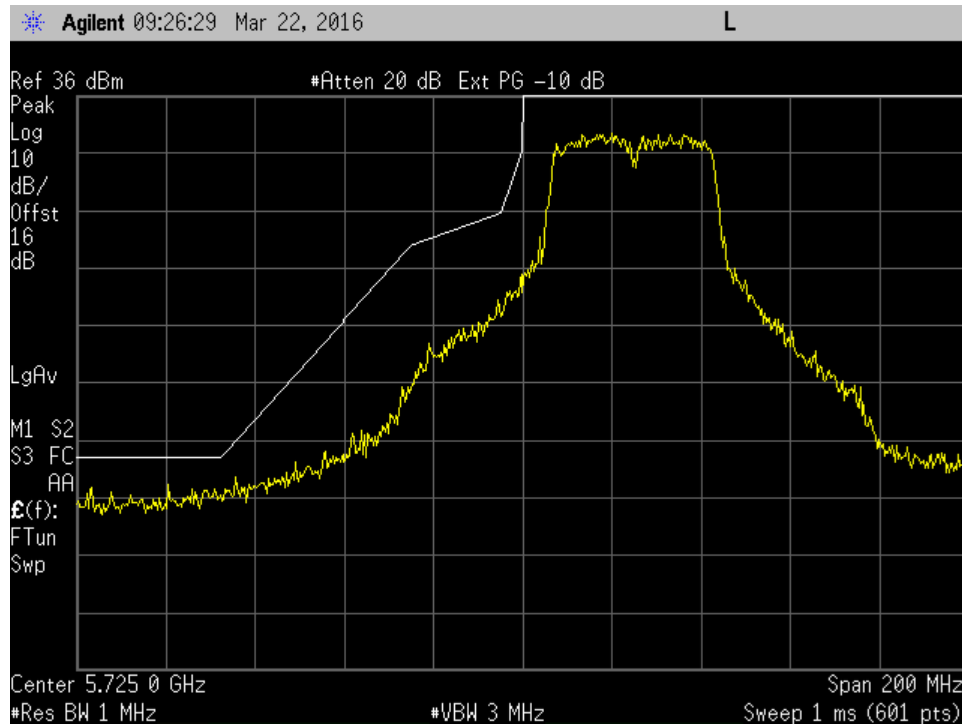


Low ch port 1 30MHz BW

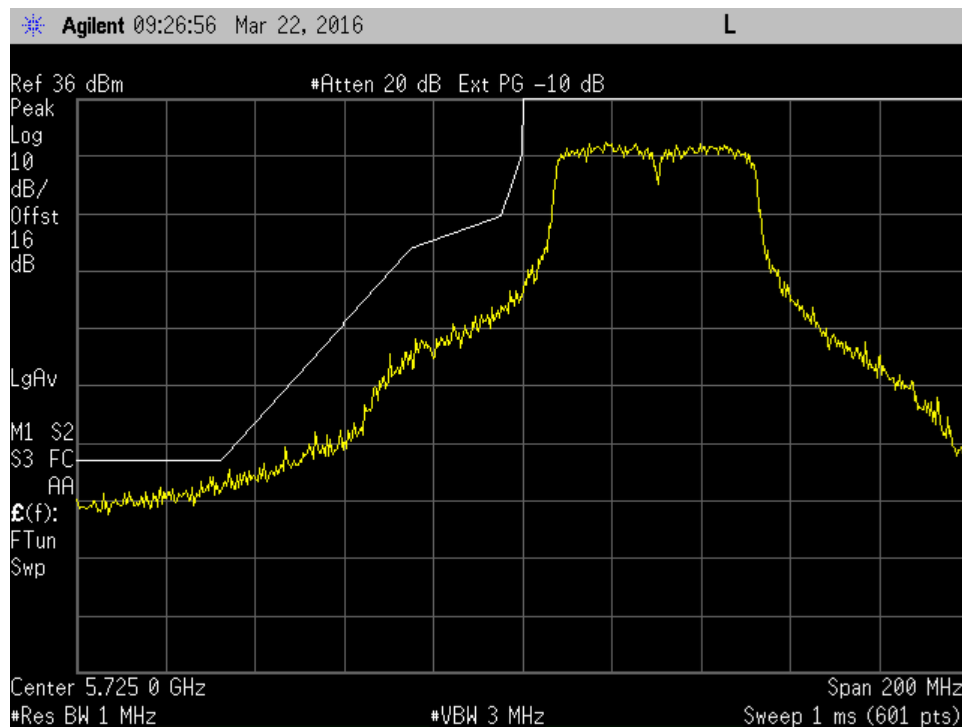




Low ch port 1 40MHz BW

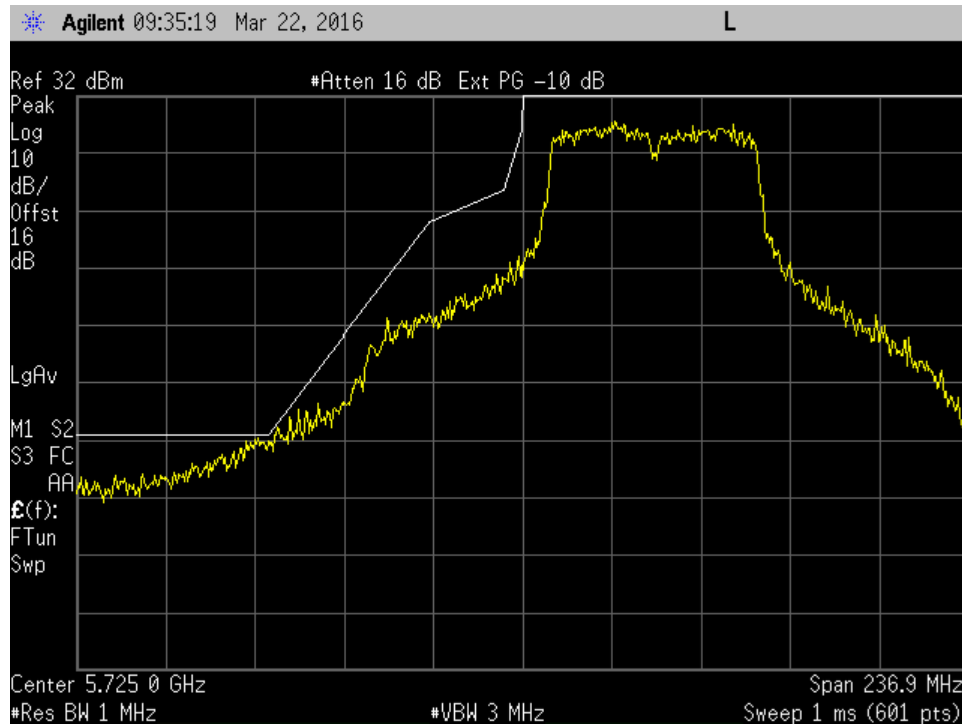


Low ch port 1 50MHz BW

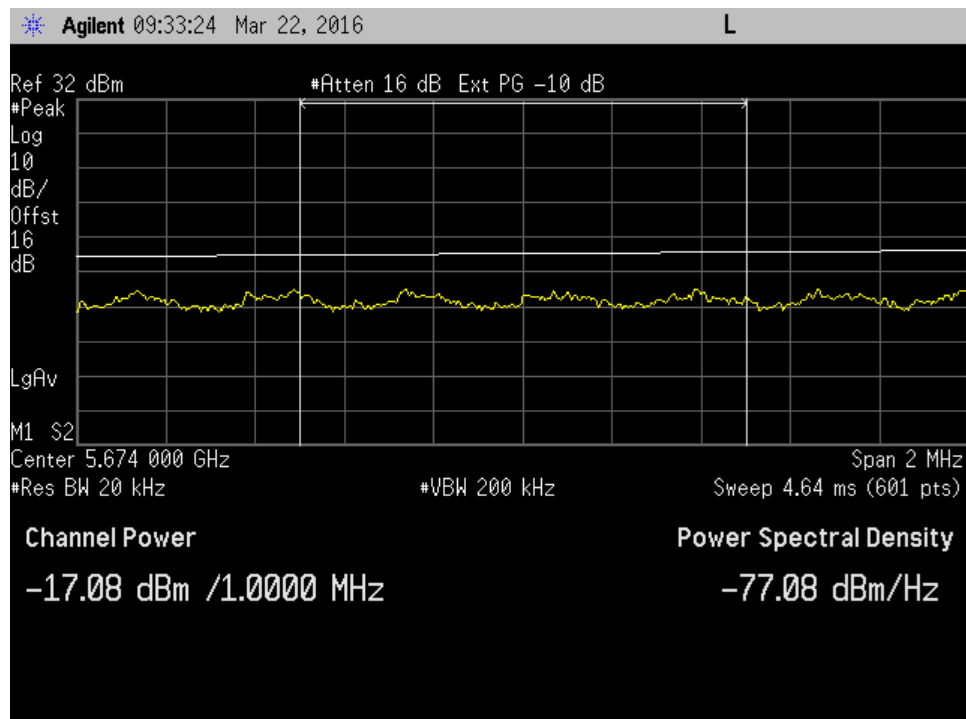




Low ch port 1 60MHz BW

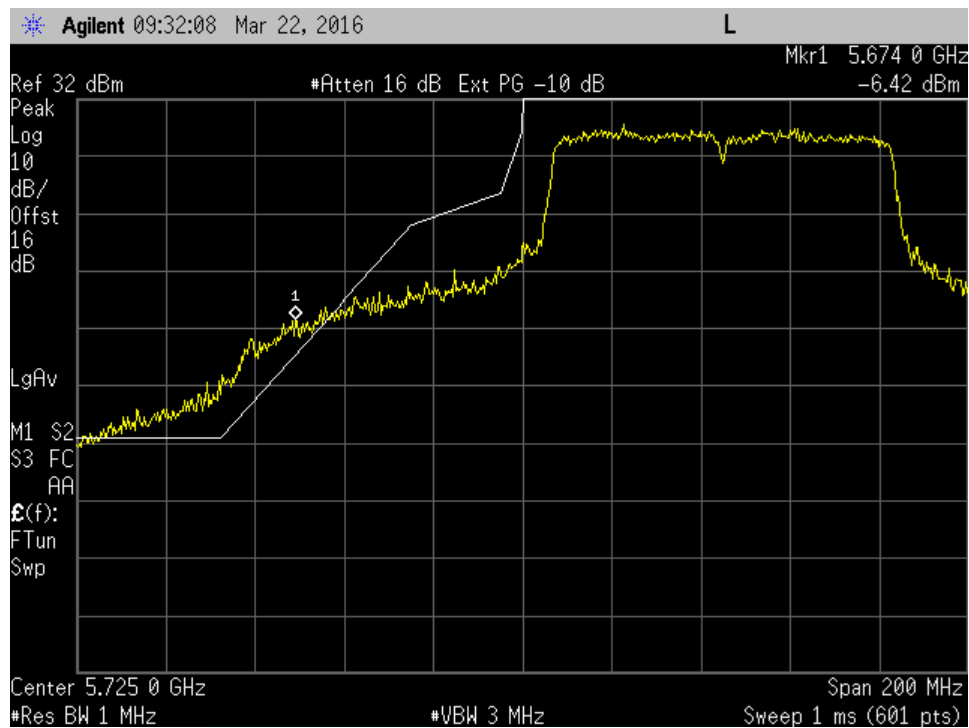


Low ch port 1 80MHz BW channel integration

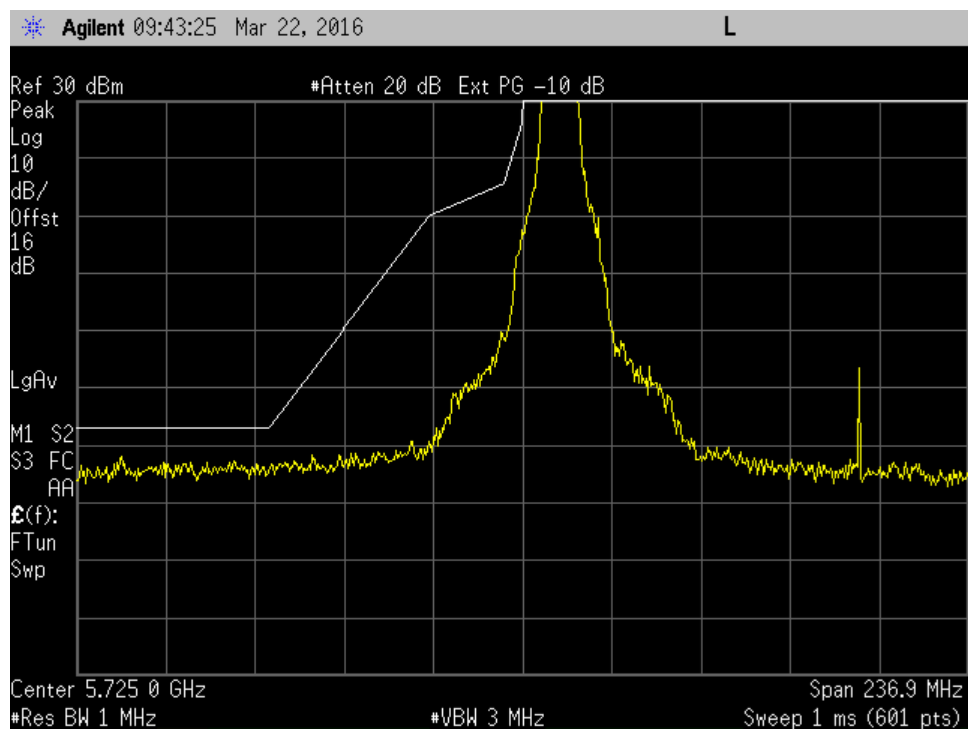




Low ch port 1 80MHz BW

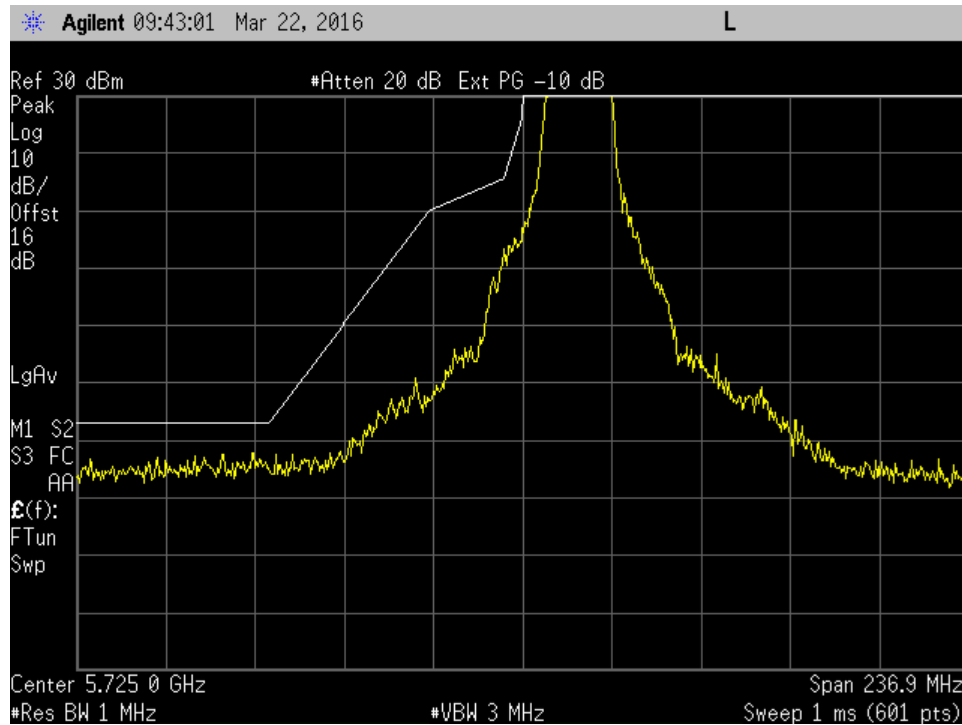


Low ch port 2 10MHz BW

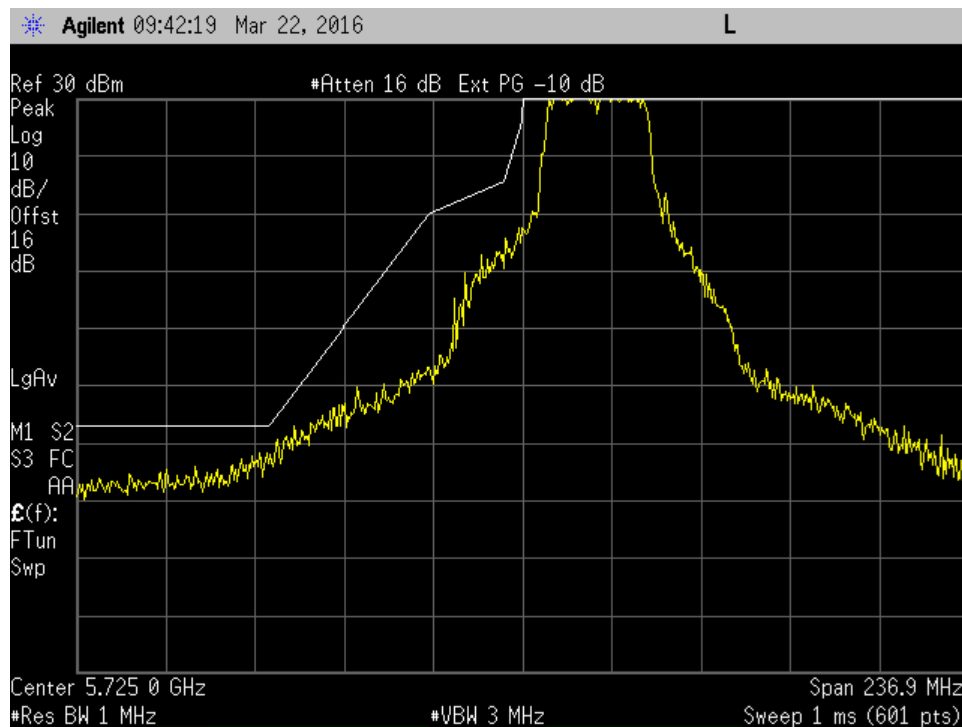




Low ch port 2 20MHz BW

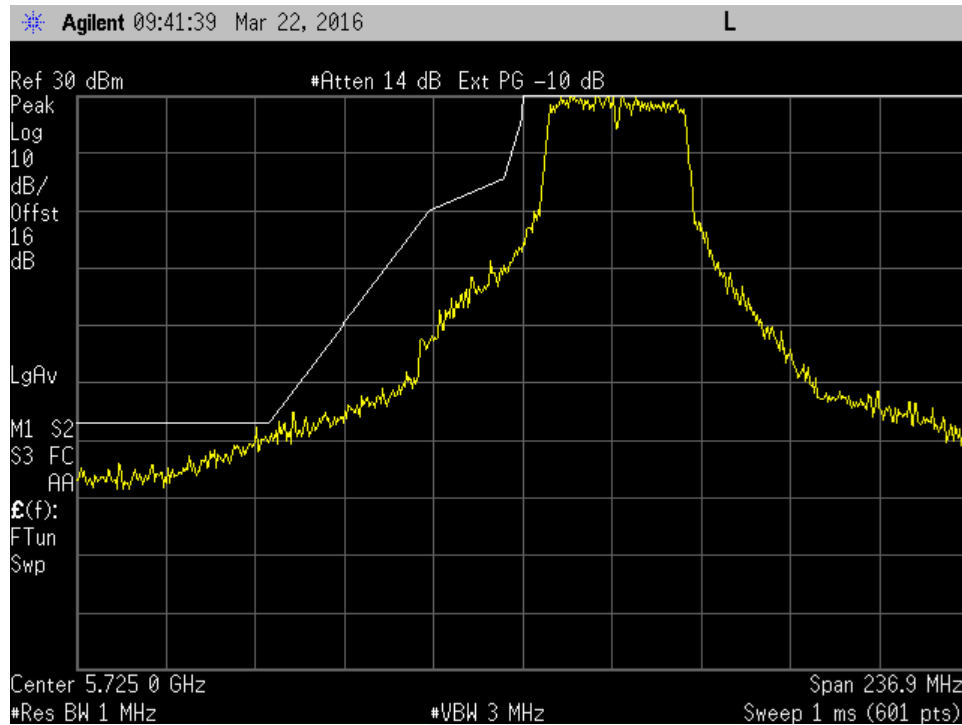


Low ch port 2 30MHz BW

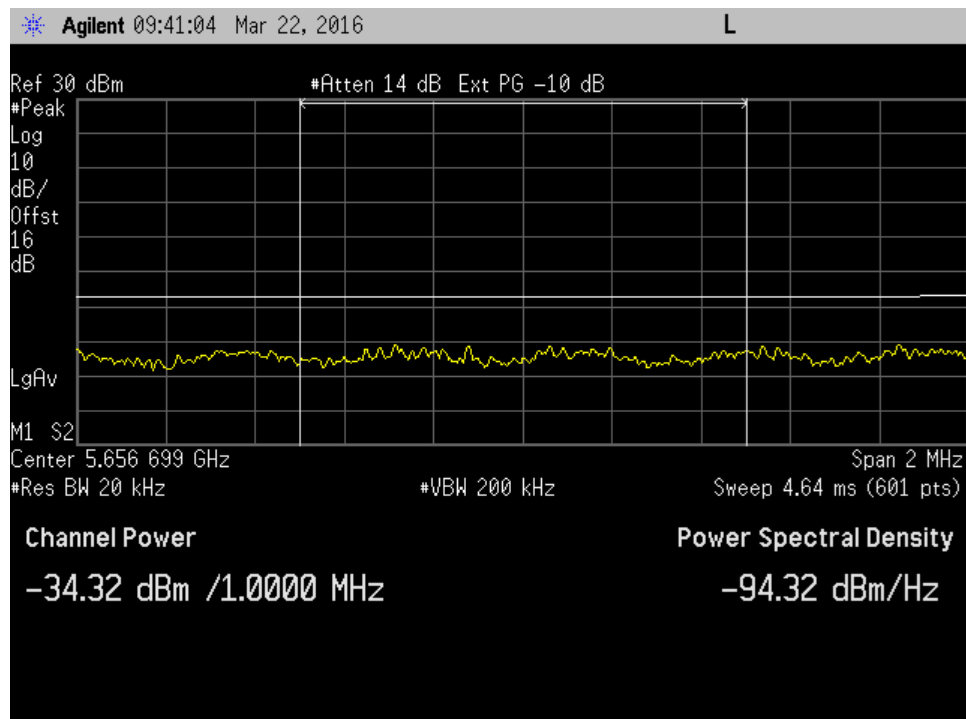




Low ch port 2 40MHz BW

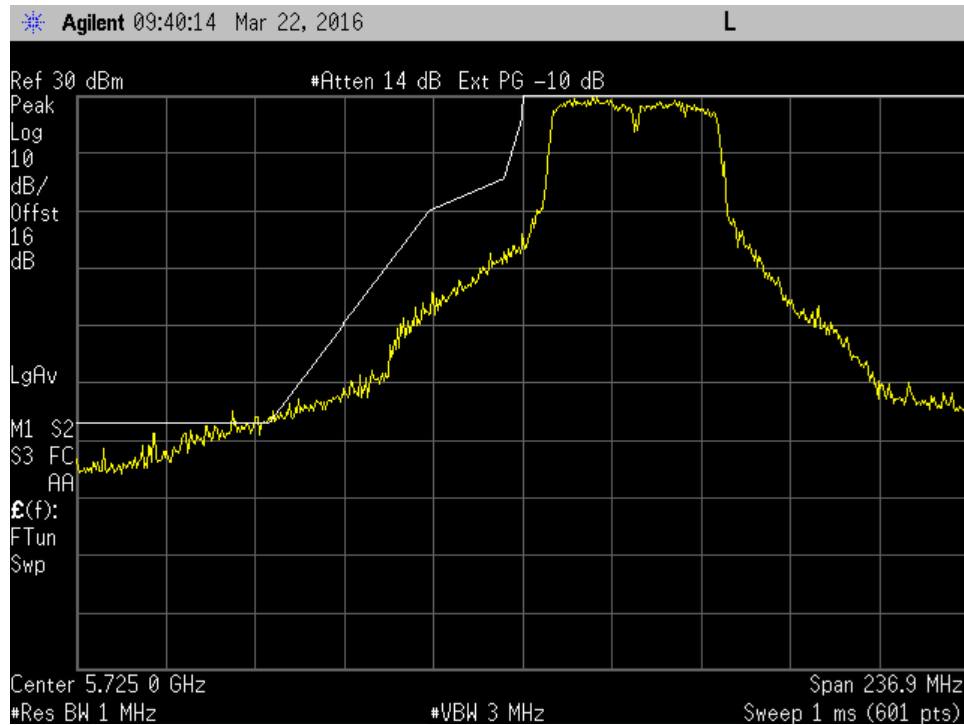


Low ch port 2 50MHz BW channel integration

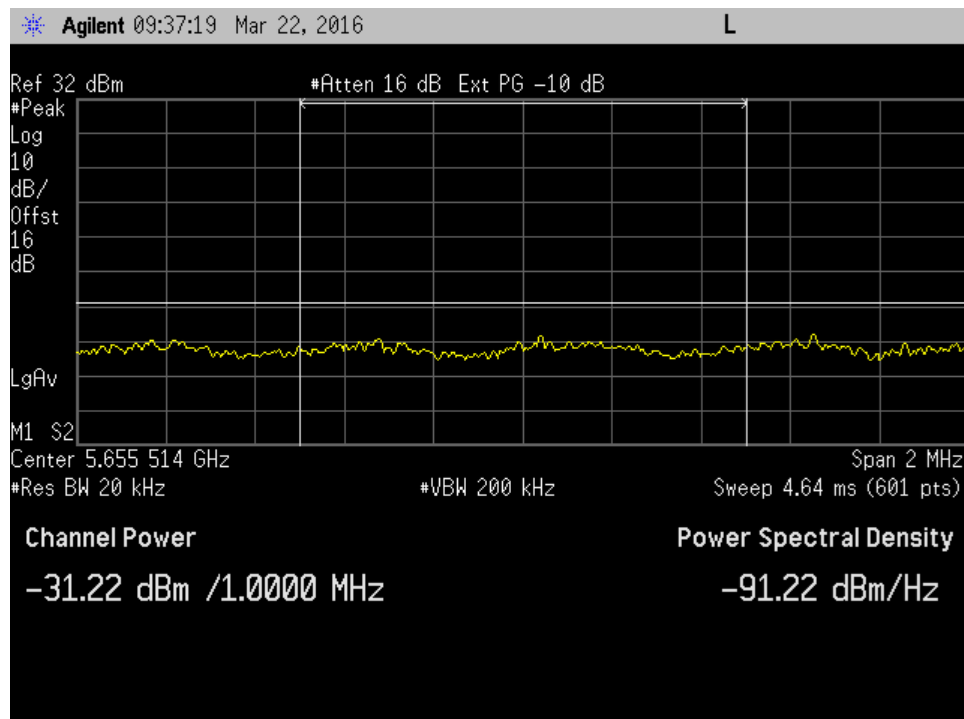




Low ch port 2 50MHz BW

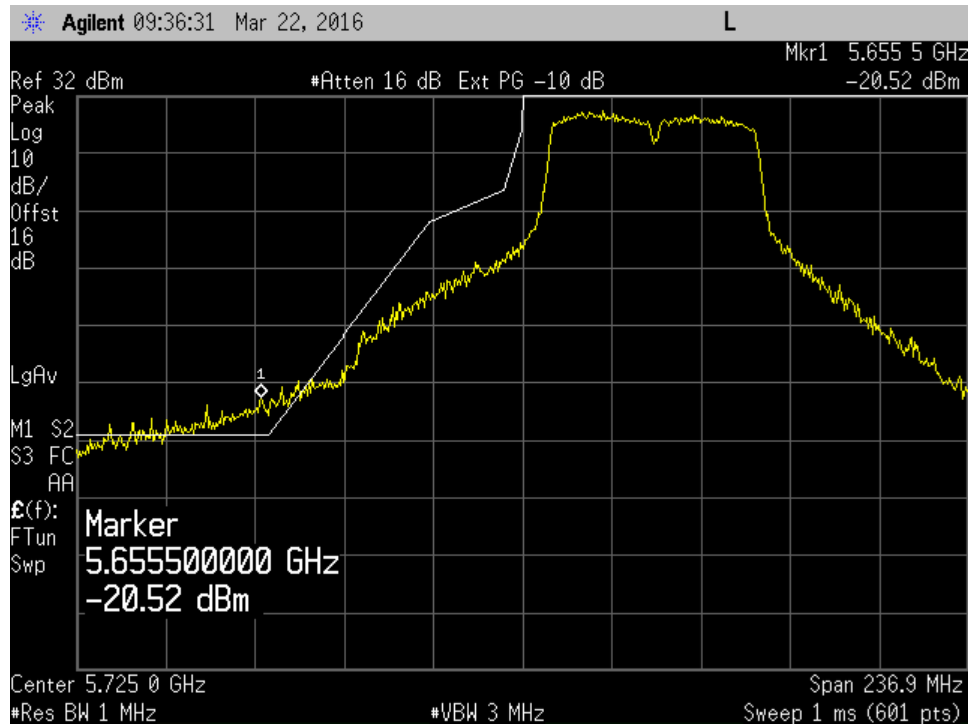


Low ch port 2 60MHz BW channel integration

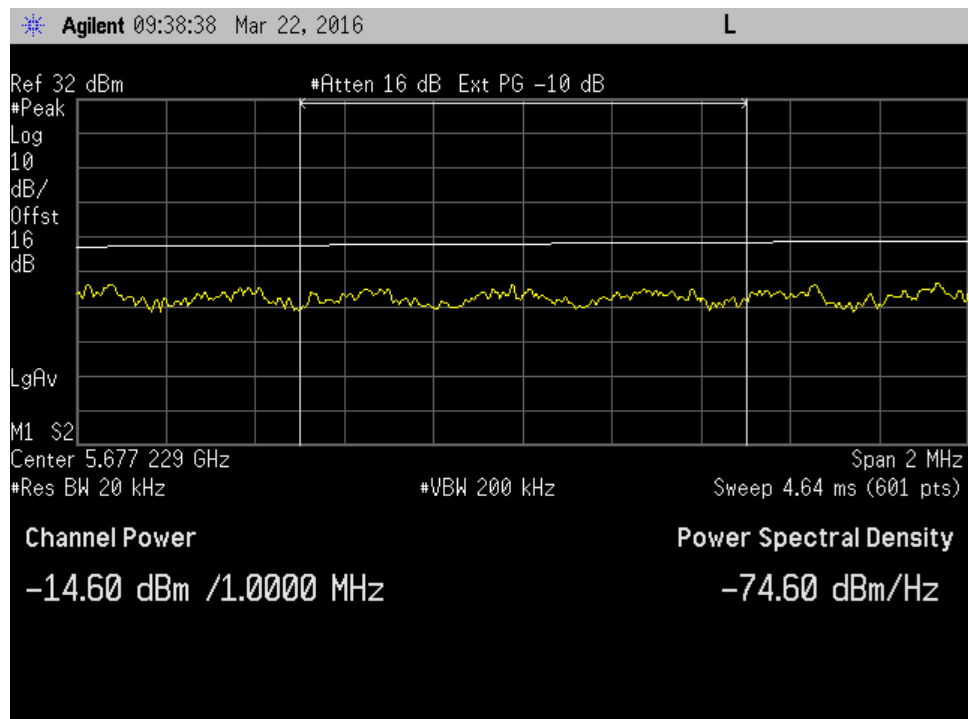




Low ch port 2 60MHz BW



Low ch port 2 80MHz BW channel integration





Low ch port 2 80MHz BW

