

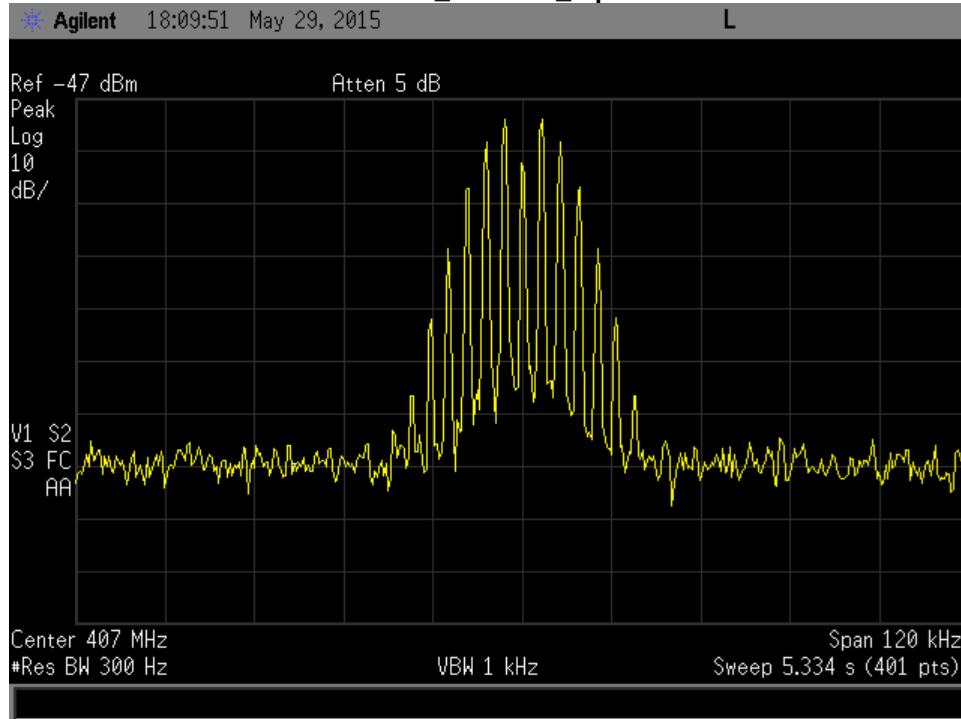


Annex C

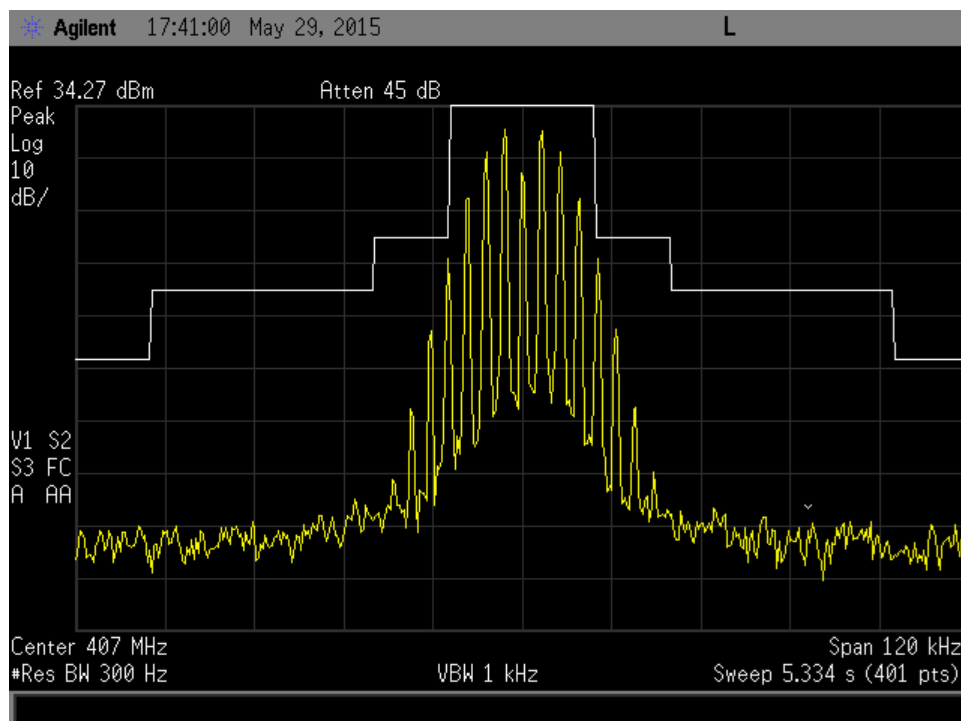
Emission Masks



Mask B_407 MHz_Input

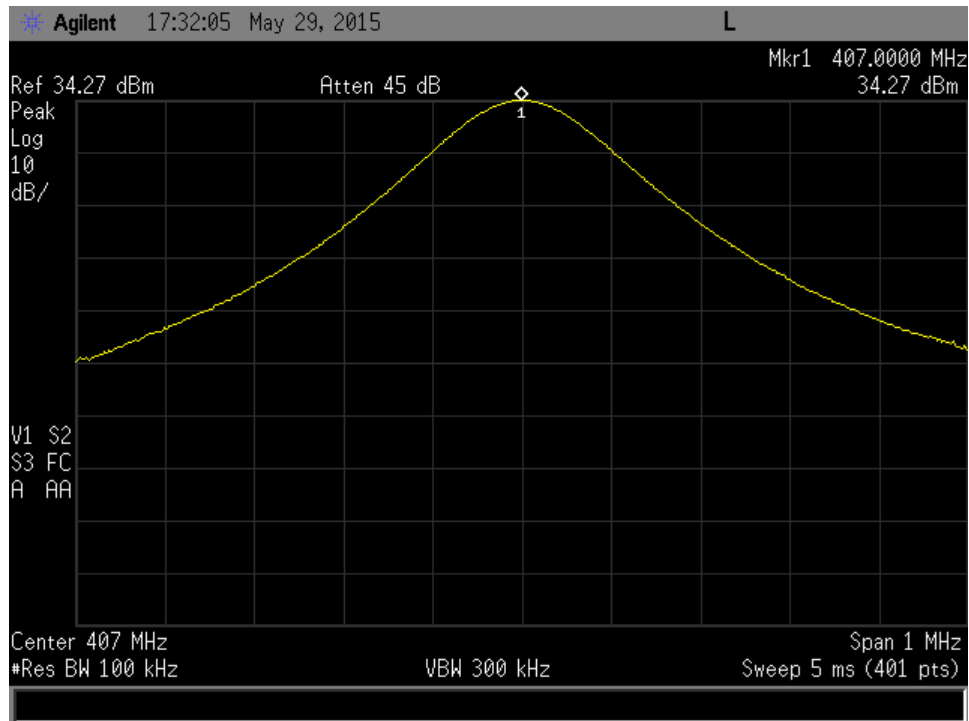


Mask B_407 MHz_Output

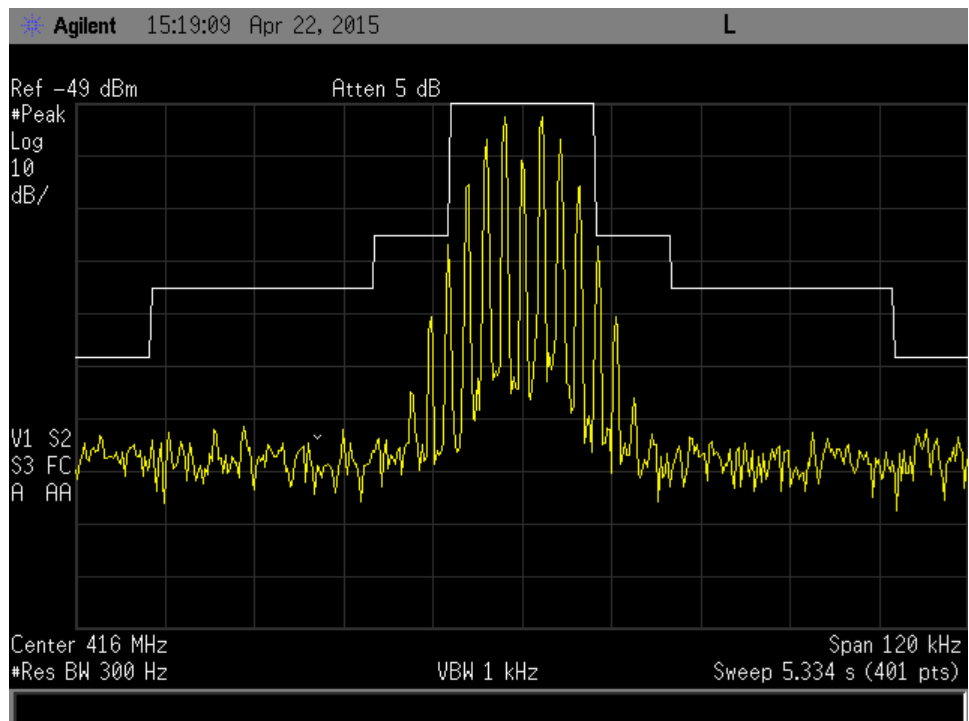




Mask B_407 MHz_Ref

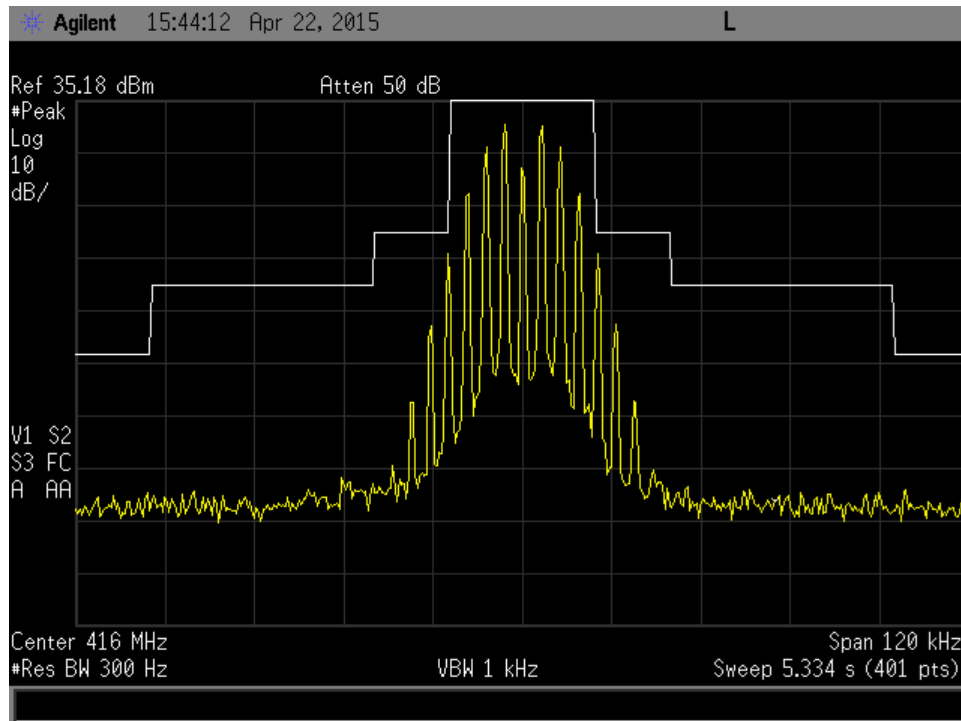


Mask B_416 MHz_Input

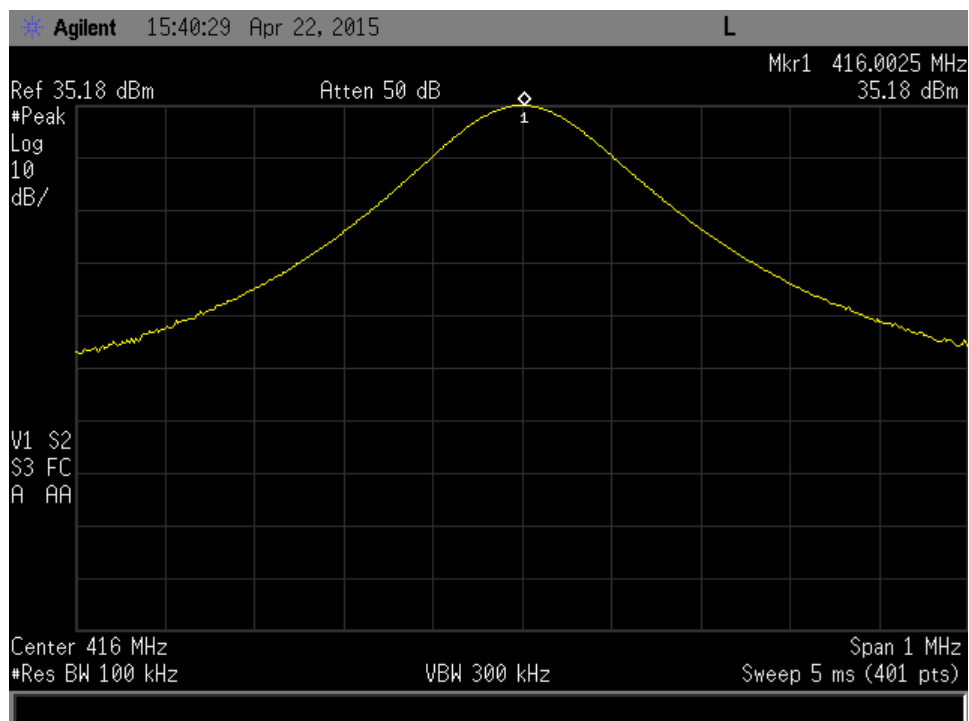




Mask B_416 MHz_Output

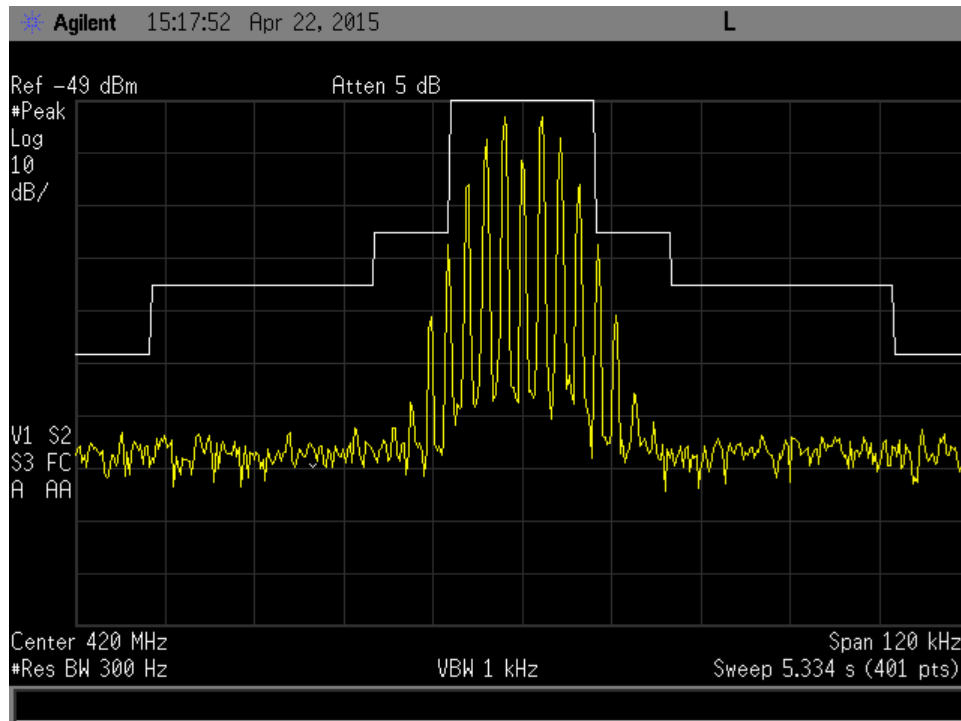


Mask B_416 MHz_Ref

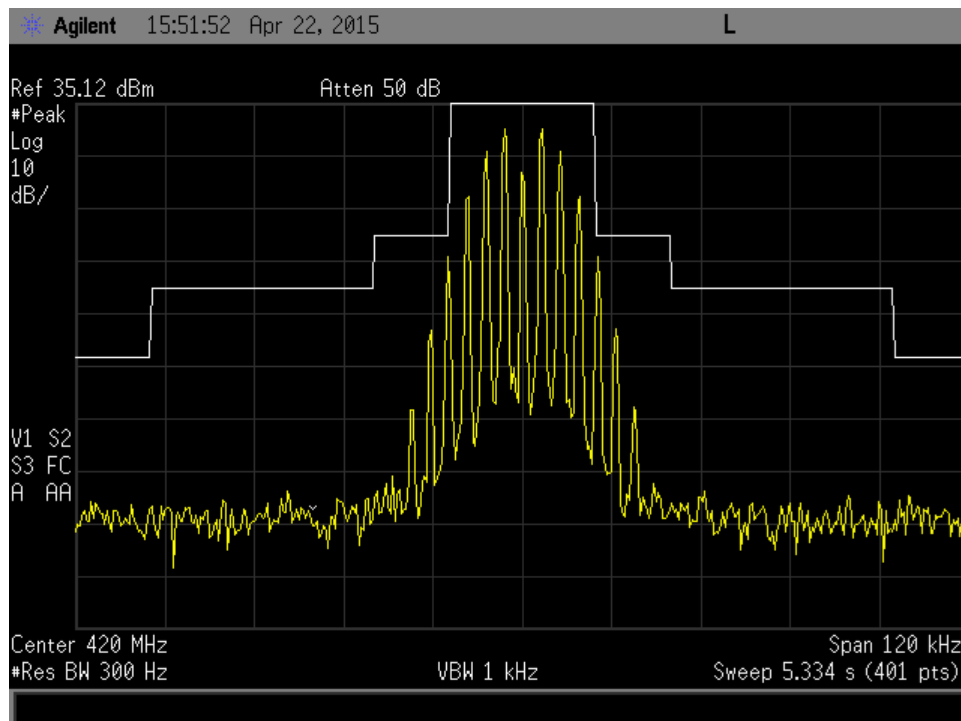




Mask B_420 MHz_Input

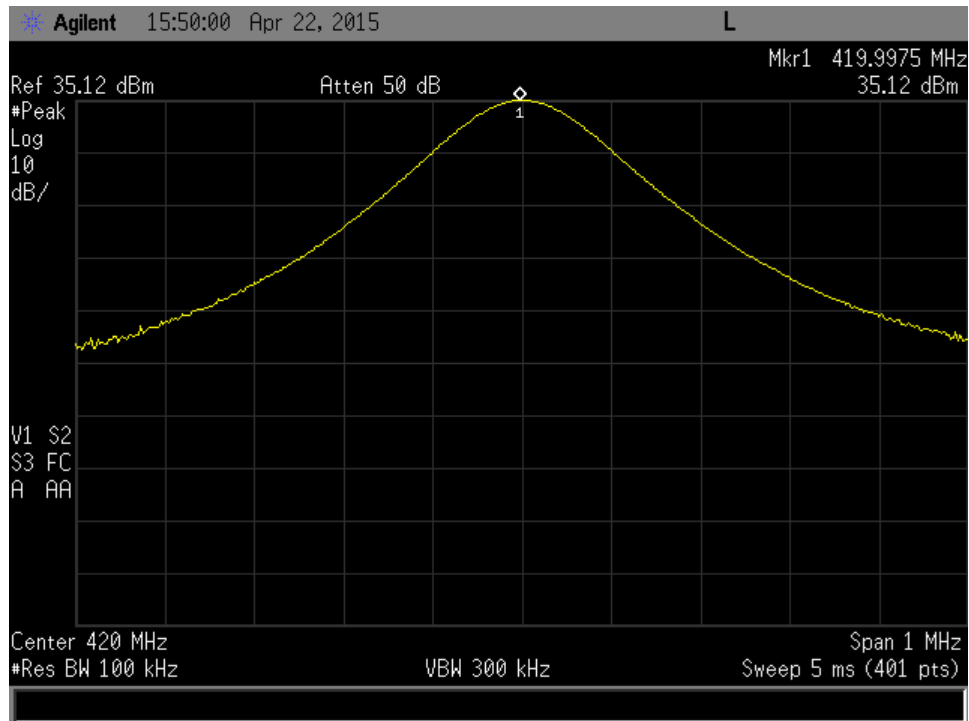


Mask B_420 MHz_Output

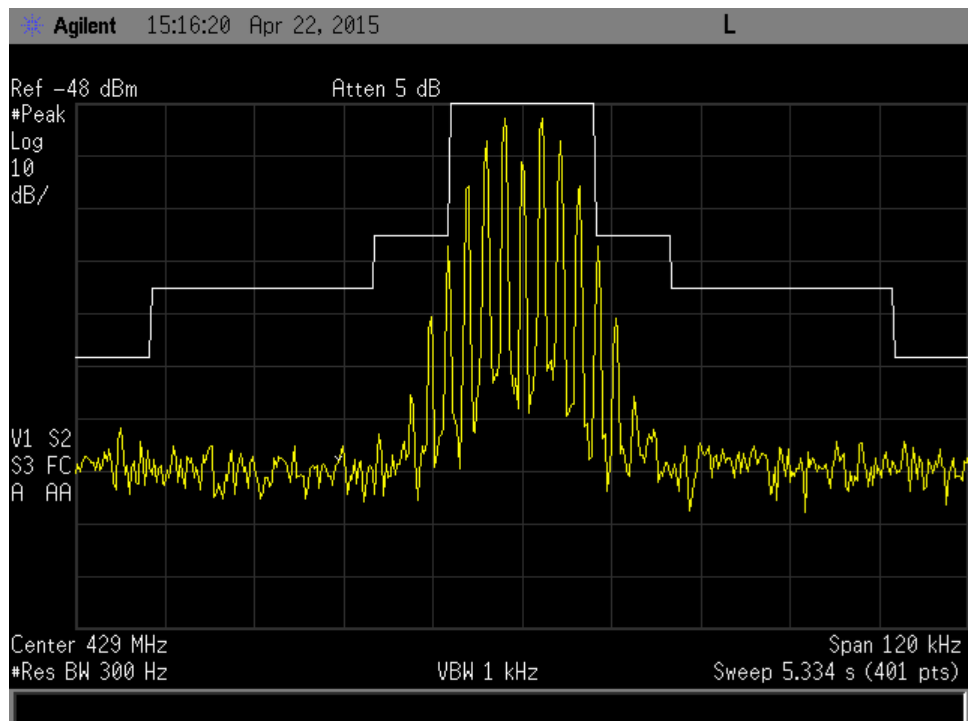




Mask B_420 MHz_Ref

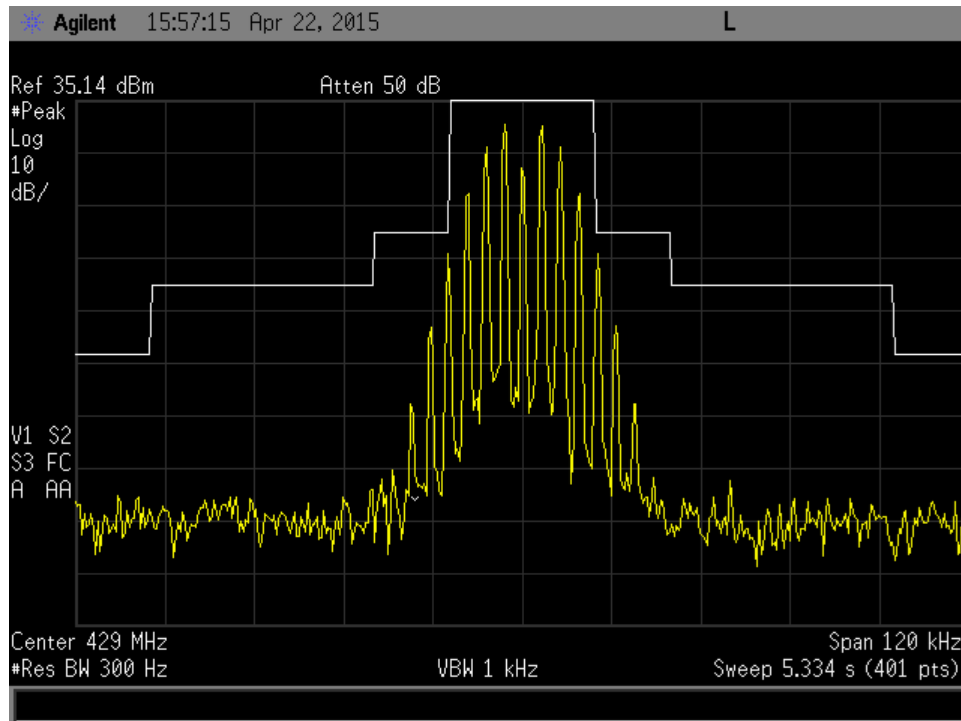


Mask B_429 MHz_Input

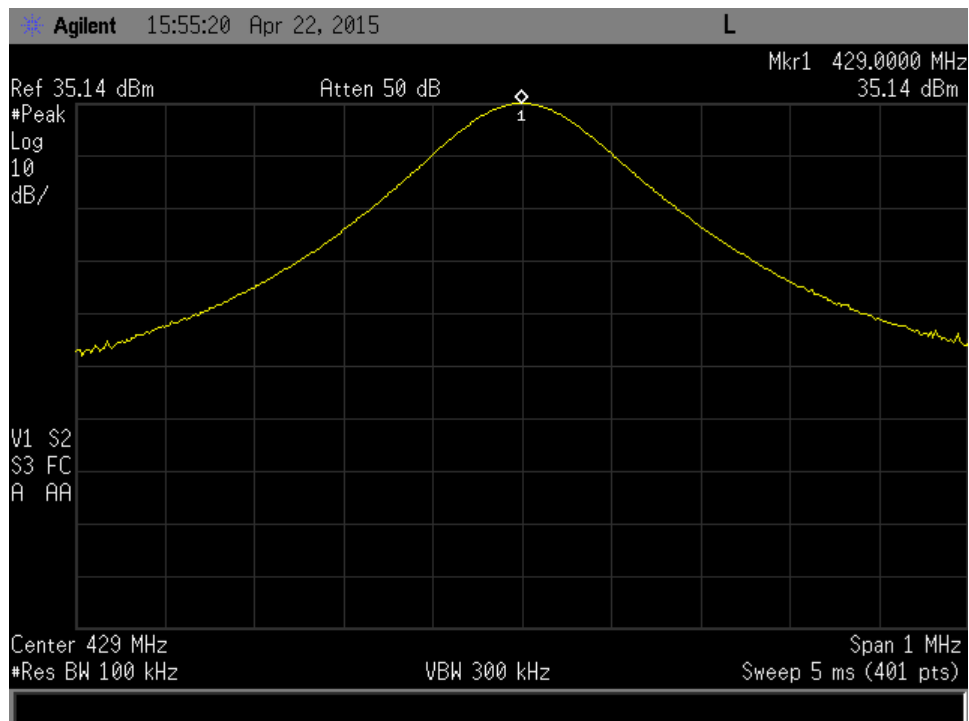




Mask B_429 MHz_Output

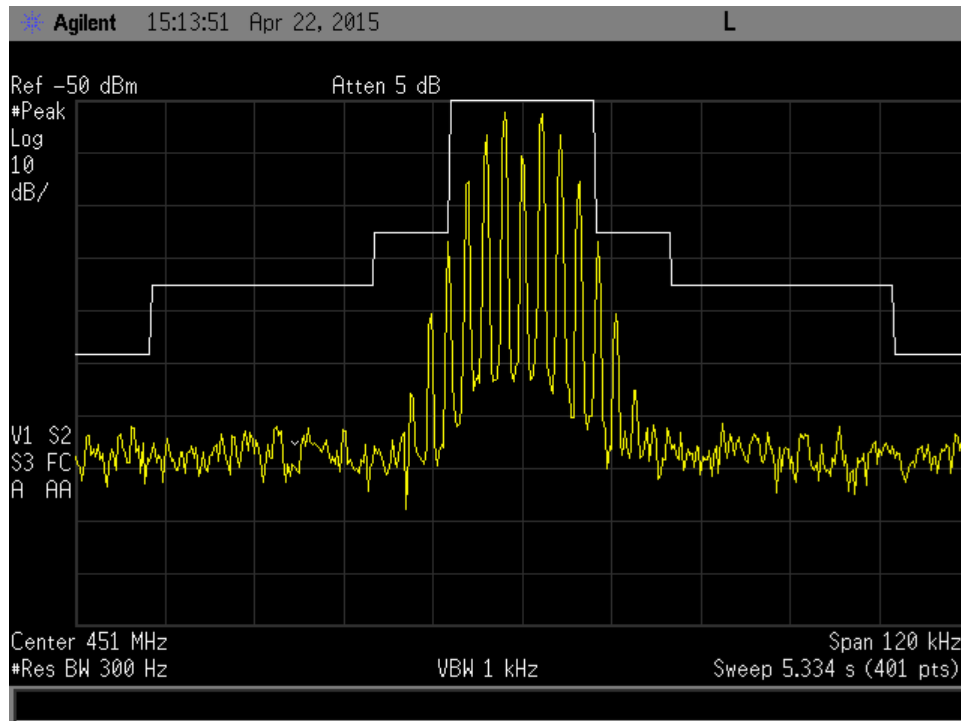


Mask B_429 MHz_Ref

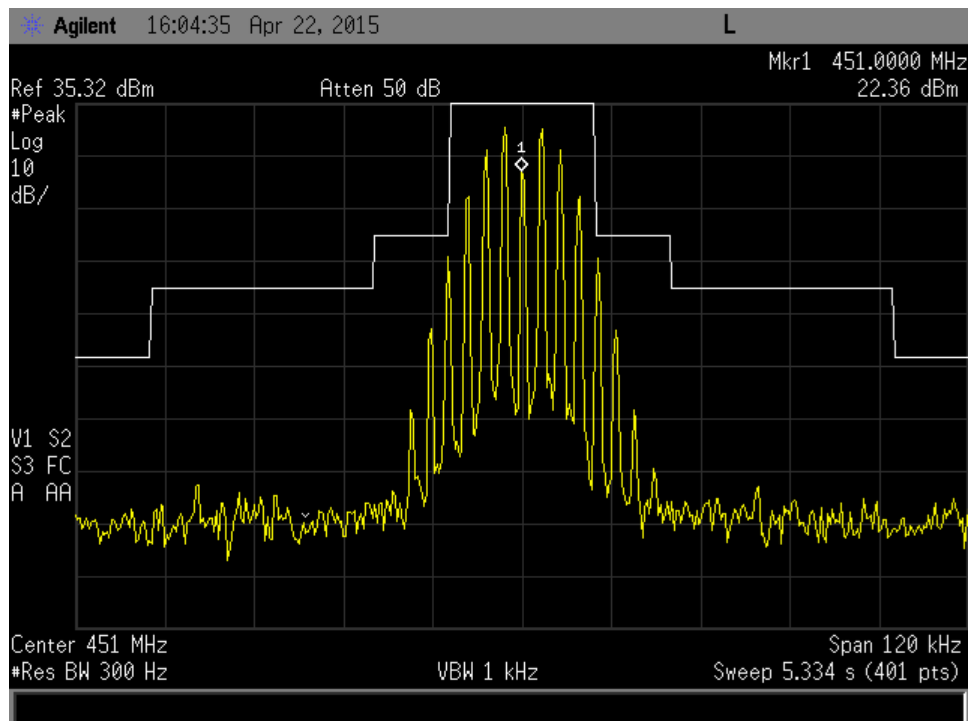




Mask B_451 MHz_Input

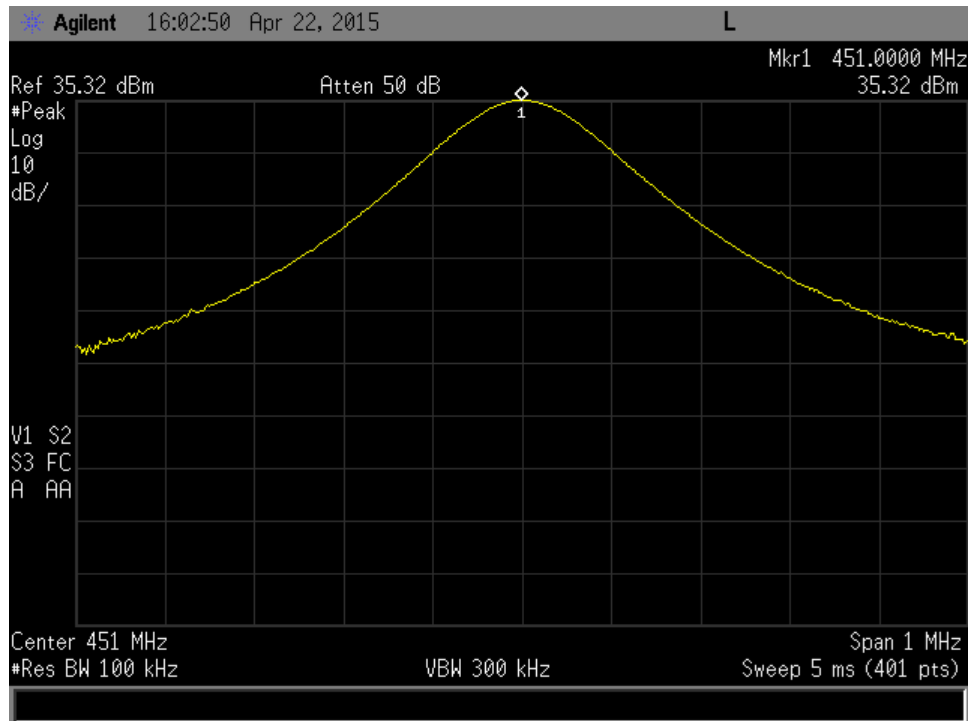


Mask B_451 MHz_Output

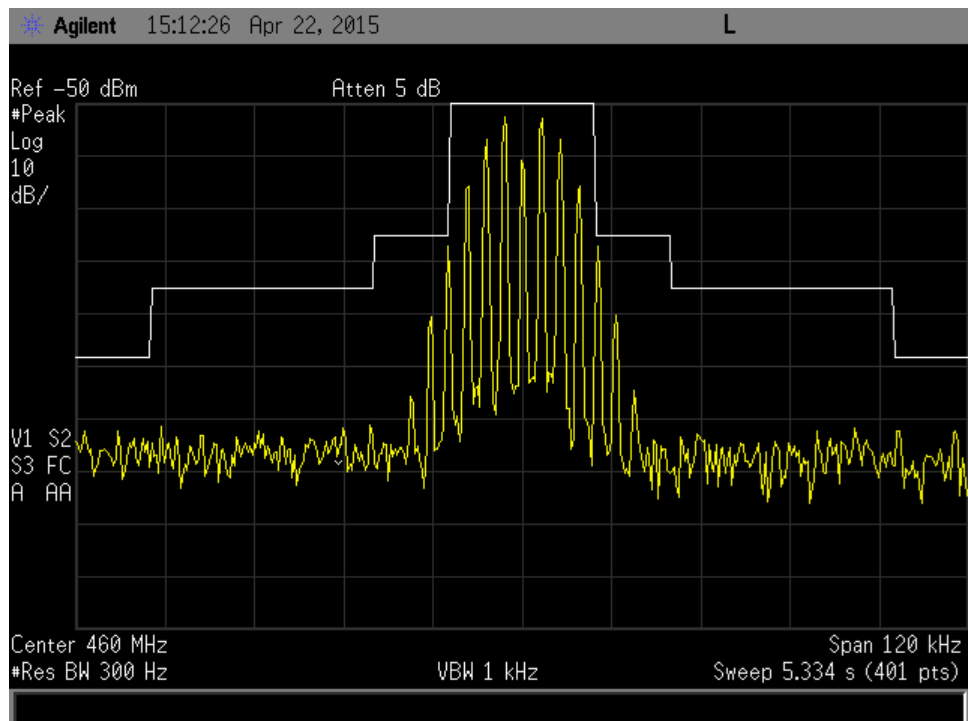




Mask B_451 MHz_Ref

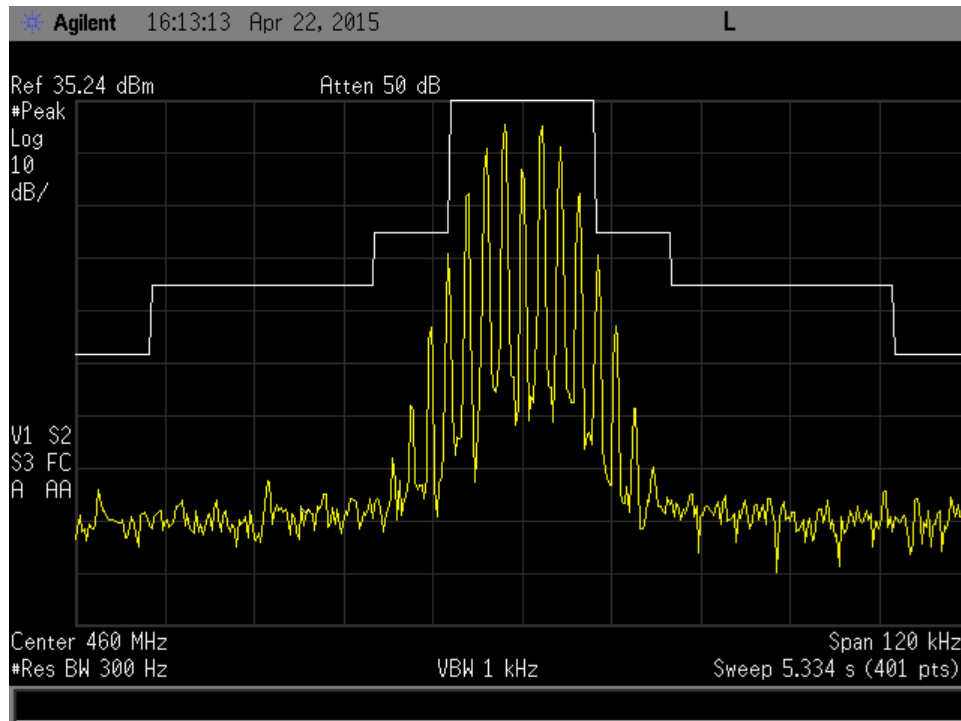


Mask B_460 MHz_Input

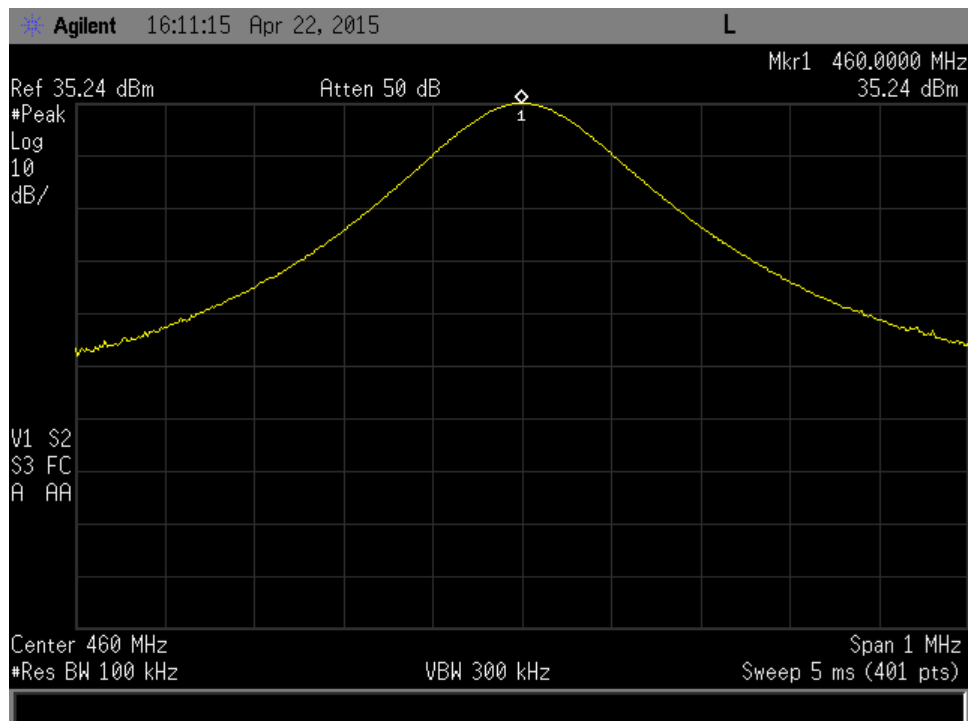




Mask B_460 MHz_Output

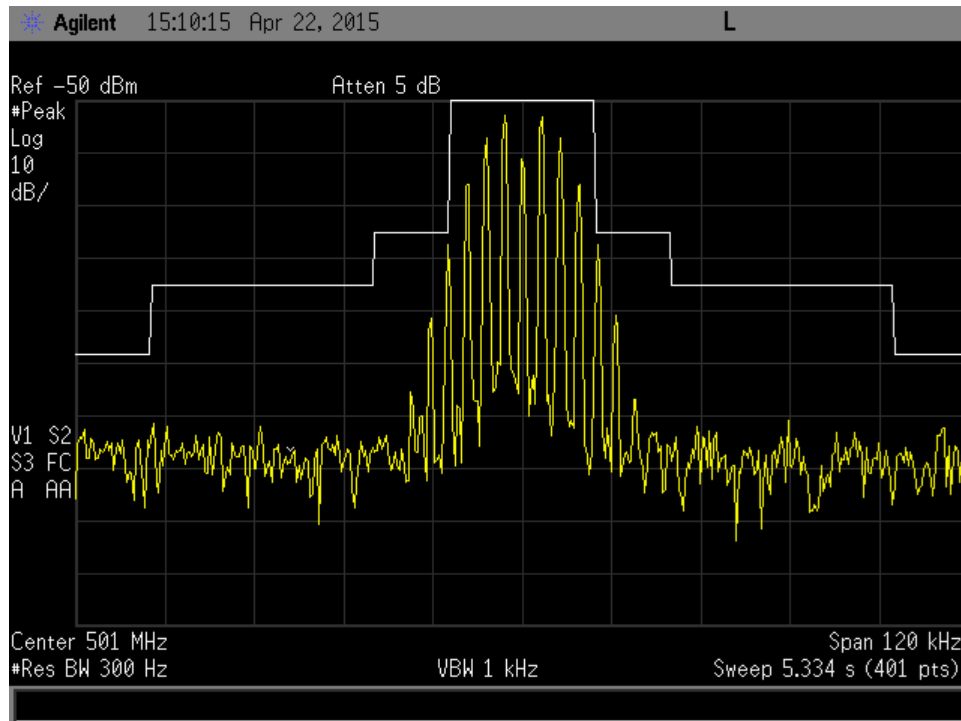


Mask B_460 MHz_Ref

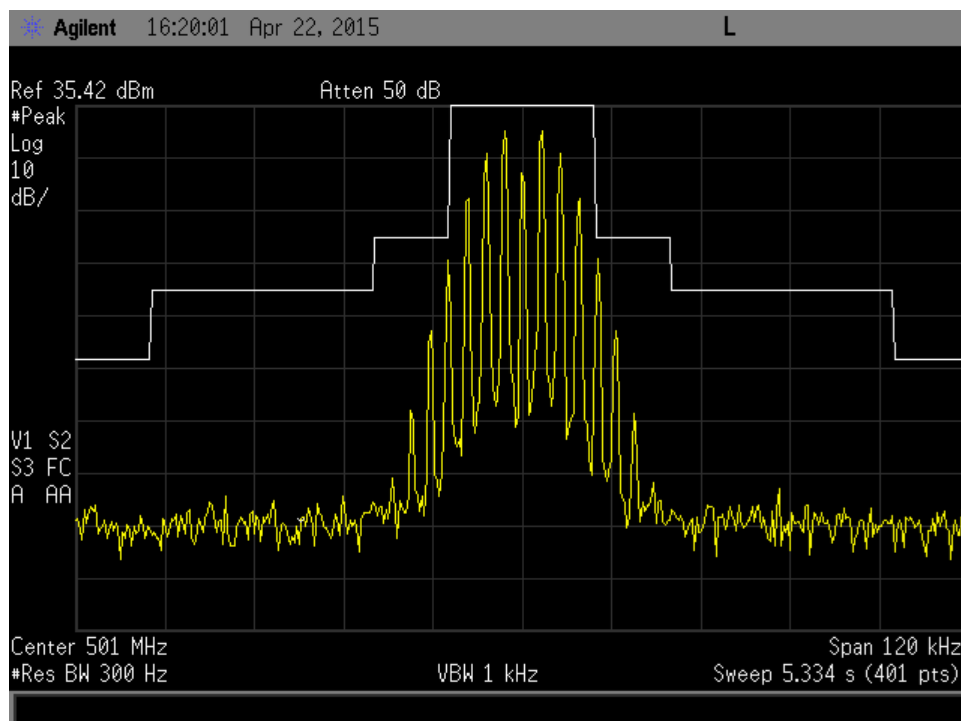




Mask B_501 MHz_Input

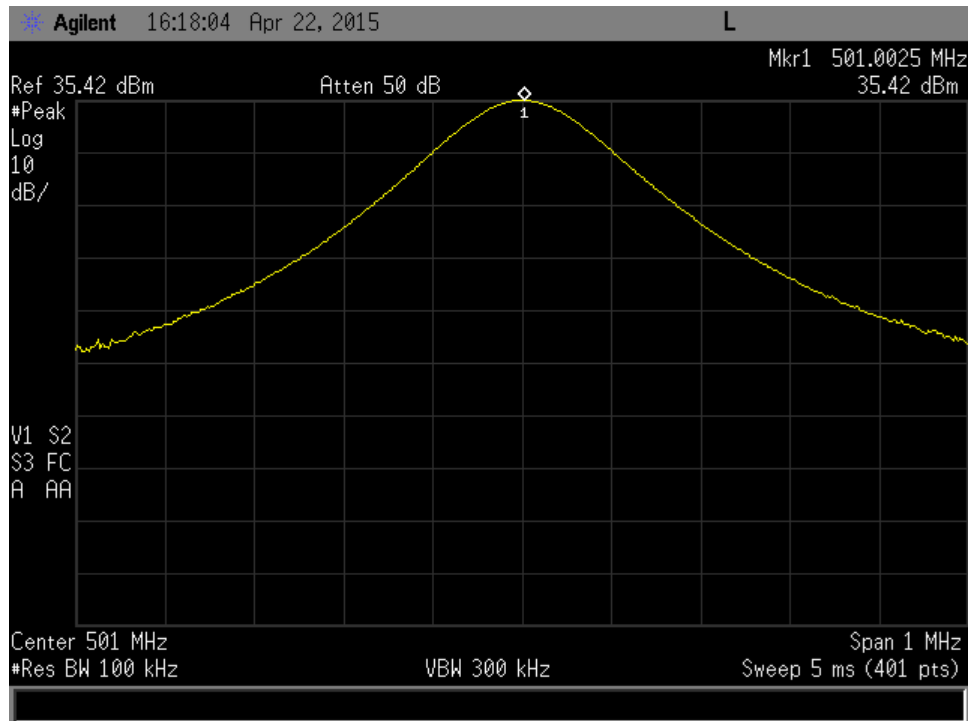


Mask B_501 MHz_Output

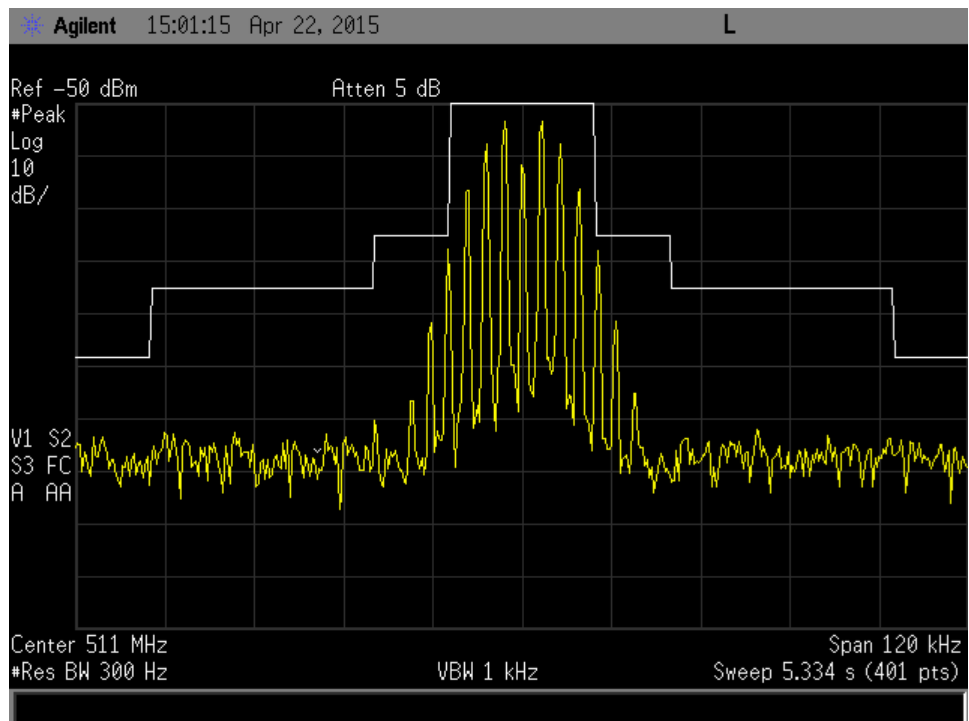




Mask B_501 MHz_Ref

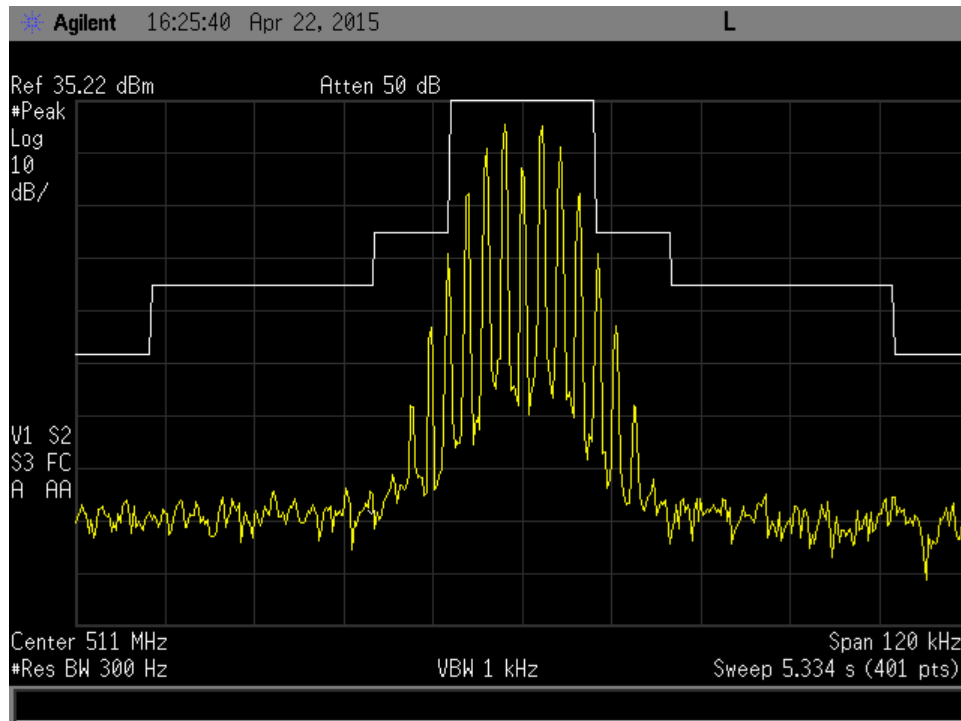


Mask B_511 MHz_Input

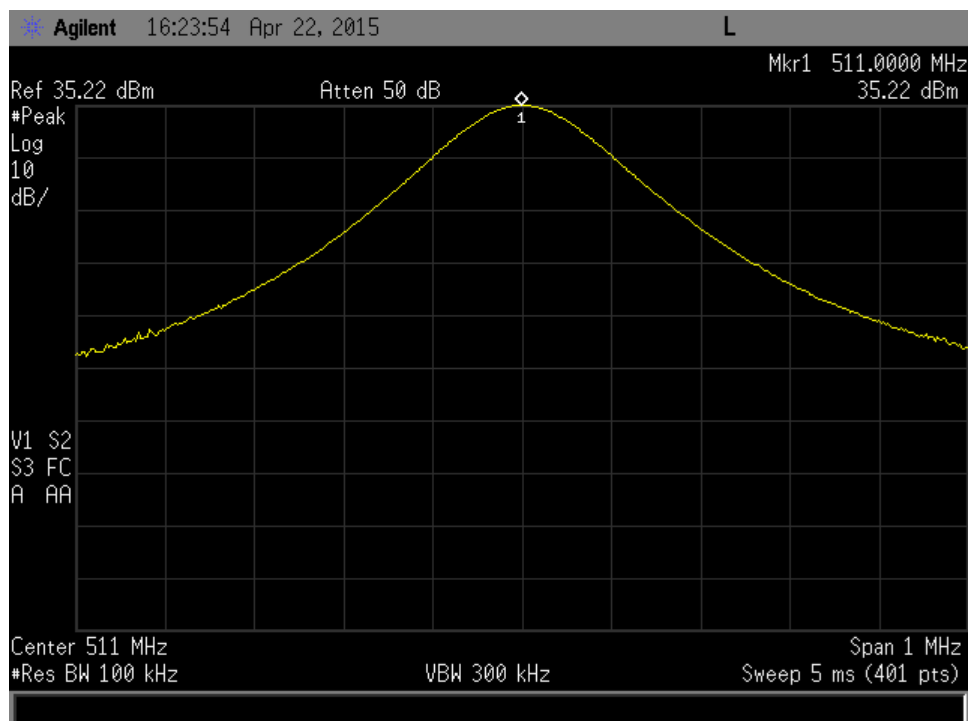




Mask B_511 MHz_Output

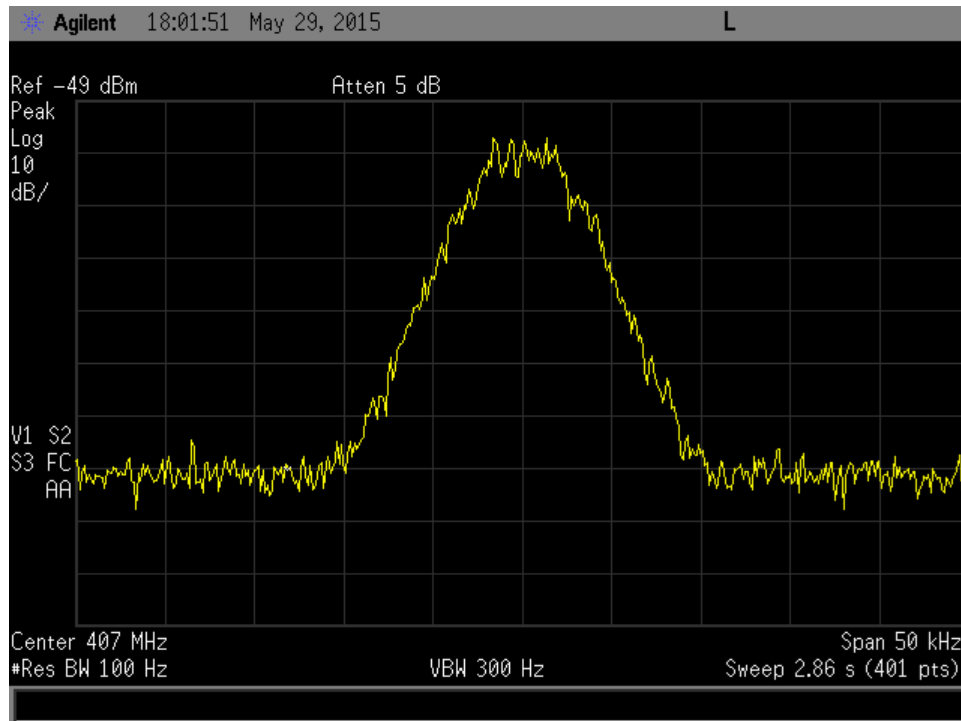


Mask B_511 MHz_Ref

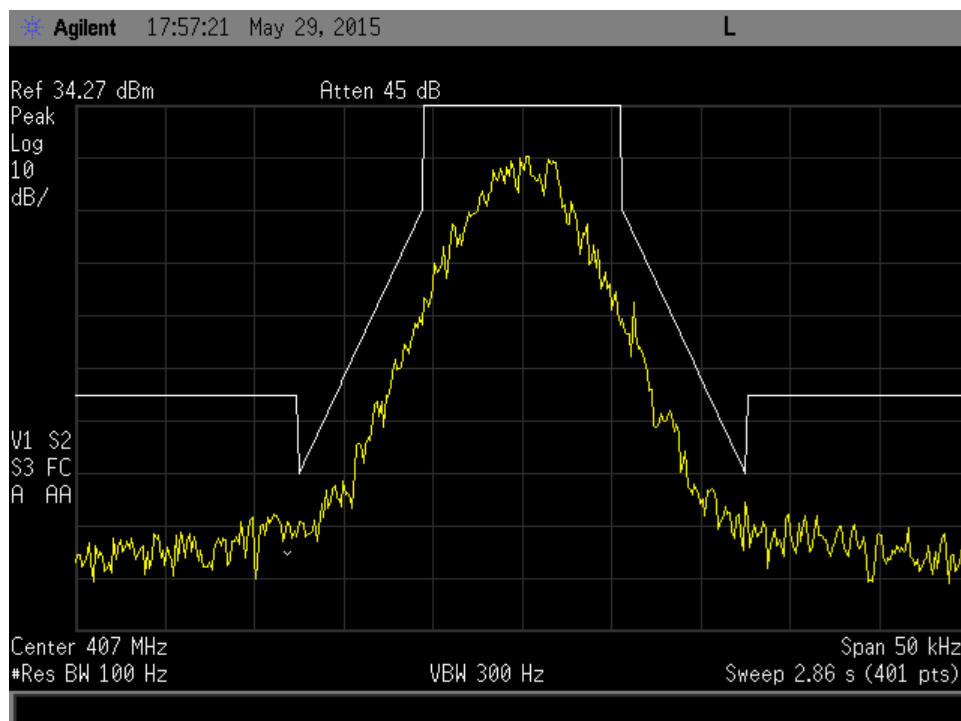




Mask D_407 MHz_ C4FM Input

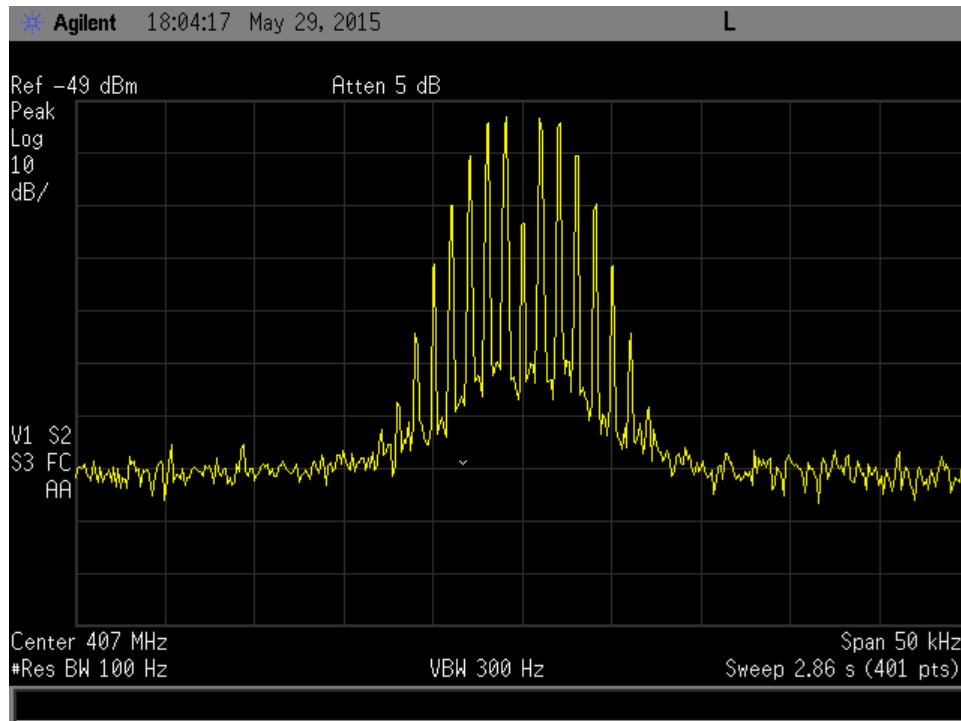


Mask D_407 MHz_ C4FM Output

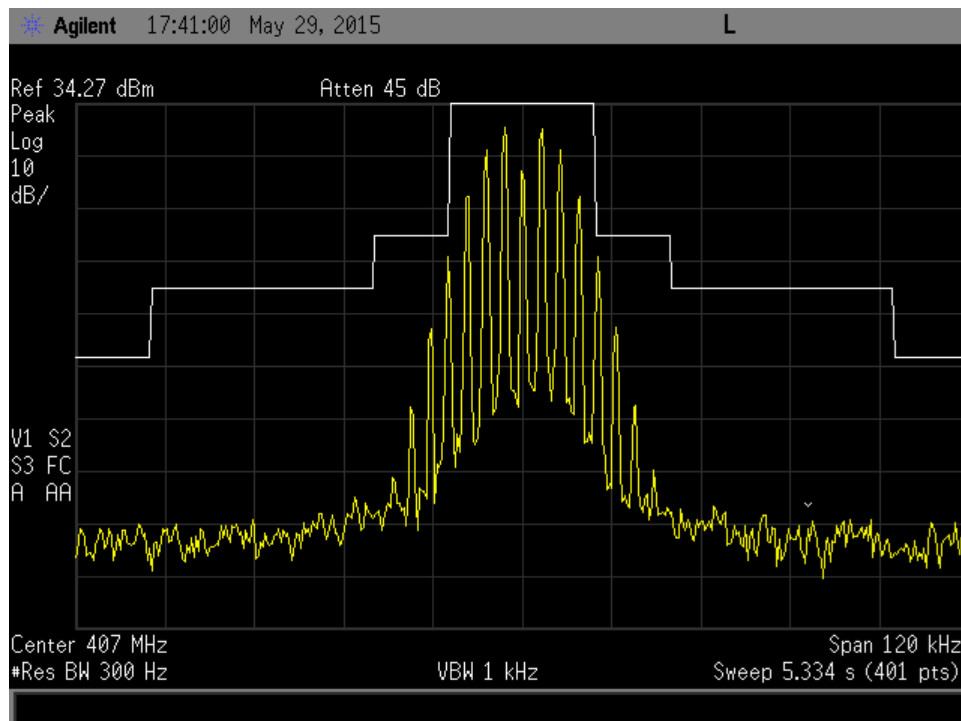




Mask D_407 MHz_ Input

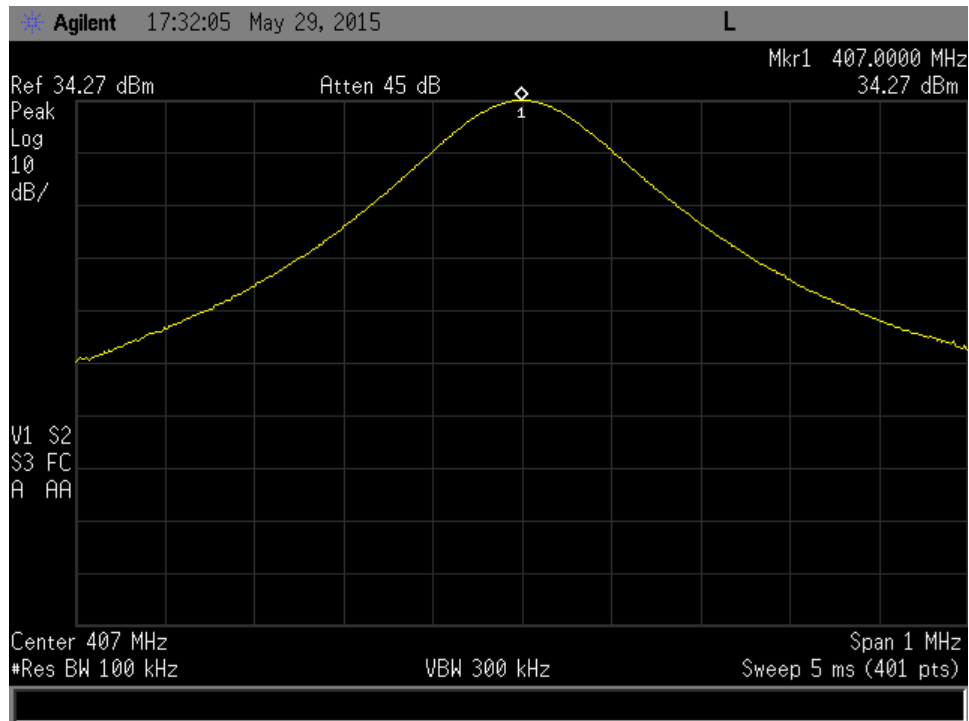


Mask D_407 MHz_ Output

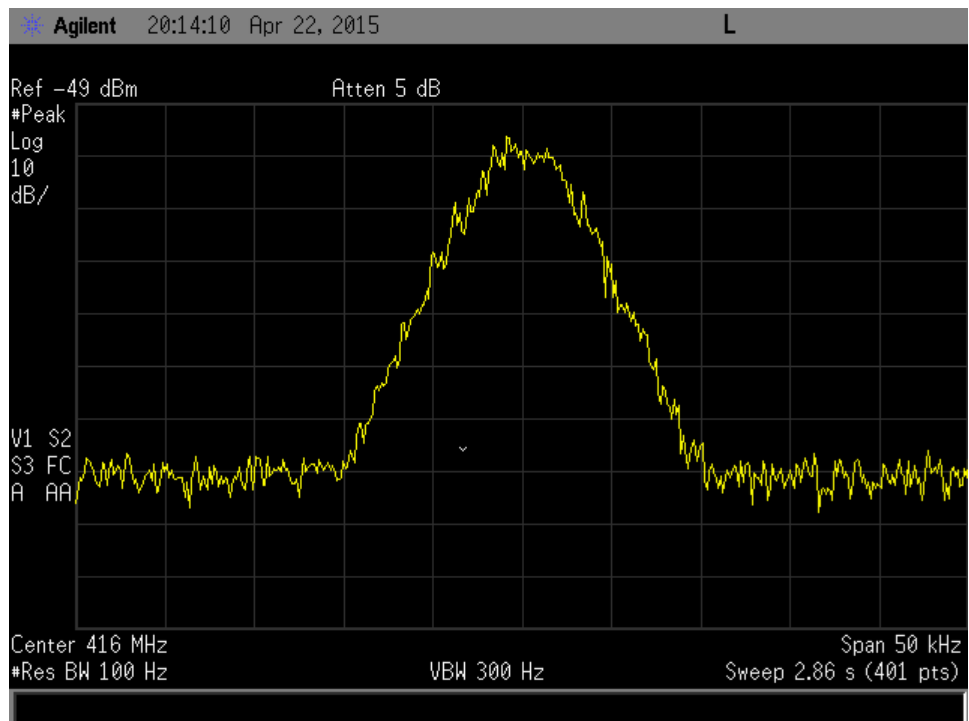




Mask D_407 MHz_Ref

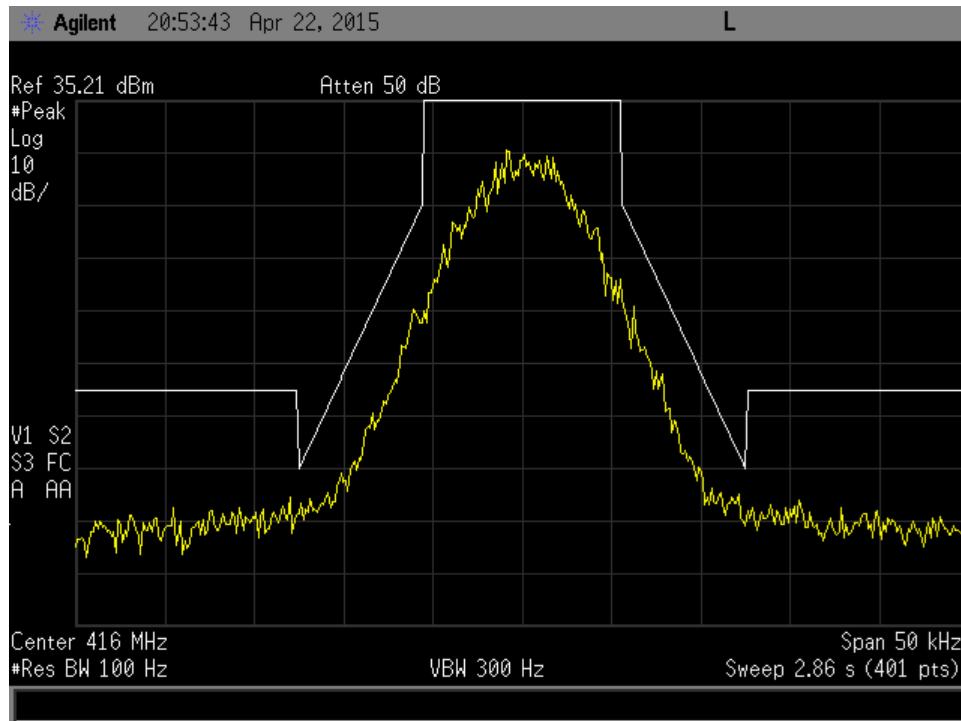


Mask D_416 MHz_ C4FM Input

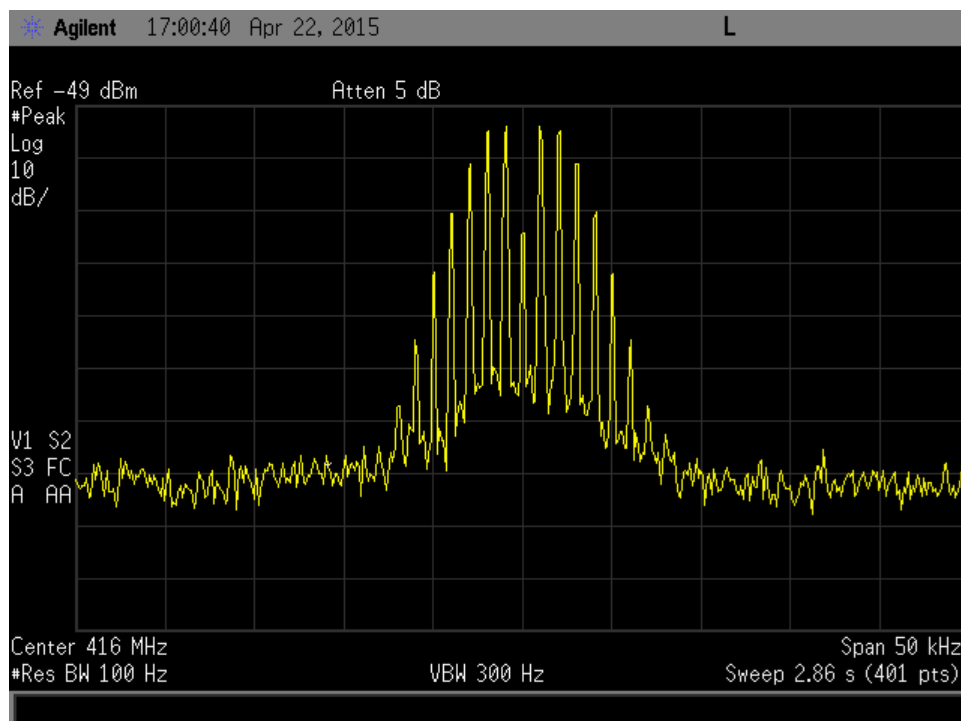




Mask D_416 MHz_ C4FM Output

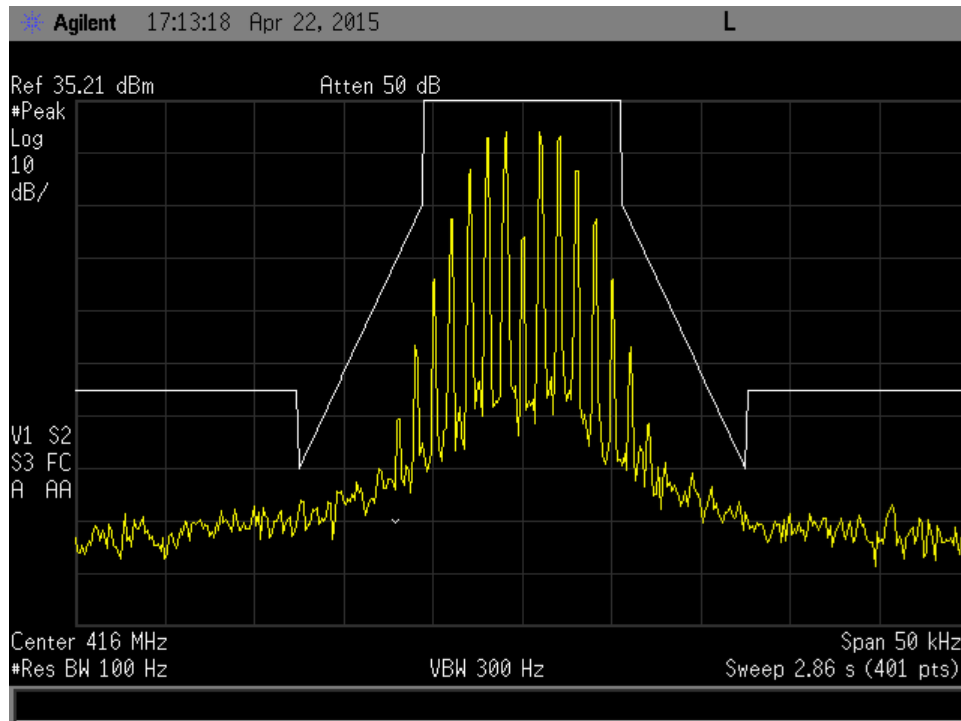


Mask D_416 MHz_ Input

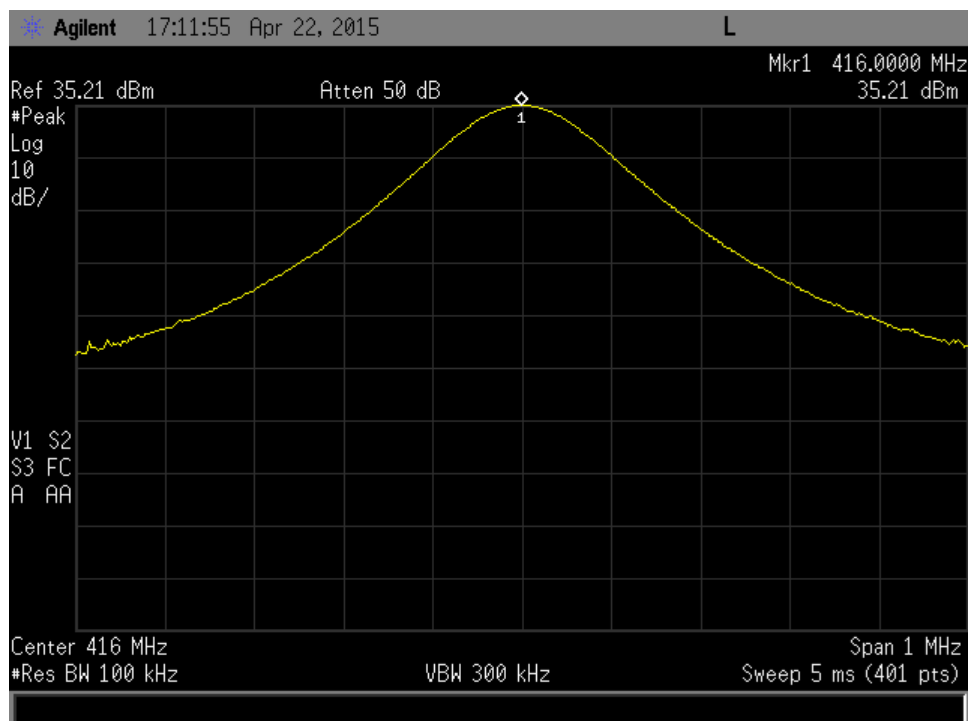




Mask D_416 MHz_ Output

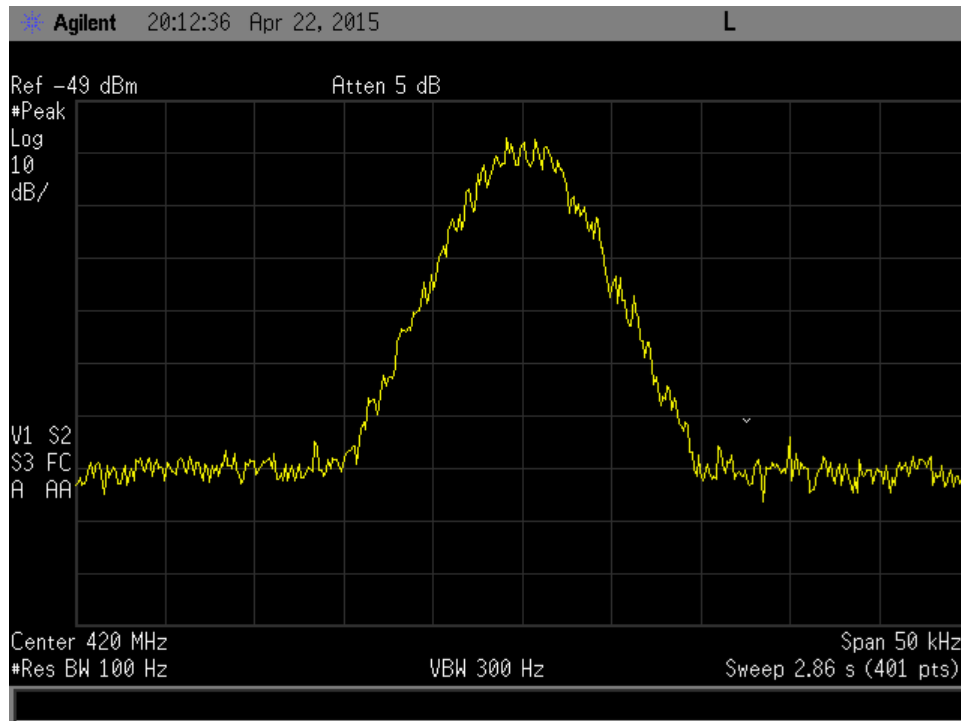


Mask D_416 MHz_ Ref

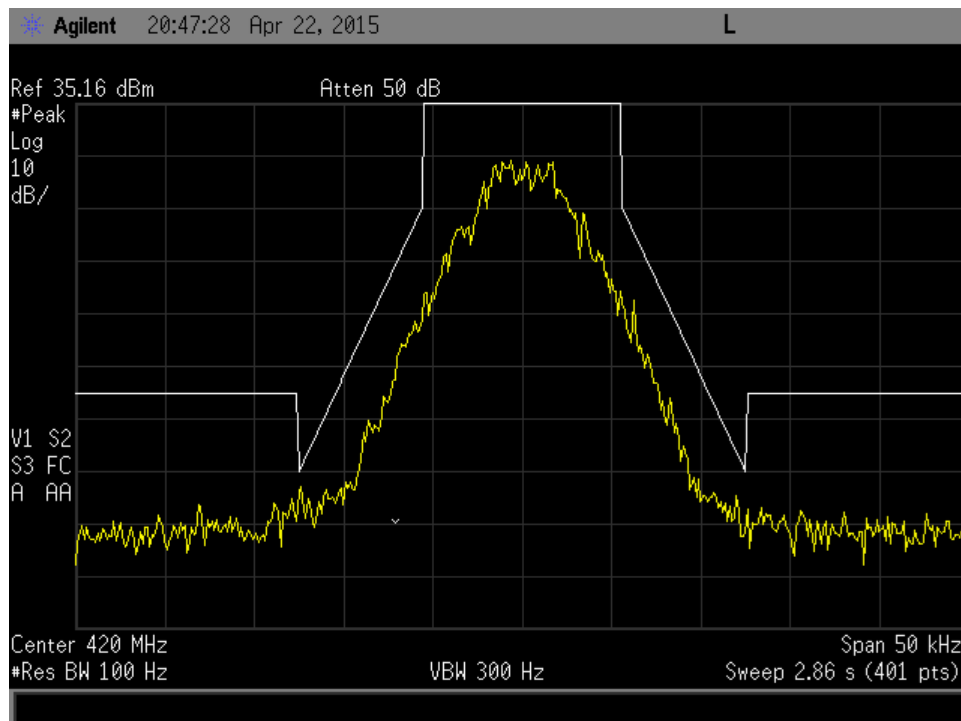




Mask D_420 MHz_ C4FM Input

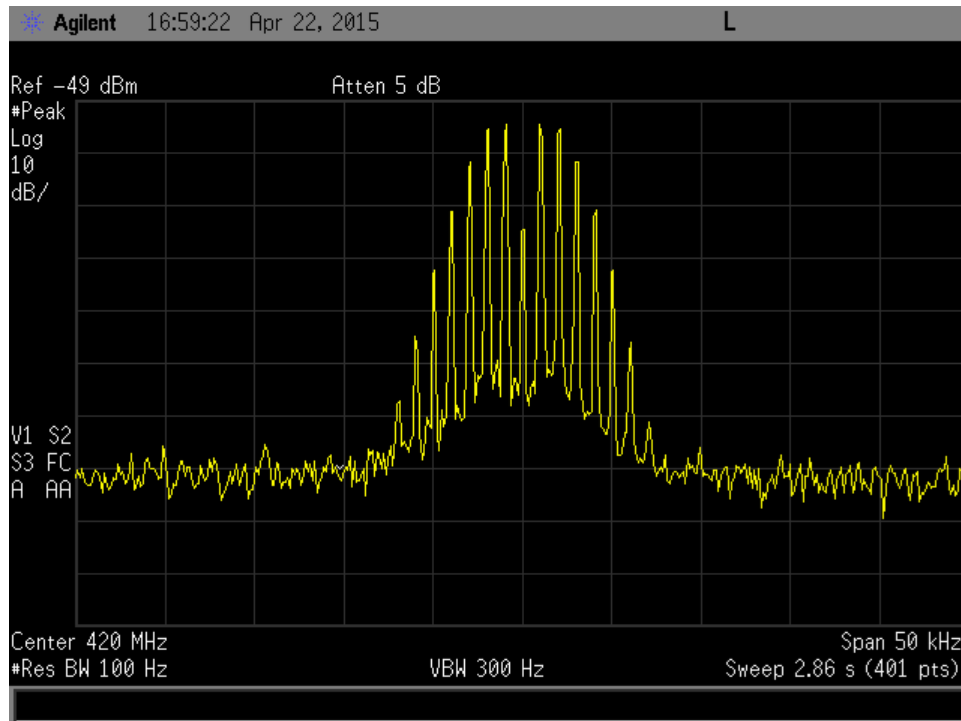


Mask D_420 MHz_ C4FM Output

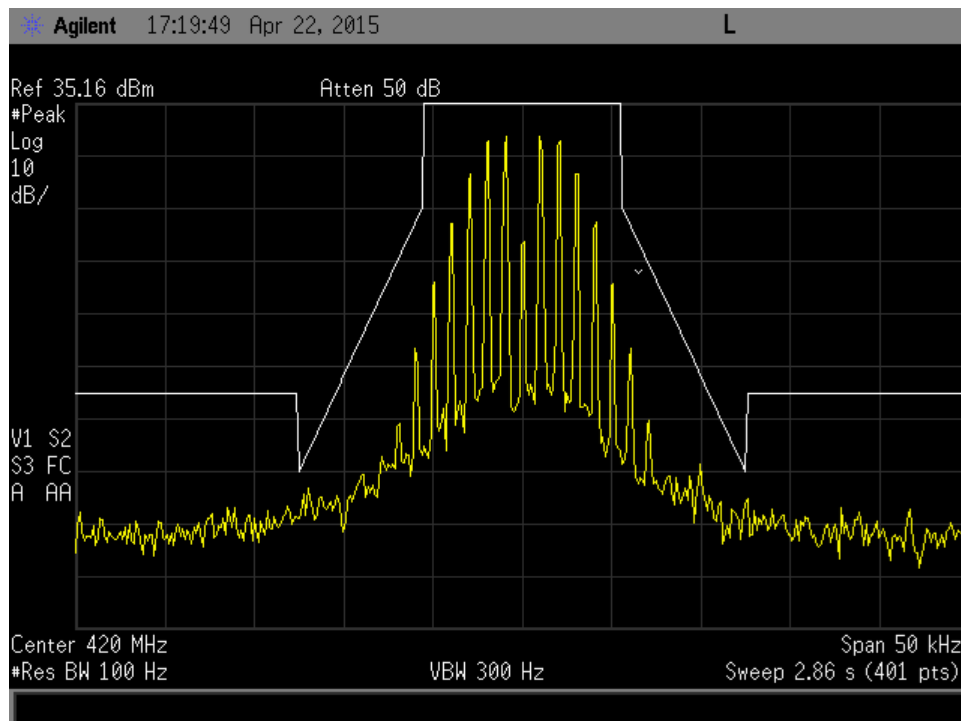




Mask D_420 MHz_ Input

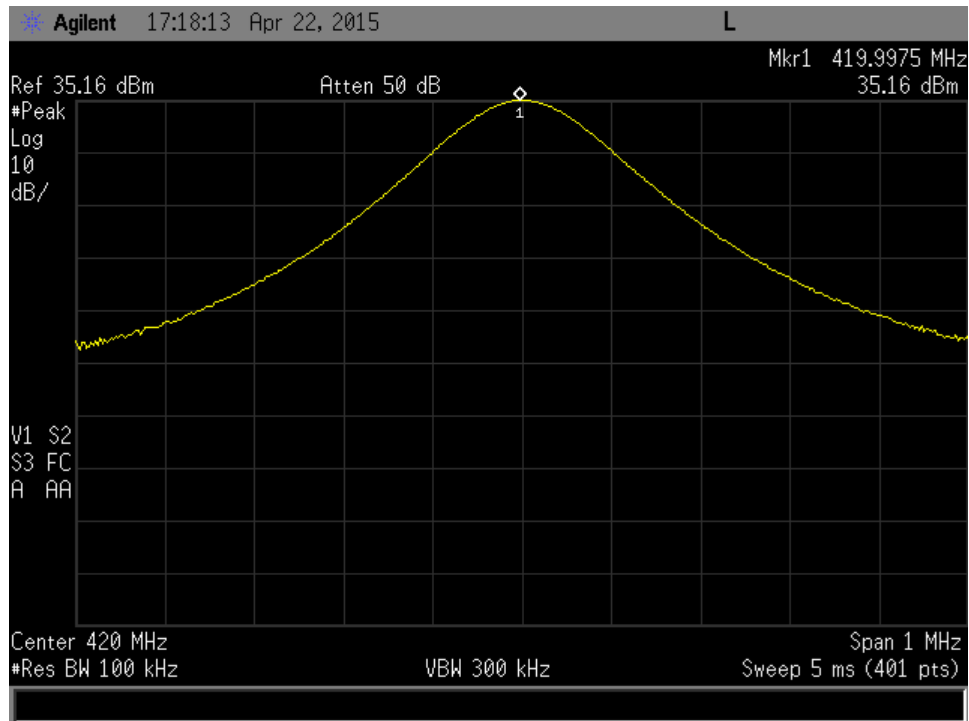


Mask D_420 MHz_ Output

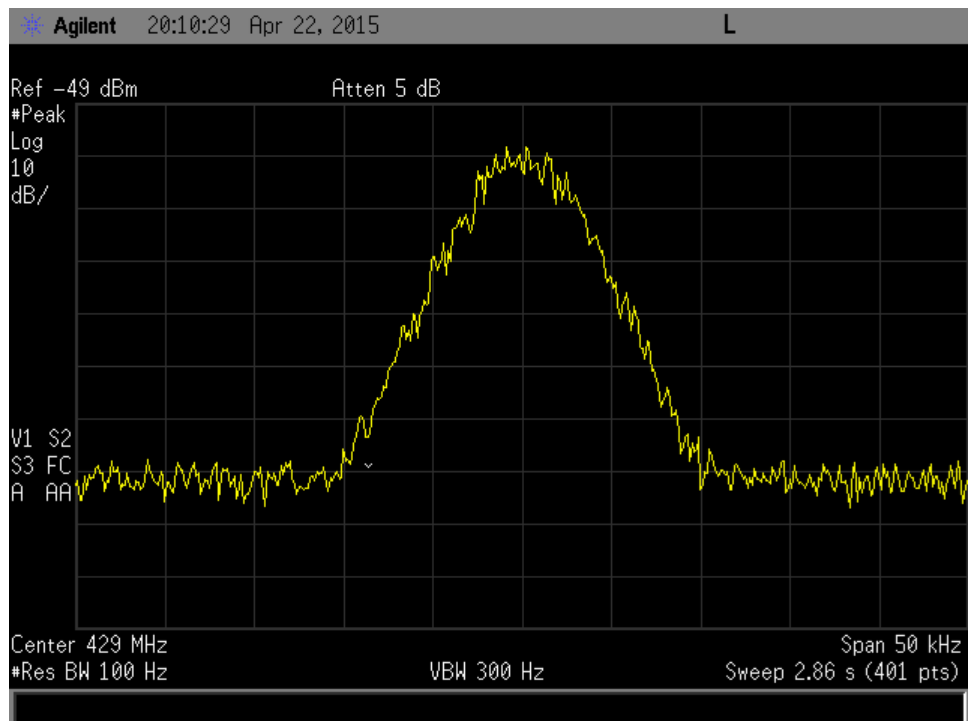




Mask D_420 MHz_ Ref

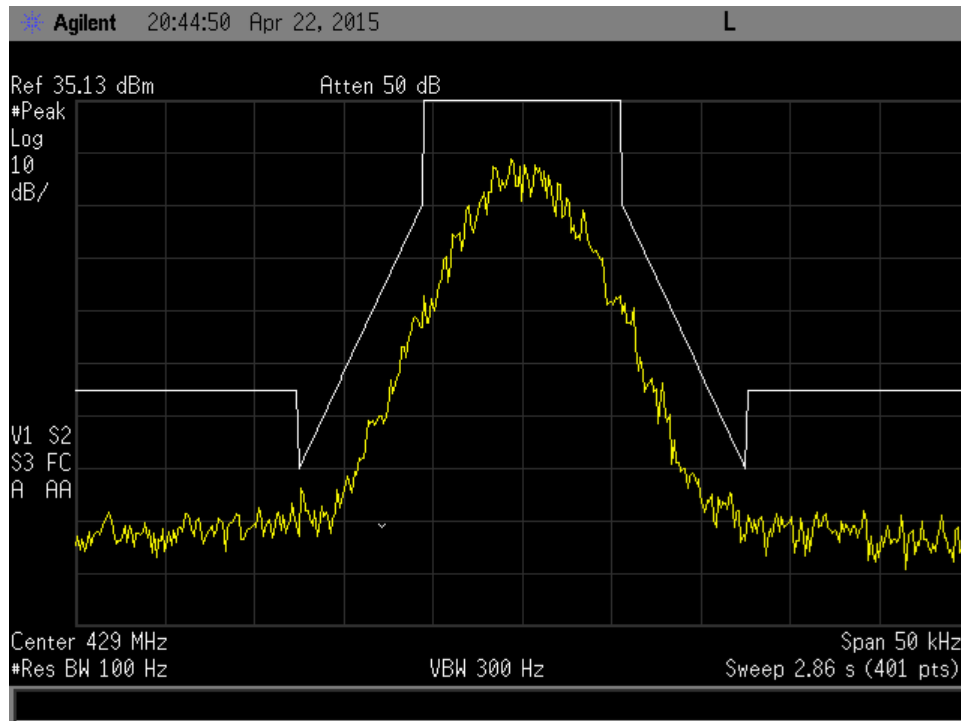


Mask D_429 MHz_ C4FM Input

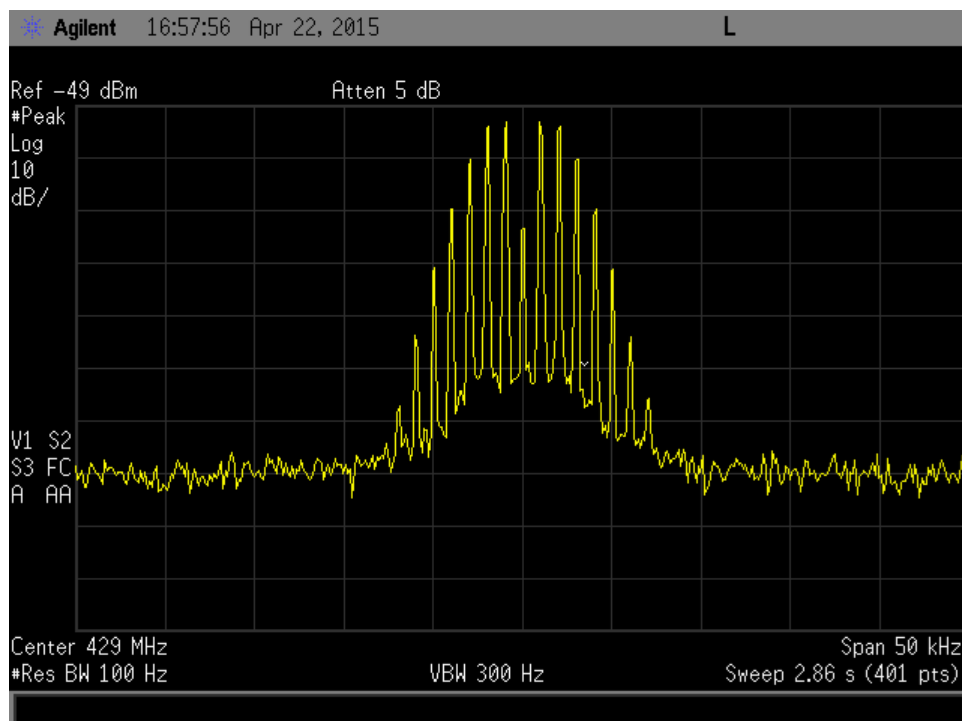




Mask D_429 MHz_ C4FM Output

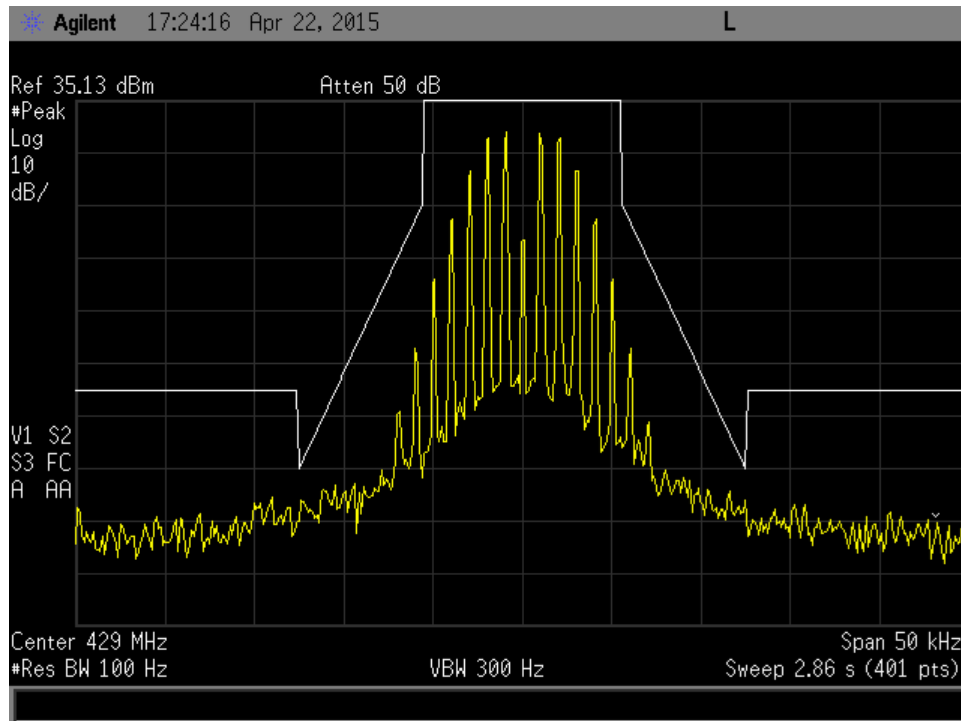


Mask D_429 MHz_ Input

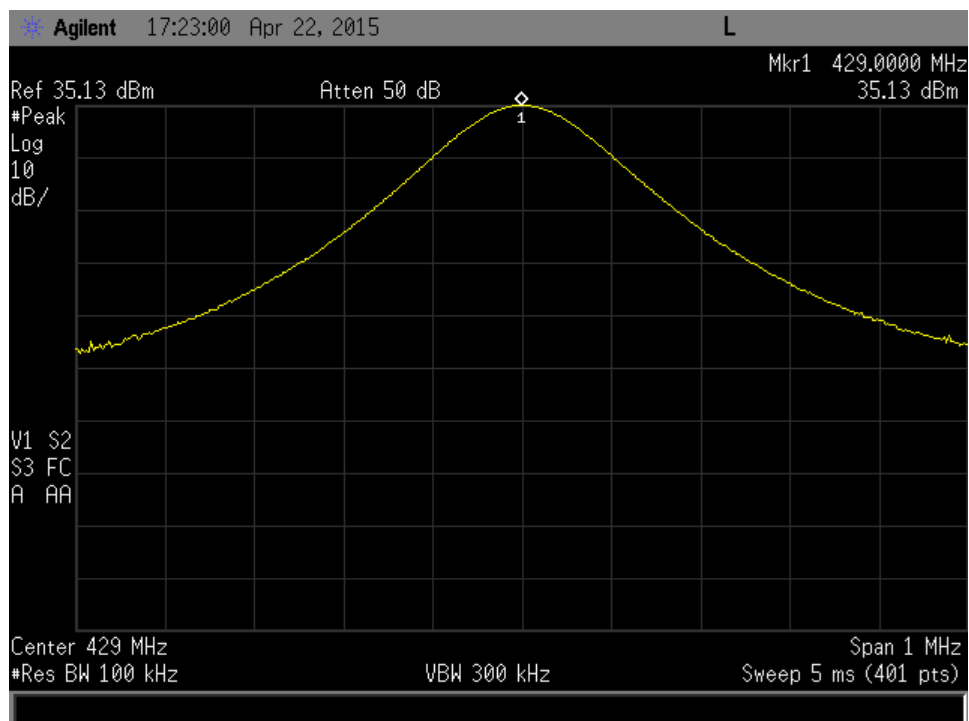




Mask D_429 MHz_ Output

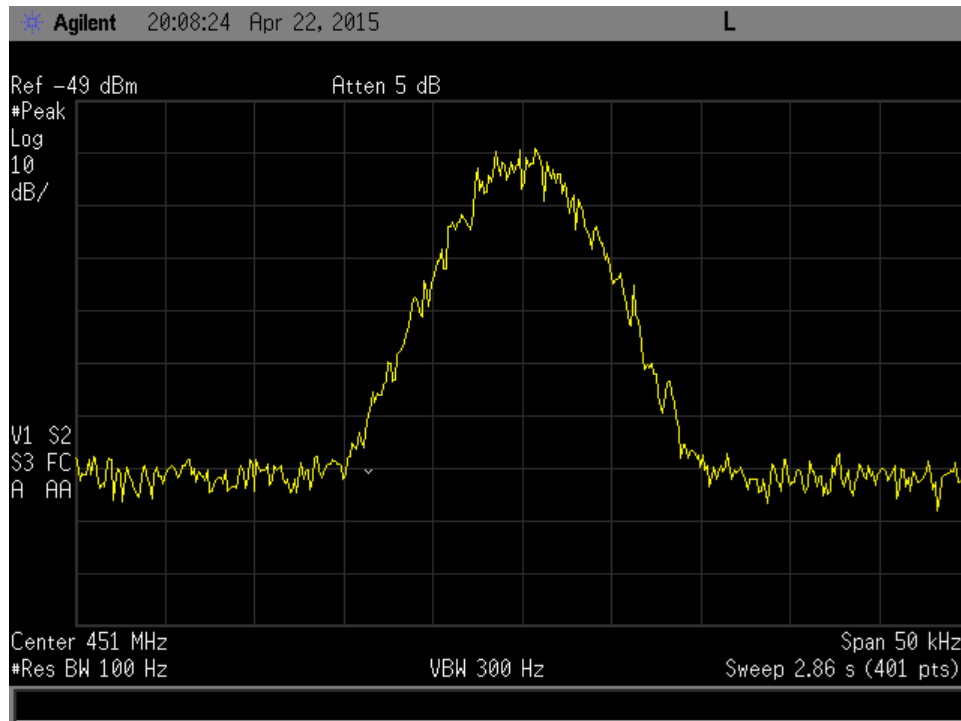


Mask D_429 MHz_ Ref

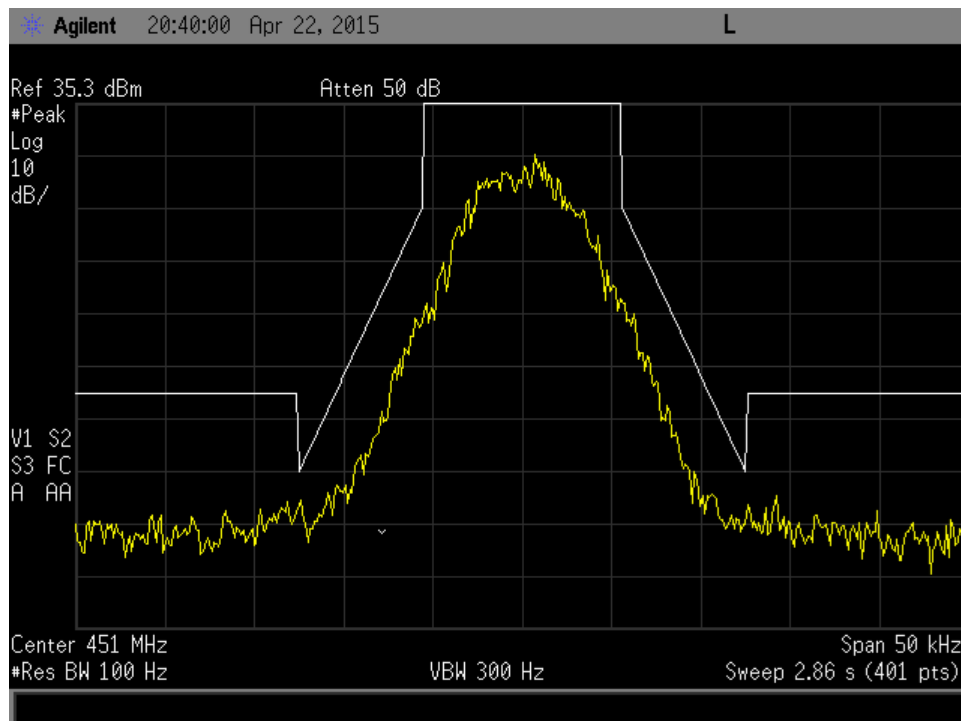




Mask D_451 MHz_ C4FM Input

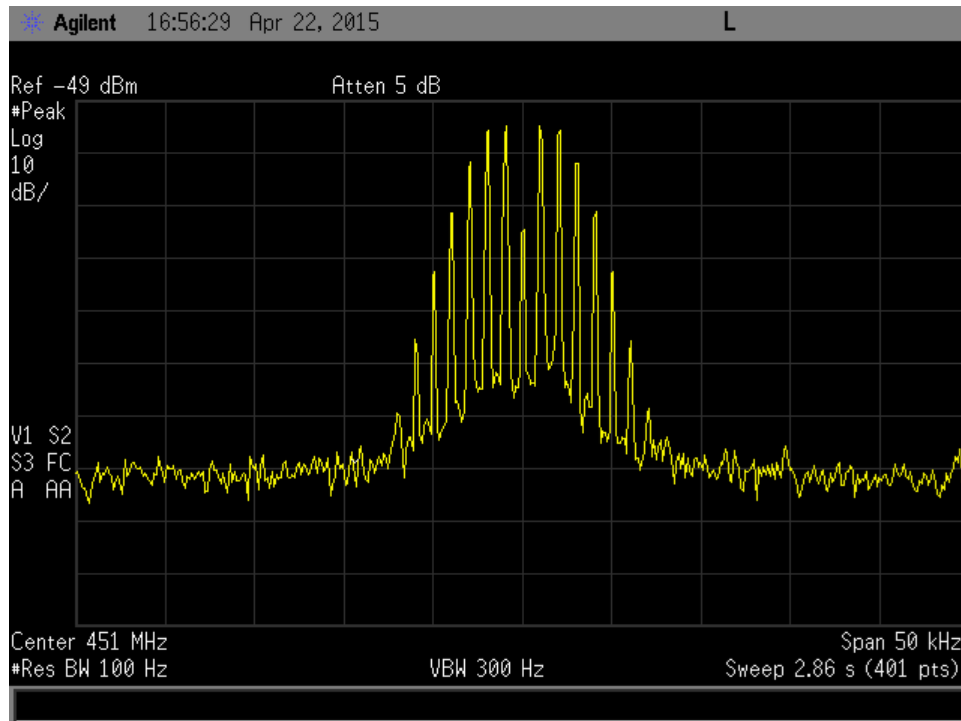


Mask D_451 MHz_ C4FM Output

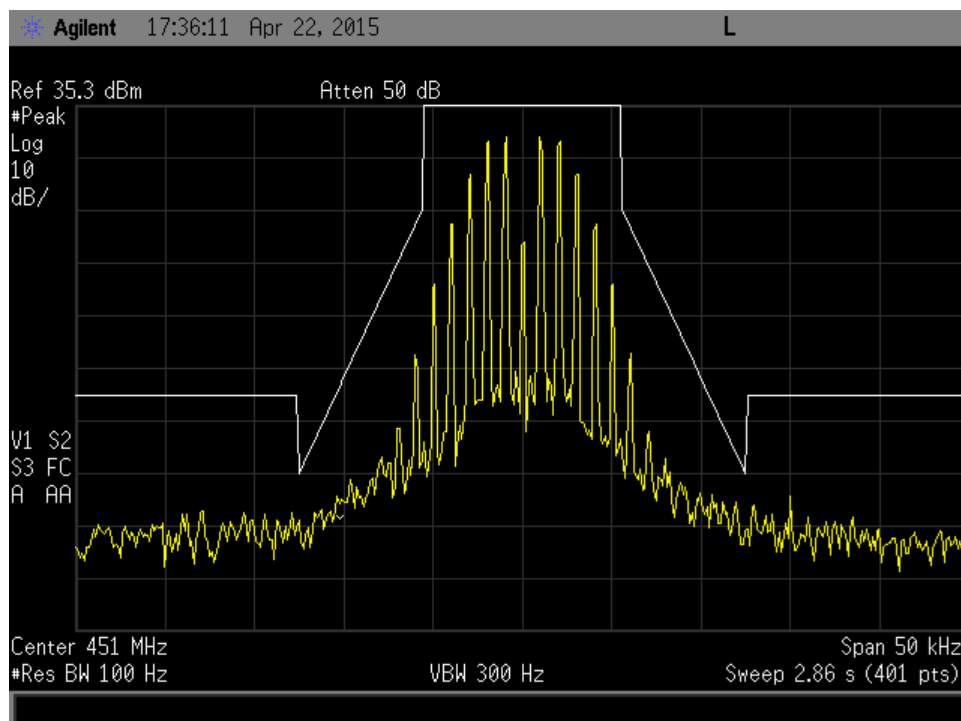




Mask D_451 MHz_ Input

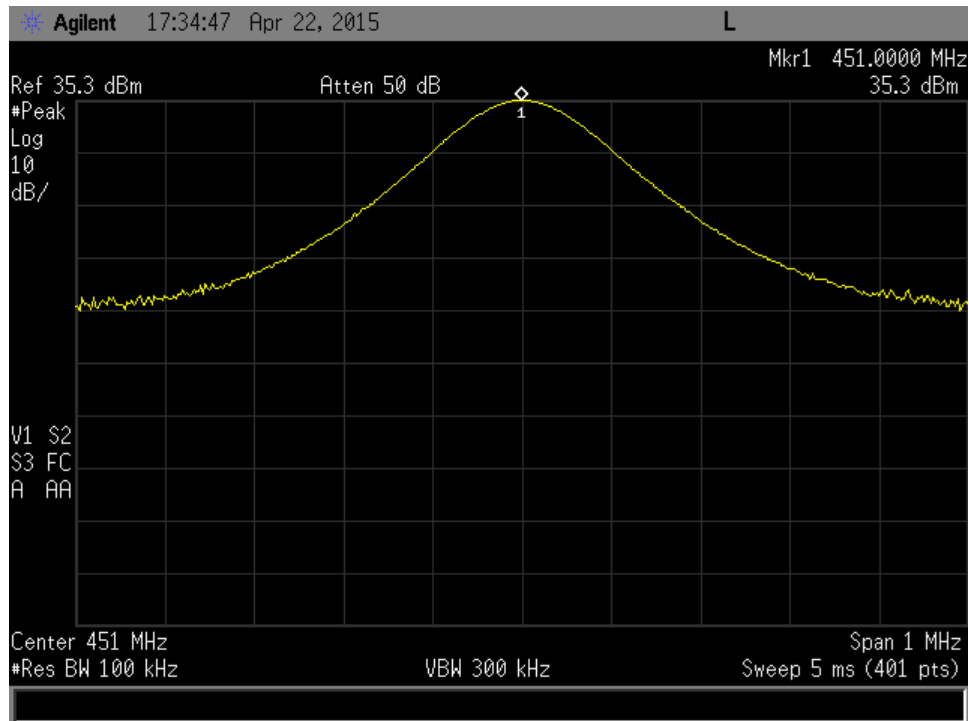


Mask D_451 MHz_ Output

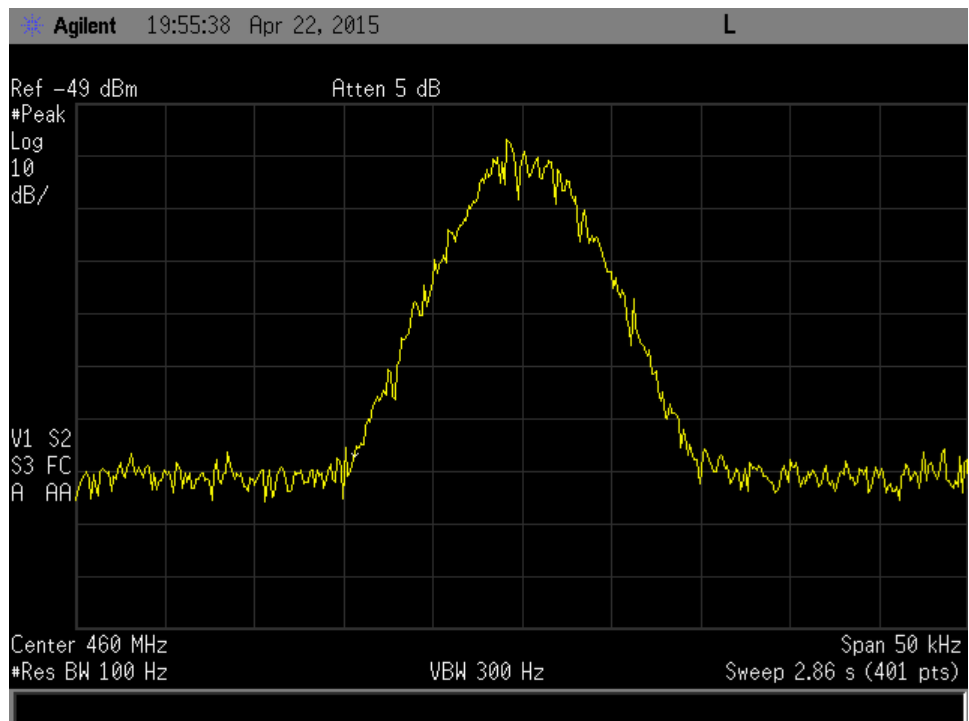




Mask D_451 MHz_ Ref

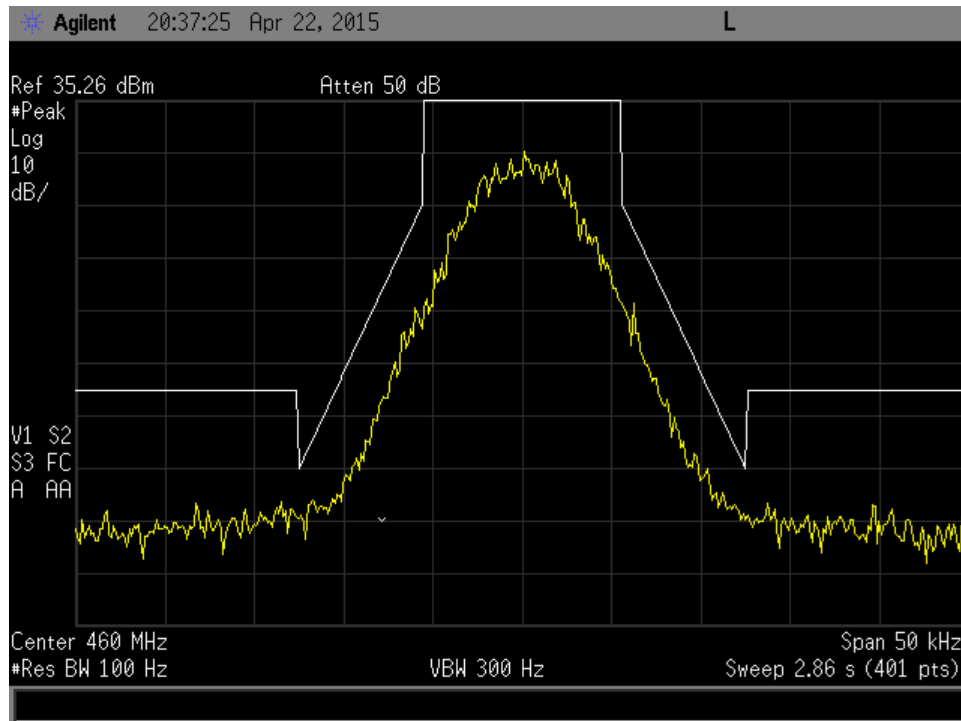


Mask D_460 MHz_ C4FM Input

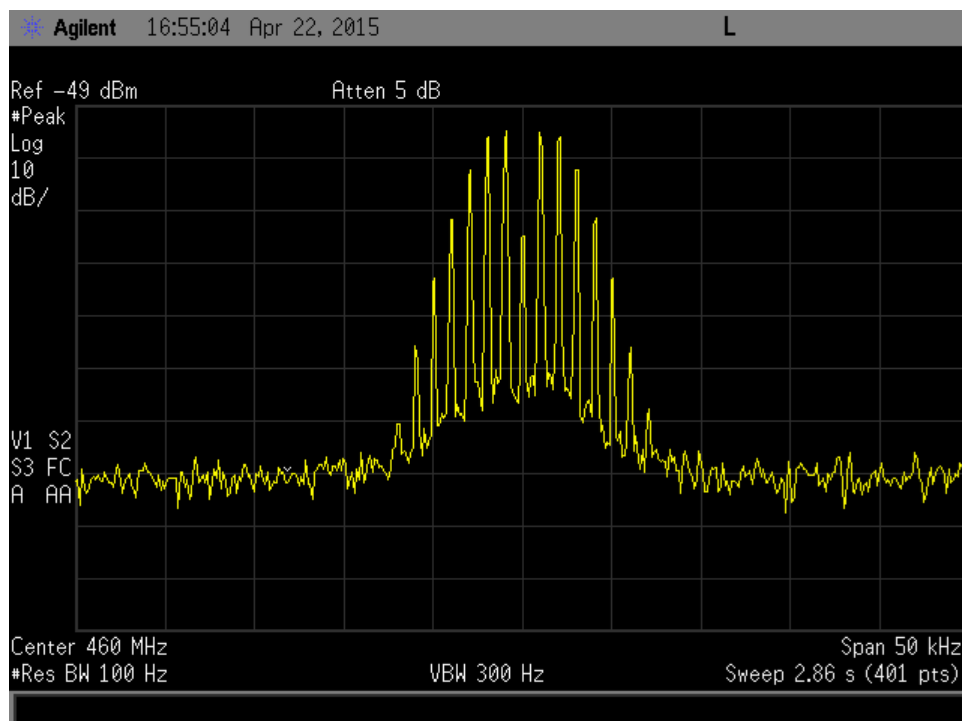




Mask D_460 MHz_ C4FM Output

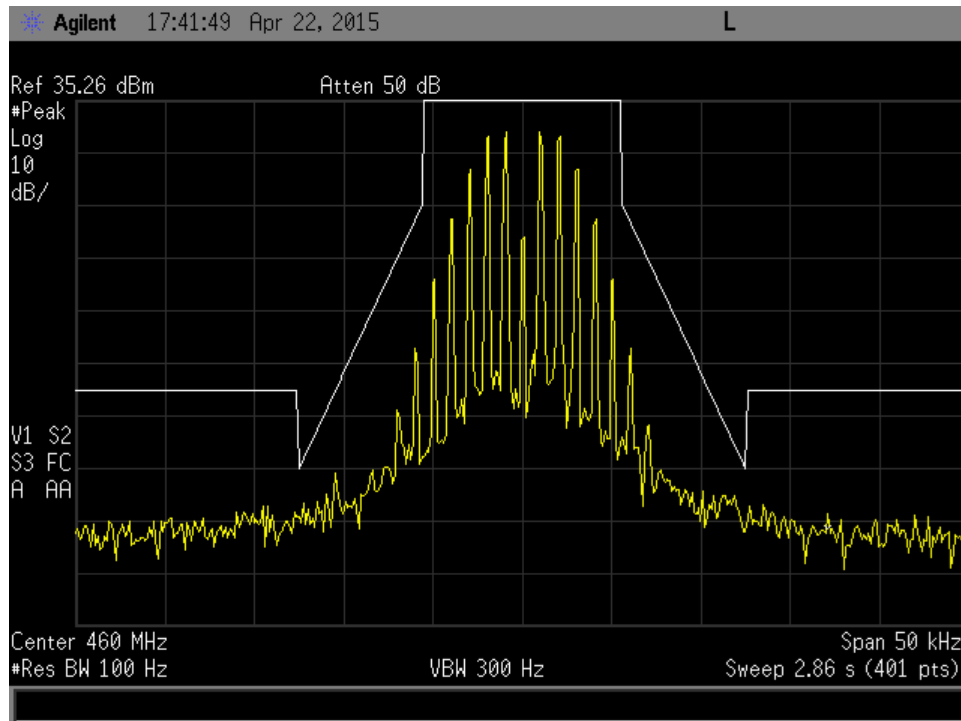


Mask D_460 MHz_ Input

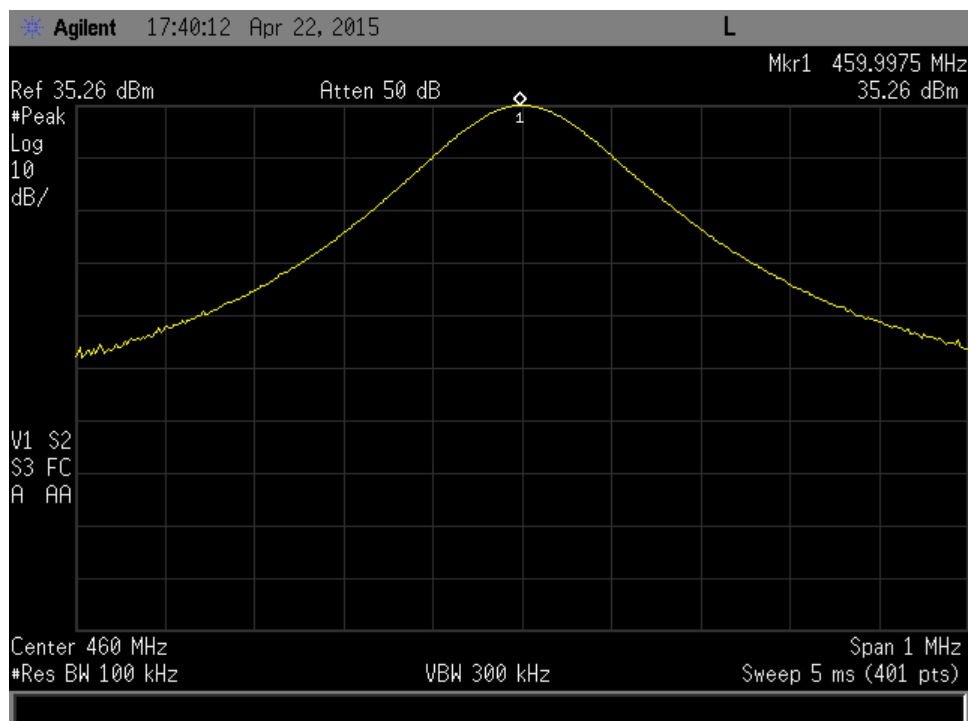




Mask D_460 MHz_ Output

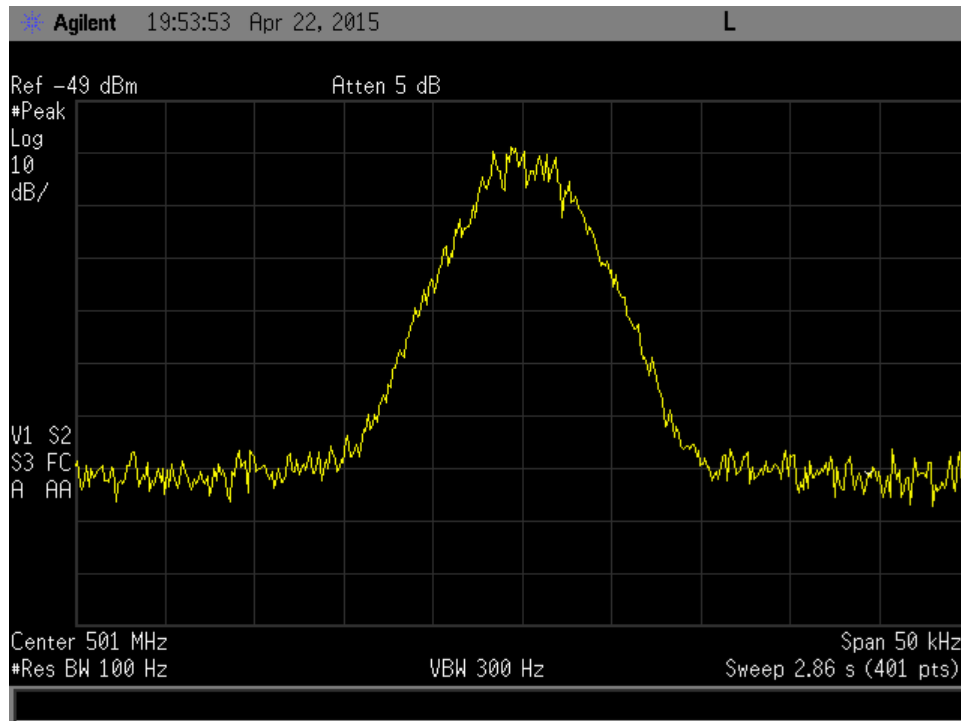


Mask D_460 MHz_ Ref

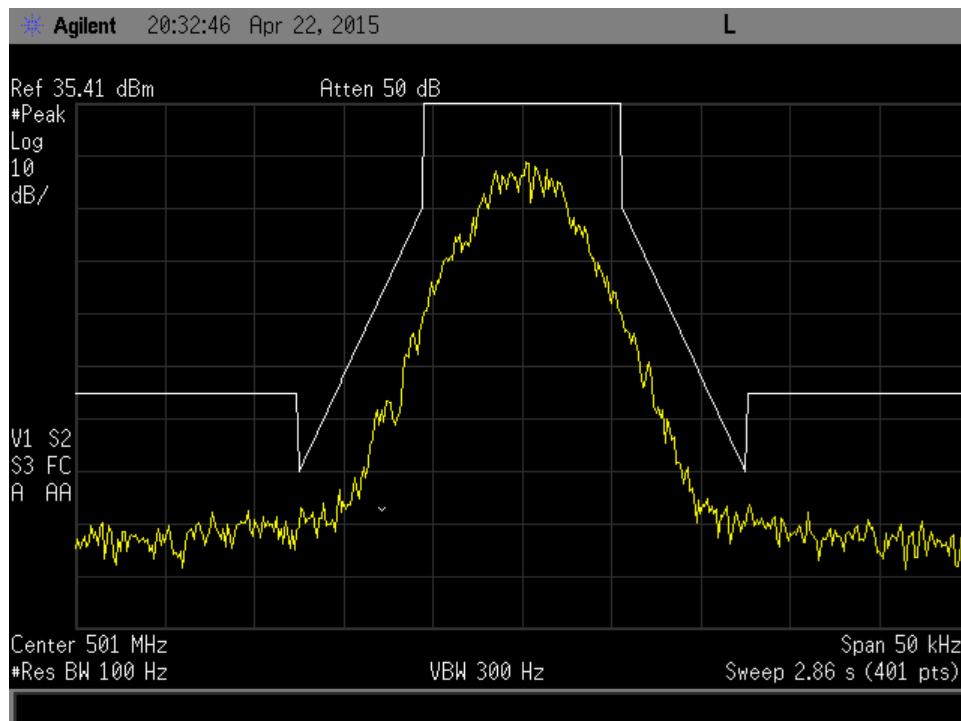




Mask D_501 MHz_ C4FM Input

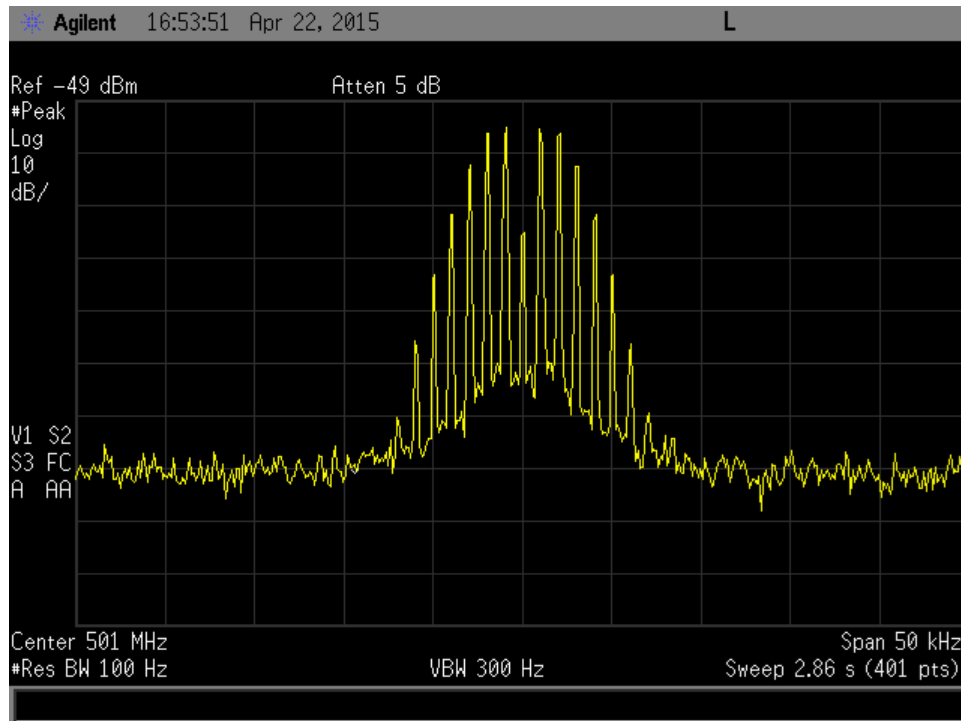


Mask D_501 MHz_ C4FM Output

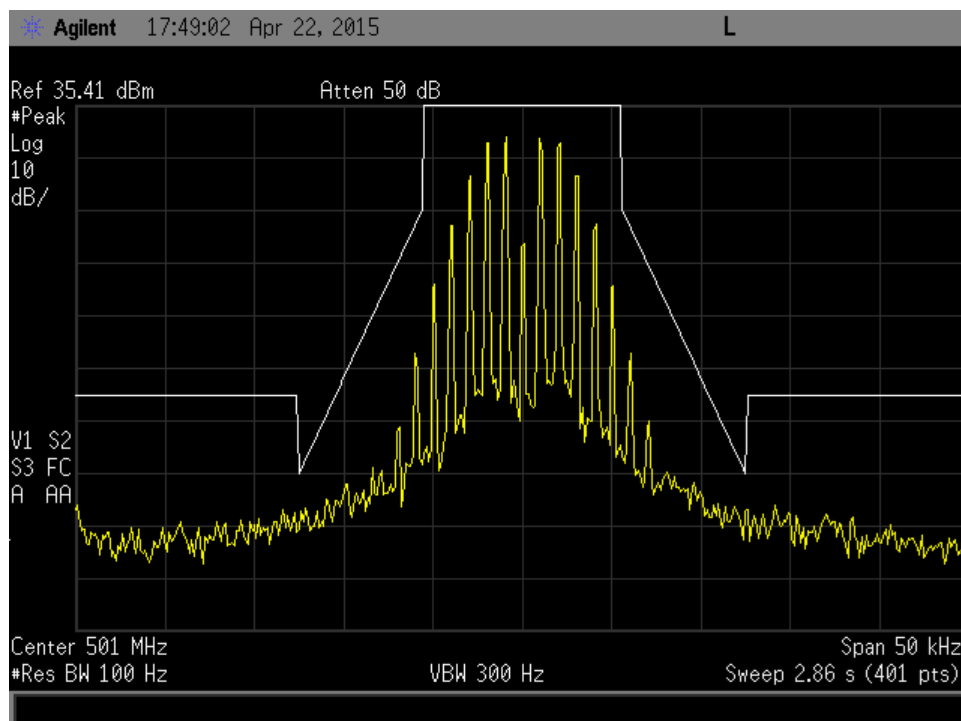




Mask D_501 MHz_ Input

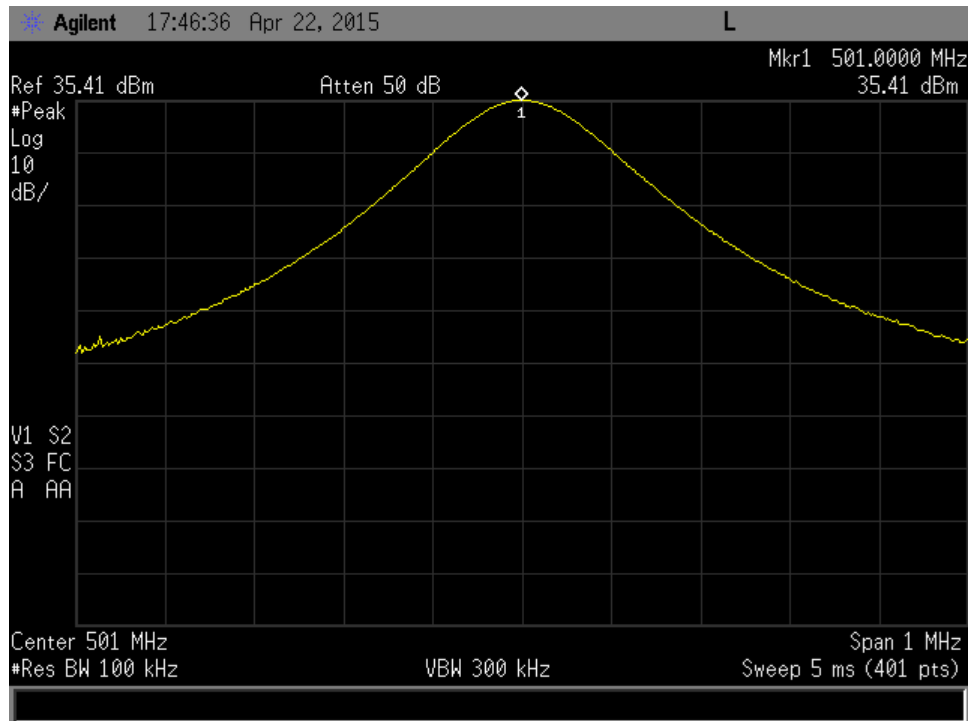


Mask D_501 MHz_ Output

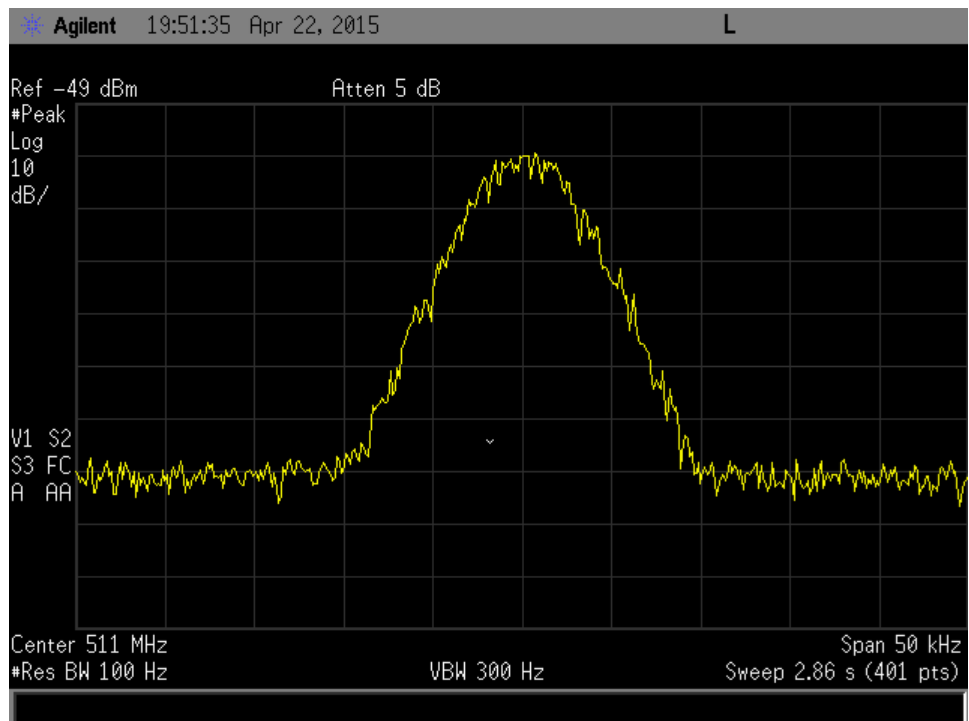




Mask D_501 MHz_ Ref

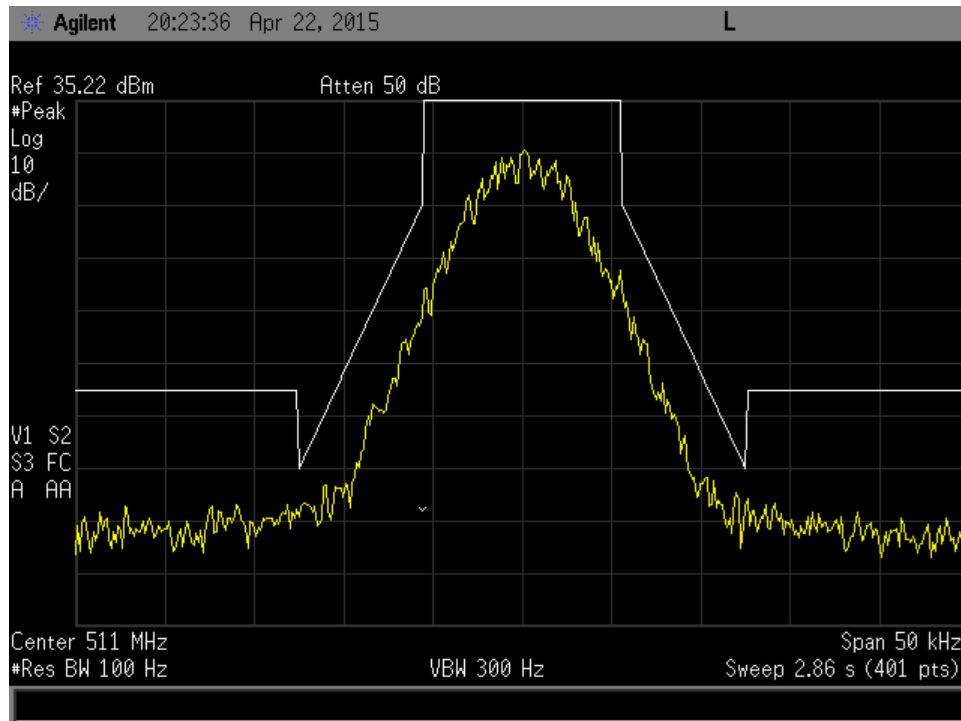


Mask D_511 MHz_ C4FM Input

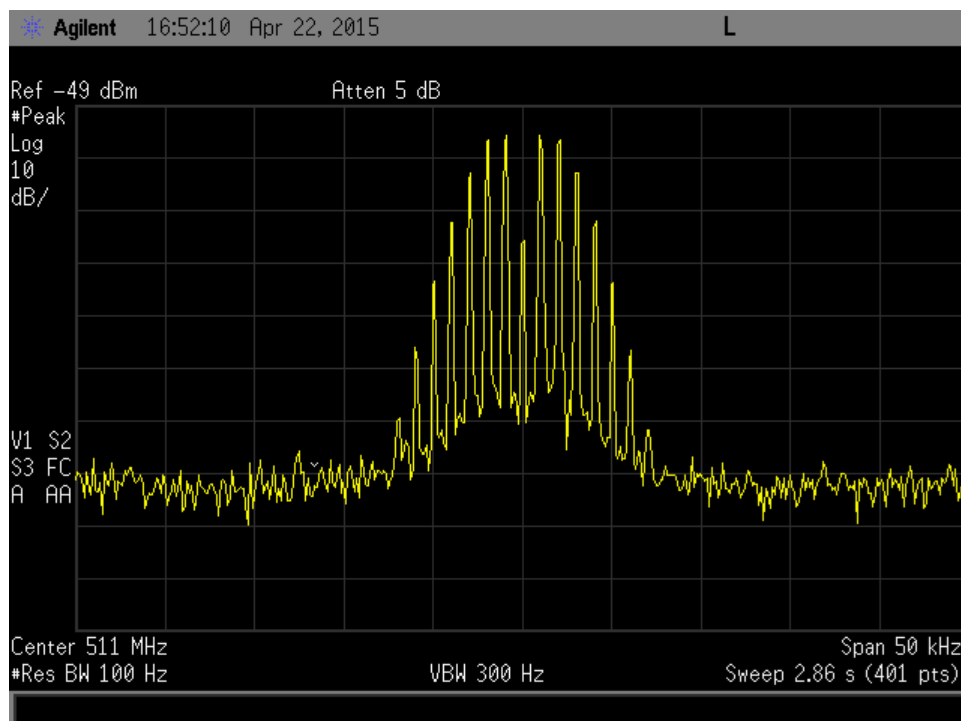




Mask D_511 MHz_ C4FM Output

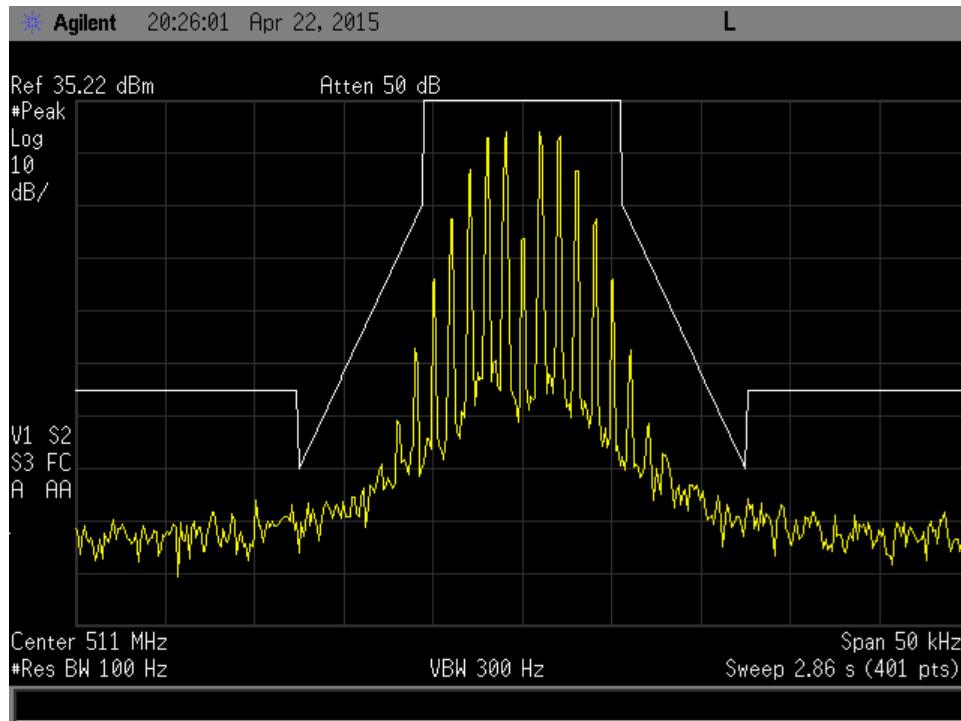


Mask D_511 MHz_ Input

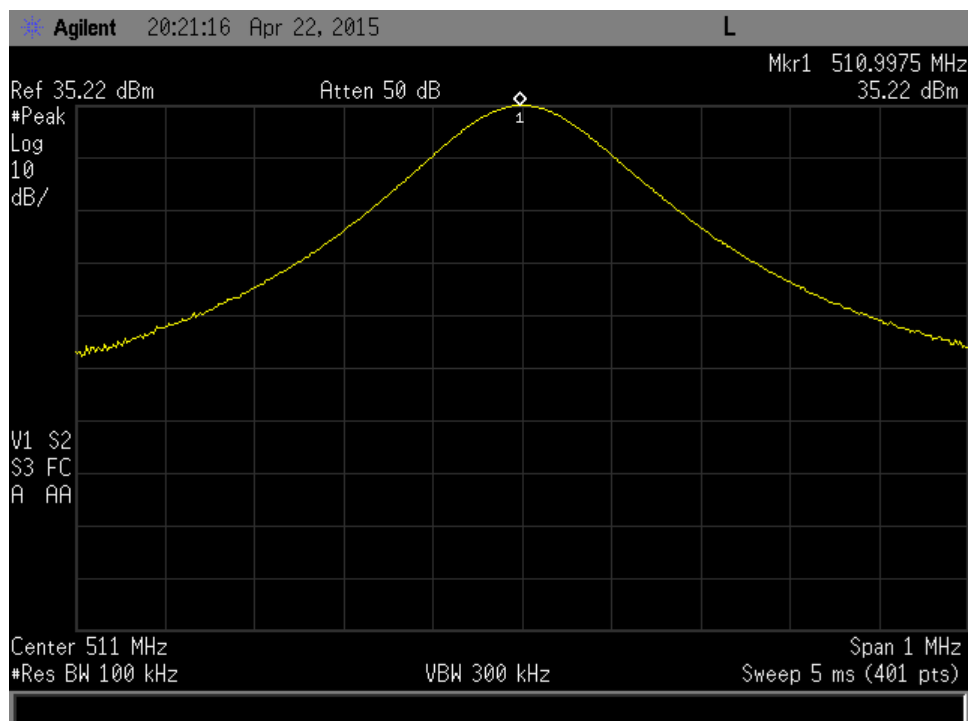




Mask D_511 MHz_ Output

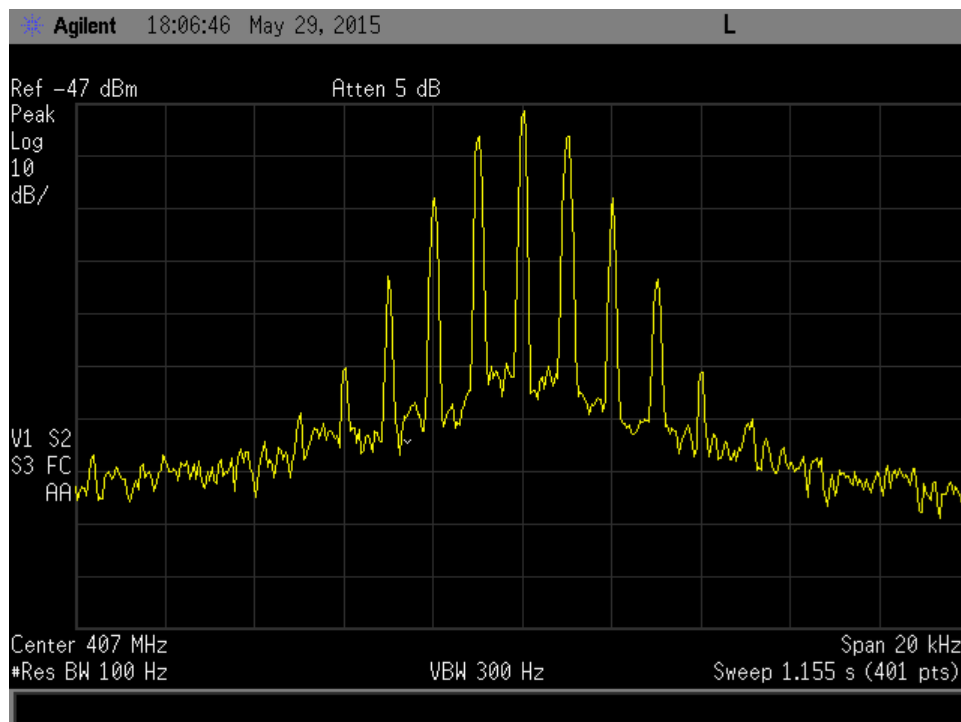


Mask D_511 MHz_ Ref

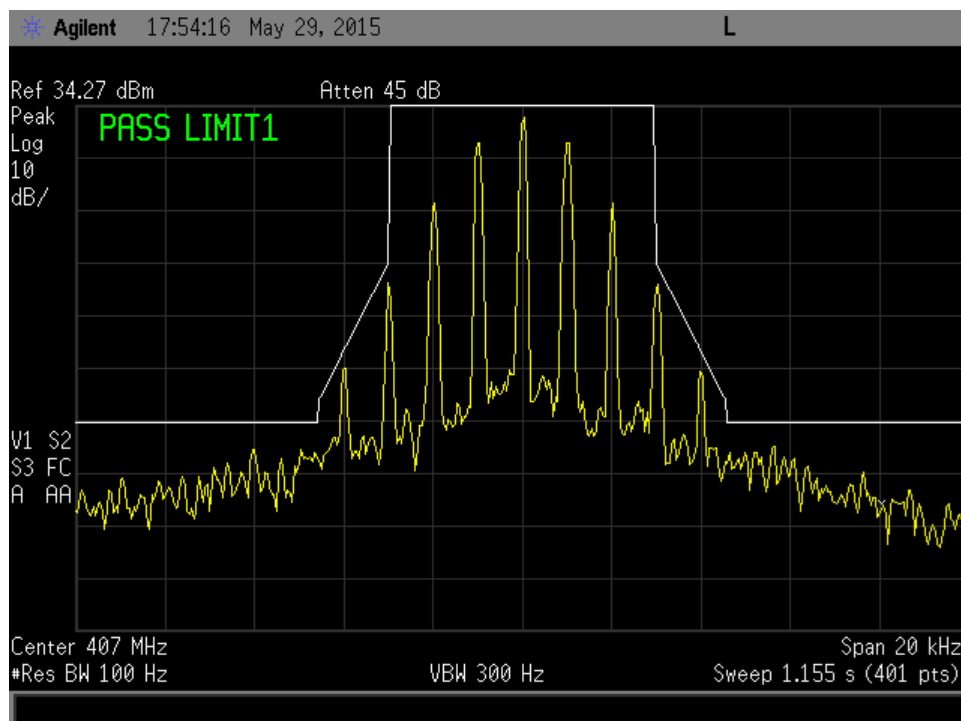




Mask E_407 MHz_Input

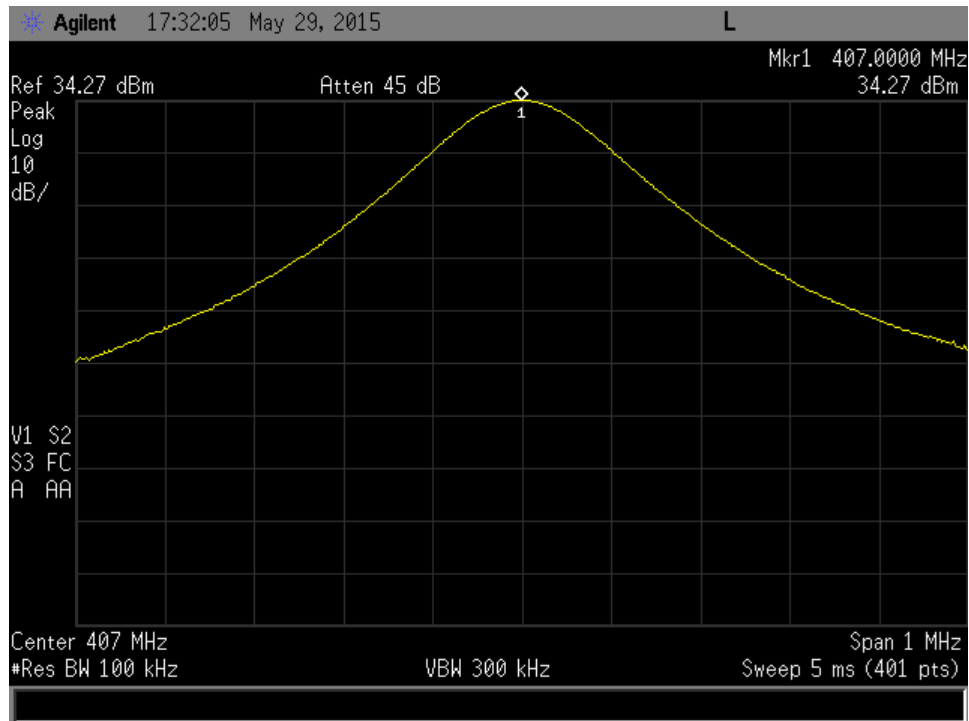


Mask E_407 MHz_Output

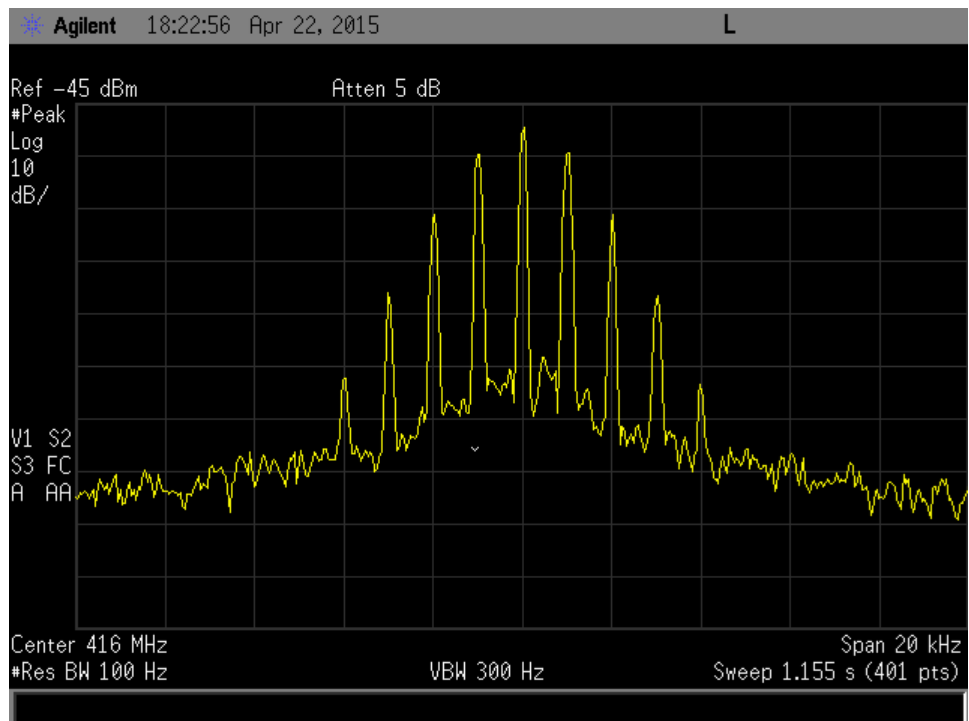




Mask E_407 MHz_Ref

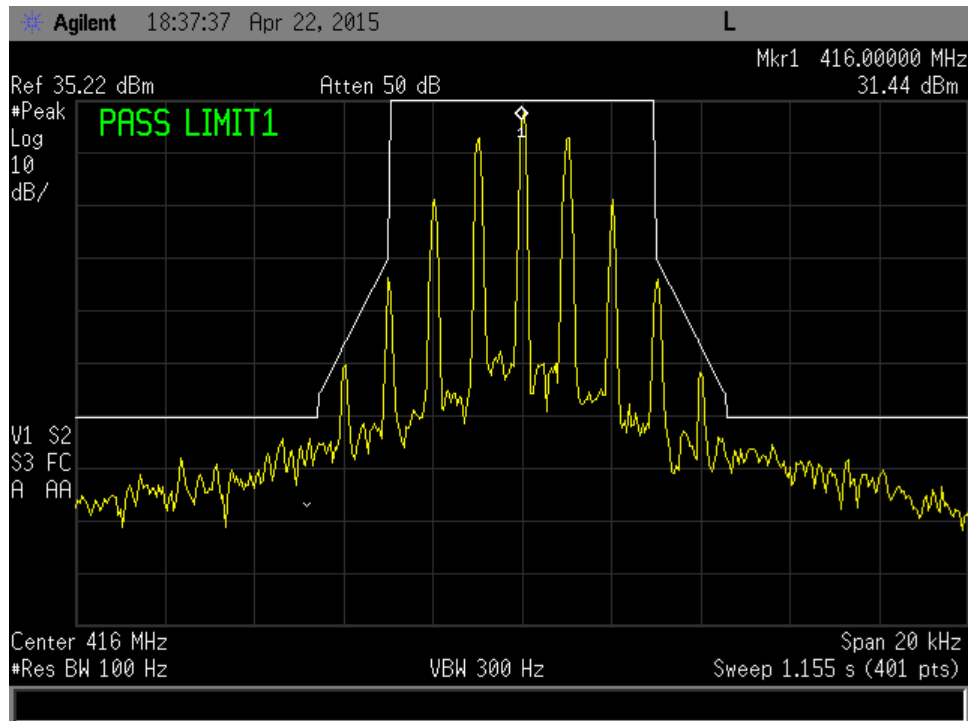


Mask E_416 MHz_Input

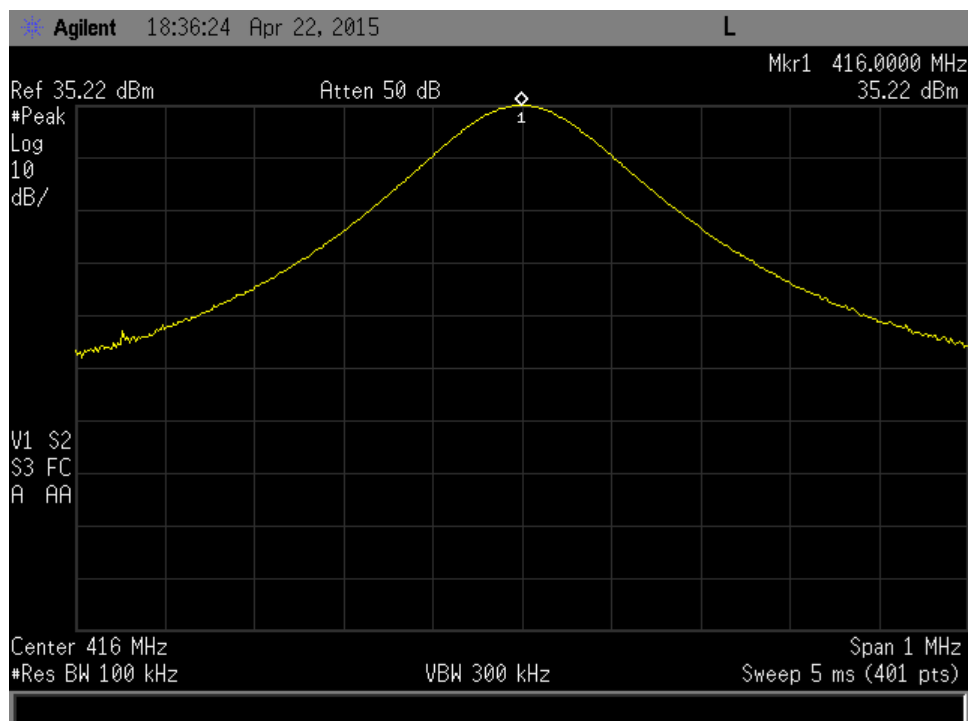




Mask E_416 MHz_Output

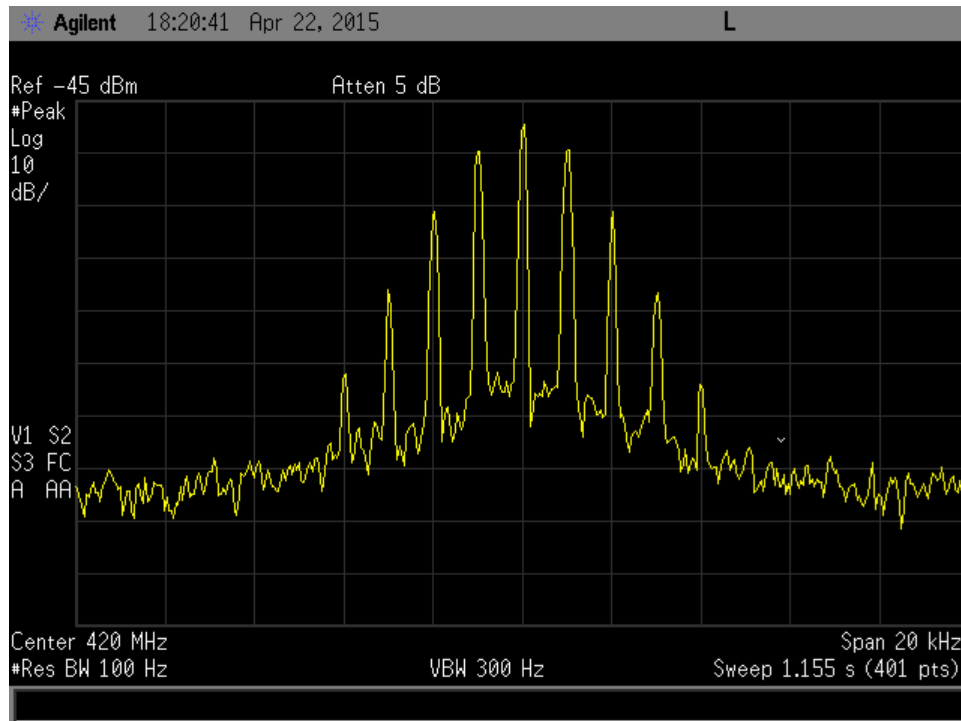


Mask E_416 MHz_Ref

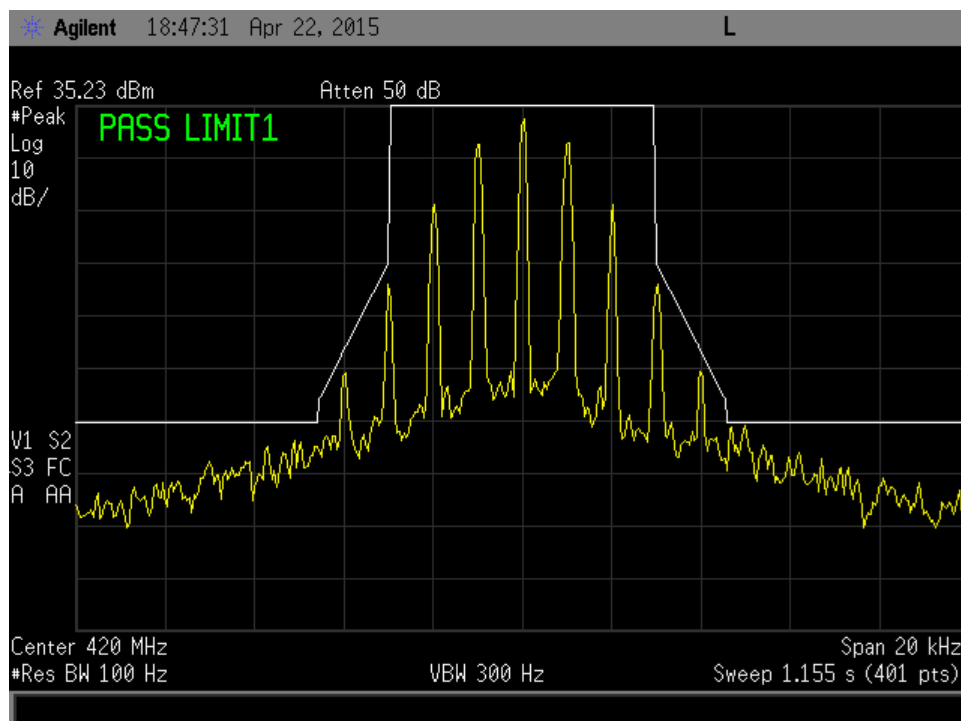




Mask E_420 MHz_Input

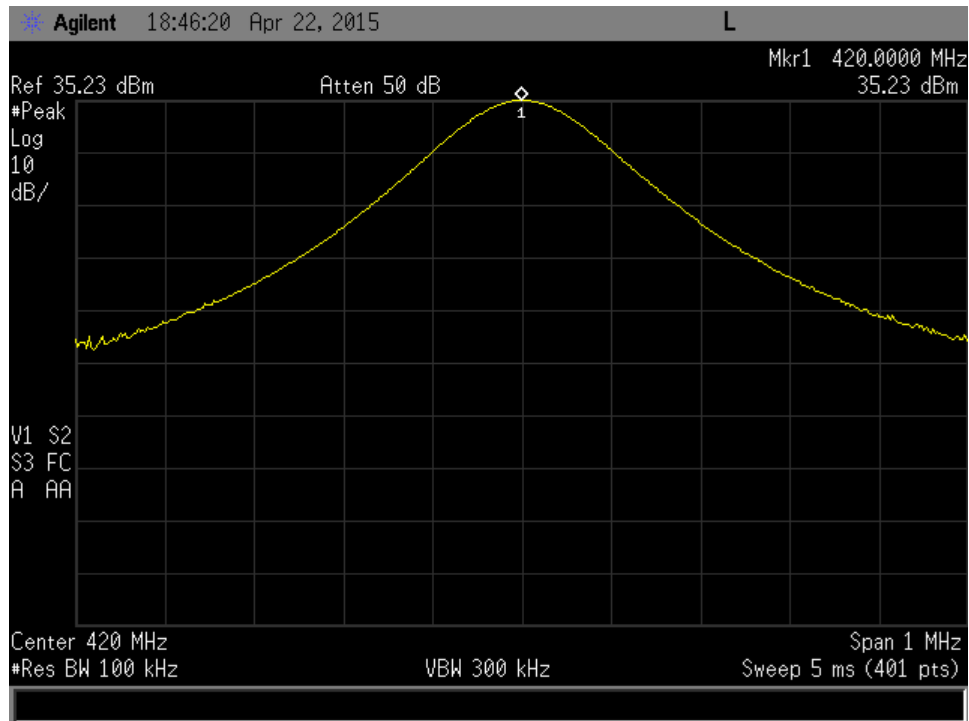


Mask E_420 MHz_Output

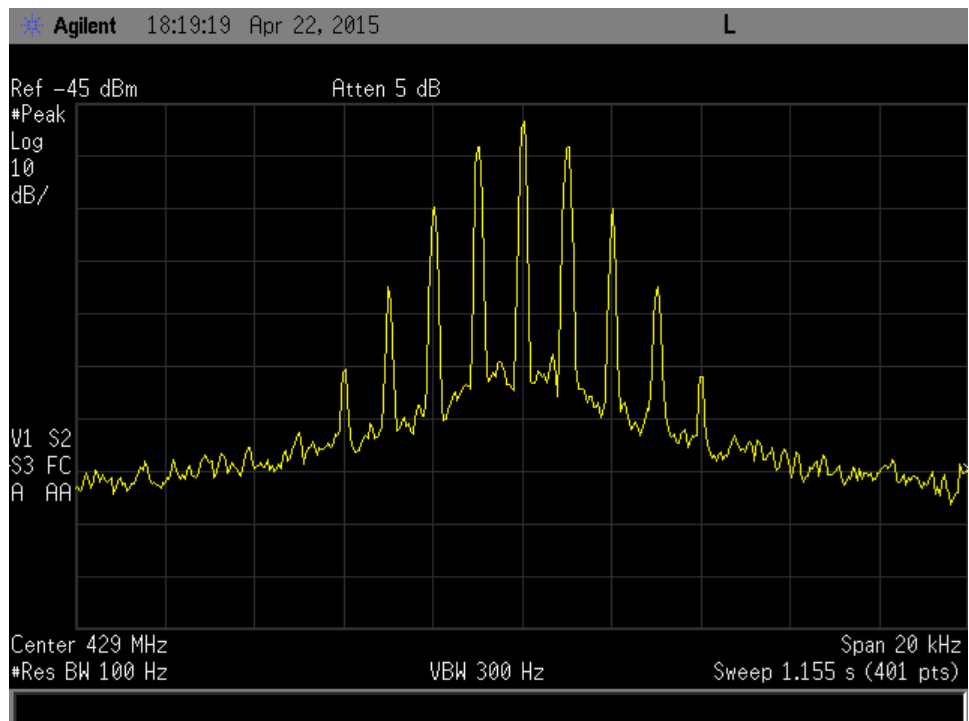




Mask E_420 MHz_Ref

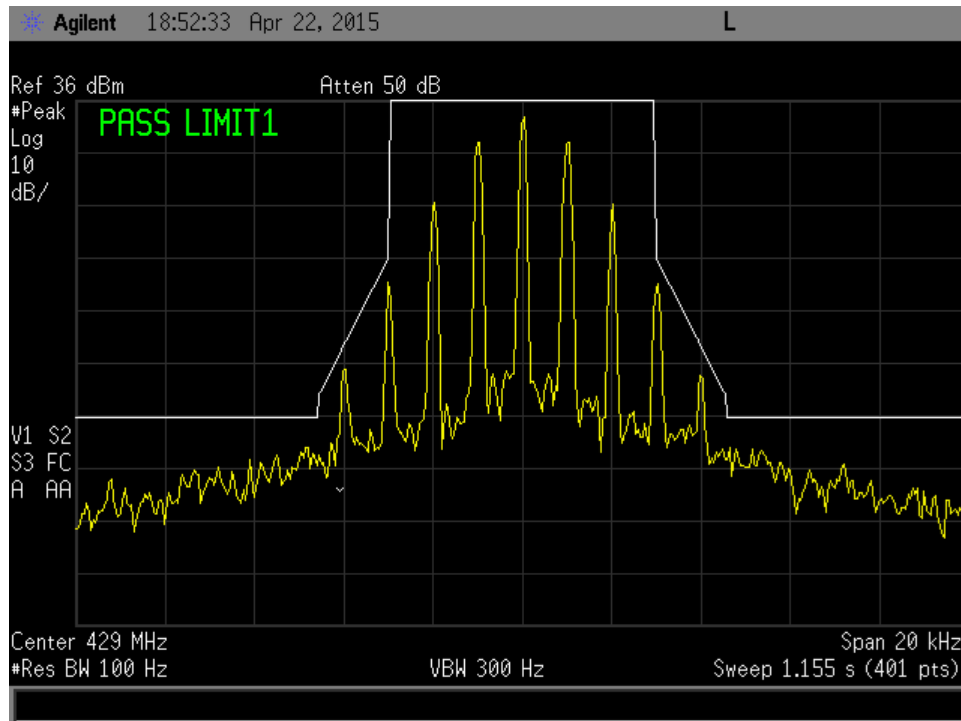


Mask E_429 MHz_Input

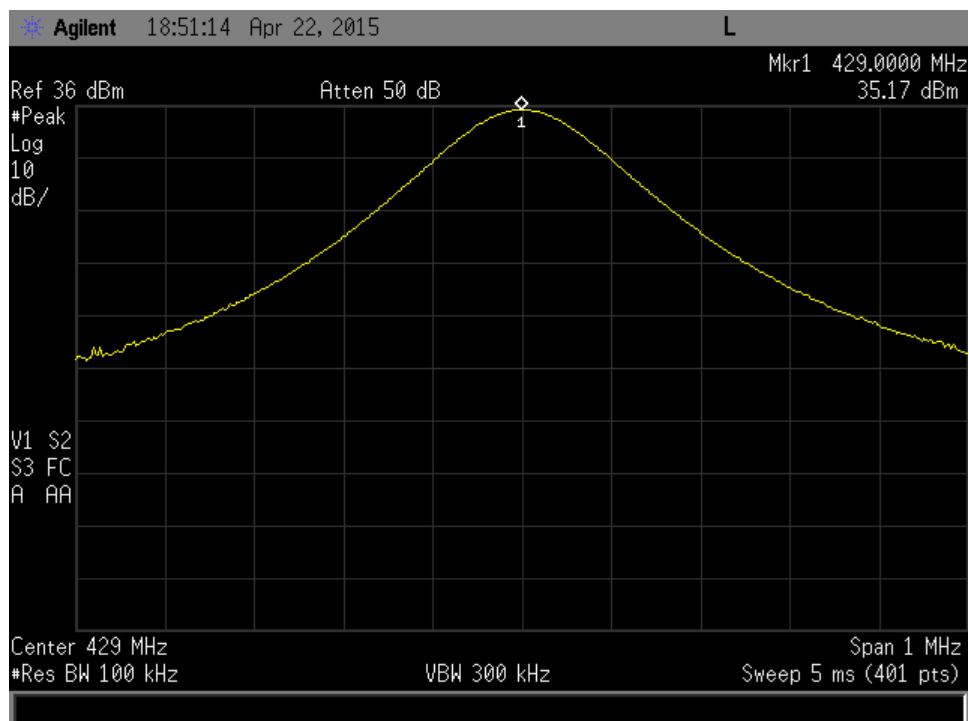




Mask E_429 MHz_Output

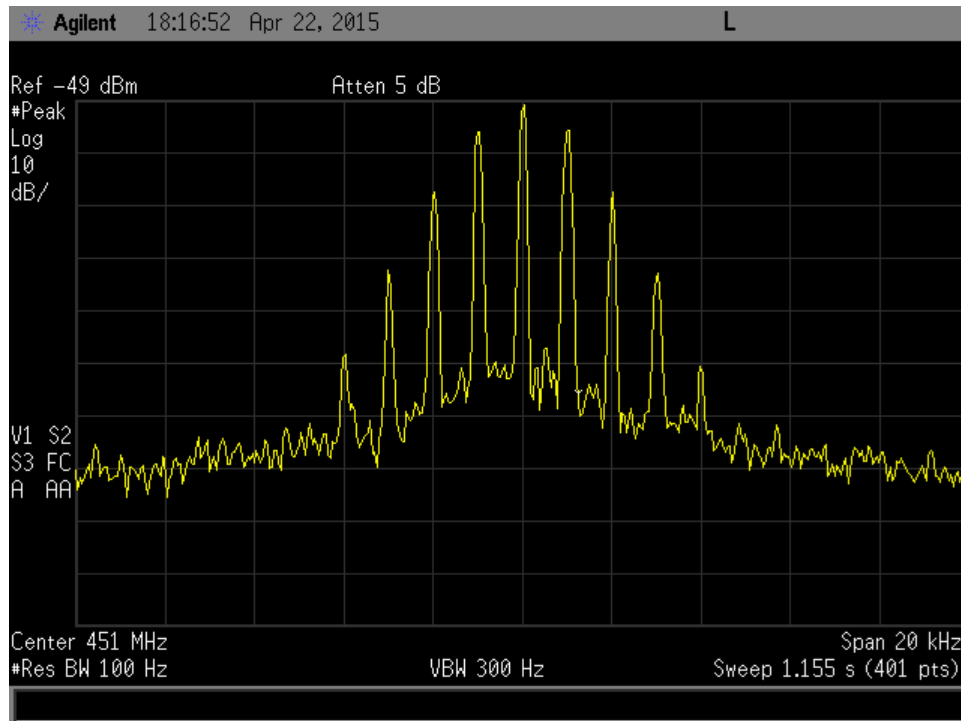


Mask E_429 MHz_Ref

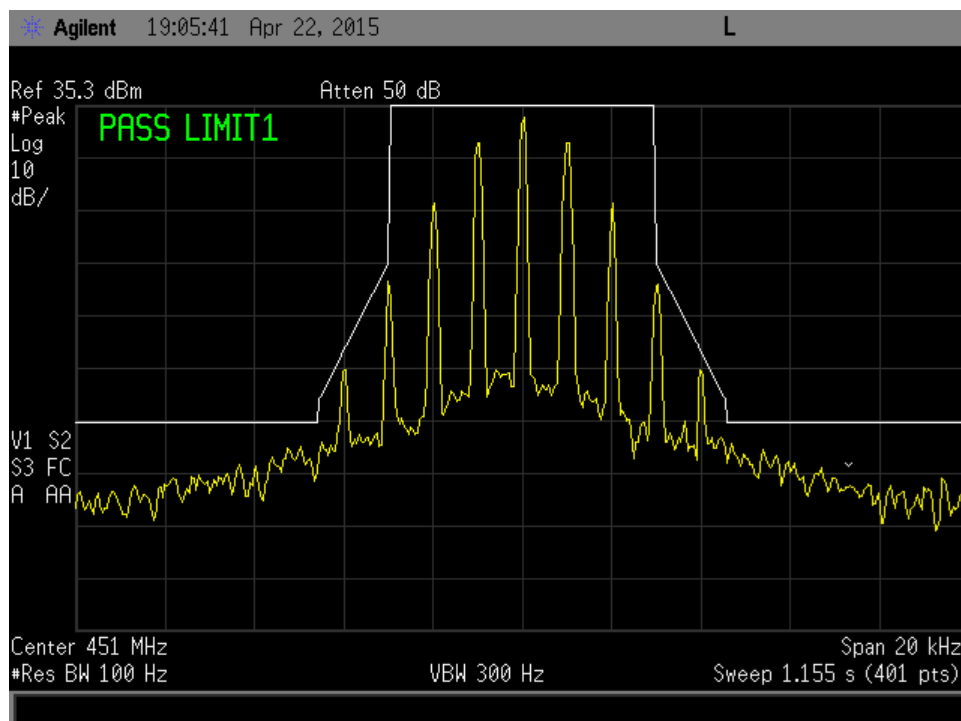




Mask E_451 MHz_Input

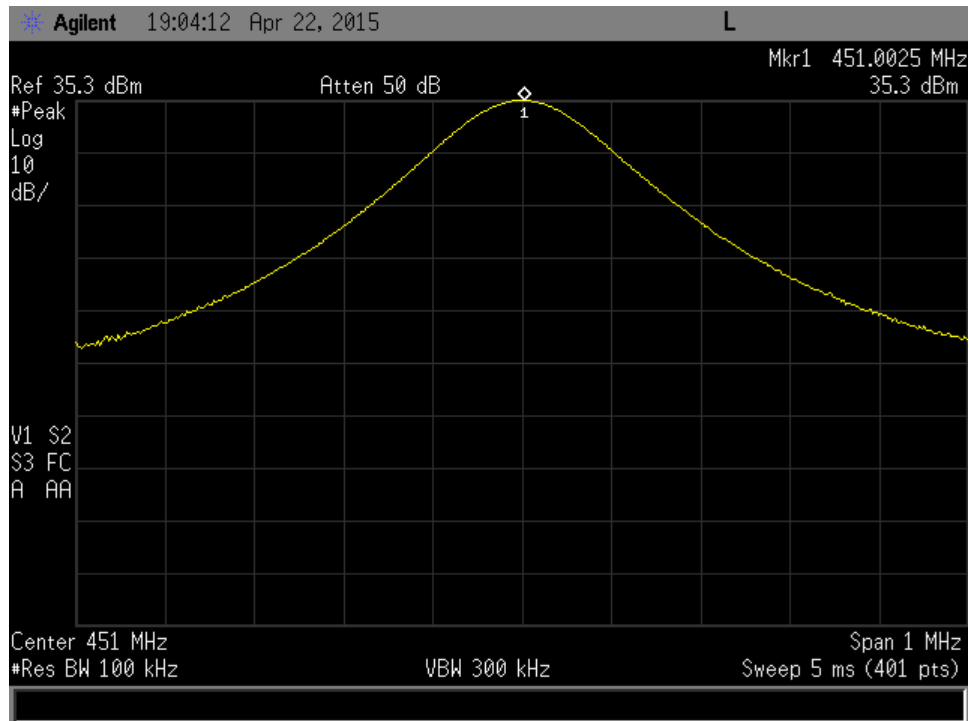


Mask E_451 MHz_Output

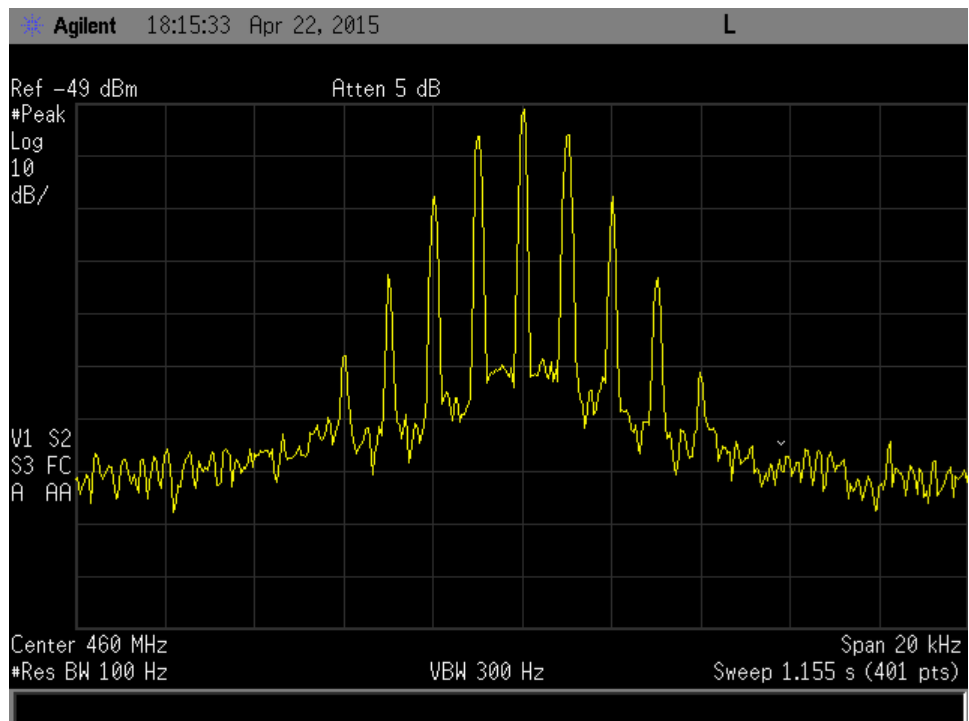




Mask E_451 MHz_Ref

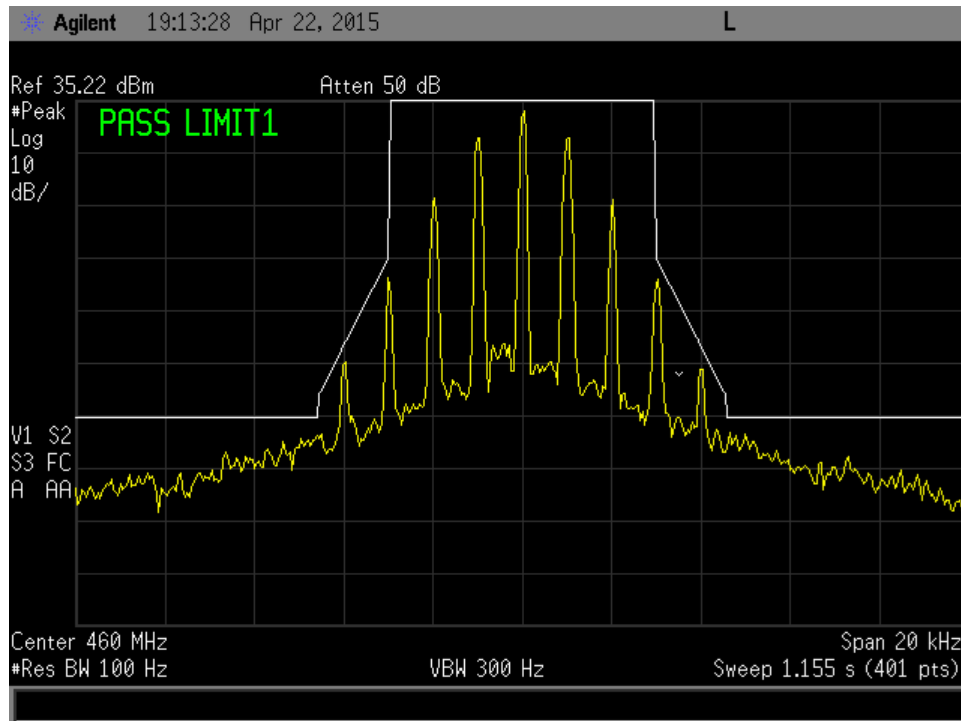


Mask E_460 MHz_Input

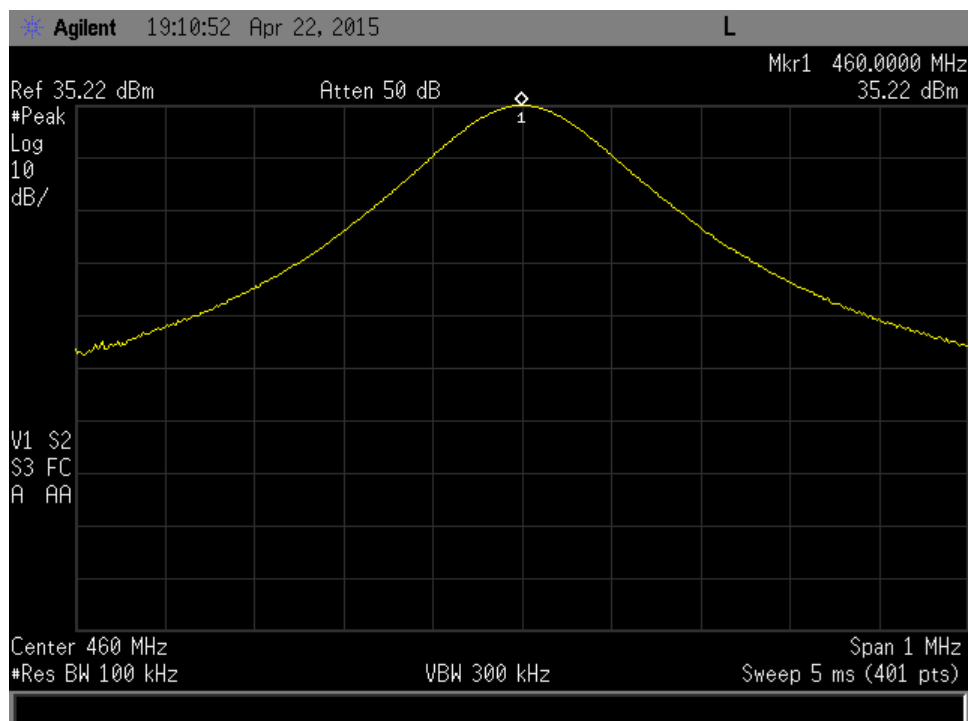




Mask E_460 MHz_Output

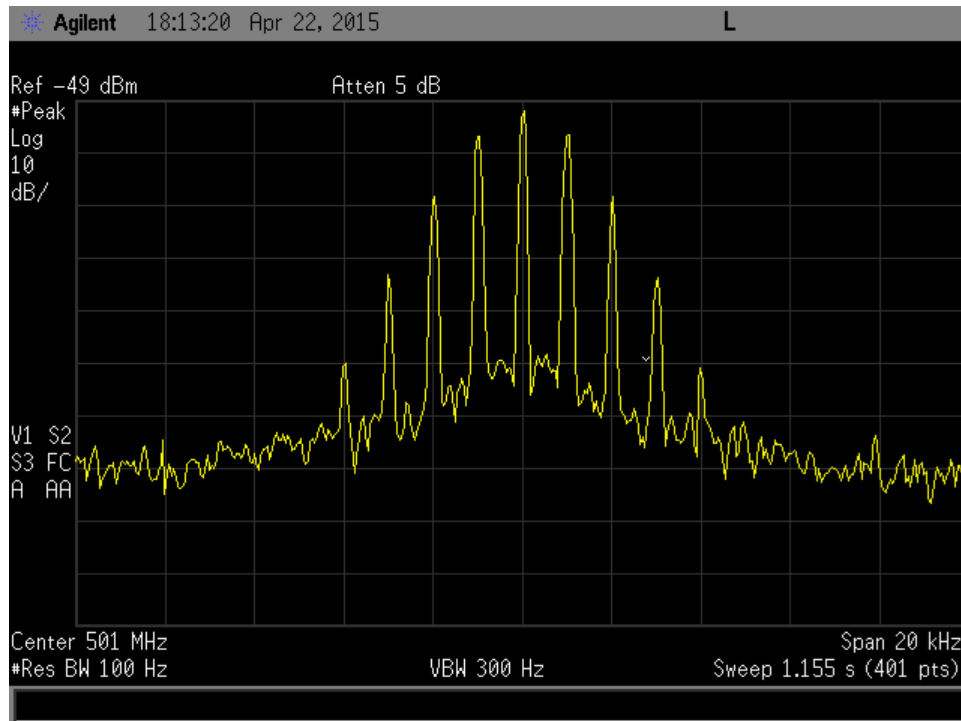


Mask E_460 MHz_Ref

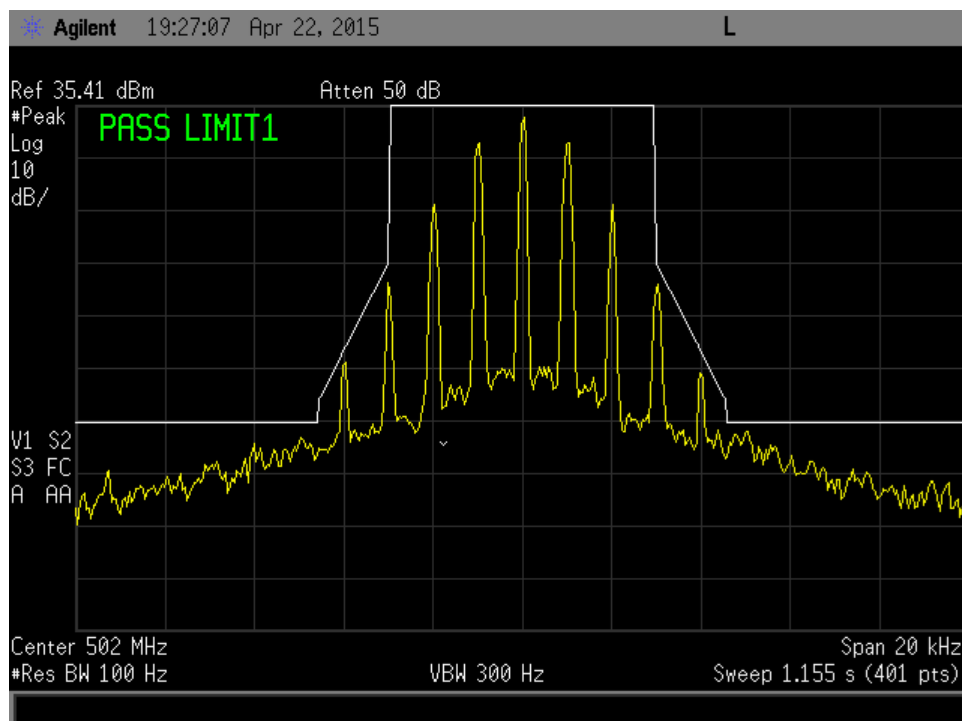




Mask E_501 MHz_Input

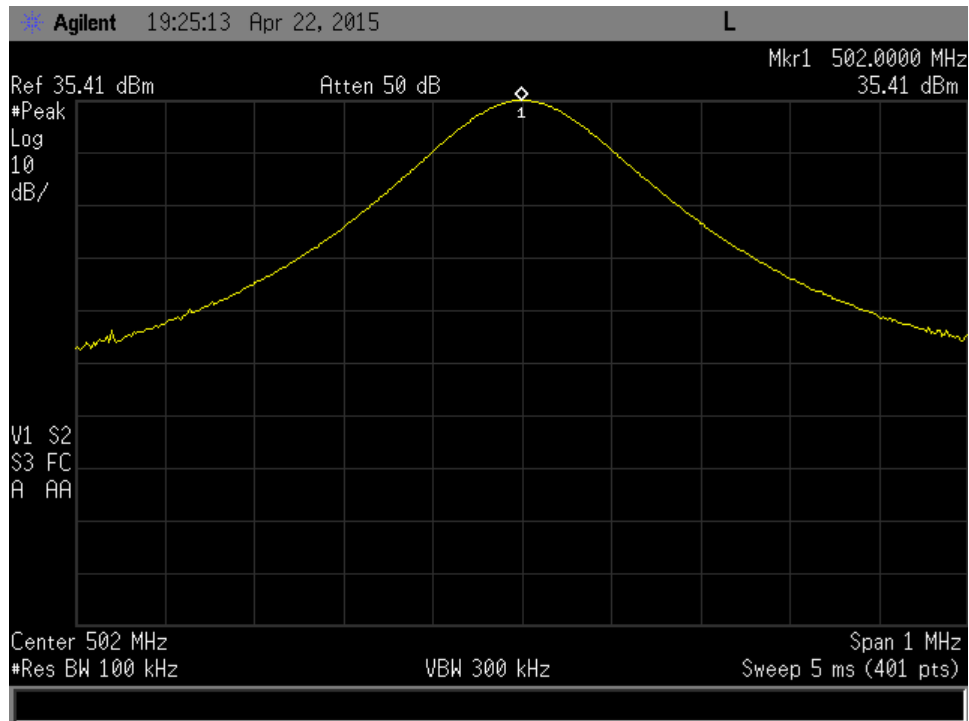


Mask E_501 MHz_Output

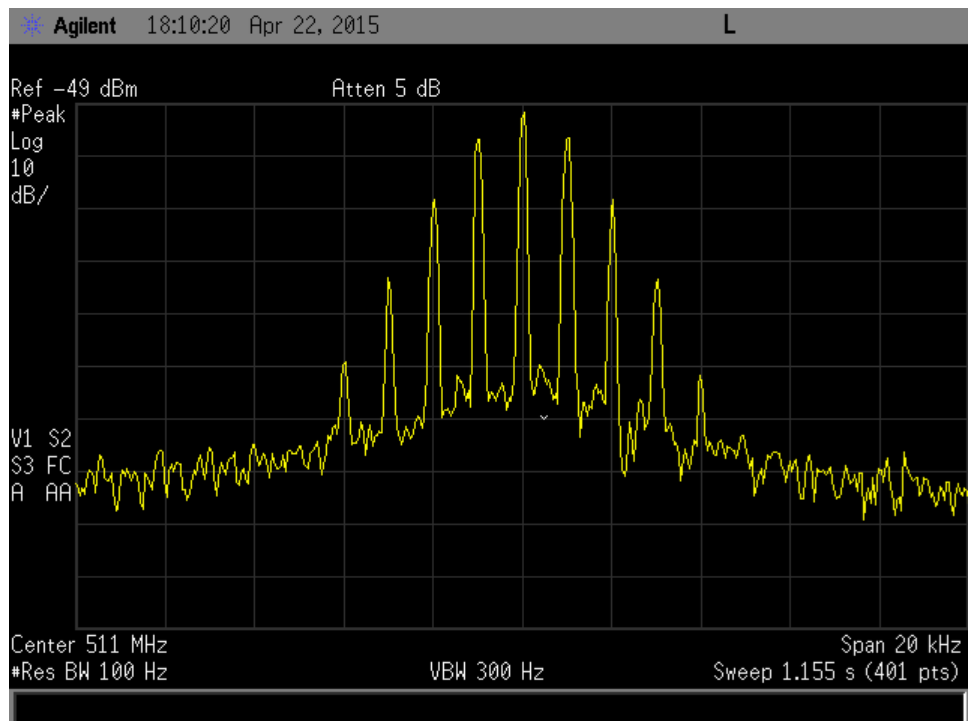




Mask E_501 MHz_Ref

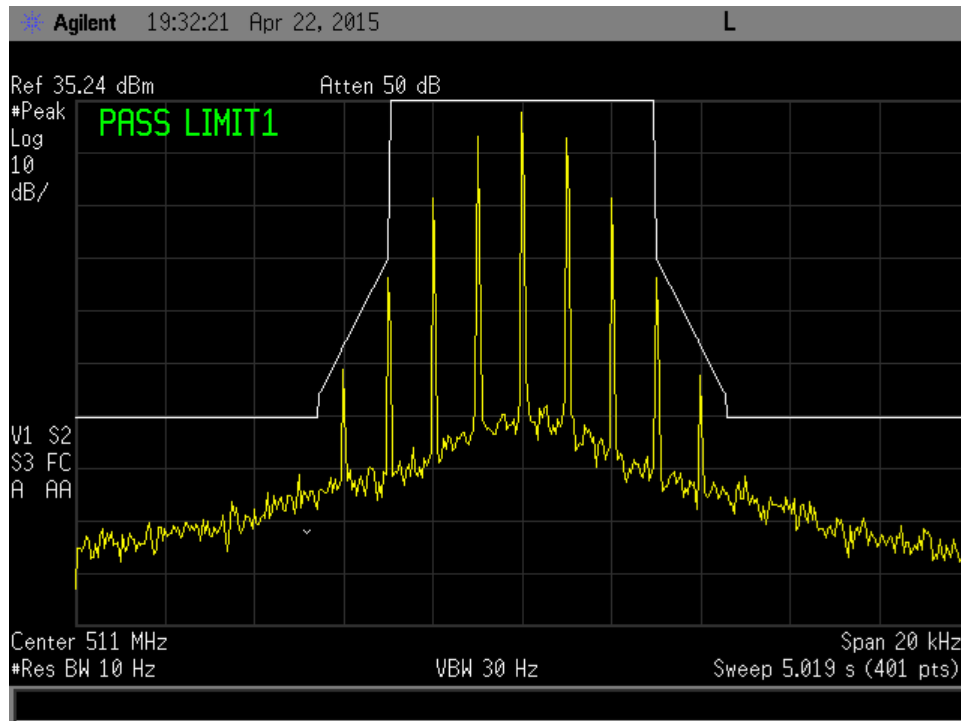


Mask E_511 MHz_Input





Mask E_511 MHz_Output



Mask E_511 MHz_Ref

