

AlphaLAN-2000 P/D

Technical Specifications

Processor

AMD80188-20 (Embedded)

MAC (Media Access Control)

AMD79C930

Core Logic (DSP)

Harris 3824 / Harris 3724

Memory

32KB SRAM

System BIOS

128KB Flash ROM

Protocol

IEEE 802.11 Compliant.
Media Access Protocol: CSMA / CA

Modulation

Direct Sequence Spread Spectrum

Frequency Band

2.4 Ghz (ISM band)

Band Width

2.4 12 Ghz ~ 2.484 Ghz (ISM band)
12 Channel

DSSS PHY Frequency Channel Plan

FCC Channel: 2.412Ghz ~ 2.462Ghz (11 Channels)
ETSI Channel: 2.422Ghz ~ 2.462Ghz(9 Channels)
MKK(Japan): 2.484Ghz (1 Channel)

Sensitivity

-90 dBm (+5 dBm, -5 dBm) @ 8E-2 FER

Power TX

17 dBm (+3 dBm, -3dBm)
100 mW (Max.)

TX/RX Range

Up to 350 meter (in free space)
2.5 Km (by high Gain Antenna upon the tower of building)

Storage

Data Rate: 2 Mbps
Through Put: 1.48 Mbps

PCMCIA

Type II
Support Dipole Antenna
Support Patch Antenna
Power / Link / Transmission LED Signal



AlphaLAN-2000 P

ISA Board (OEM Base)

Daughter Board optional for POS device.
PC card optional for Desk top computer.
Sample Delivery since contract allowed by 2 months.

Power Consumption

Stand by: 178 mA
RX : 262 mA
TX : 350 mA
(O/P 50 mW)

Safety

FCC / MKK / ETSI / CNS (Optional)

Access Point

Support for IEEE 802.11 PS-poll operation.
Frame Forwarding for Wireless and Ethernet Environment.
Frame Filtering.
Station Initiated Roaming.
Seamless Roaming.
Configurable ESSID and Radio Channel.
Web Browser Management Reporting.
Command line Management.
Trivial File Transfer Protocol.
Ping Function.
User: 32 Persons for @ Channel.
Support Net PC Hard ware (Core Logic / IBM PC compatible).
Flash ROM for kernel optional
16 MB Memory
PS/2 Mouse Port.
PS/2 Keyboard Port.
VGA Display.
IDE BUS for Hard Disk.
PCMCIA Slot X 2.

