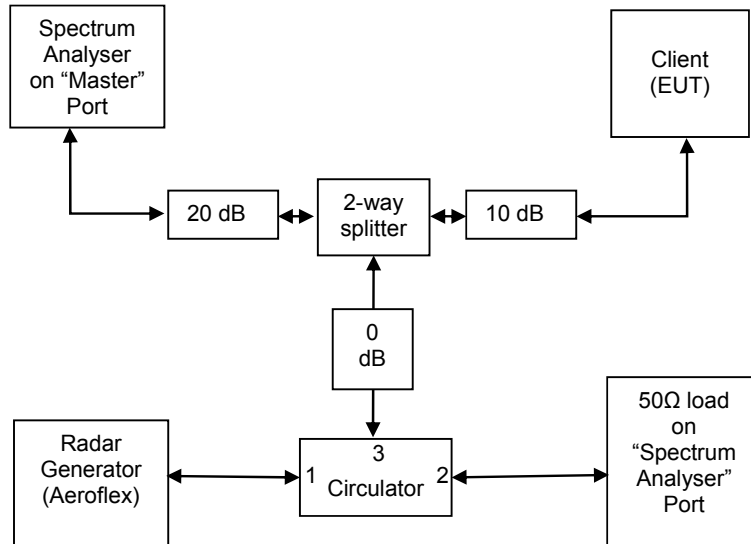


Appendix 4. Radar Calibration and Verification Data

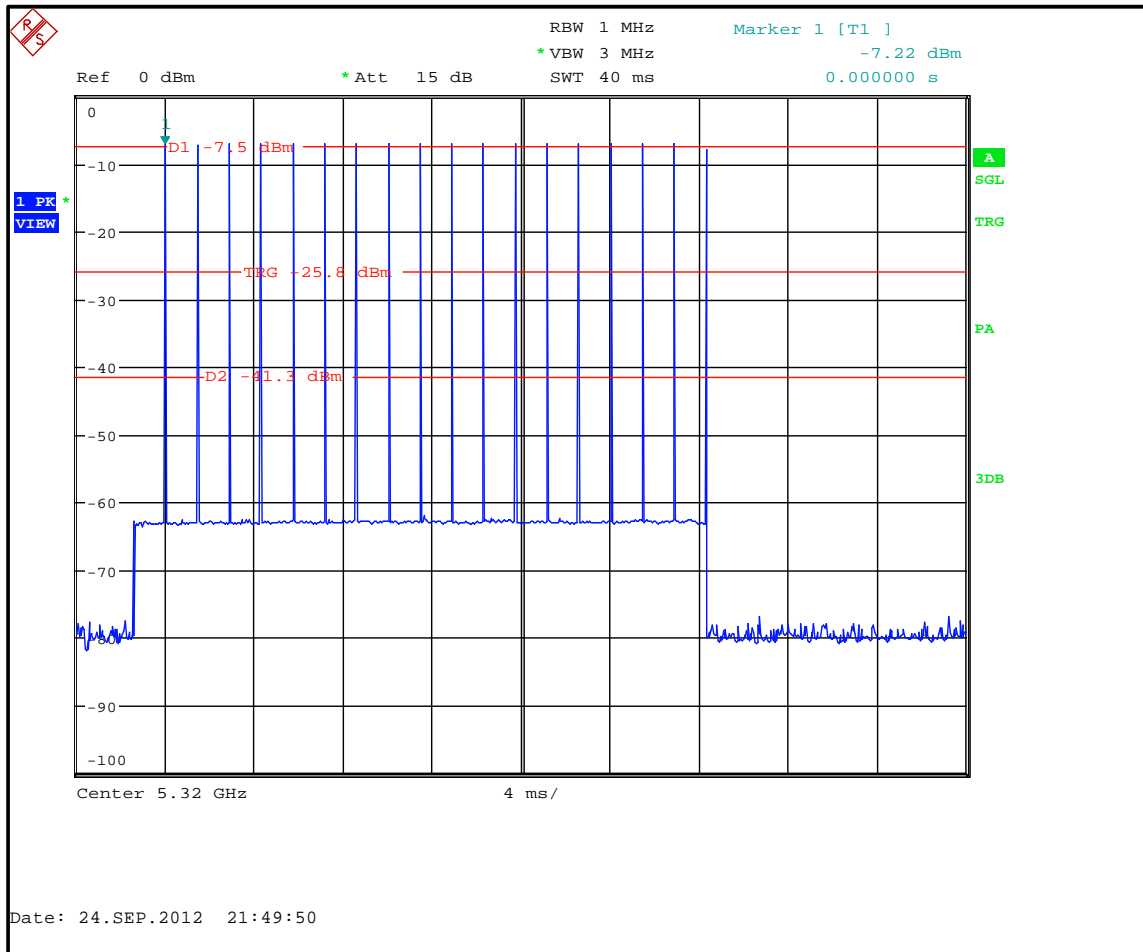
Radar calibration procedure.

The system was configured as shown in the diagram below, with all ports terminated into their respective loads. The EUT was substituted with the spectrum analyser and the output level was adjusted so that the required DFS threshold was observed. The EUT was then re-connected.

Radar Calibration Setup Block Diagram



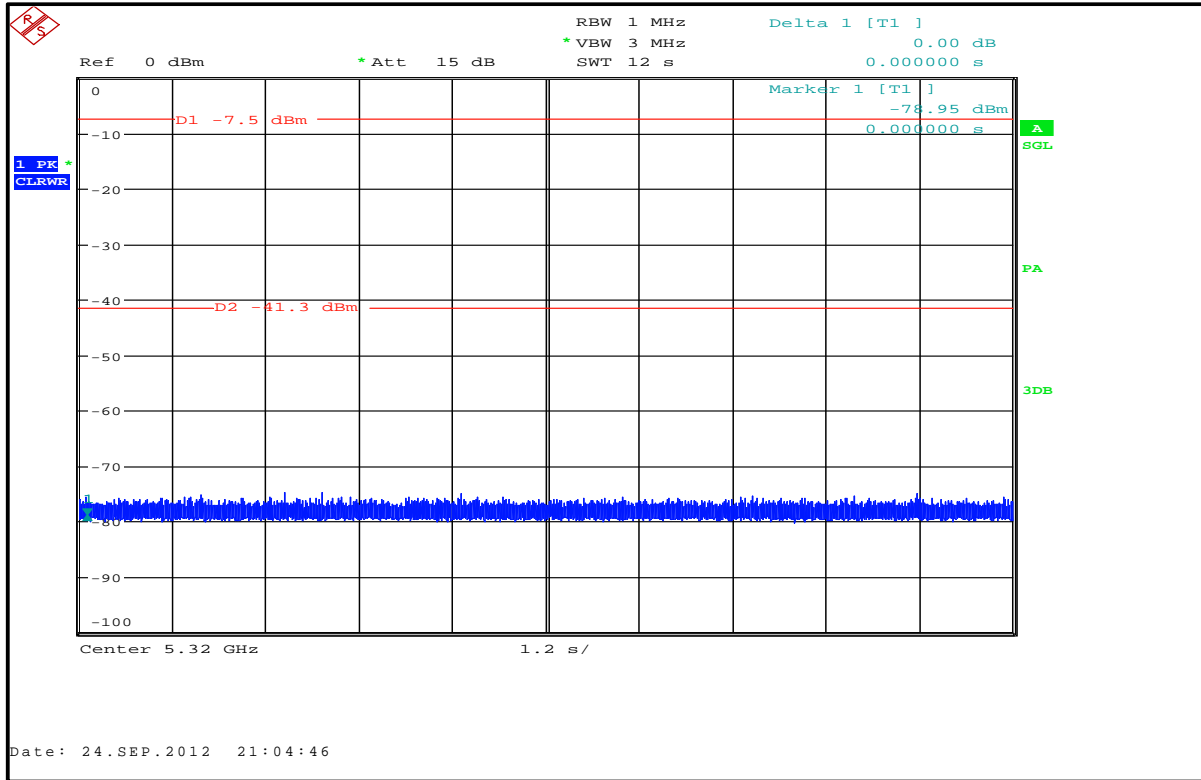
Radar Type 1 Verification



Radar Pulse

Appendix 5. System Noise Floor Reference Plots

As required by Section 8.3.18(iii) of FCC 06-96, the following plots show the reference noise floor of the system used during measurement.



Noise Floor of Spectrum Analyser.