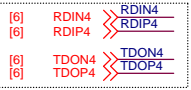
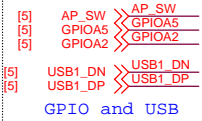


10/100M PHY Interface

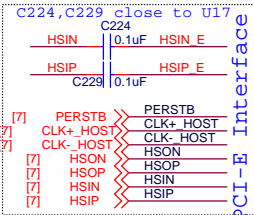


Configure Interface



GPIO and USB

LED Interface

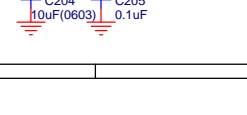
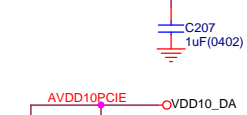
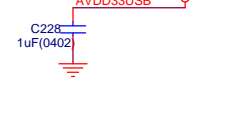
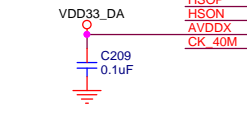
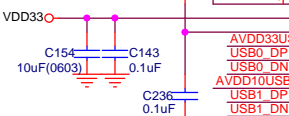
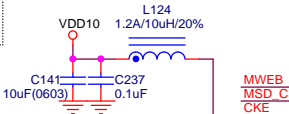
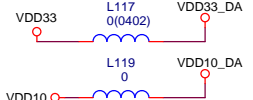
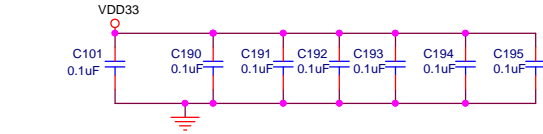


PCI-E Interface

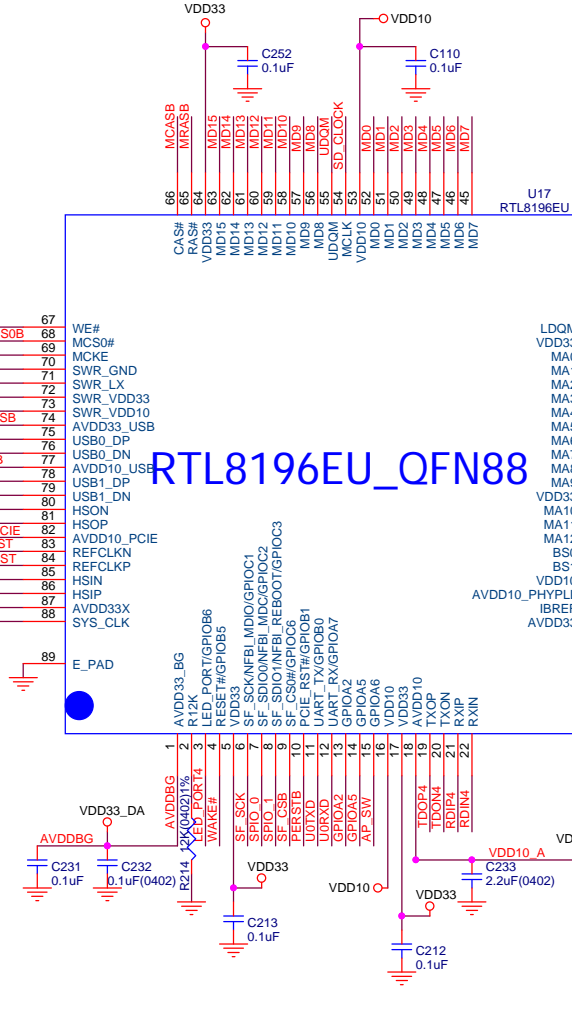
CK_40M

WAKE# Input

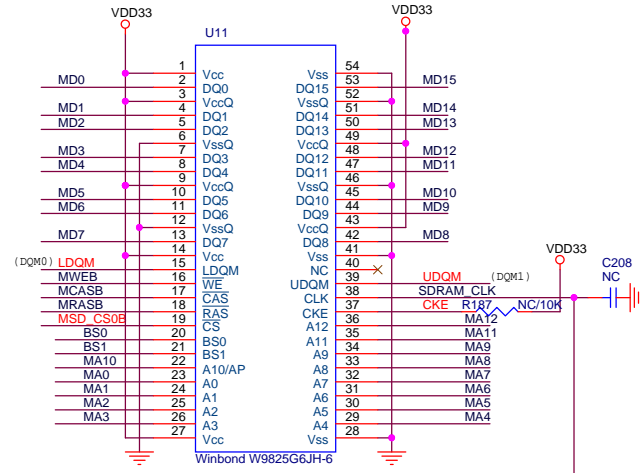
VDD33 Capacitors For SDRAM



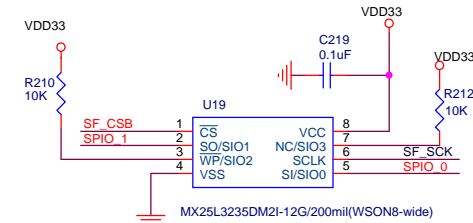
RTL8196EU_QFN88



16M 16bit SDRAM xl



R201 49.9 ohms for lowering EMI



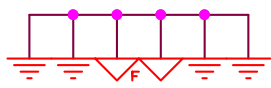
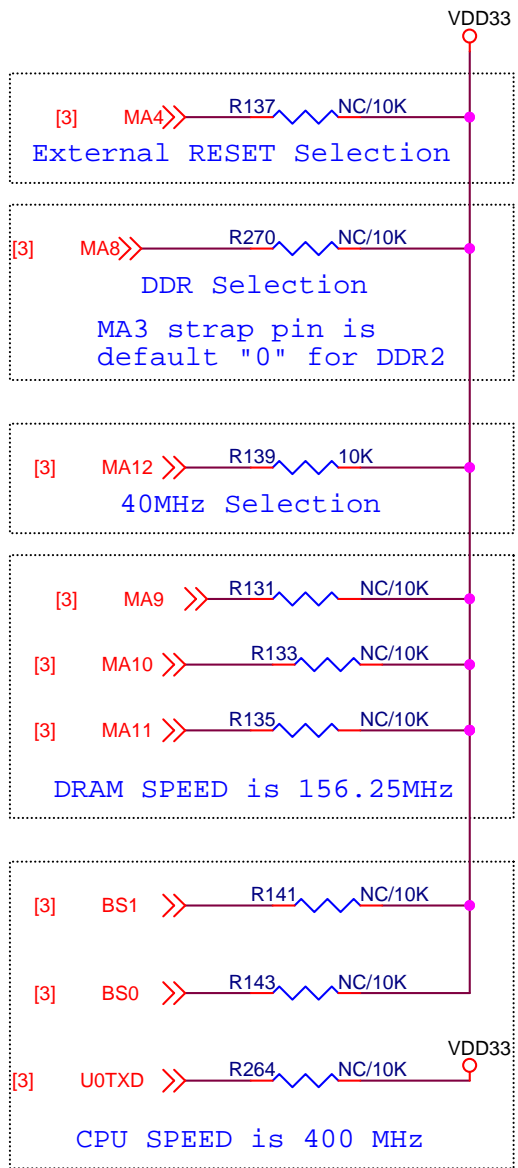
8 pins SPI Flash Only for Single I/O or Dual I/O

UART

NC/HEADER 4X1 (pitch=1.27mm)

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Strap Pin Definition



pin name	function name	function detail
PIN_M_A4	ENABLE_EXT_RSTN	ENABLE_EXT_RSTN 1=enable 0=disable
PIN_M_A8	DRAM_TYPE[1]	DRAM_TYPE[1] 1=DDR 0=SDR (Default)
PIN_M_A3	DRAM_TYPE[0]_INV	DRAM_TYPE[0]_INV 1=DDR1 0=DDR2 (Default)
PIN_M_A12	sel_40m	1=40MHZ 0=25MHZ(default)

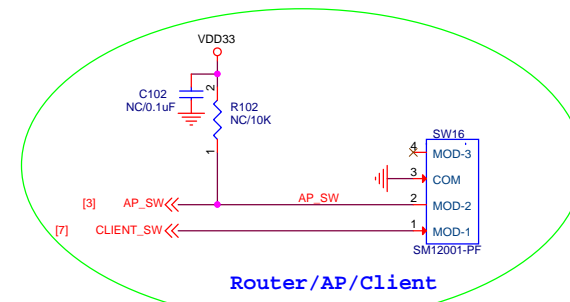
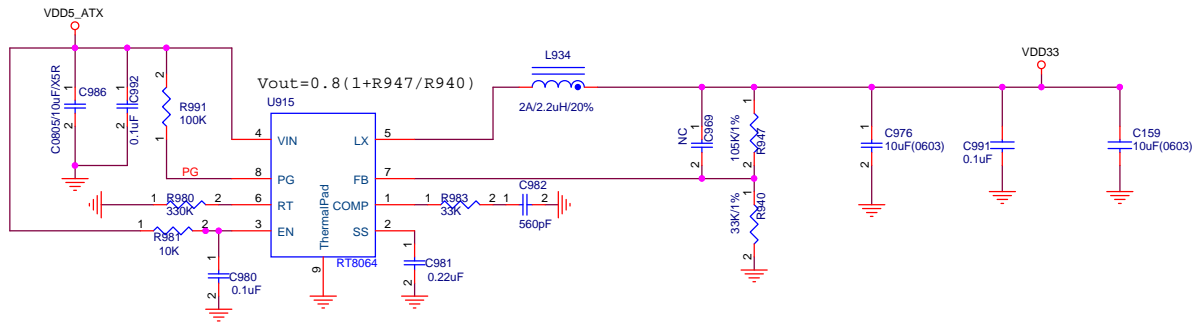
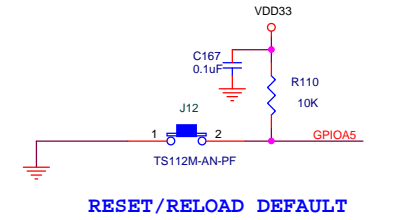
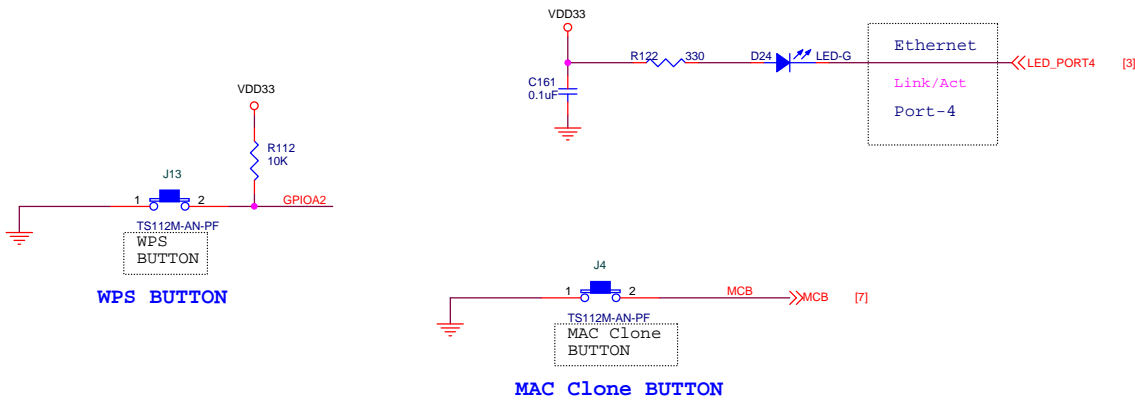
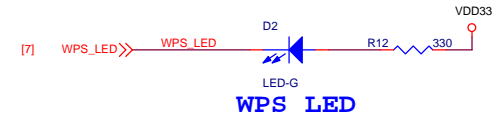
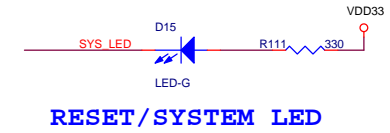
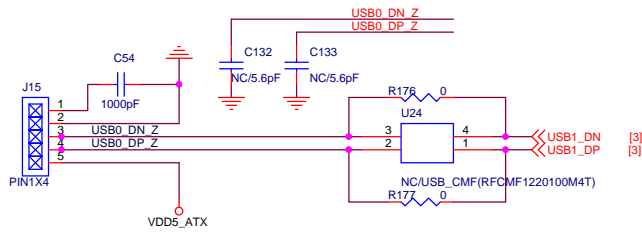
		ck_m2x_freq_sel[2:0] (MHZ)	
PIN_M_A9	ck_m2x_freq_sel[0]	000: 156.25	100: Reserved
PIN_M_A10	ck_m2x_freq_sel[1]	001: Reserved	101: 125
PIN_M_A11	ck_m2x_freq_sel[2]	010: Reserved	110: Reserved
		011: Reserved	111: 168.75

		ck_cpu_freq_sel[3:1] (MHZ)	
DDR_BS1	ck_cpu_freq_sel[3]	000: 400MHz	100: 320MHz
DDR_BS0	ck_cpu_freq_sel[2]	001: 380MHz	101: 300MHz
U0TXD	ck_cpu_freq_sel[1]	010: 360MHz	110: 280MHz
		011: 340MHz	111: 260MHz

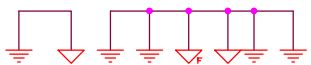
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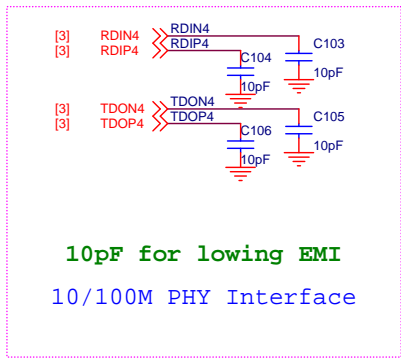
GPIO
LED/Push Button
Interface

[3] GPIOA5 <-> SYS_LED
[7] SYS_LED <-> SYS_LED
[3] GPIOA2 <-> GPIOA2

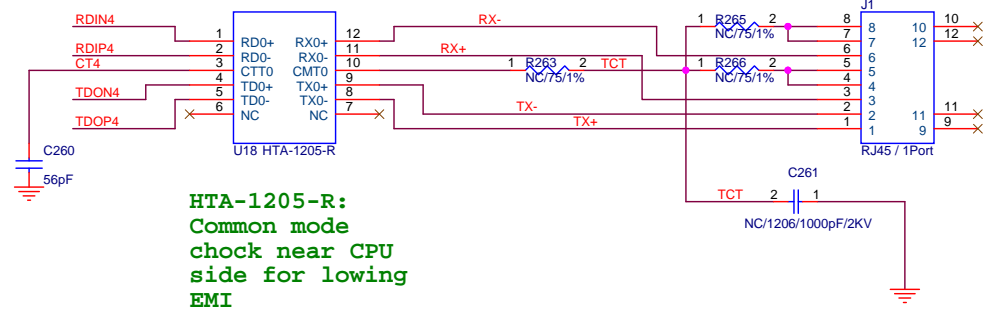


	Router	AP	Client
AP_SW	1	0	1
CLIENT_SW	1	1	0

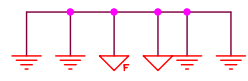




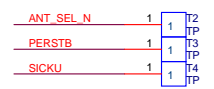
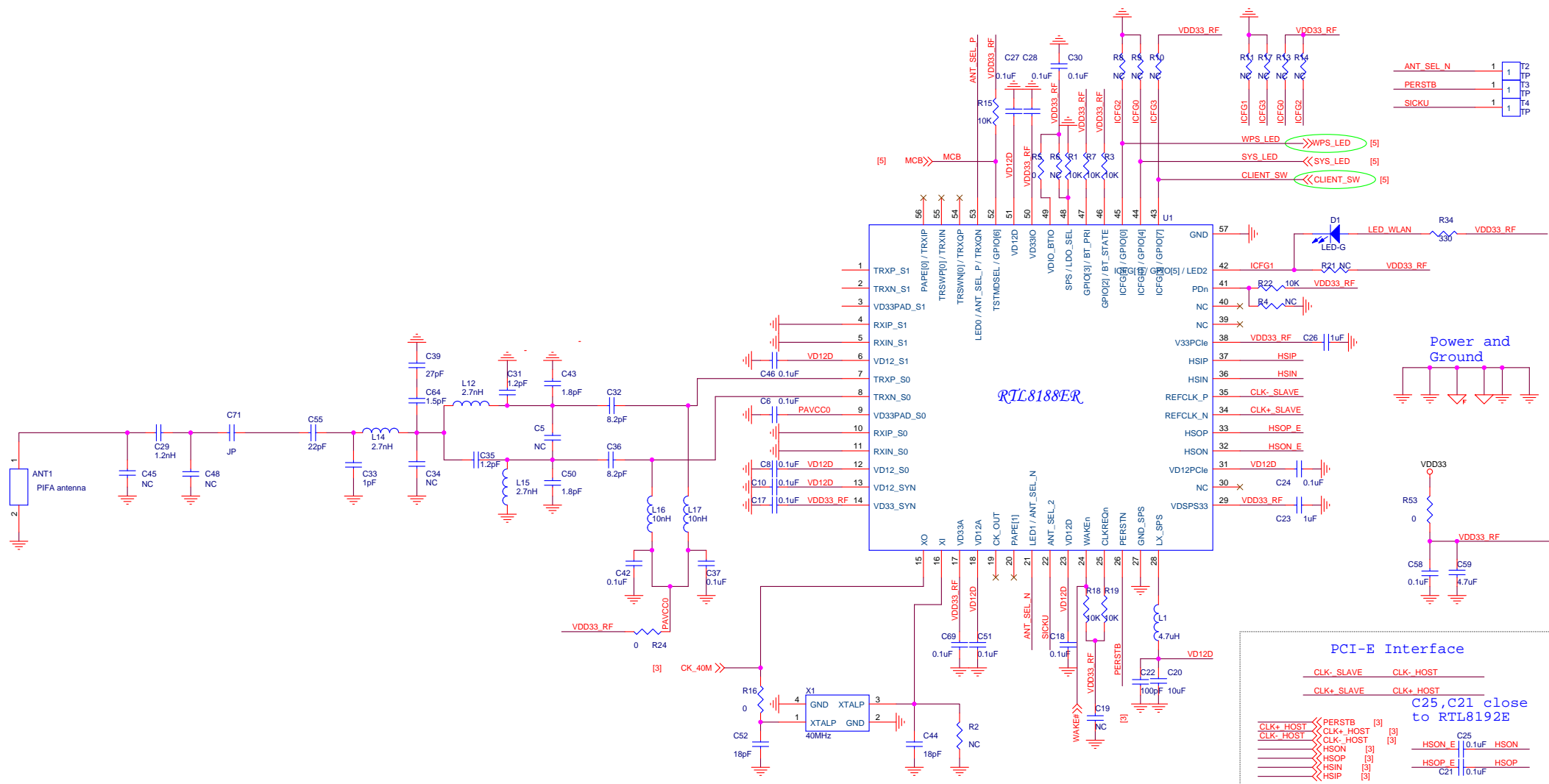
Ethernet Transformer



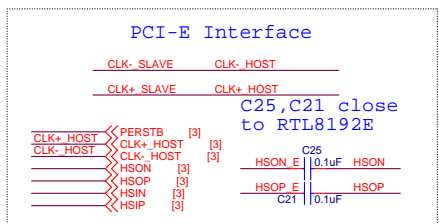
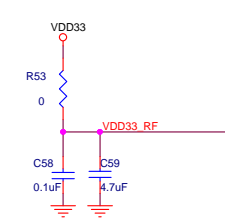
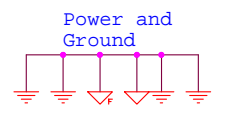
HTA-1205-R:
Common mode
check near CPU
side for lowering
EMI



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WPS_LED [5]
SYS_LED [5]
CLIENT_SW [5]



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