OPERATIONAL DESCRIPTION

APPLICANT: Ambitio LLC, The Owner of unnecto ™

Manufacturer: Shenzhen Xiangyue Perfect Digital Science & Technology Co., Ltd.

FCC ID: ZU3UNNECTOECO2

Instruction Book:

Users Manual

Frequency Range

GSM

Transmit Frequency Band:	824.2-848.8 MHz
Receive Frequency Band:	849.2-893.8 MHz
Transmit Frequency Band:	1850.2-1909.8 MHz
Receive Frequency Band:	1930.2-1989.8 MHz

Specific Operating Power Range:

+30dBm +2.0dB/-2.0dB
+28dBm +3.0dB/-3.0dB
+26dBm +3.0dB/-3.0dB
+24dBm +3.0dB/-3.0dB
+22dBm +3.0dB/-3.0dB
+20dBm +3.0dB/-3.0dB
+18dBm +3.0dB/-3.0dB
+16dBm +3.0dB/-3.0dB
+14dBm +3.0dB/-3.0dB
+12dBm +4.0dB/-4.0dB
+10dBm +4.0dB/-4.0dB
+8dBm +4.0dB/-4.0dB
+6dBm +4.0dB/-4.0dB
+4dBm +4.0dB/-4.0dB
+2dBm +5.0dB/-5.0dB
+0dBm +5.0dB/-5.0dB
-3.7dB
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DC Voltage and Current into the Final Amplifier Module:

Supply Voltage	= 5.0Vdc
Supply Current	= ~0.5Amp.

Tune-up Procedure:

Tune-up Procedure

Equipment Identification:

Equipment's Identification label and its intended Location are as shown in EXHIBIT Type "ID Label / Location Information" (FCC ID Nameplate), and in EXHIBIT Type "Internal Photo" (Photograph of inside)

Photographs:

A complete set of the Photographs showing External and Internal Views of Circuit Details and Construction are provided by from EXHIB IT Type "External Photos" and "Internal Photos".

Accessories:

AC Charger	Battery

Figure 1.2: Terminal Components (Accessories)

Technical Descriptions

TX synthesizer uses an Offset-PLL circuit. The Offset-PLL circuit consists of PLL-IC and TX_VCO and loop filter. Reference frequency of the above synthes izers is 26MHz and generated by VCTCXO. Control signal is sent from Baseband section and keeps frequency stability.

TX IQ signal is input from Baseband secti on to MT6235, The Bluetooth signal is input form MT6601 and FM signal is input form TLG1120 section to MT6235.

TX IQ signal is applied to the quadrature up-converter, and the up-converter LO signal is generated from the transceiver VCO within MT6139B. This up-converter translates the GMSK-modulated signal to an intermediate fr equency (IF) that forms one input to a frequency/phase detector circ uit. This IF signal is the reference input to an Offset-PLL circuit. The feedback path of this Offset-PLL circuit includes a down-conversion from the RF output frequency range to the IF range. The two inputs to this down-conversion mixer are formed the TX_VCO output and the LO generation circuits. The mixer output is the offset feedback signal that forms the variable input to a frequency/ phase detector circuit. The detector compares its variable input to its reference input and generates an error signal that is low-pass filtered by the loop f ilter and applied to TX_VCO to force the RF frequency in the direction that minimizes errors.

The TX_VCO output passes PA, Antenna SW, Coaxial connector with switch and emitted from embedded antenna.

Digital Modulation:

Analog speech produced by a microphone is encoded to digital PCM samples using a CODEC. The PCM samples are passed to a Vocoder for AMR (Advanced Multi Rate) encoding, to compress the speech samples. The encoding rate is determined by the Vocoder which formats the speech samples as data packets. A new data packet with data rate information is read by the microprocessor every 20 ms. The speech CODEC becomes AMR, FR(Full Rate), EFR(Enhanced FR) and HR(Half Rate). The microprocessor then sends the data packet to the transmit subsystem, and passed DAC to covert analog signal. I/Q base band signal is filtered and is input tted into 6M4003 containing quadrature phase modulator.

2. Bluetooth INFORMATION

2.1 Product Information

GSM Mobile Phone
unnecto ™
U-100-2
N/A
N/A
DC 5V by AC/DC adapter 100-240V~50/60Hz DC 3.7V by battery
2402MHz -2480MHz
FHSS
Internal Fixed
1MHz
79(CH Low: 2402MHz, CH Mid: 2441MHz, CH High: 2480MHz)
-20°C ~ 50°C

NOTE:

1. Please refer to Appendix I for the photographs of the EUT. For a more detailed features description about the EUT, please refer to User's Manual.