

Circuit Description

RF (Radio Frequency) Part:

1. The signal from the ANT is selected by F1 and amplified by Q1, then is sent to the mixer IC1.
2. The PLL circuit, comprised of Q3, Q4 and IC4, produces the first local frequency. After amplified by the Q5, the local frequency is sent to mixer IC1 to produce the 56MHz IF.
3. After selected by F2 and amplified by Q2, the mixing signal is sent to IC2 to demodulate
4. The demodulated signal is amplified by IC8A and then sent to IC7 for stereo decoding.

AF (Audio Frequency) Part:

1. The decoded "L" and "R" signal is filtered and expander amplified by the circuit comprised of IC1, IC2, IC3 and IC4.
2. The processed signal is sent to the mono and stereo switcher K1, and then amplified by IC5 to drive the earphone.

Utilized OSC

IC2 utilizes 45.3MHz OSC

IC3 utilizes 8MHz OSC

IC4 utilizes 24MHz OSC