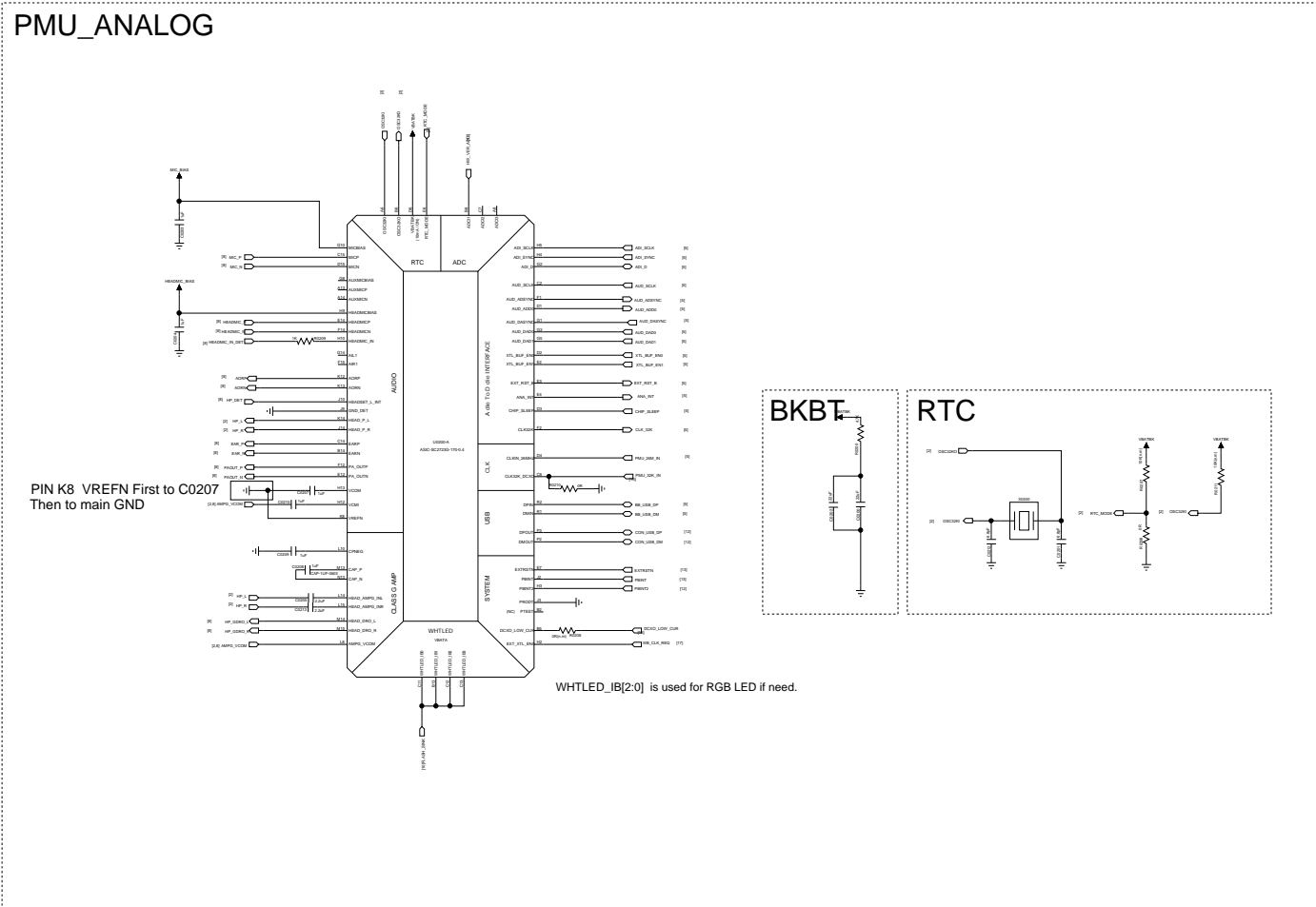
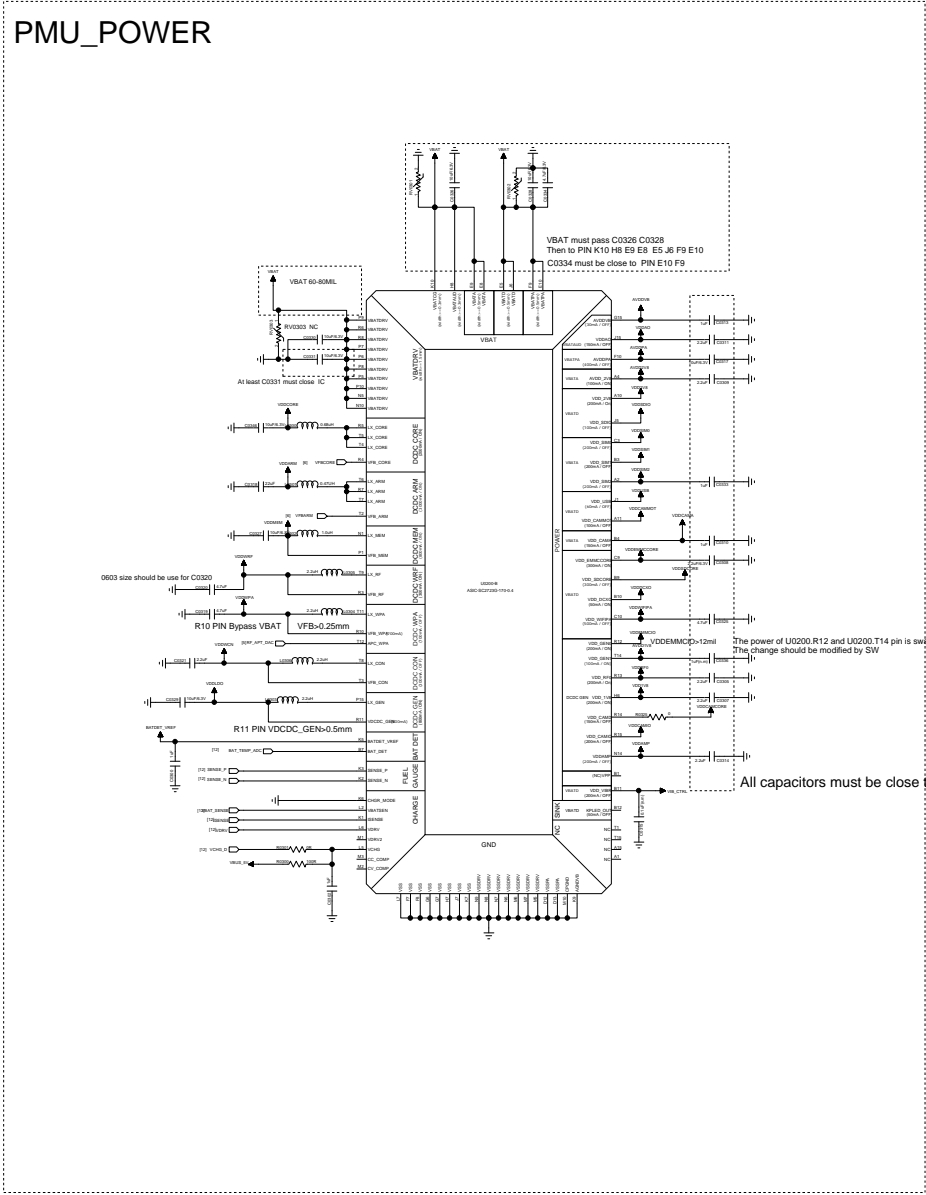


REVISION RECORD			
DATE	REV. NO.	DESCRIPTION	DATE



TITLE & REV:			
SP9832A-2_CS_SCH_V0.1.0			
DESIGNER:			
River Du	DATE:	2015-10-12	
DRAWN:			
Mulder Han	DATE:	2015-10-12	
CHECKED:			
Gang Xu	DATE:	2015-10-12	
APPROVED:			
Spreadtrum communications, Inc.			
COMPONENTS AND INTERFACES:			

REVISION RECORD			
REV.	DESCRIPTION	DATE	BY

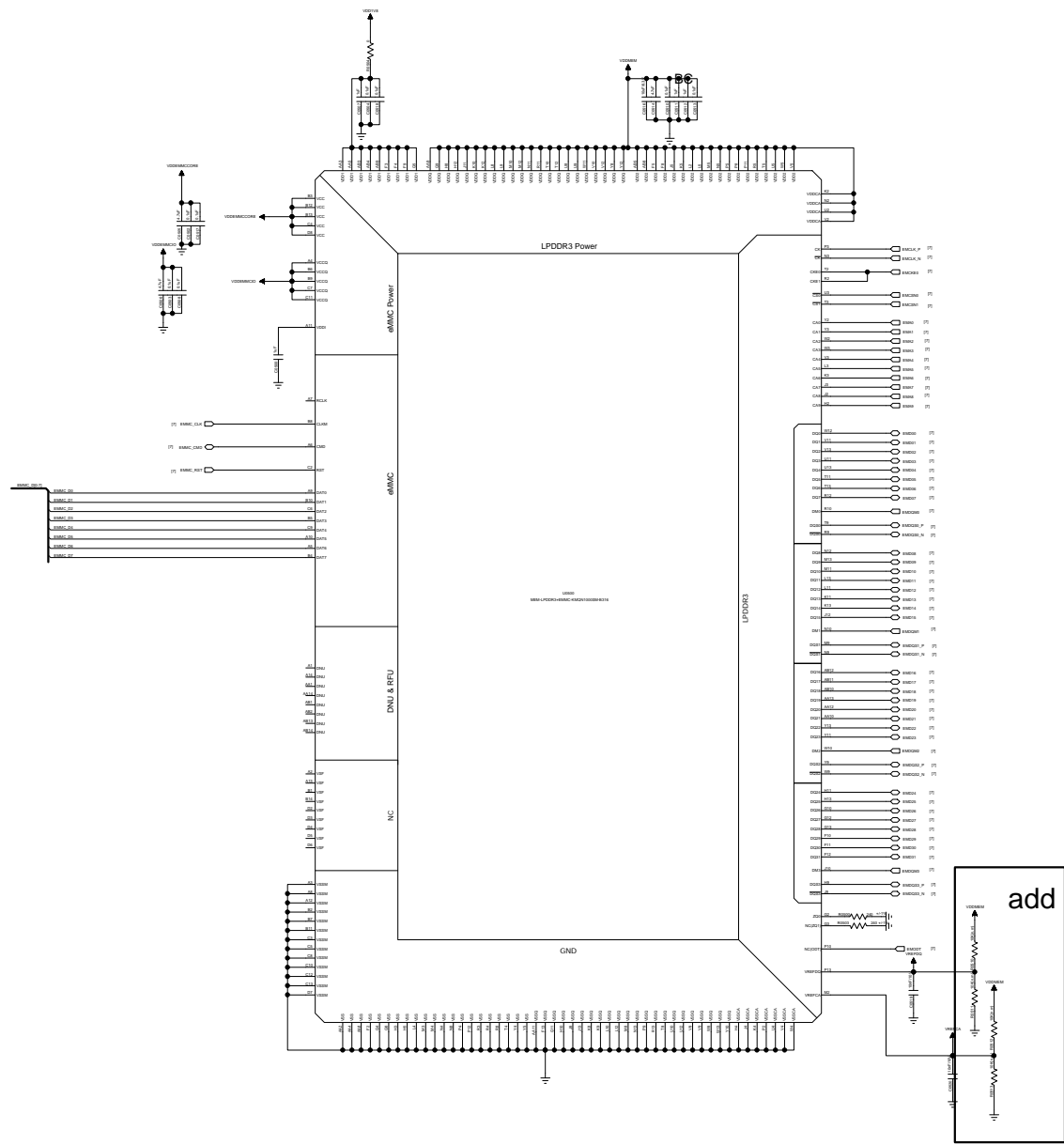


### SC2723 LDO ouput

Power	Output Voltage (V)	Output Current	Default Voltage	Default
VDD1V8	1.2-1.8	200mA	1.8V	ON
AVDD28	1.8-3.3	100mA	2.8V	ON
VDD2V8	1.8-3.3	200mA	2.8V	ON
VDDSIM0	1.8-3.3	200mA		OFF
VDDSIM1	1.8-3.3	200mA		OFF
VDDSIM2	1.8-3.3	200mA		OFF
VDDRF0	1.2-1.8	200mA	1.8V	ON
VDDCAMA	1.8-3.3	150mA		OFF
VDDCAMMOT	1.8-3.3	100mA		OFF
VDDMEMCCORE	1.8-3.3	300mA	3.0V	ON
VDDSDCORE	1.8-3.3	300mA		OFF
VDDSDIO	1.8-3.3	100mA		OFF
VBATBK	2.6/2.8/3.0/3.2	10mA	2.8V	ON
VDDVIB	1.8-3.3	200mA		OFF
KPLED_OUT	1.8-3.3	50mA		OFF
VDD_WIFIPA	1.8-3.3	500mA		OFF
VDD_DCXO	1.8-3.3	50mA	1.8V	ON
VDDCAMD	1.2-1.8	150mA		OFF
VDDUSB	1.8-3.3	60mA	3.3V	OFF
VDDAO	2.9/3.1/3.2/3.3	150mA		OFF
AVDDVB	2.9/3.1/3.2/3.3	30mA		OFF
AVDDPA	2.9/3.1/3.2/3.3	400mA		OFF
VDDCAMIO	1.2-1.8	200mA		OFF
AVDDAMP	1.2-1.8	200mA		OFF
VDDGEN0	1.2-1.8	200mA	1.8V	ON
VDDGEN1	1.2-1.8	100mA	1.8V	ON
DCDCARM	0.6-3.7	3000mA	0.9V	ON
DCDCCORE	0.6-3.7	2000mA	0.9V	ON
DCDCMEM	0.6-3.7	800mA	1.2V	ON
DCDC_GEN	0.6-3.7	800mA	2.2V	ON
DCDCRF	0.6-3.7	300mA	1.5V	ON
DCDCWPA	0.6-3.7	700mA		OFF
DCDCCON	0.6-3.7	300mA		OFF

REVISION RECORD			
VER.	REV. NO.	DESCRIPTION	DATE

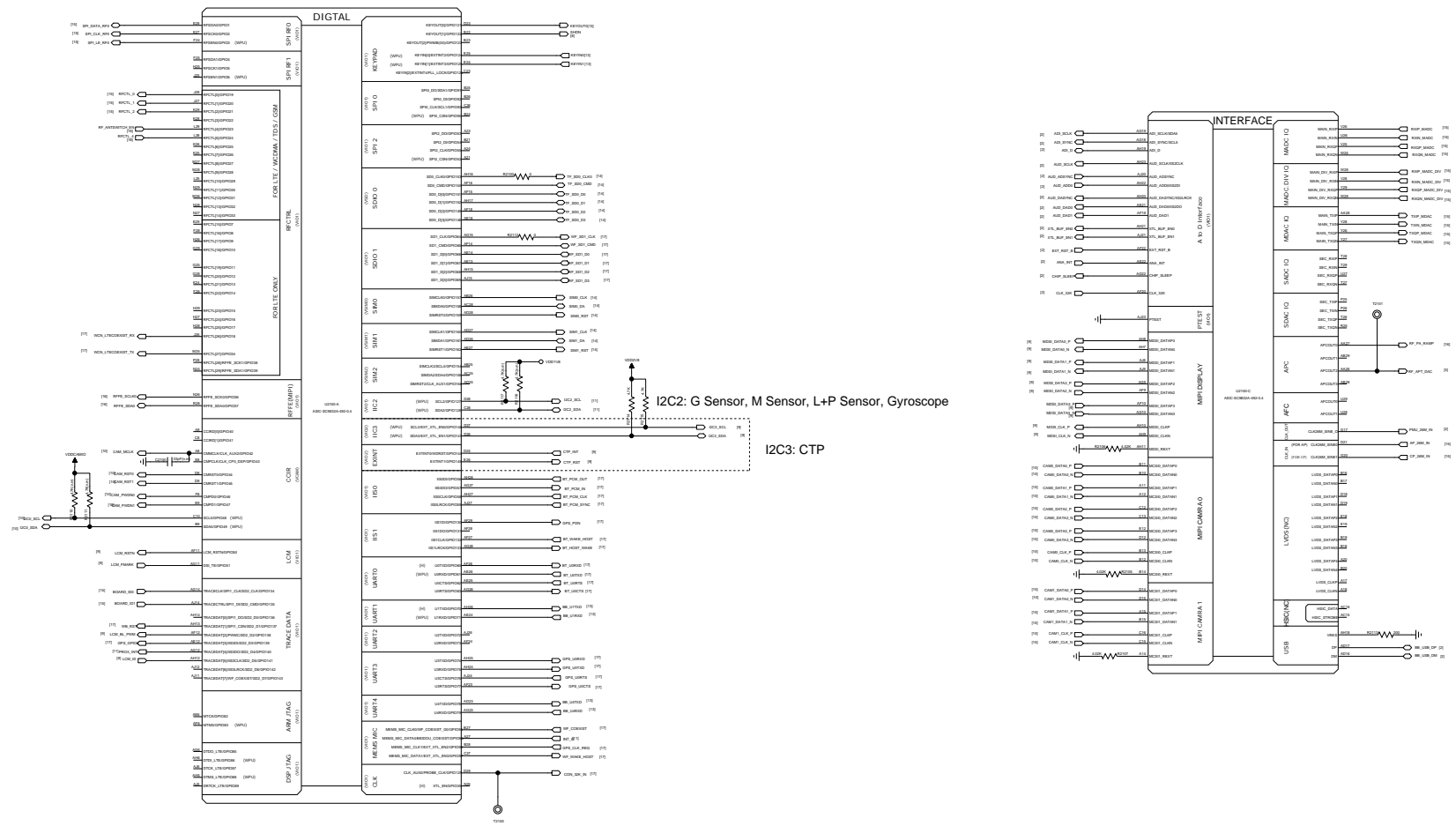
# eMMC+LPDDR3



add

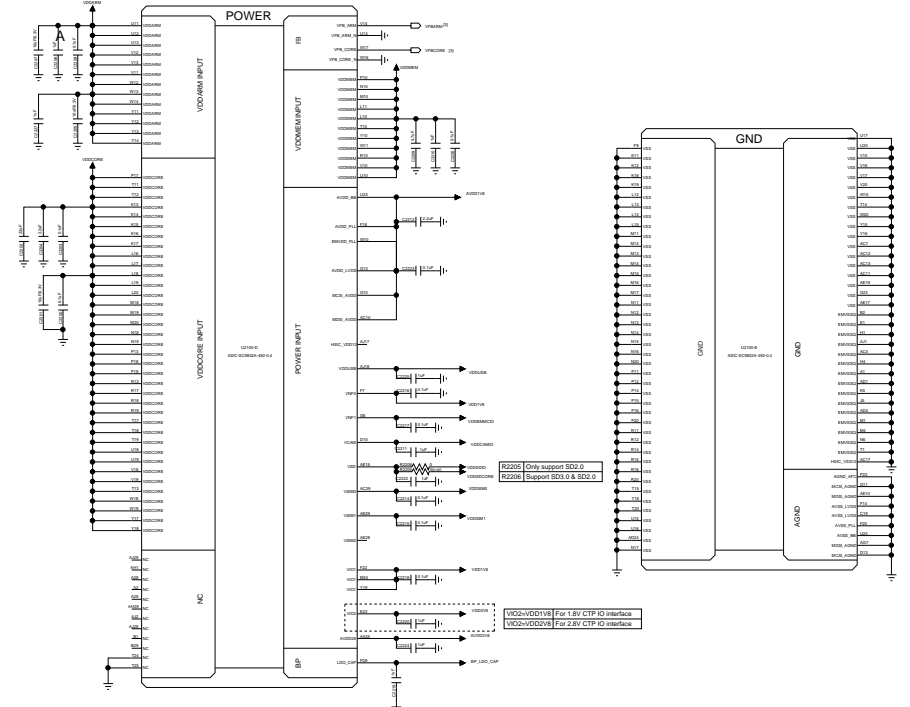
TITLE & REV.			
SP9832A-2_CS_SCH_V0.1.0			
DESIGNER:			
River Du	DATE:	DEPARTMENT:	
	2015-10-12	Hardware DEPT.	
DRAWN:			
Muler Han	DATE:	COMPANY:	
	2015-10-12	Spreadtrum communications, Inc.	
APPROVED:			
Gang Xu	DATE:	CONTROLLER AND PROJECT MANAGER	
	2015-10-12		

# DIGITAL



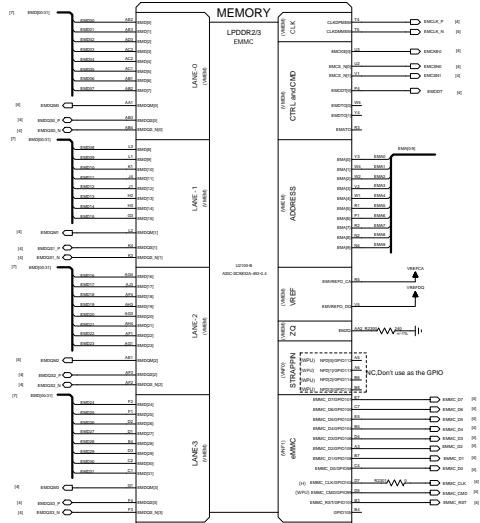
REVISION RECORD			
VER	REV. NO.	DESCRIPTION	DATE

POWER



REVISION RECORD			
VER.	REVISED	APPROVED	DATE

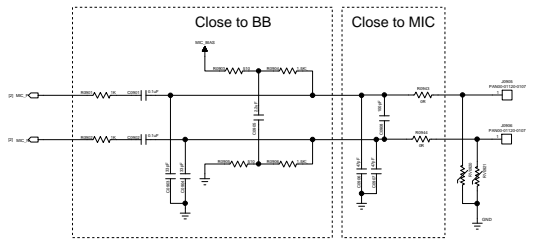
# MEMORY



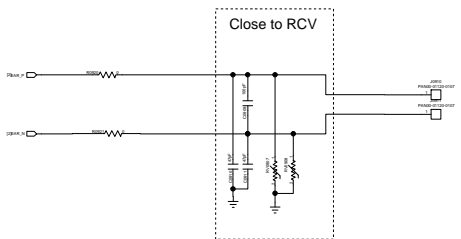
TITLE & REV:			
SP9832A-2_CS_SCH_V0.1.0			
DESIGNER:			
River Du	DATE:	DEPARTMENT:	
2015-10-12	2015-10-12	Hardware DEPT.	
DRAWN:			
Muher Han	DATE:	COMPANY:	
2015-10-12	2015-10-12	Spreadtrum communications, Inc.	
APPROVED:			
Gang Xu	DATE:	COMPONENTS AND PREPARED:	
2015-10-12	2015-10-12	Sheet: 12 of 22	

REVISION RECORD			
TIME	REV. NO.	DESCRIPTION	DATE

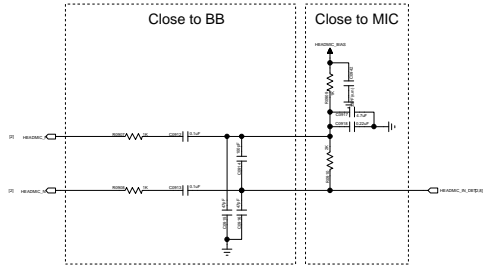
### MAIN MIC



### RECEIVER

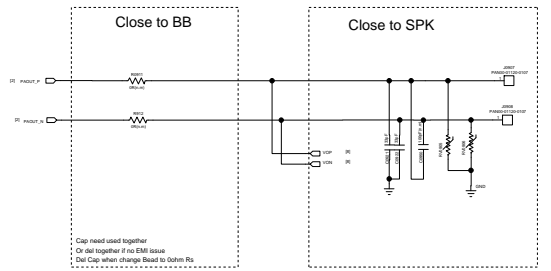


### HEAD MIC



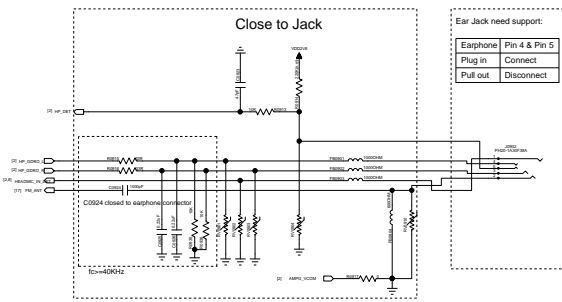
Note: C0912, C0913 use 1uF Cap

### SPEAKER

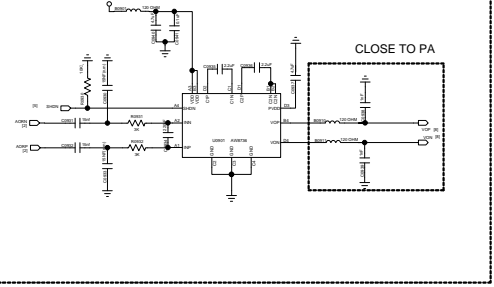


Cap need used together  
Or del together if no EMI issue  
Del Cap when change Res to 50ohm Rs

### 3.5mm Headphone Jack

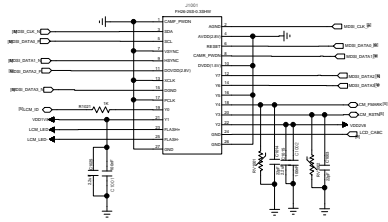


### Audio PA

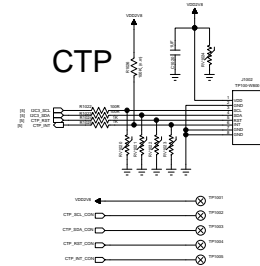


REVISION RECORD			
VER.	REV. NO.	DESCRIPTION	DATE

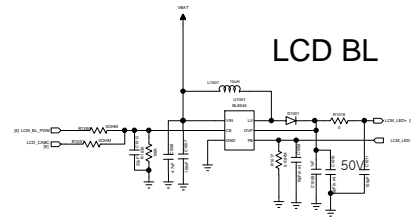
### 4.5" HVGA LCM



### CTP



### LCD BL



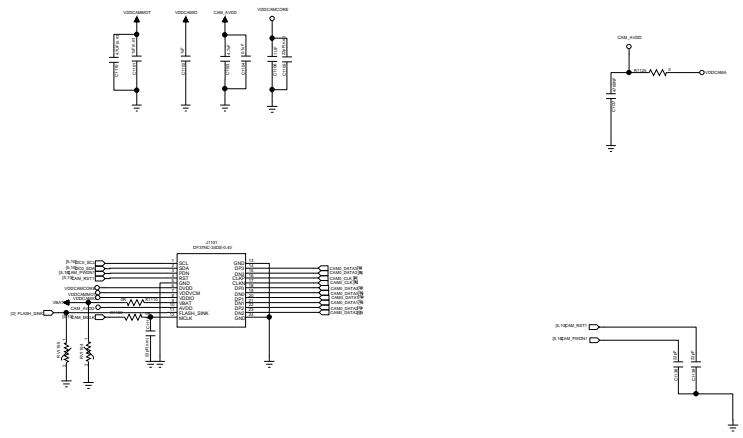
TITLE & REV: SP9832A-2\_CS\_SCH\_V0.1.0

DESIGNER: River Zu	DATE: 2015-10-12	DEPARTMENT: Hardware DEPT.
CHK: Mukder Han	DATE: 2015-10-12	COMPANY: Spreadtrum communications, Inc.
APPROVED: Gang Xu	DATE: 2015-10-12	COMPONENTS AND PREPARED BY: Sheet: 1 of 10

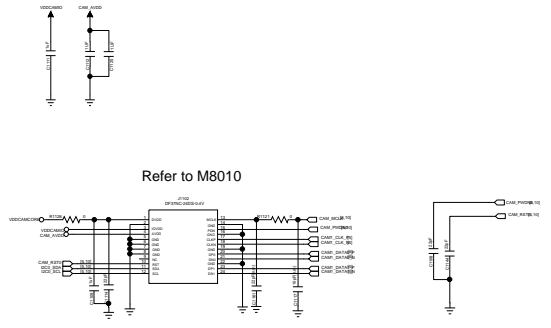


REVISION RECORD			
VER.	REV. NO.	DESCRIPTION	DATE

### 8M Sensor

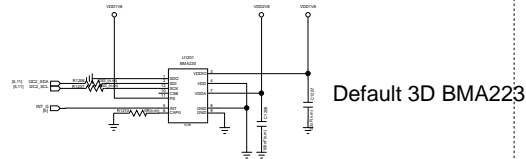


### 5M Sensor



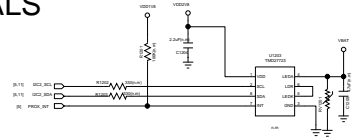
REVISION RECORD			
DATE	REVISION	DESCRIPTION	DATE

### G Sensor



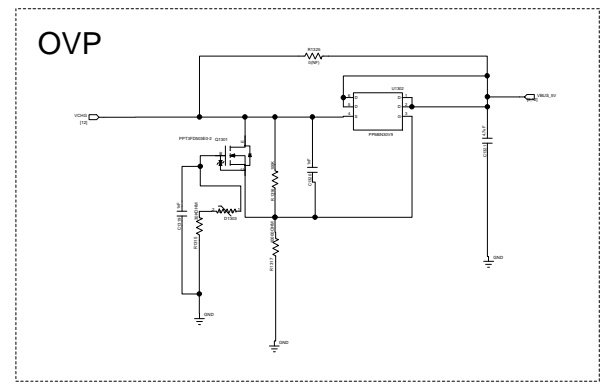
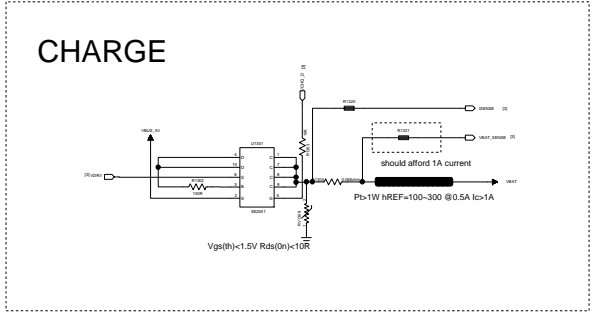
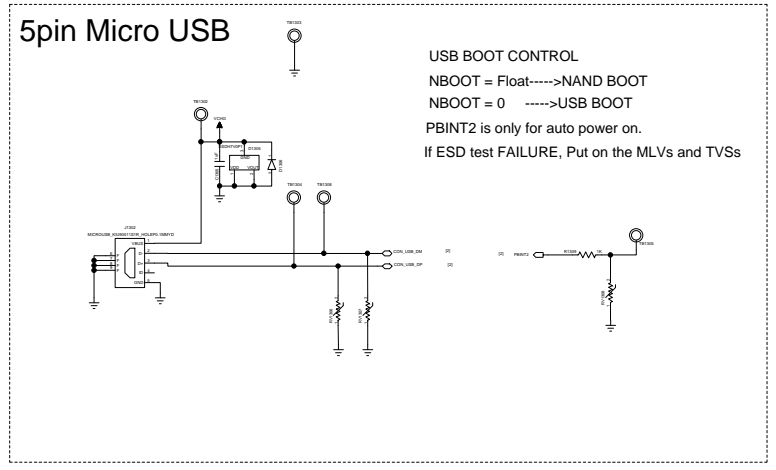
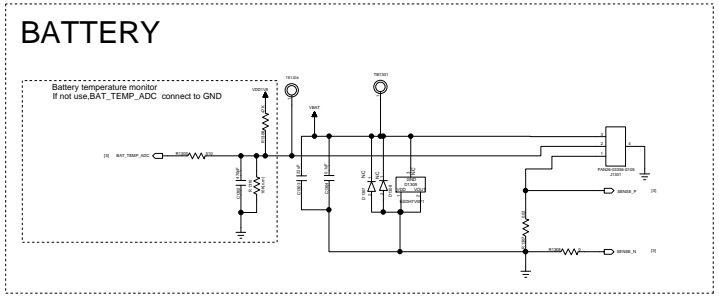
Default 3D BMA223

### ALS

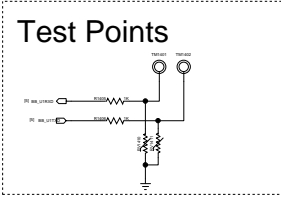
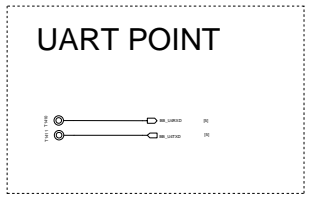
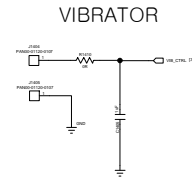
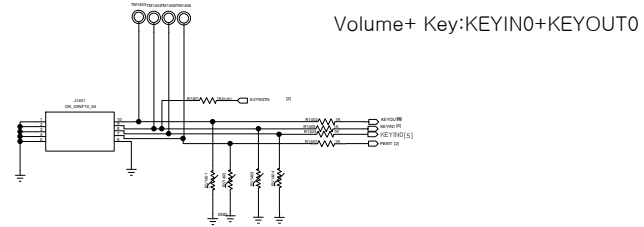


TITLE & REV:		SP9832A-2_CS_SCH_V0.1.0	
DESIGNER:		RIVER DU	
DATE:	2015-10-12	DEPARTMENT:	HARDWARE DEPT.
BY:	MULDER HAN	COMPANY:	SPREADTRUM COMMUNICATIONS, INC.
APPROVED:	GANG XU	DATE:	2015-10-12
DRAWN BY:		GANG XU	
CHECKED BY:		GANG XU	
DATE:		2015-10-12	
SCALE:		1:1	
SHEET NO.:		10 OF 10	

REVISION RECORD			
VER.	CHANGED	BY	DATE



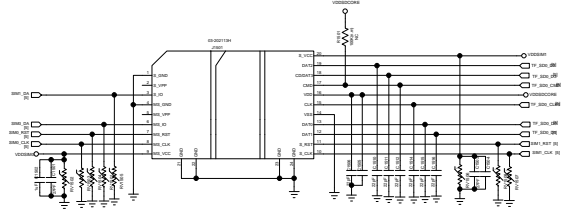
REVISION RECORD			
VER.	REVISED	REVISIONS	DATE



TITLE & REV.			
SP9832A-2_CS_SCH_V0.1.0			
DESIGNED BY	DATE	DEPARTMENT	
River Du	2015-10-12	Hardware DEPT.	
CHK BY	DATE	COMPANY	
Mulder Han	2015-10-12	Spreadtrum communications, Inc.	
APPROVED BY	DATE	COMPONENTS AND INTERFACES	
Gang Xu	2015-10-12		

REVISION RECORD			
VER.	REASON	DATE	BY

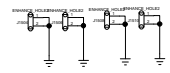
### SIM0 & SIM1&T-Flash 3in1



### Rear cam enhance\_element

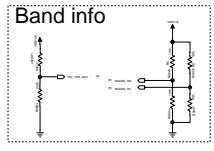
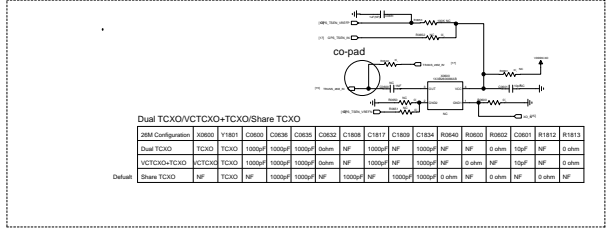
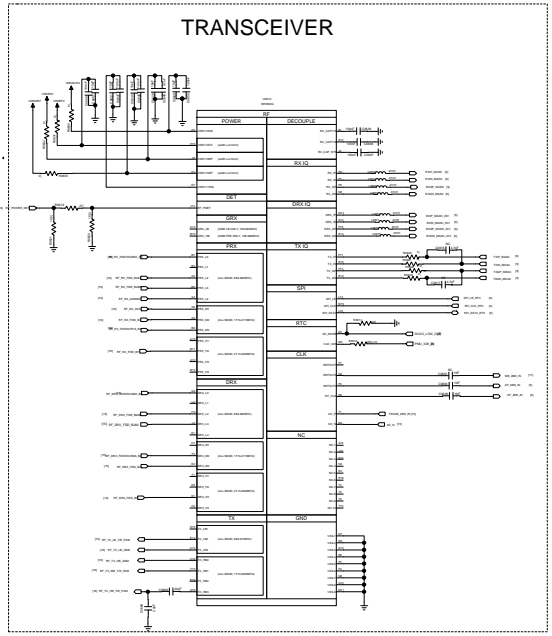
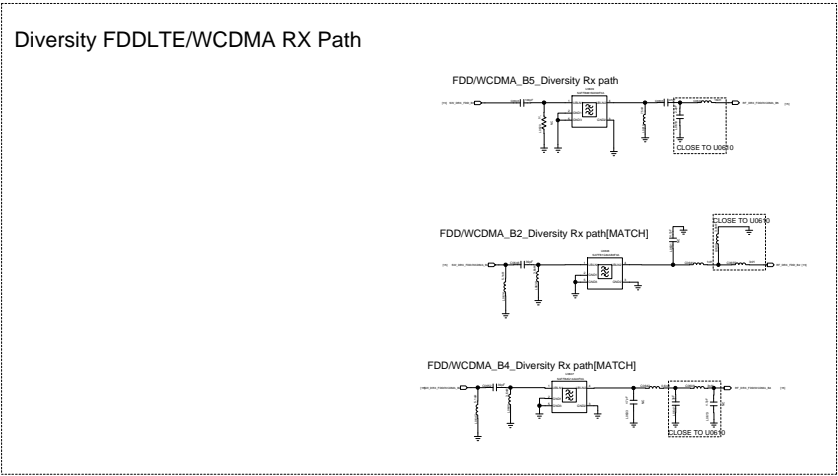
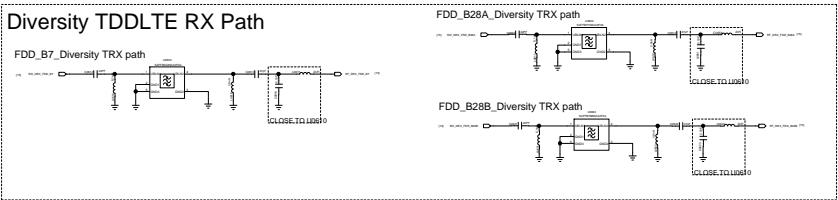
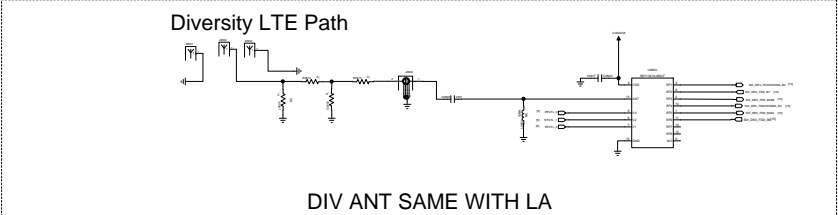


### PCB enhance\_hole



TITLE & REV:		SP9832A-2_CS_SCH_V0.1.0	
DESIGNER:		RIVER DU	
DATE:		2015-10-12	
DEPARTMENT:		Hardware DEPT.	
DRAWN:		MUSHER HAN	
DATE:		2015-10-12	
COMPANY:		SPREADTRUM COMMUNICATIONS, INC.	
APPROVED:		GANG XU	
DATE:		2015-10-12	
CONTRACT NO.:			
SHEET:		21 OF 21	

REV	DATE	DESCRIPTION



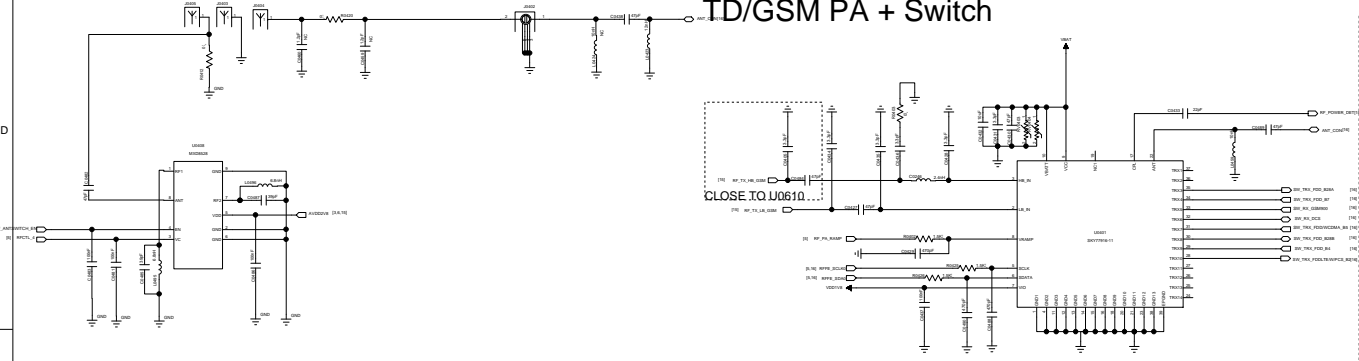
ABCDEF	00	01	10	11
0	000000	000000	000000	000000
1	000000	000000	000000	000000
2	000000	000000	000000	000000
3	000000	000000	000000	000000

SPB02A2_CS_SCH_V01.0		
Rev. No.	001	Hardware DEPT.
Model Name	SPB02A2	Spectrum communications, Inc.
Design No.	001	

# Primary Antenna

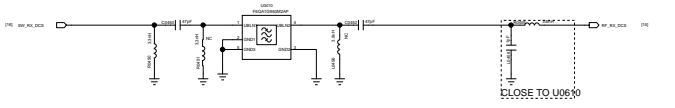
REV	REV NO	APPROVED	DATE

## TD/GSM PA + Switch

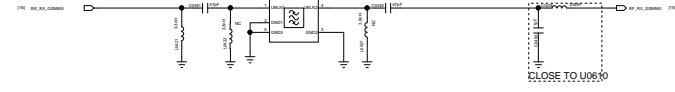


## Primary RX Path GSM/TDS

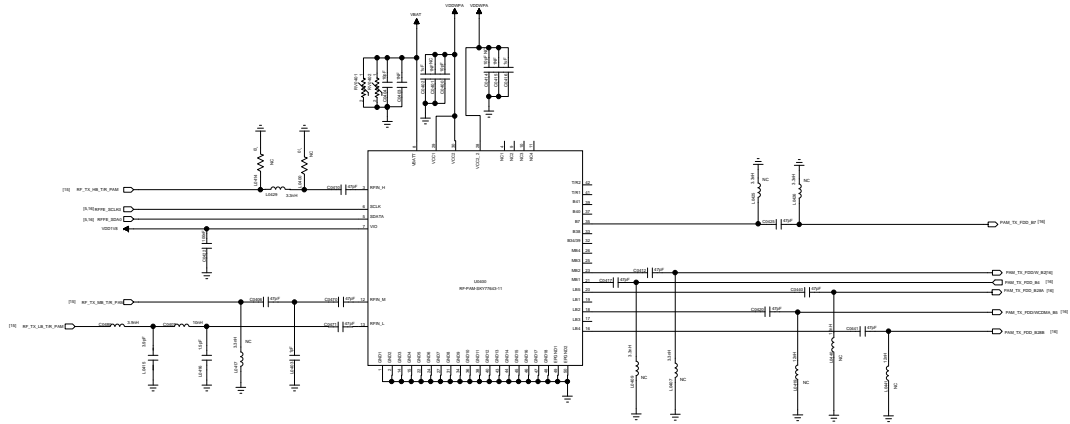
DCS\_RX



GSM\_900

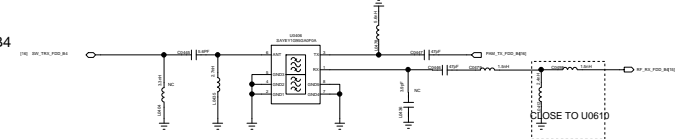


## LTE/WCDMA PAM

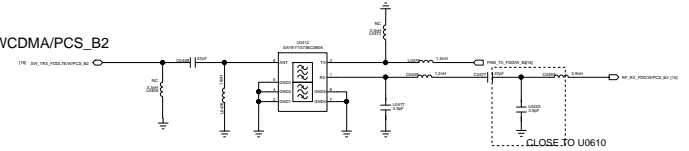


## Primary TRX Path FDD/WCDMA

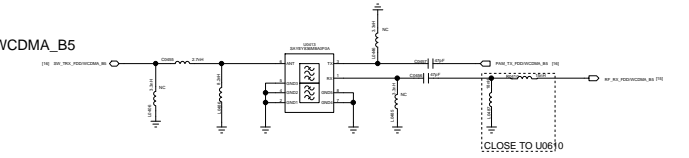
FDDLTE\_B4



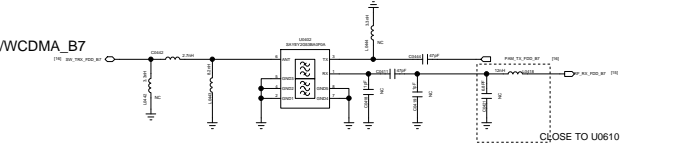
FDDLTE/WCDMA\_PCS\_B2



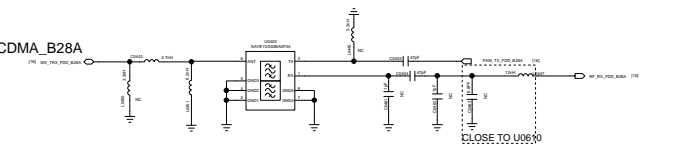
FDDLTE/WCDMA\_B5



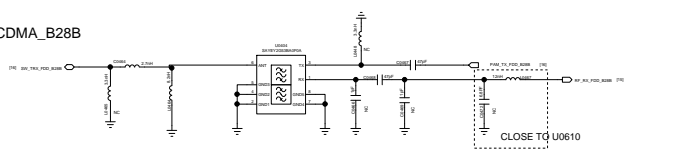
FDDLTE/WCDMA\_B7



FDDLTE/WCDMA\_B28A



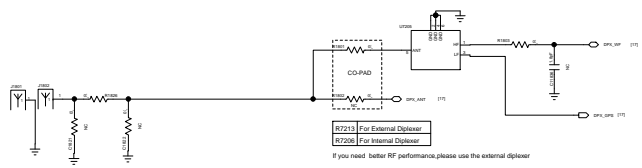
FDDLTE/WCDMA\_B28B



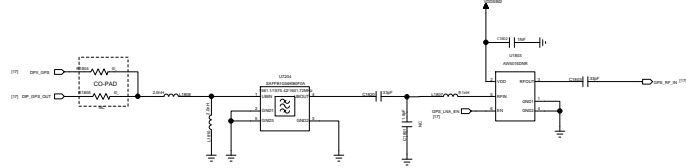
SP832A-2\_CS\_SCH\_V0.1.0

REVISION	DATE	DESCRIPTION

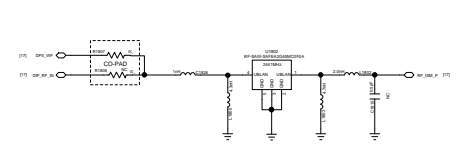
### BT&WIFI&GPS ANT



### GPS SAW LNA



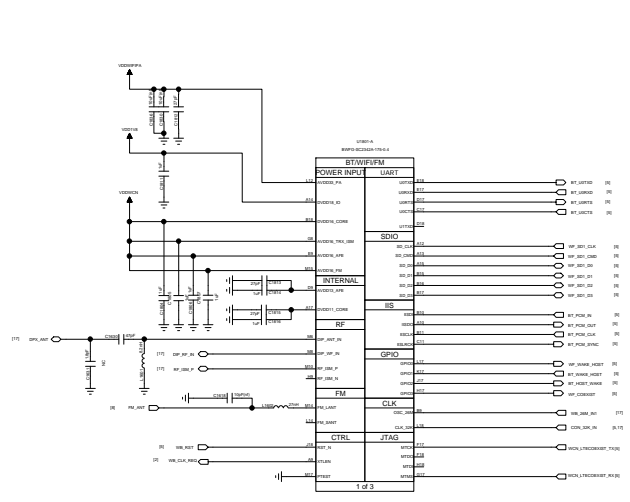
### WIFI/BT SAW LNA



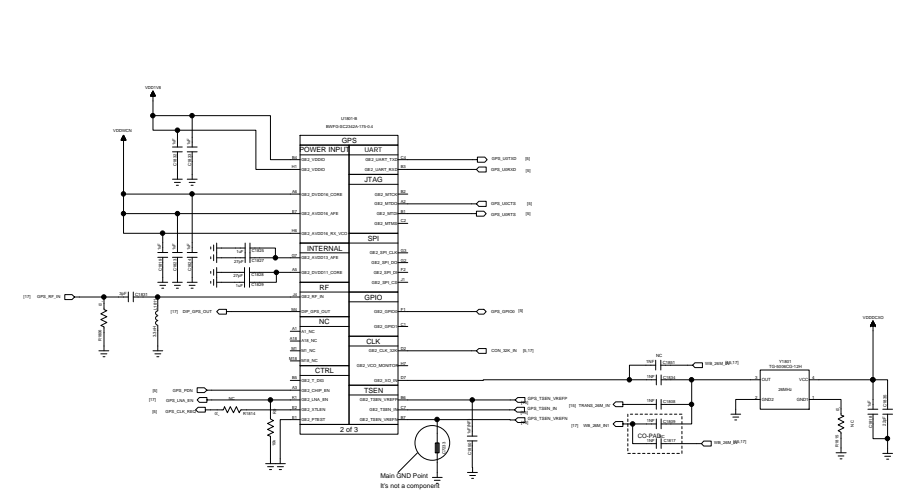
DPX selection

VALUE	R7213	R7206	R7224	R7223	R7253	R7252
External DPX	0ohm	NF	0ohm	NF	0ohm	NF
Internal DPX	NF	0ohm	NF	0ohm	NF	0ohm

### MARLIN



### GE2



### GND

