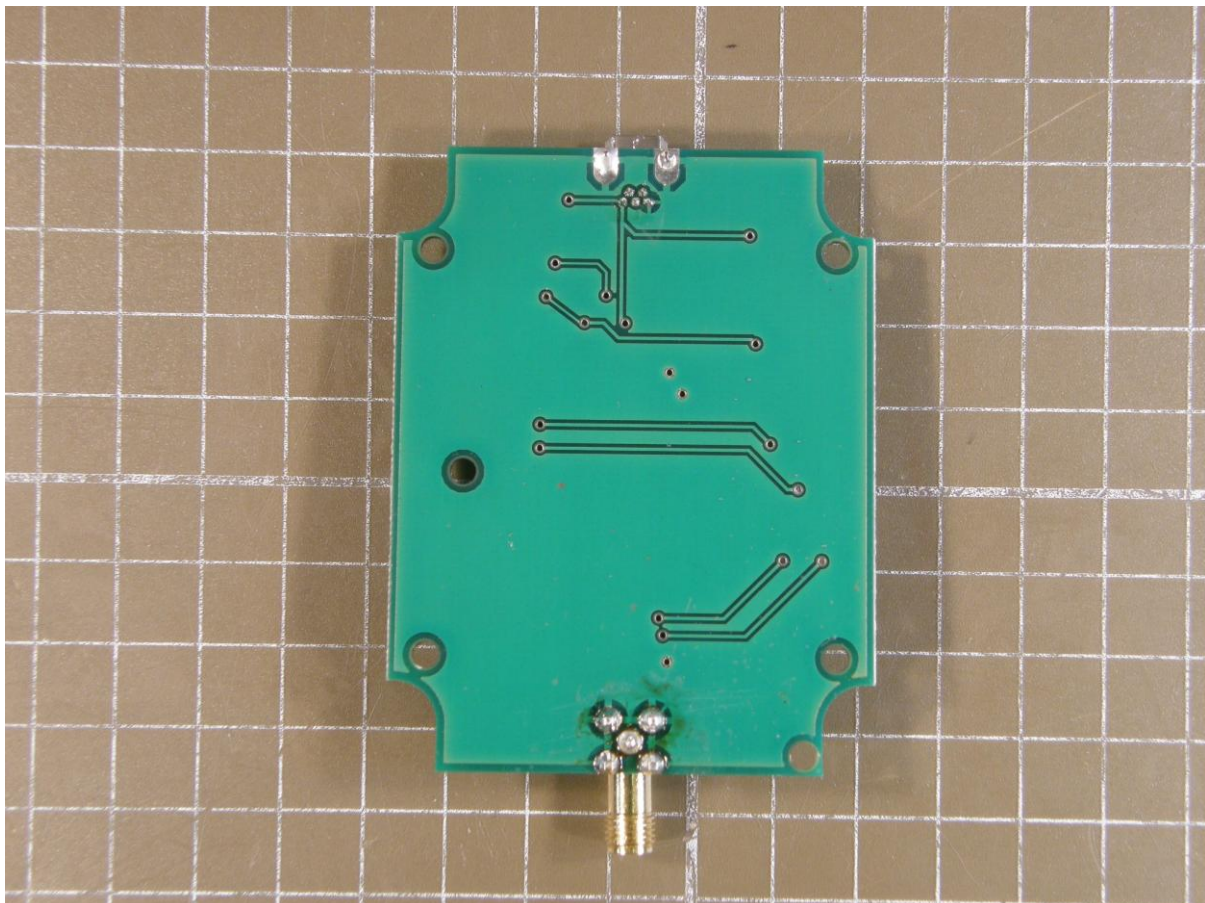
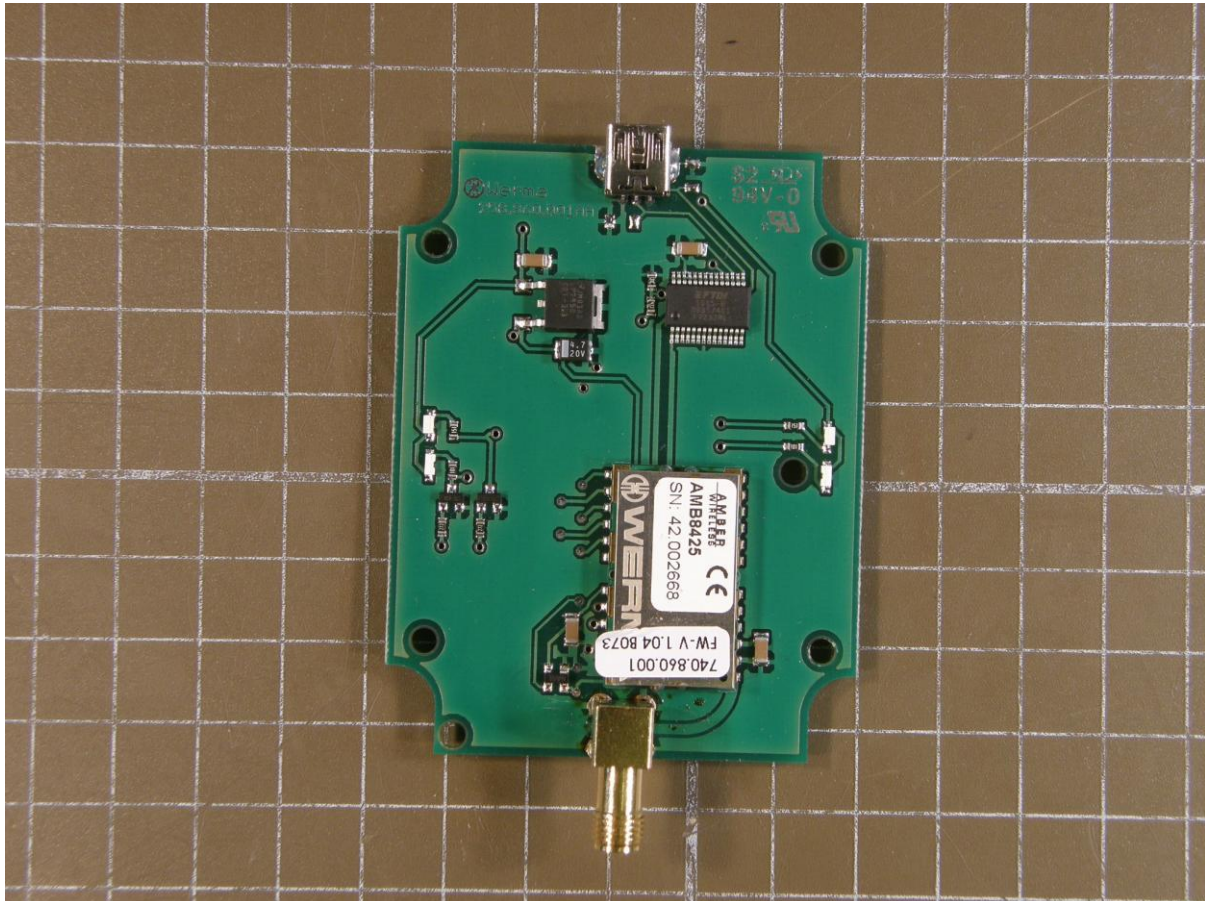


# **Annex no. 2**

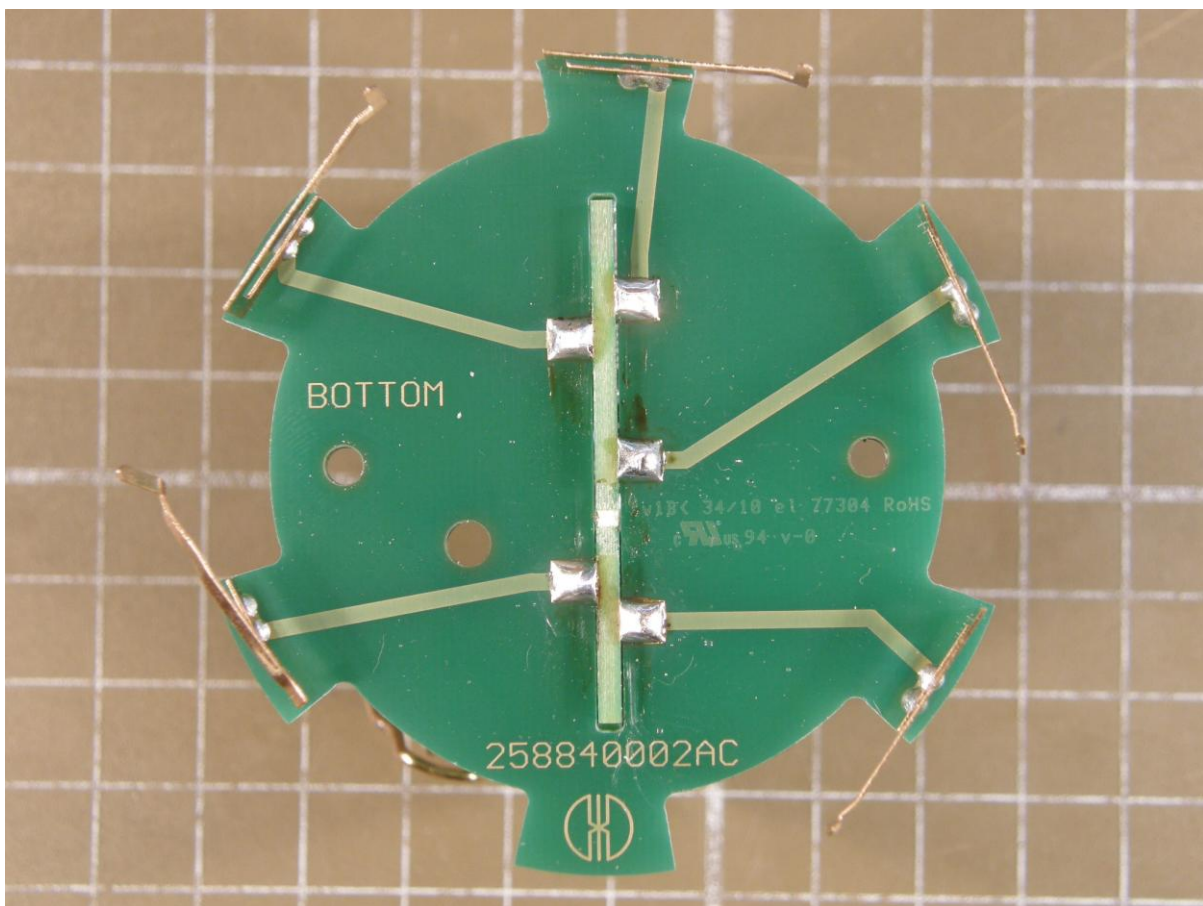
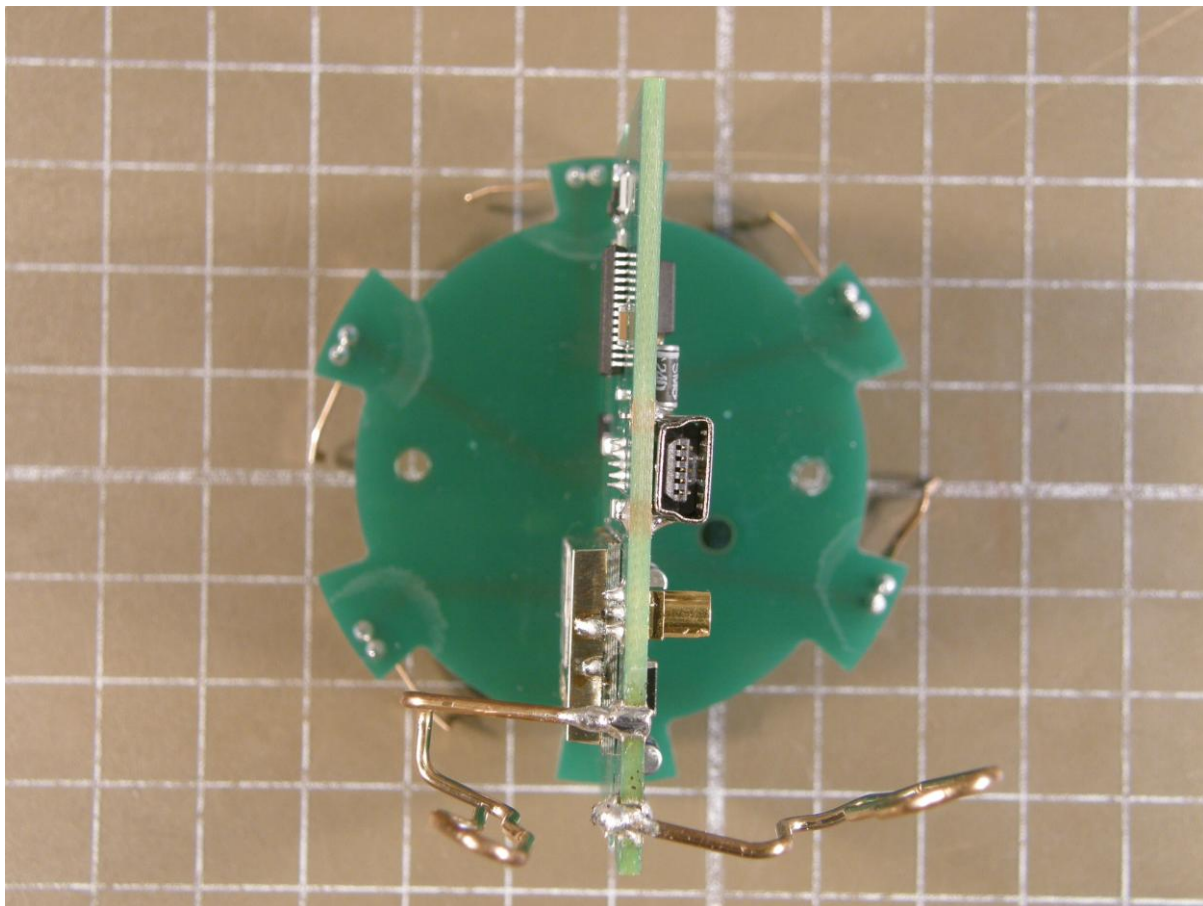
# **Internal Photographs**

**win master**



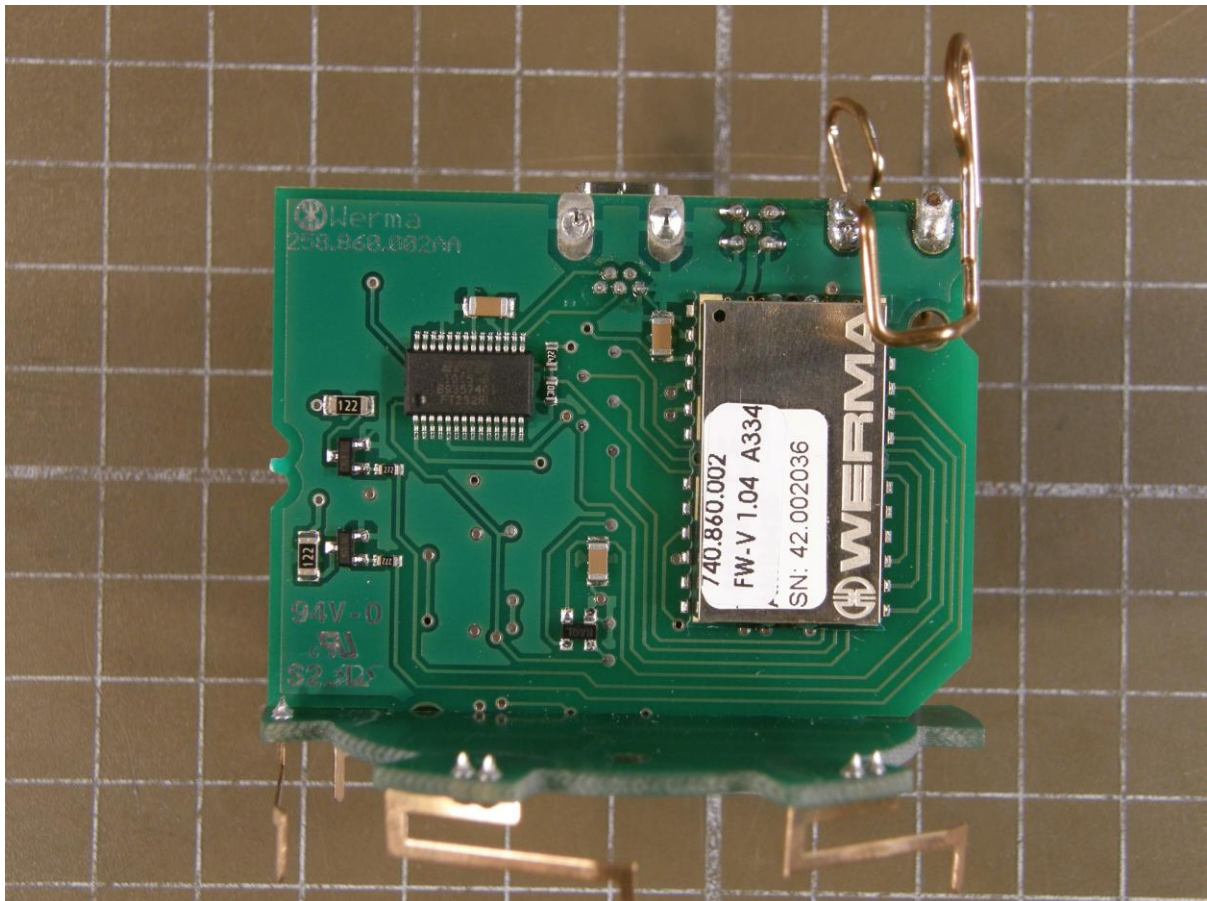
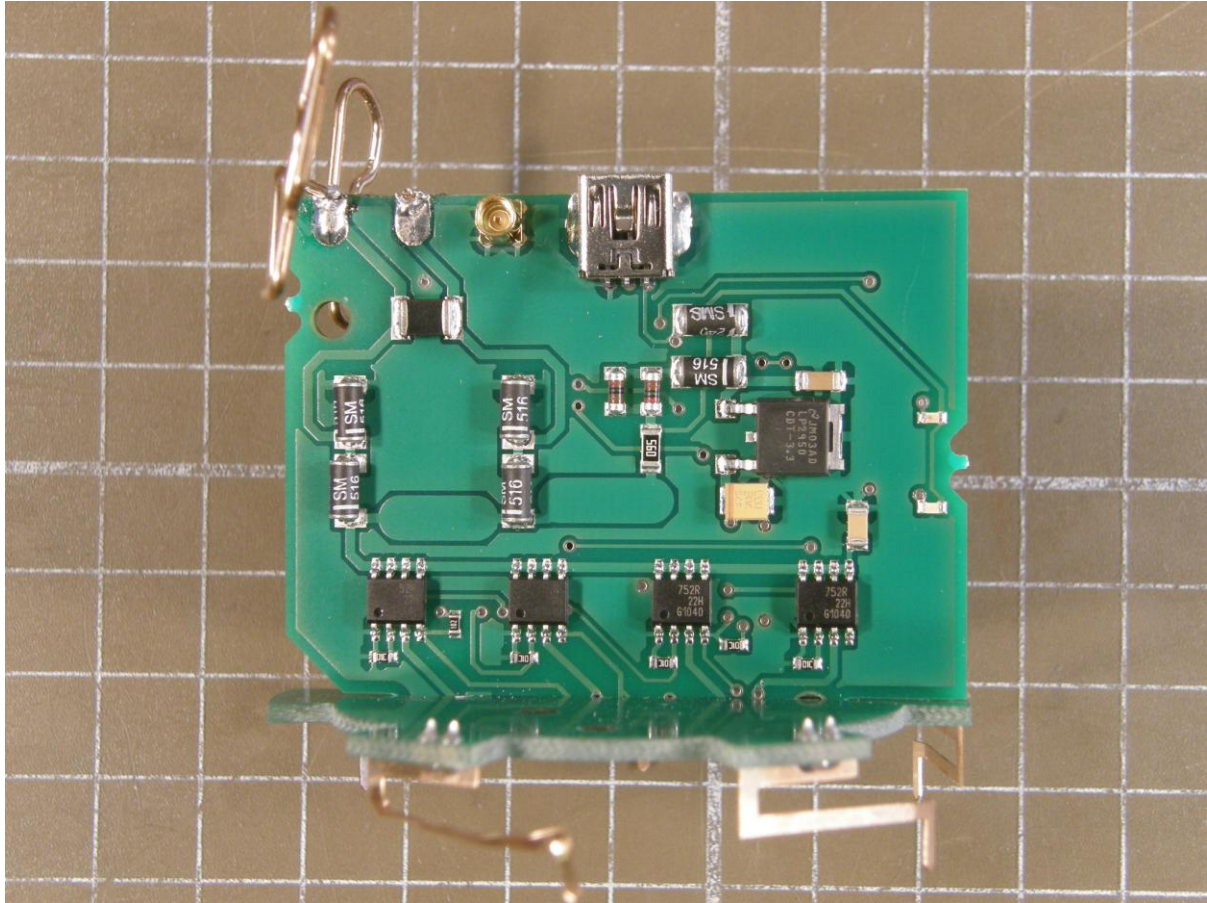


reflect master

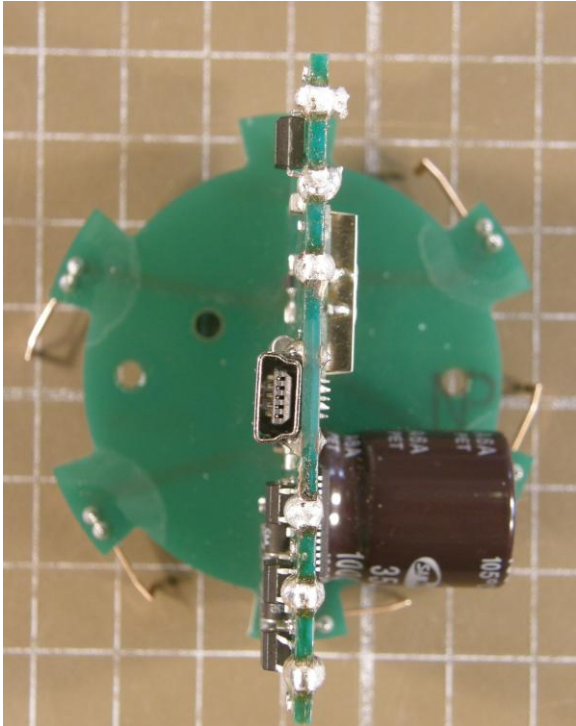




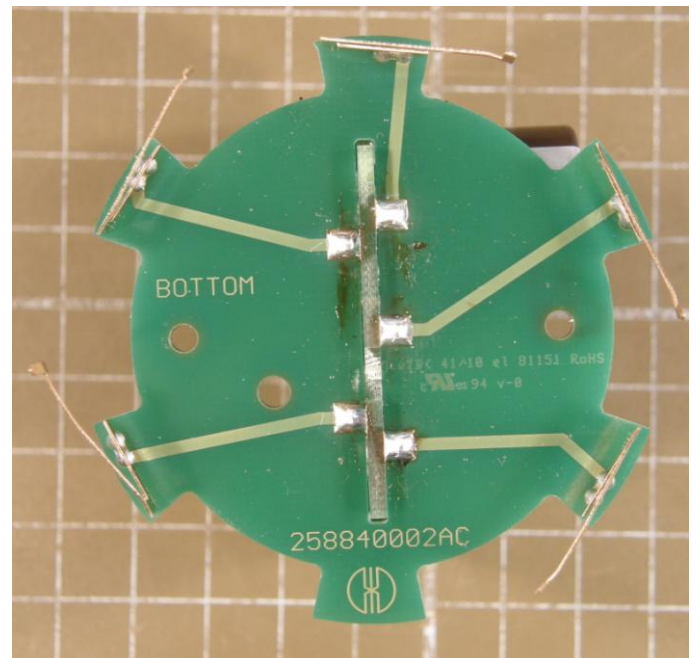
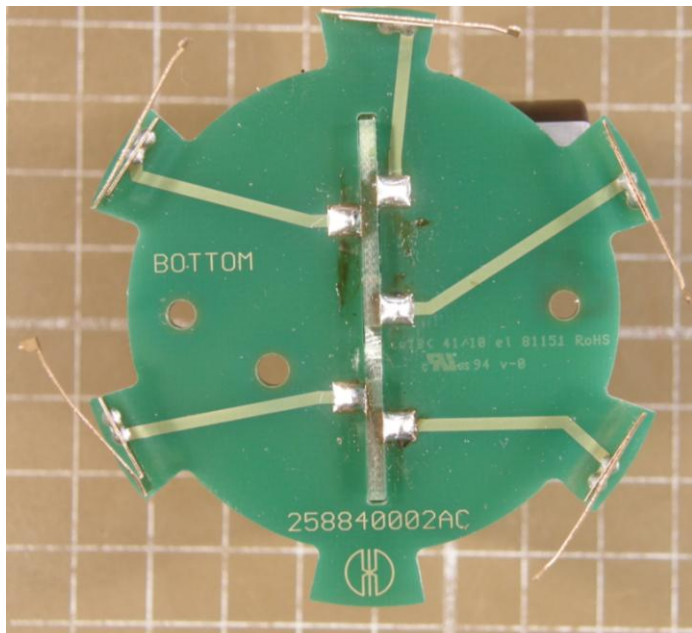
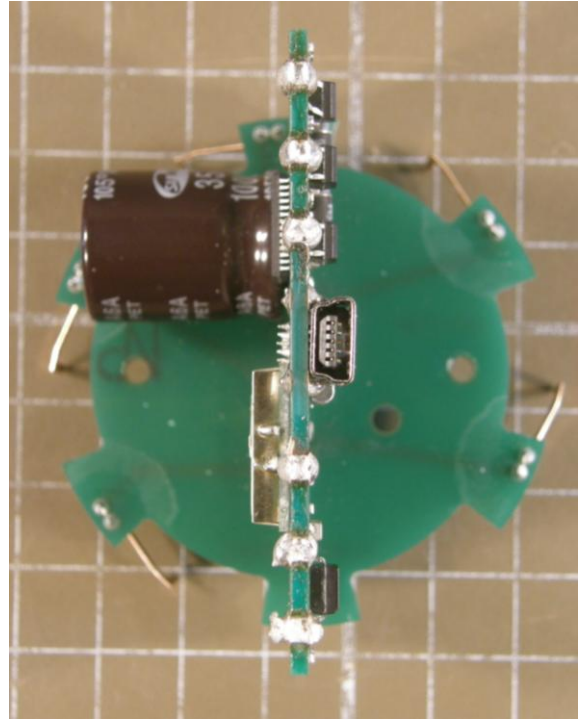
reflect master



win slave

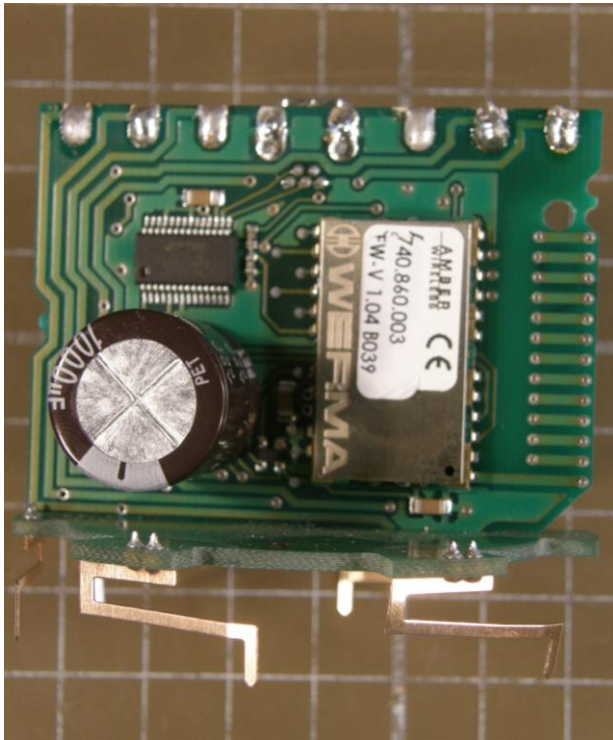


reflect slave

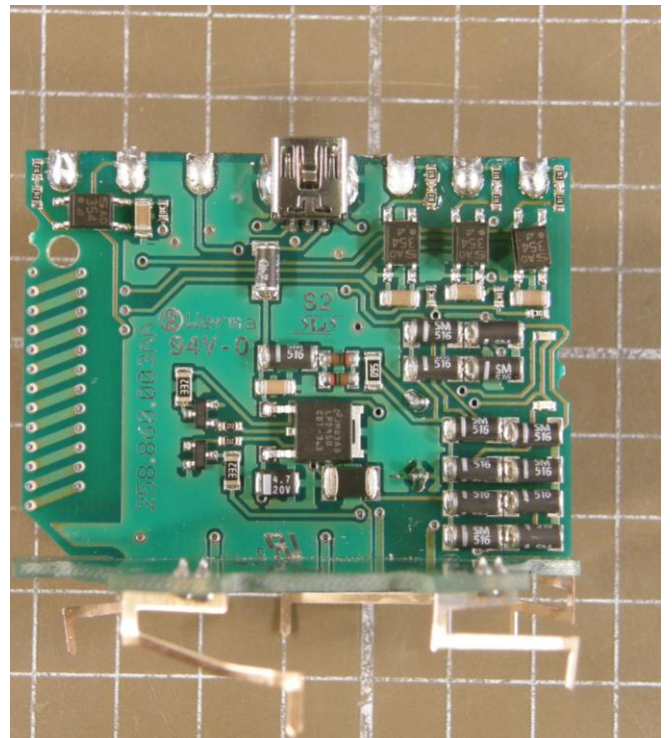
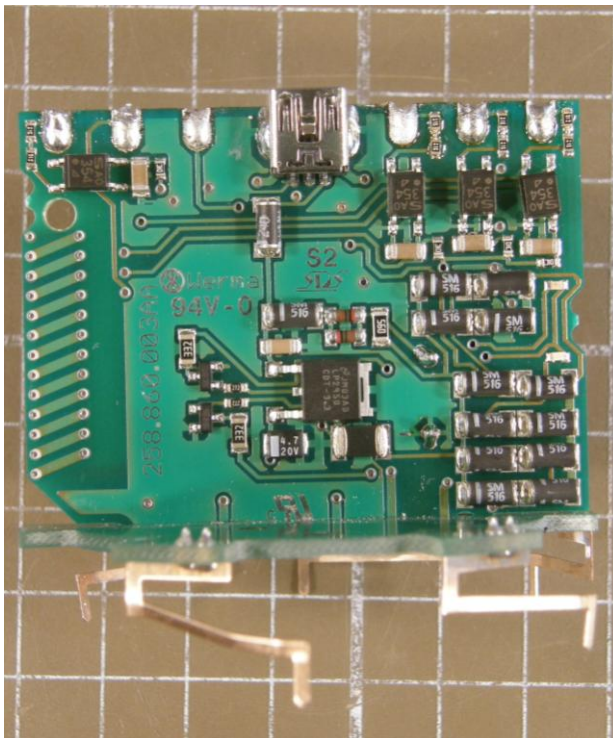




win slave



reflect slave



Radio module WIN1

