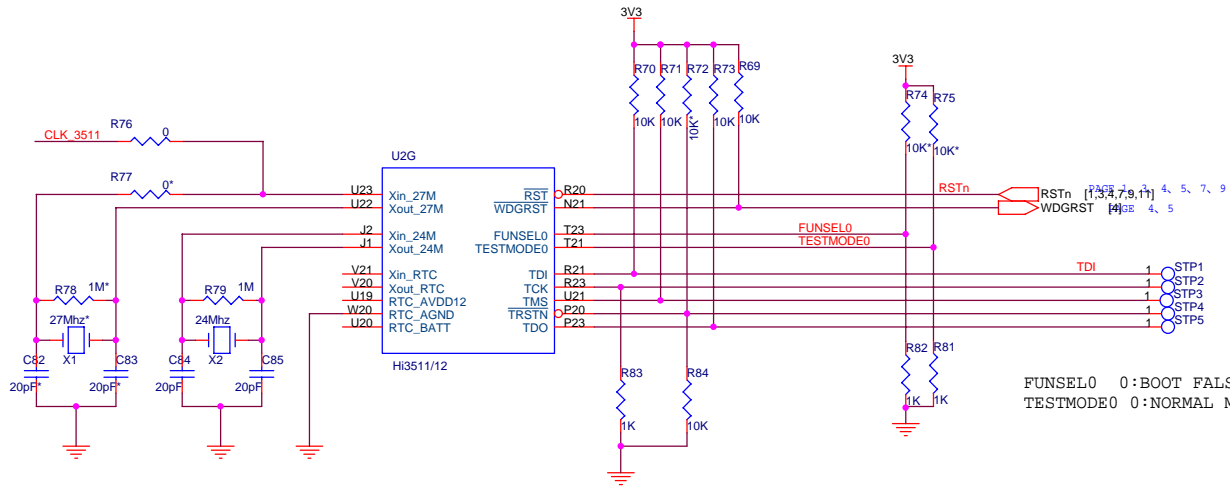
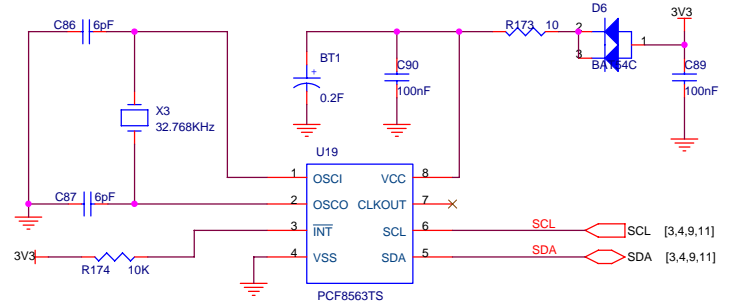


HI3511_IPCAMERA_VA

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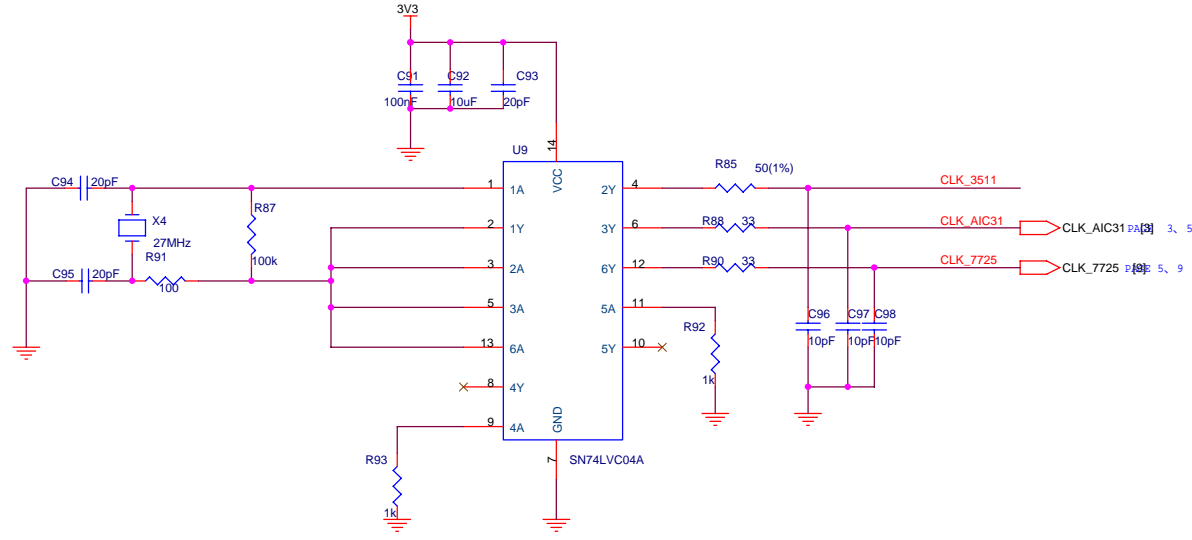


Hi3511 CLK AND JTAG

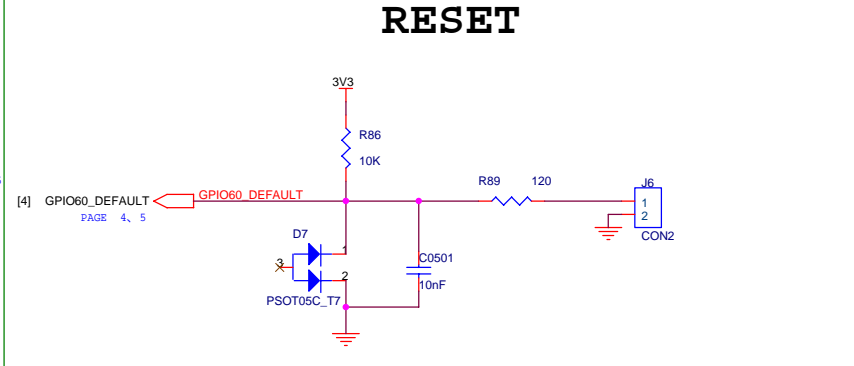


RTC

FUNSEL0 0:BOOT FALSH 1:BOOT DDR
 TESTMODE0 0:NORMAL MODE 1:TEST MODE



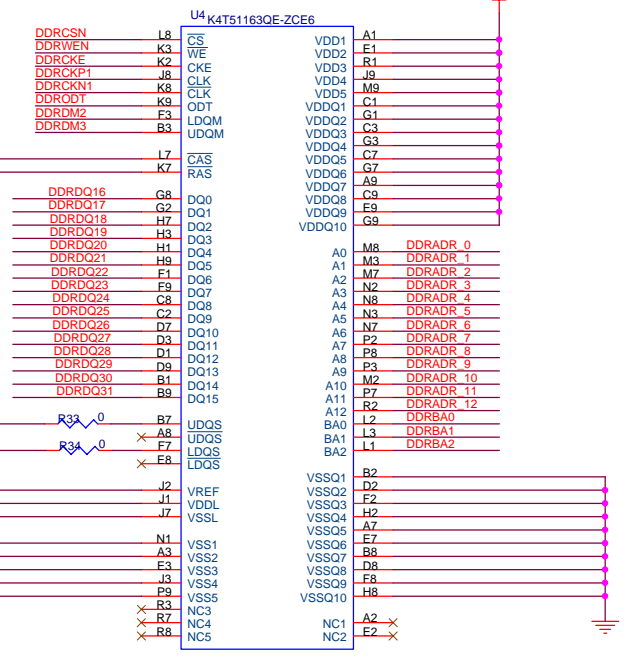
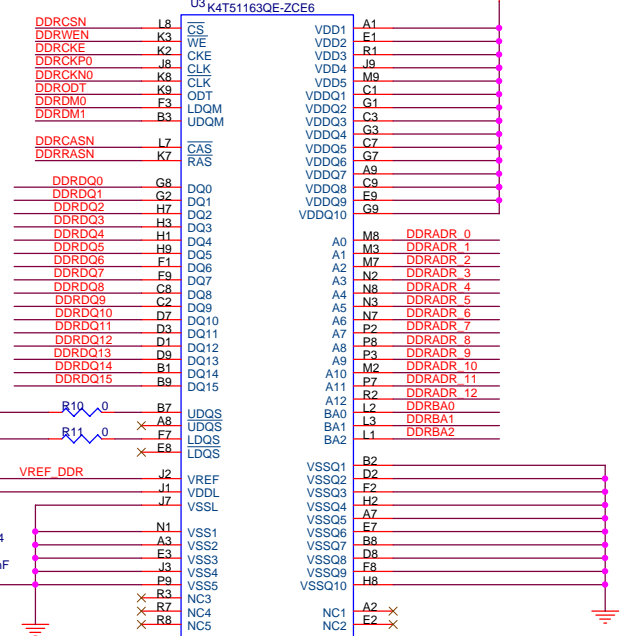
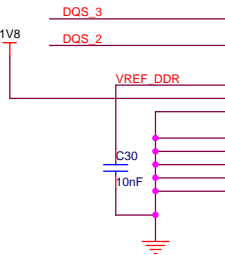
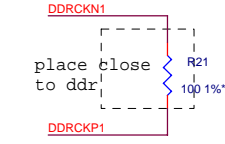
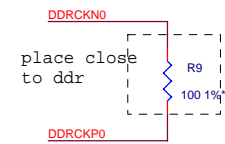
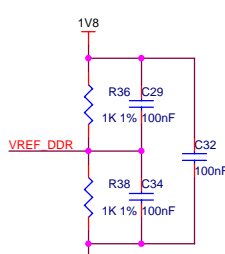
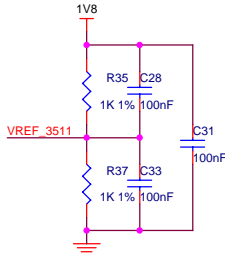
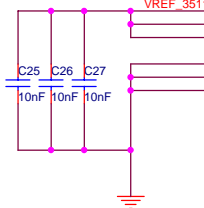
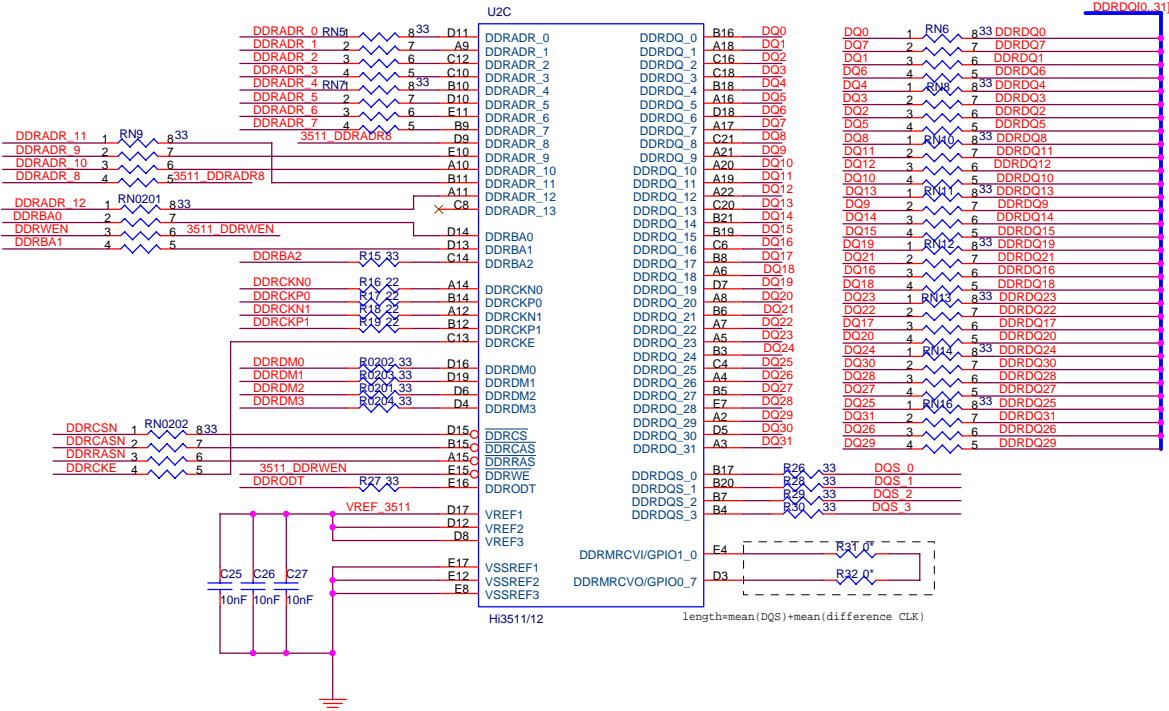
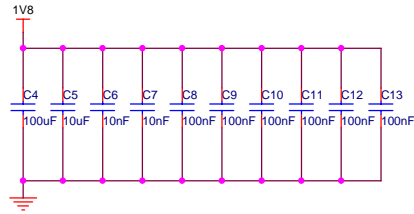
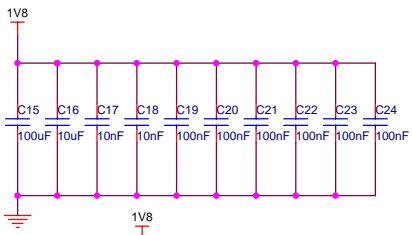
CLOCK



RESET

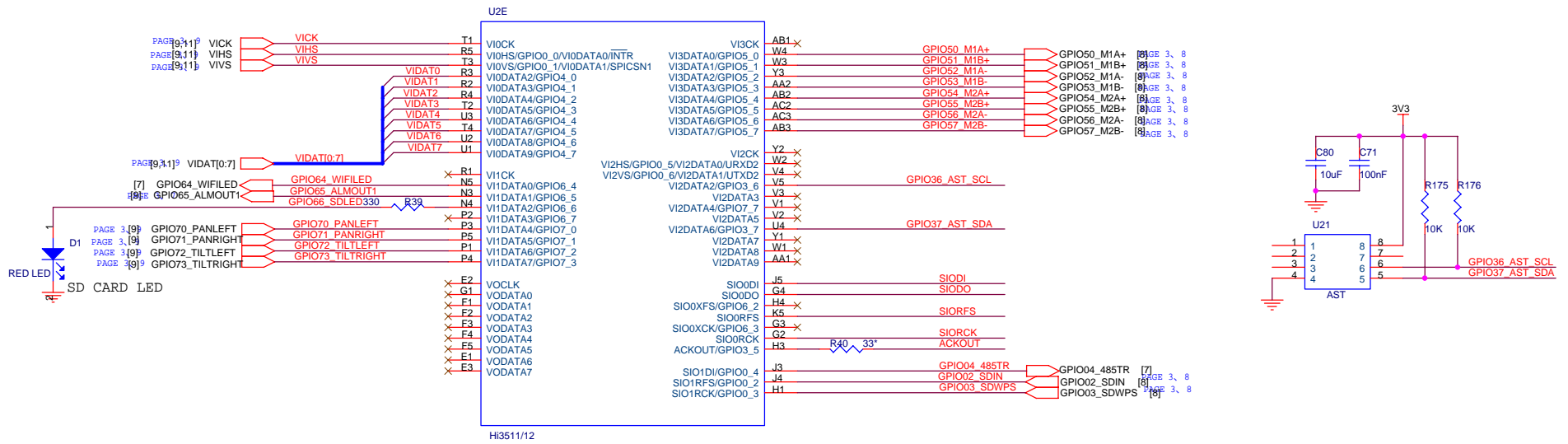
DEFAULT

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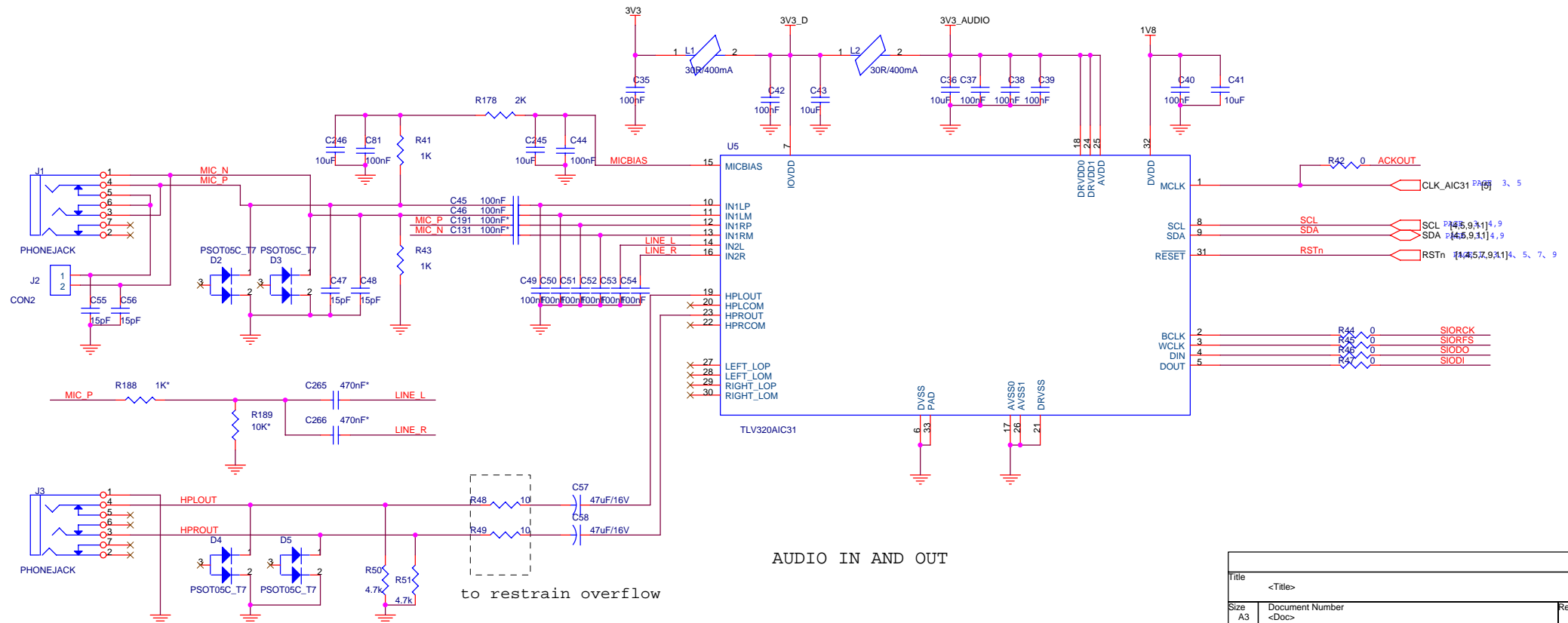


BA2 for 1Gb
16bits only
for Hi3512

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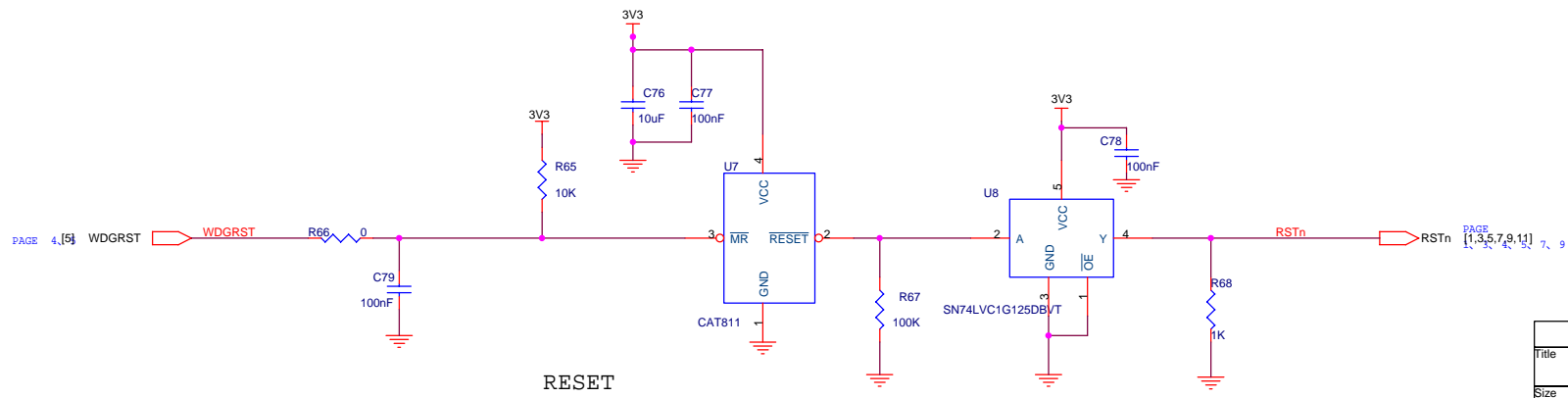
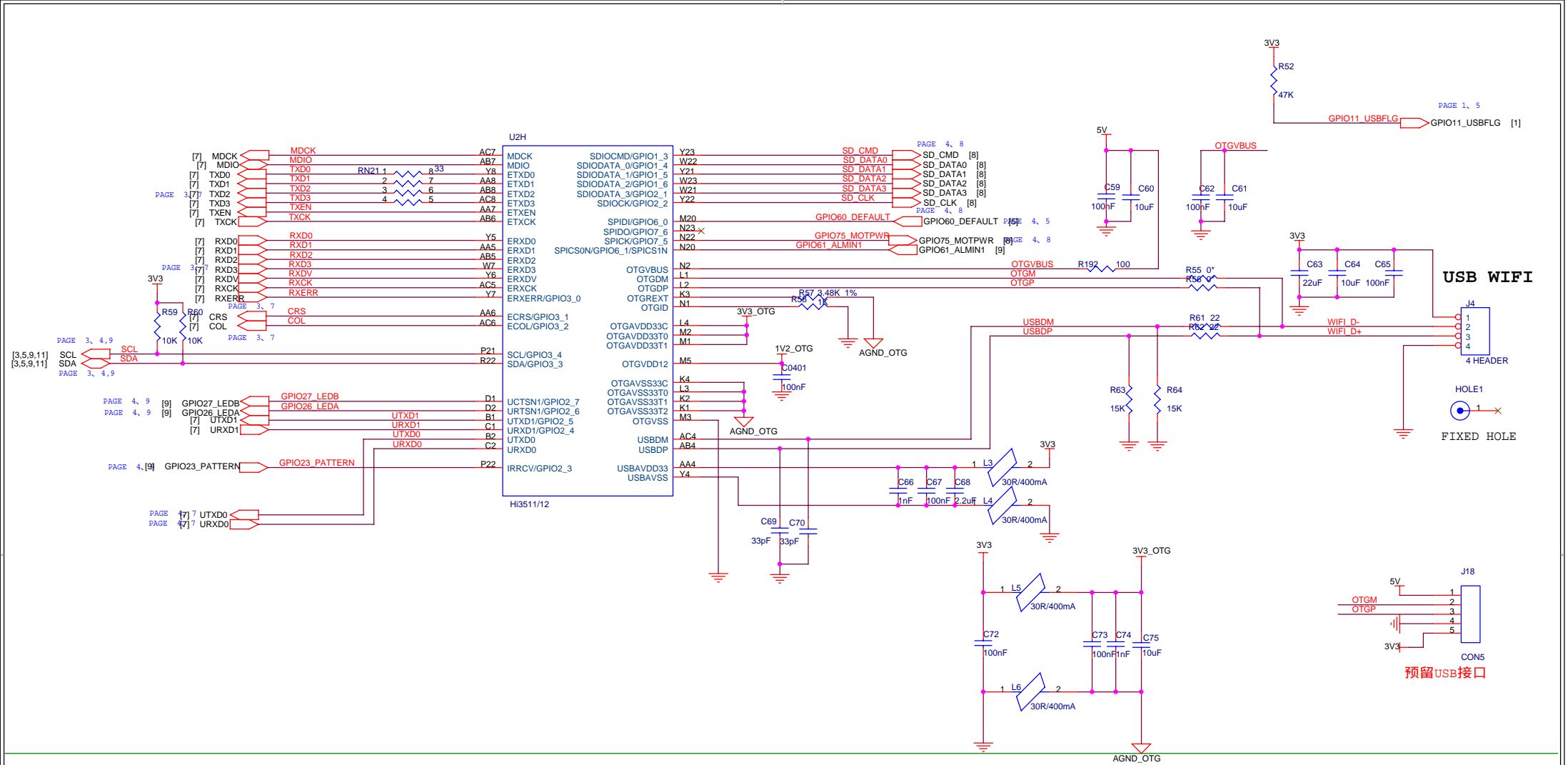


Hi3511 VIDEO AND AUDIO

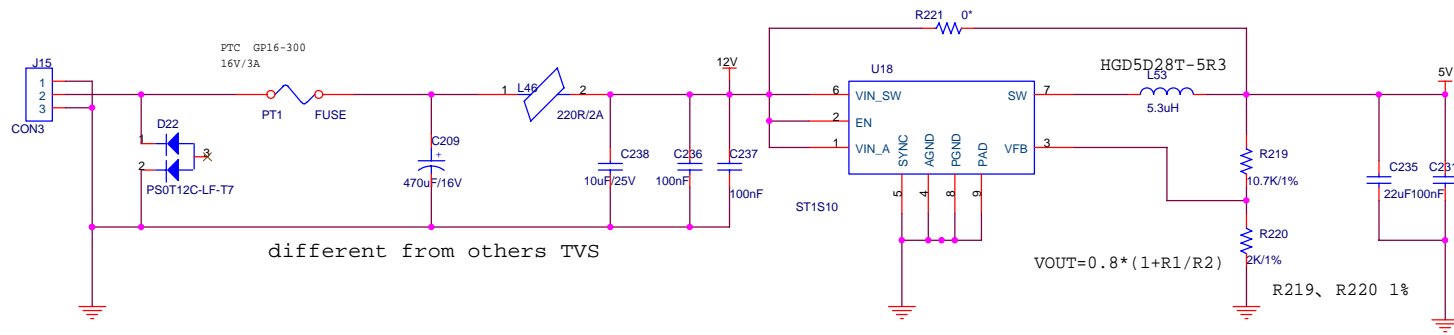


AUDIO IN AND OUT

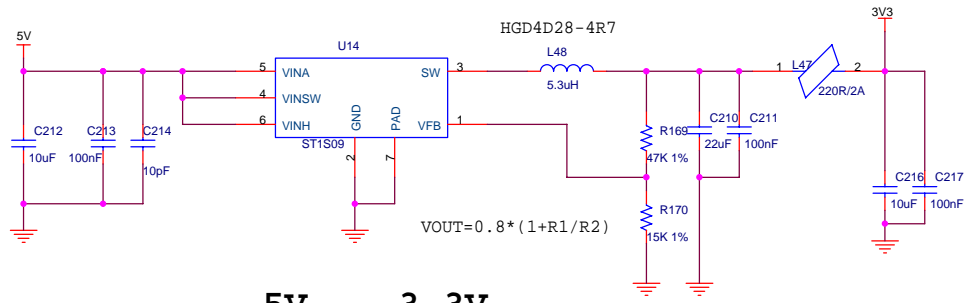
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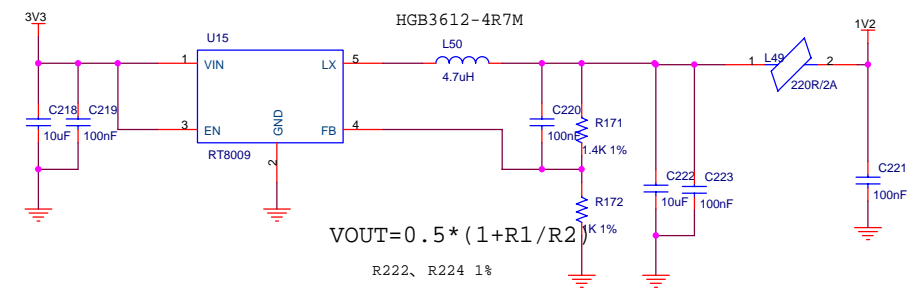
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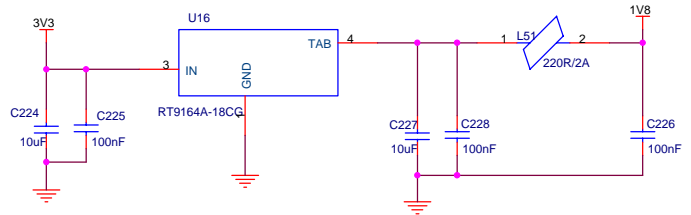
12V----5V



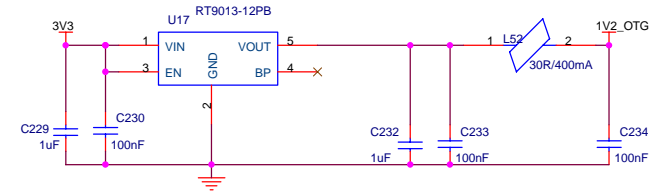
5V----3.3V



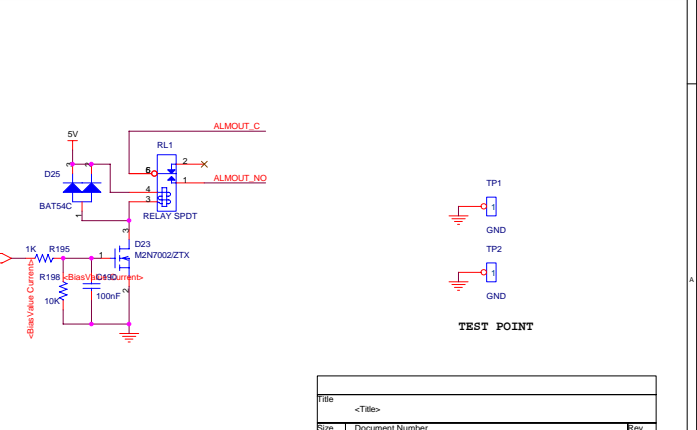
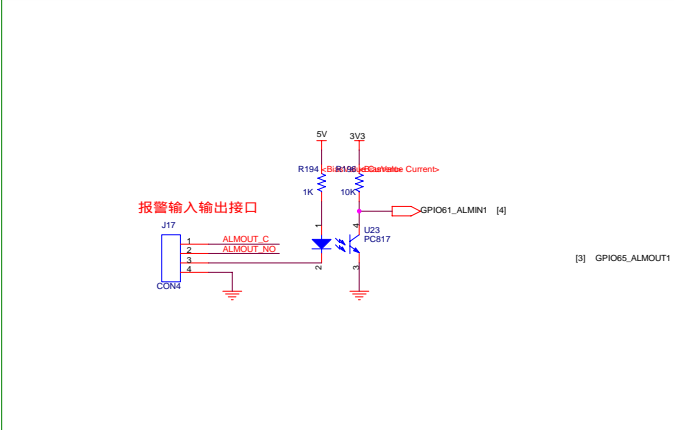
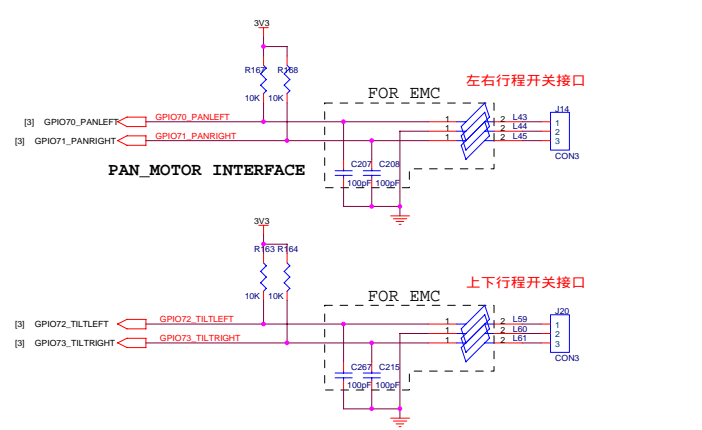
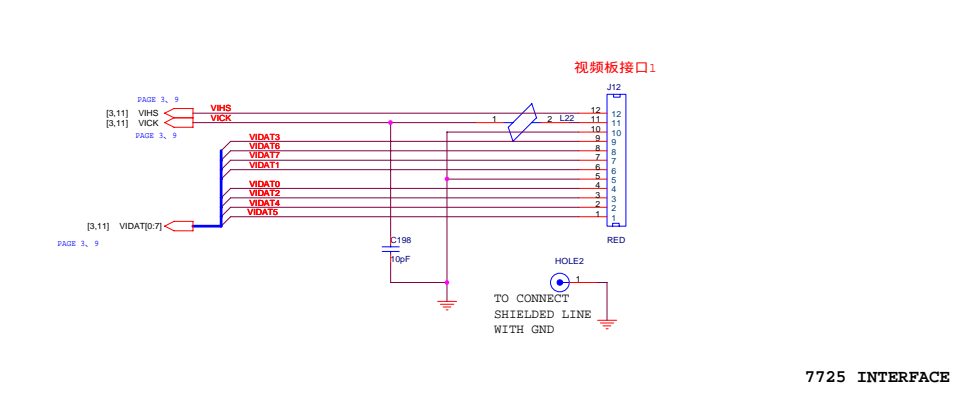
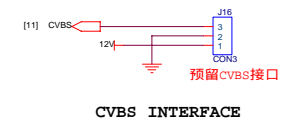
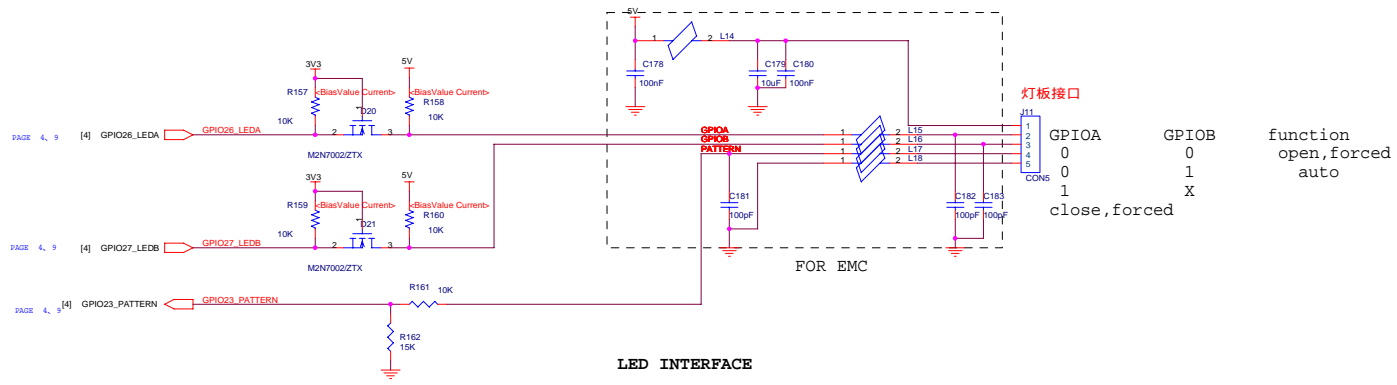
**3.3V----1.2V
Hi3511 core**



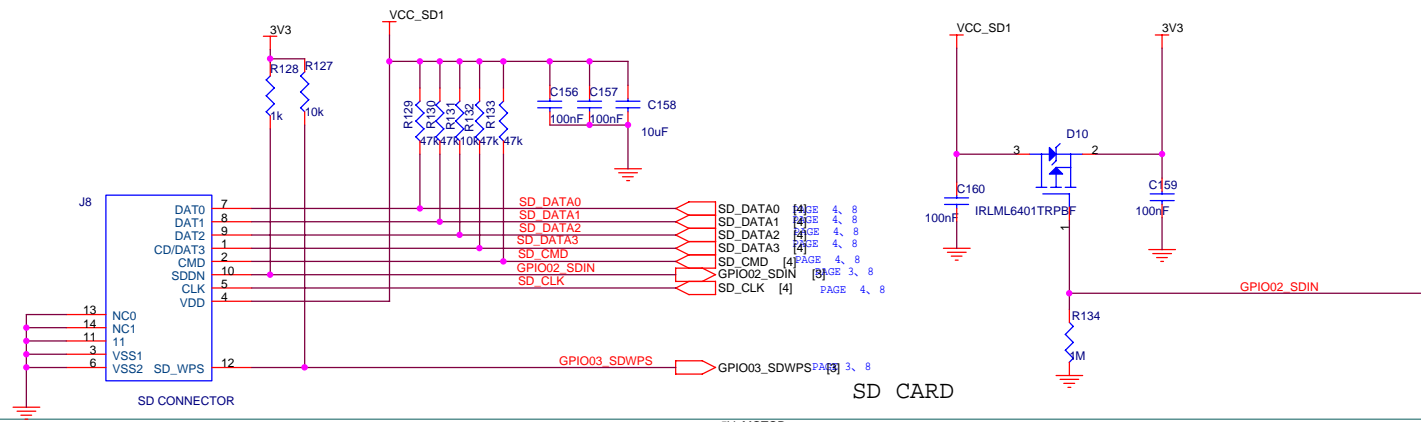
FOV7720 & TLV320AIC32 core and DDR2



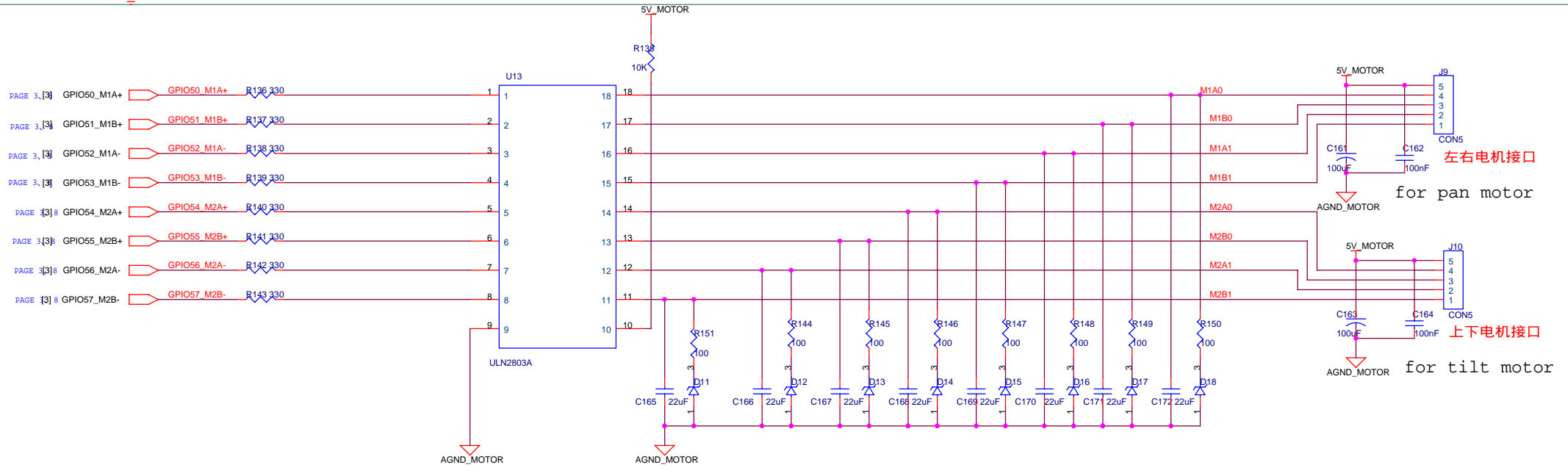
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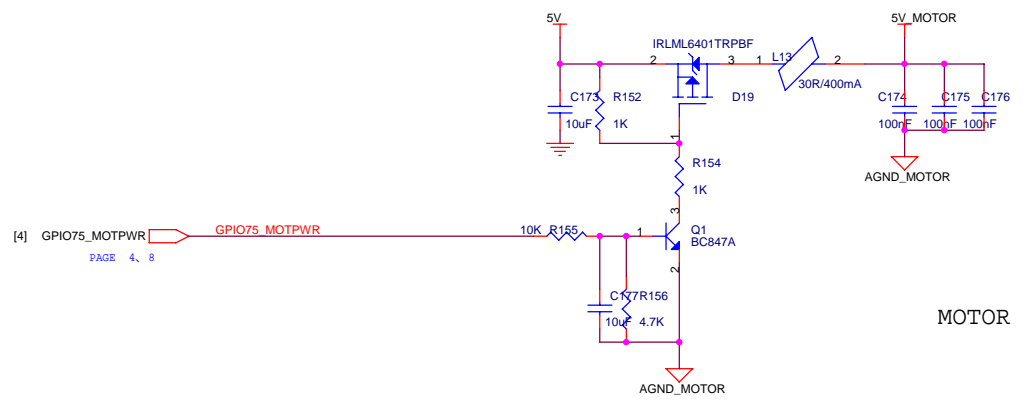
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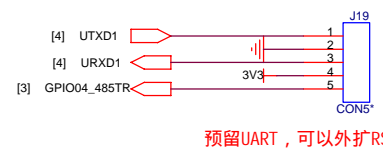
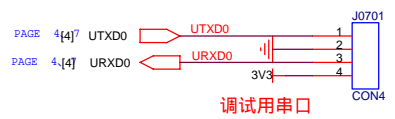
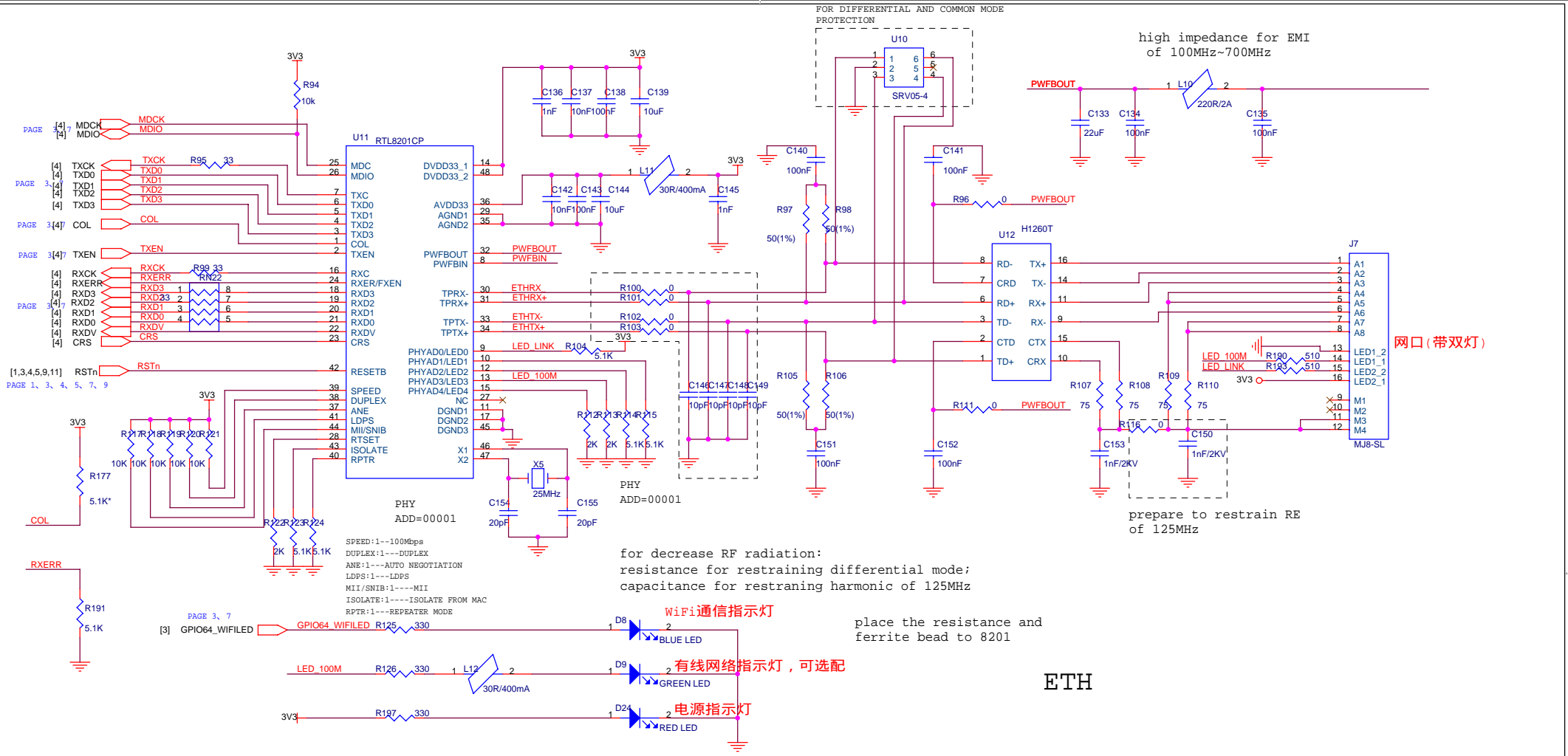
SD CARD



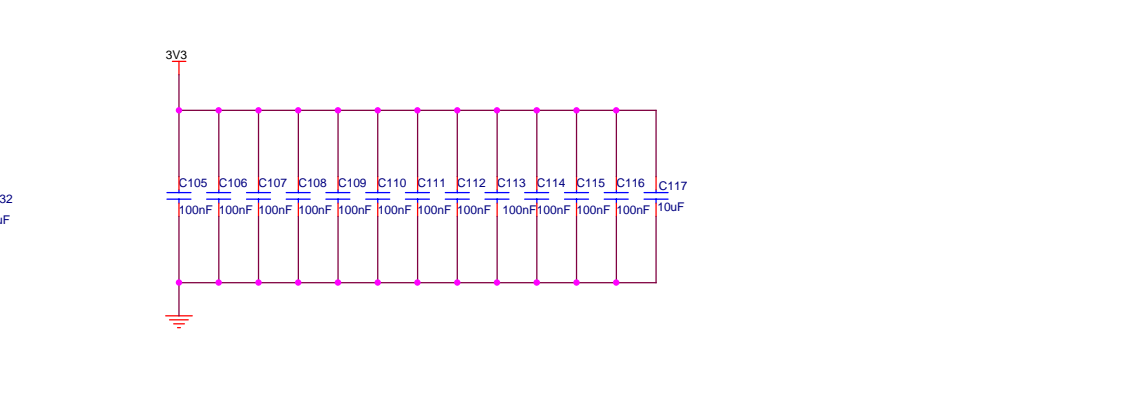
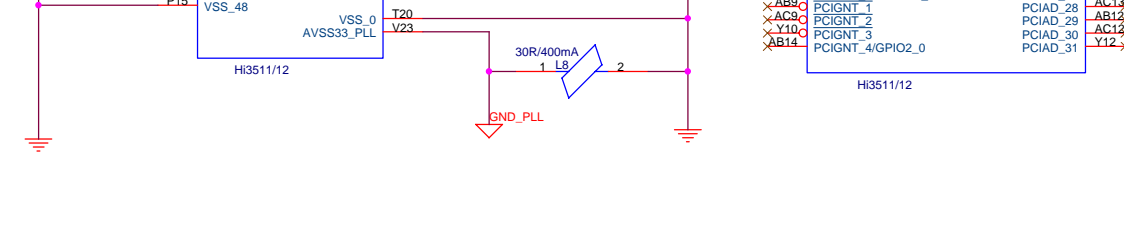
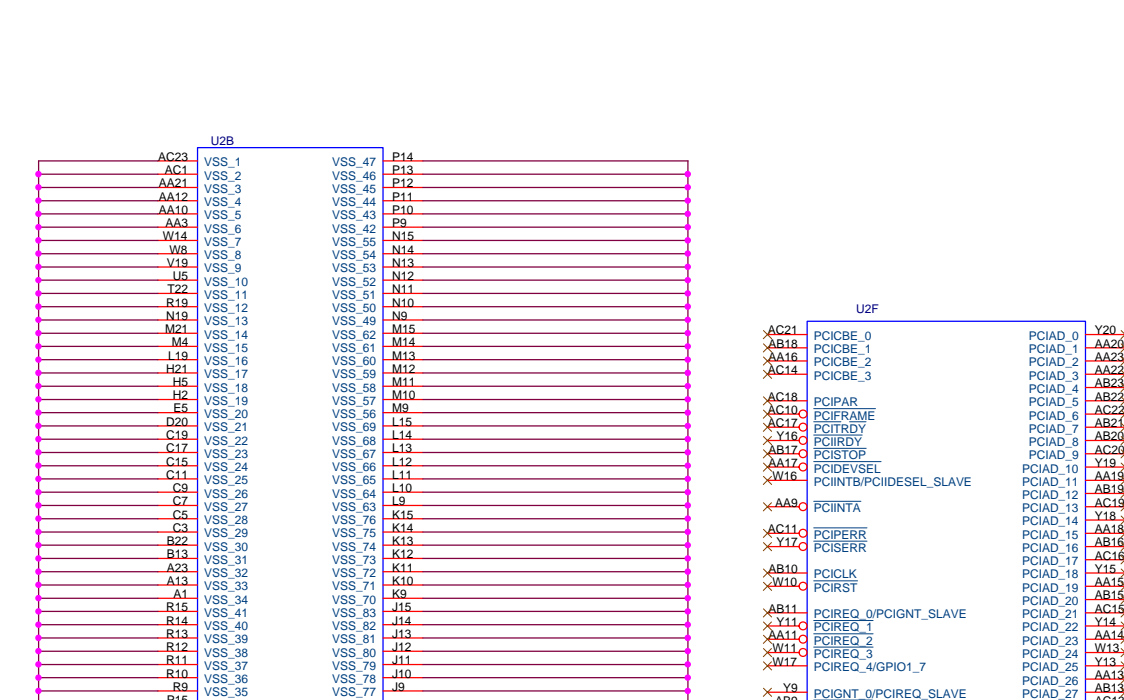
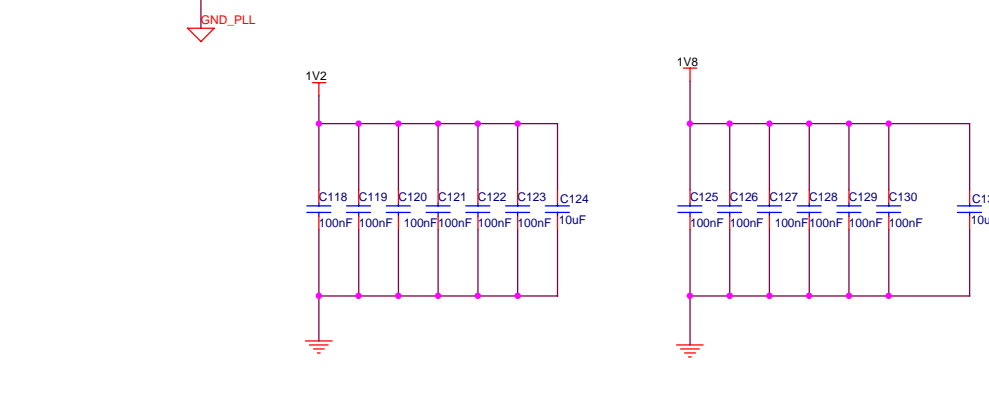
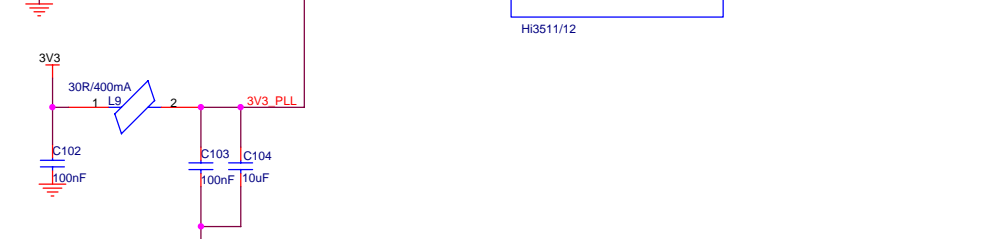
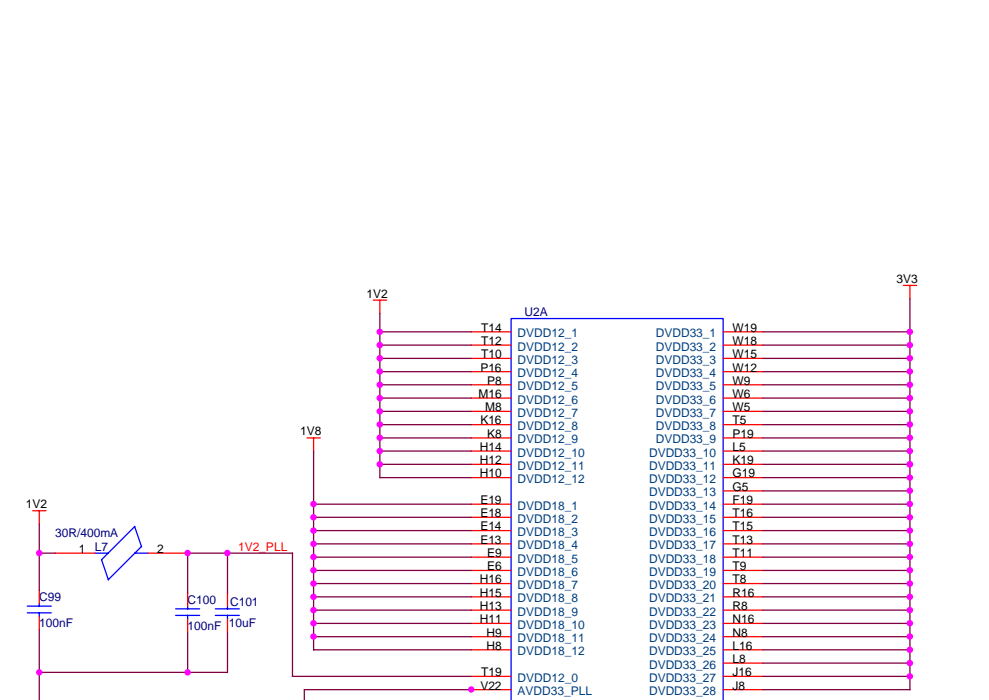
MOTOR



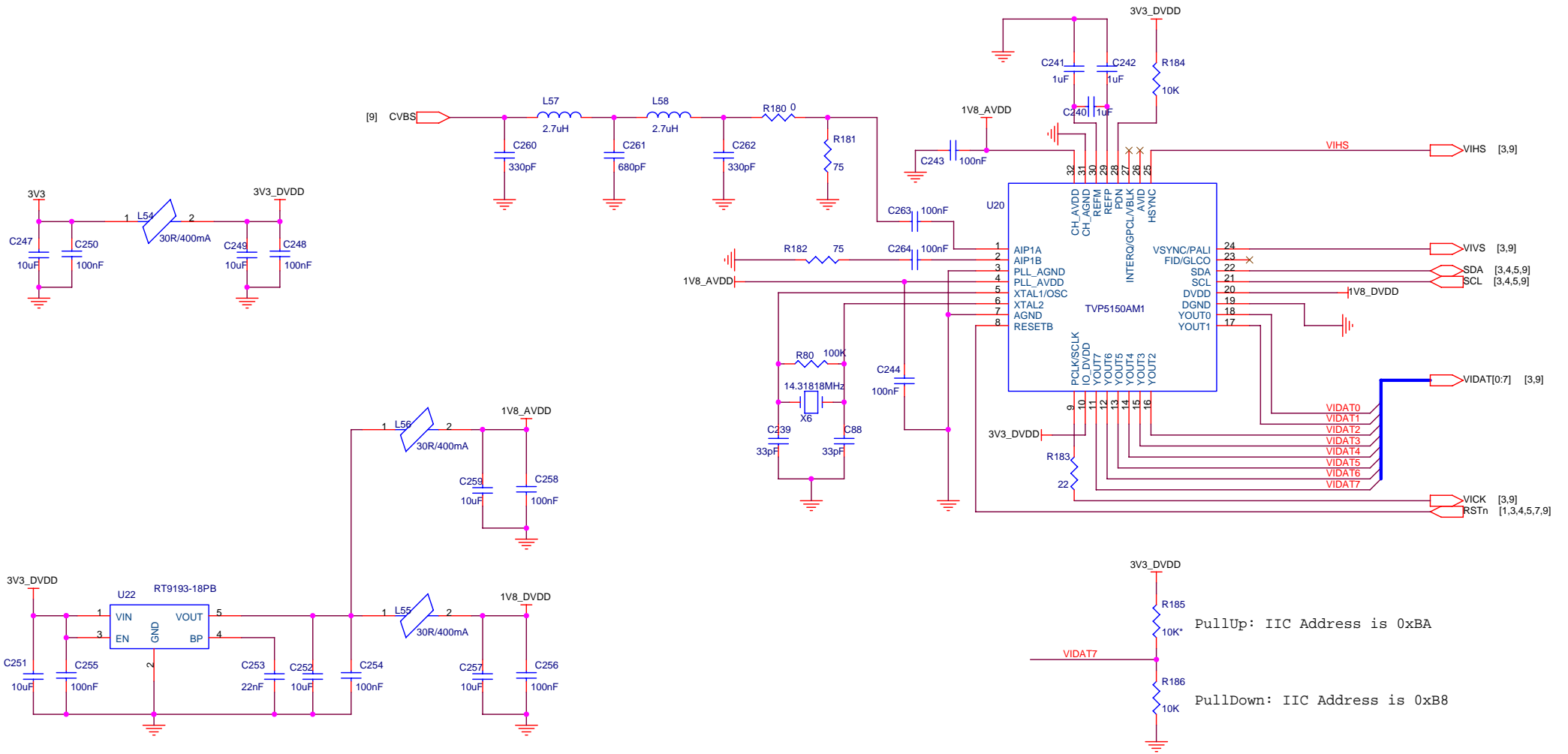
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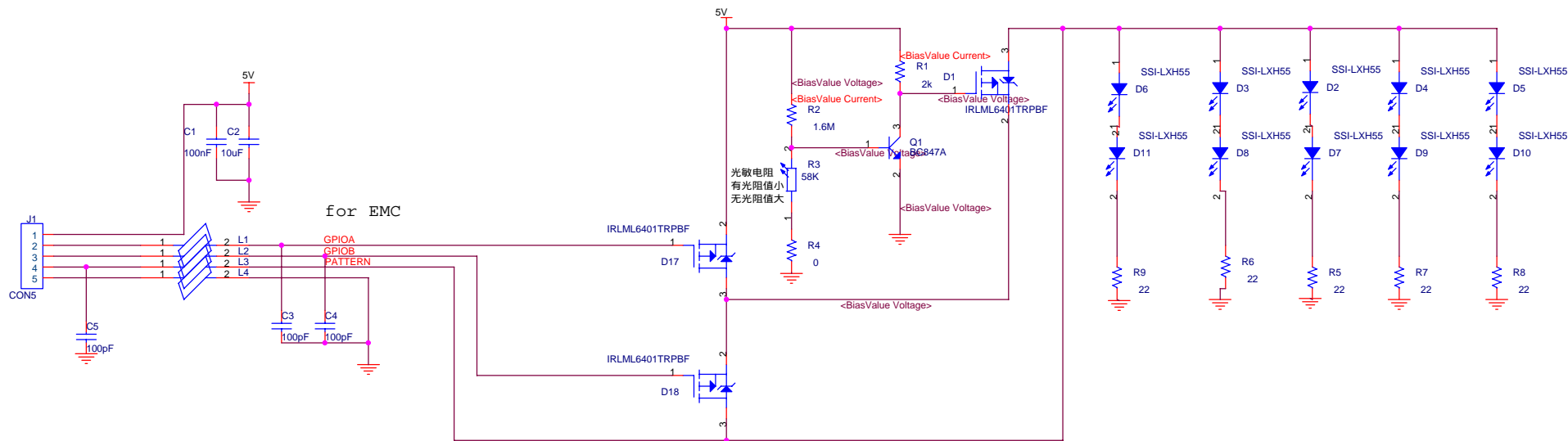
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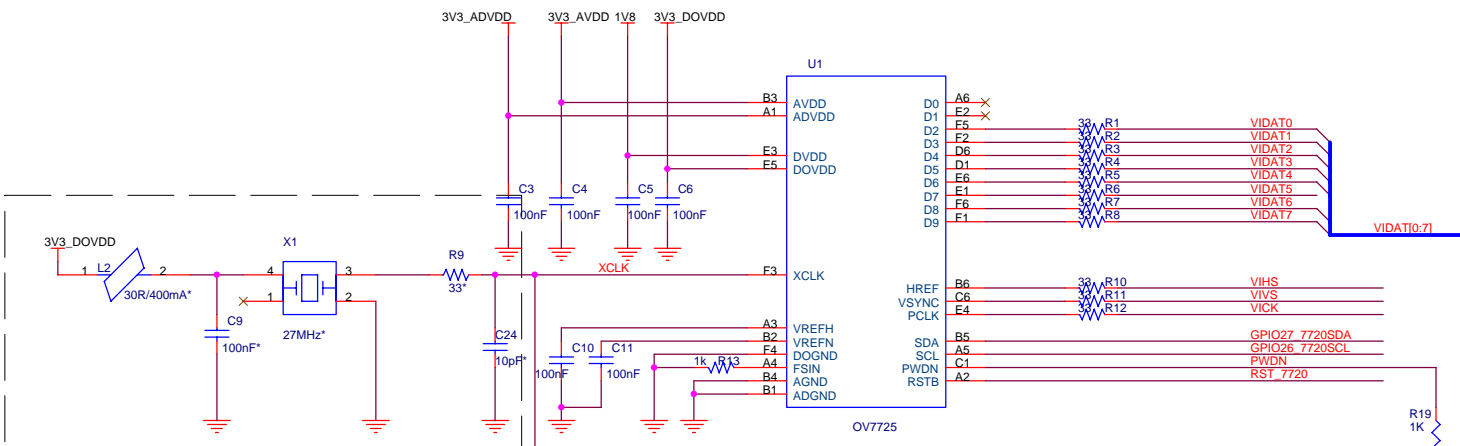


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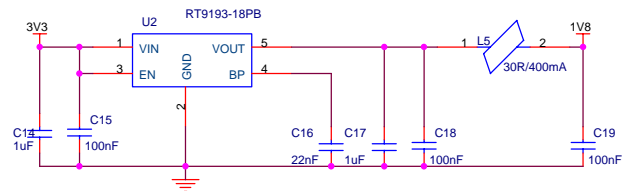
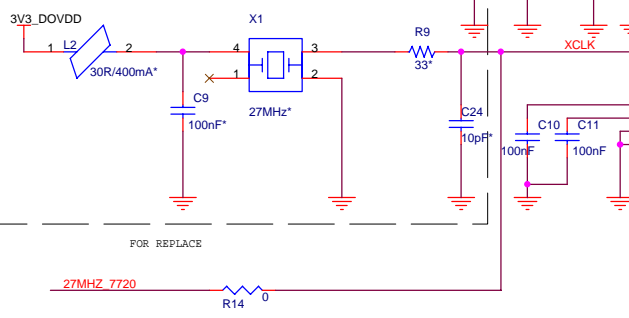
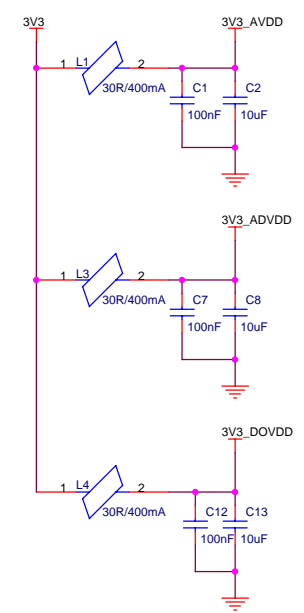
光敏电阻
有光阻值小
无光阻值大

GPIOA	GPIOB	function
0	0	open,forced
0	1	auto
1	X	close,forced

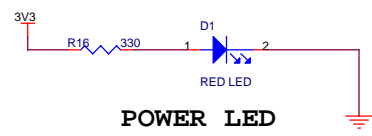
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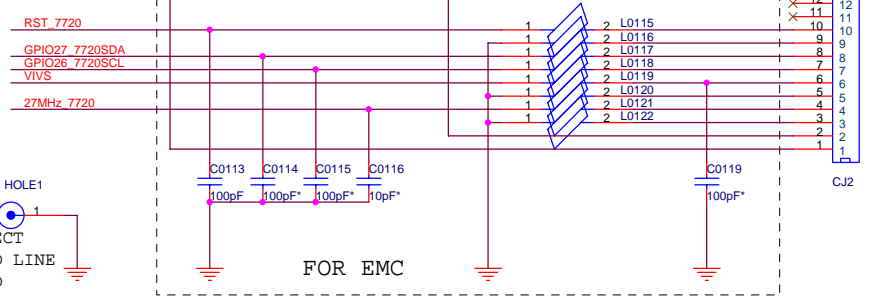
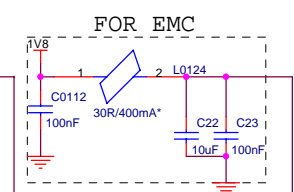
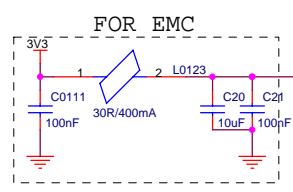
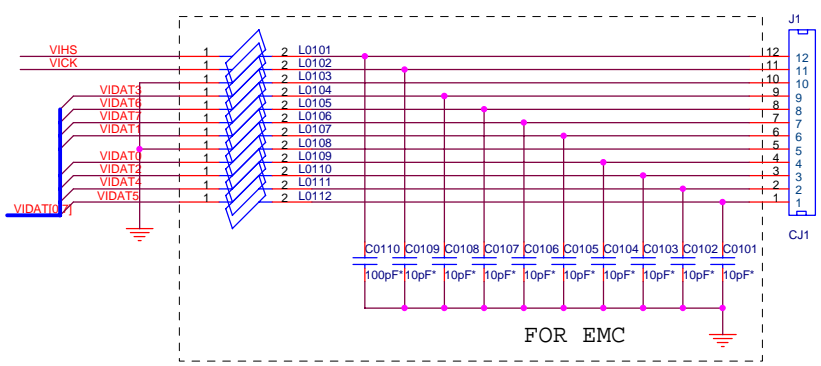
OV7725



prepared for 7725 core



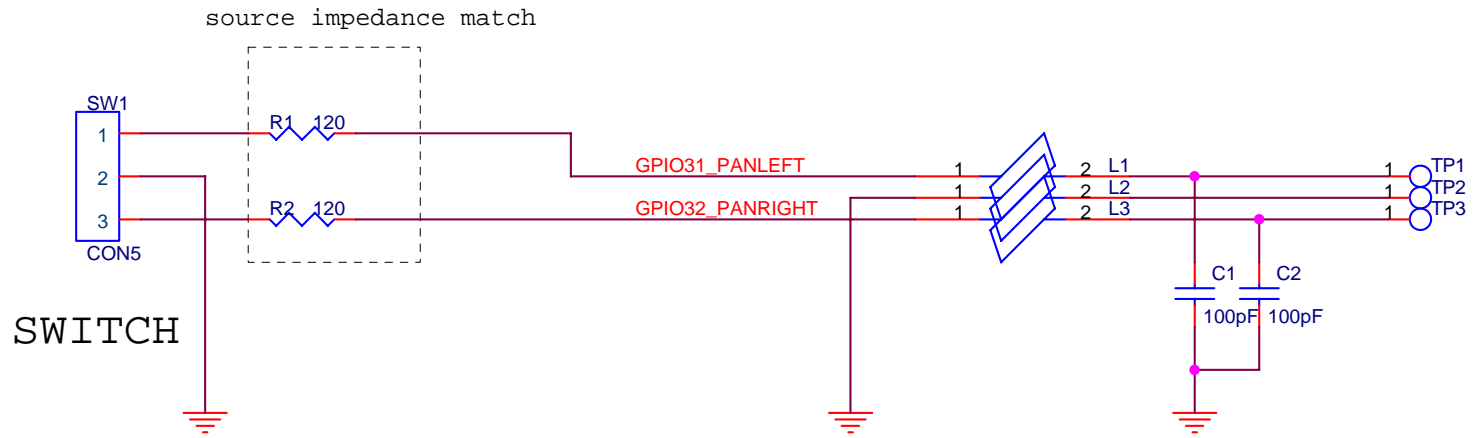
POWER LED



INTERFACE

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FOR EMC



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