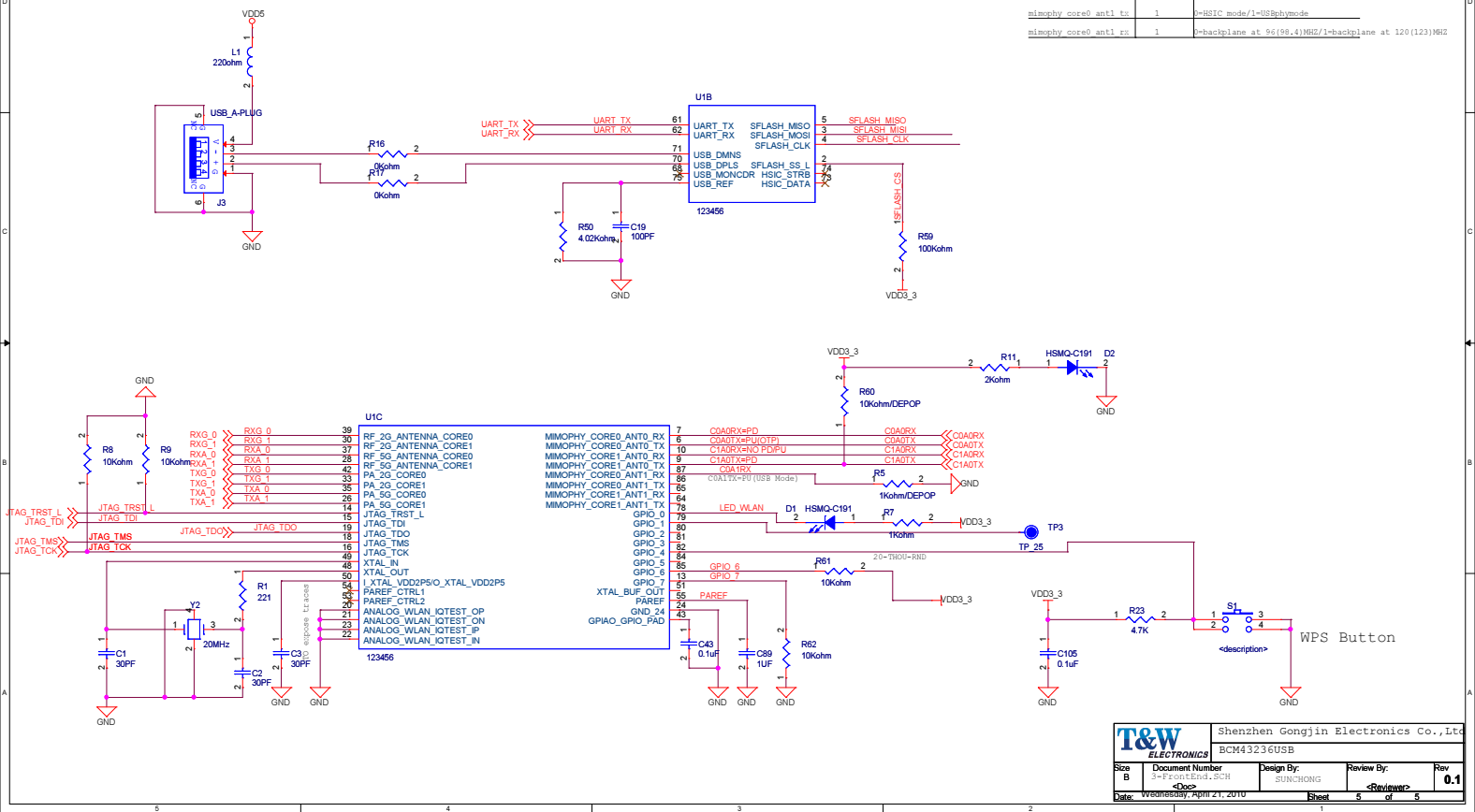






Pad	chip default	description
mimophy_core1_ant0_tx	0	=SFLASH NOT PRESENT/1=SFLASH PRESENT
mimophy_core0_ant0_rx	0	=SFLASH Type is Atmel/0=SFLASH Type is ST
mimophy_core0_ant0_tx	1	=OTP NOT PRESENT/1=OTP PRESENT
mimophy_core0_ant1_tx	1	=HSIC mode/1=USBmode
mimophy_core0_ant1_rx	1	=backplane at 96(98.4)MHz/1=backplane at 120(123)MHz



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BCM43236USB			
Size	Document Number	Design By:	Review By:
B	3-FPC43236.SCH	SUNCHONG	<Reviewer>
Date: Wednesday, April 21, 2010		Sheet 5 of 5	