

Ant 1. 2412-2462MHz

ANT1

**TX/RX
matching
circuit**

1.2V

1.5V

3.3V

Ant 2. 2412-2462MHz

ANT2

**TX/RX
matching
circuit**

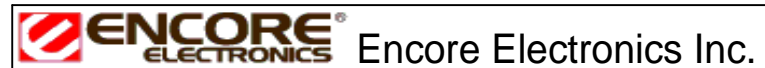
**RTL8192CE (2T2R)
RTL8188RE (1T1R)**

**PCIE
connector**

For 2T2R only

XTAL

40MHz



Title BLOCK DIAGRAM		Model ENDB-ERL8192CE	
Size A	Document Number Schematic of ENDB-ERL8192CE	Drawn By	Rev 0B
Date: Sunday, September 12, 2010		Sheet 2 of 3	