LBT-HS02 headset circuit principle description

The headset uses the ISSC chip IS1636 (U1) with bluetooth2.1+EDR protocol.

Rechargeable polymer li-ion battery supplies power for the headset, the voltage is 3.7V, the polymer li-ion battery is charged by external DC 5V through LDO IC (U3),the current and voltage is managed by U1.

L4,C20 and IC internal circuit make up of BUCK circuit and convert the LI-ION battery's voltage to 1.8v to supply power for U1.

X1 crystal oscillator (16M) supply clock frequency for bluetooth IC (U1),and through multiplicator provide 2.441G frequency for RF

C20,R6,Q1,C23 compose the pulse reset circuit.

The antenna is made on the PCB panel with L1,L2,C2 matched for the 50omh impedance 2.441G selected frequecy net.

E PROM (U2) is used to store the custom parameter by user.