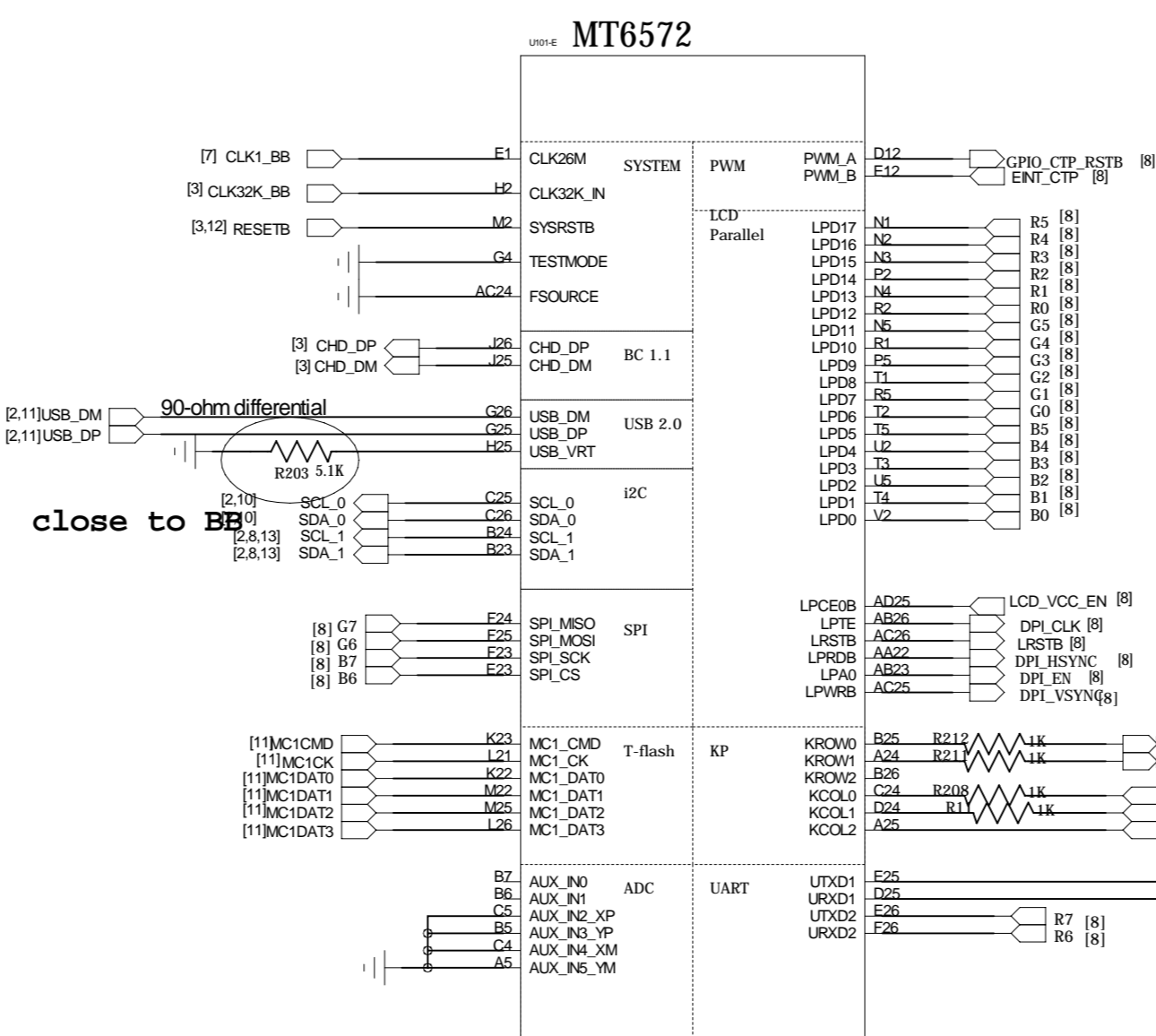
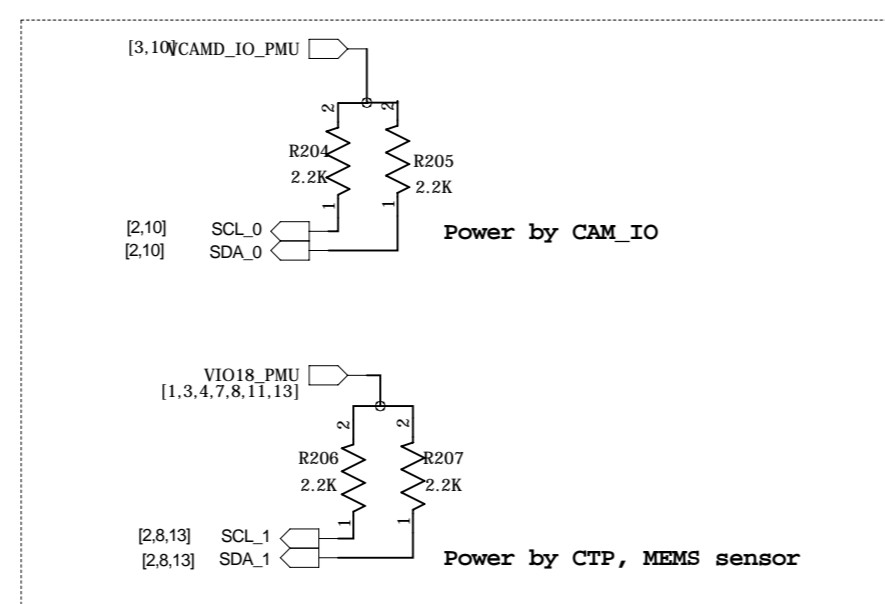
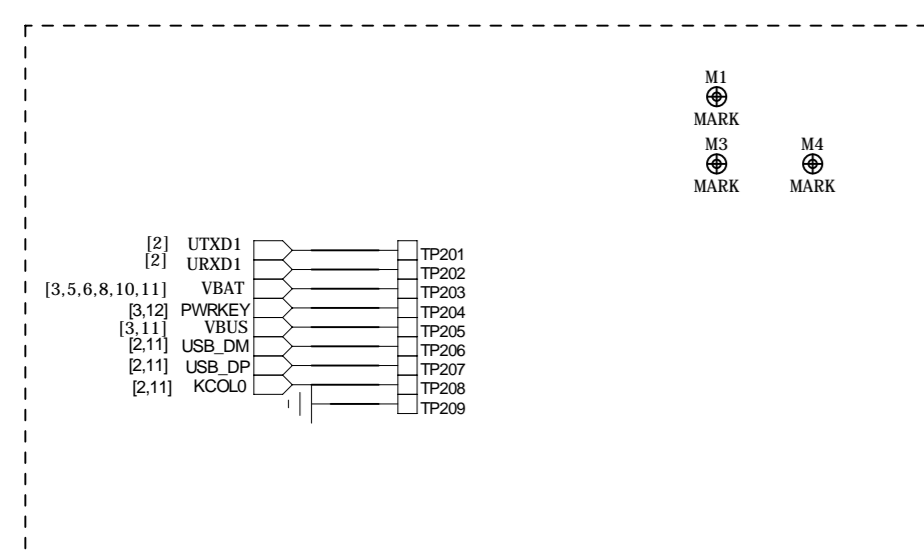
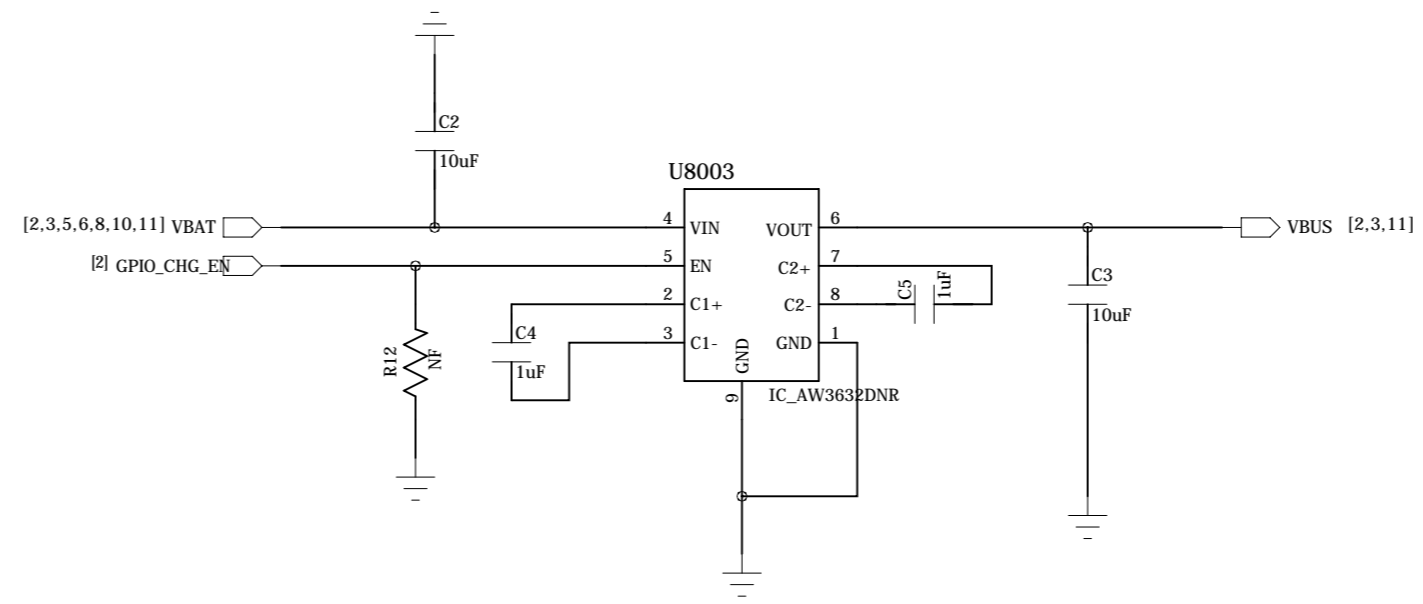


Based on your system level design, if better dense performance is needed on your system, please refer to desense performance enhance proposal

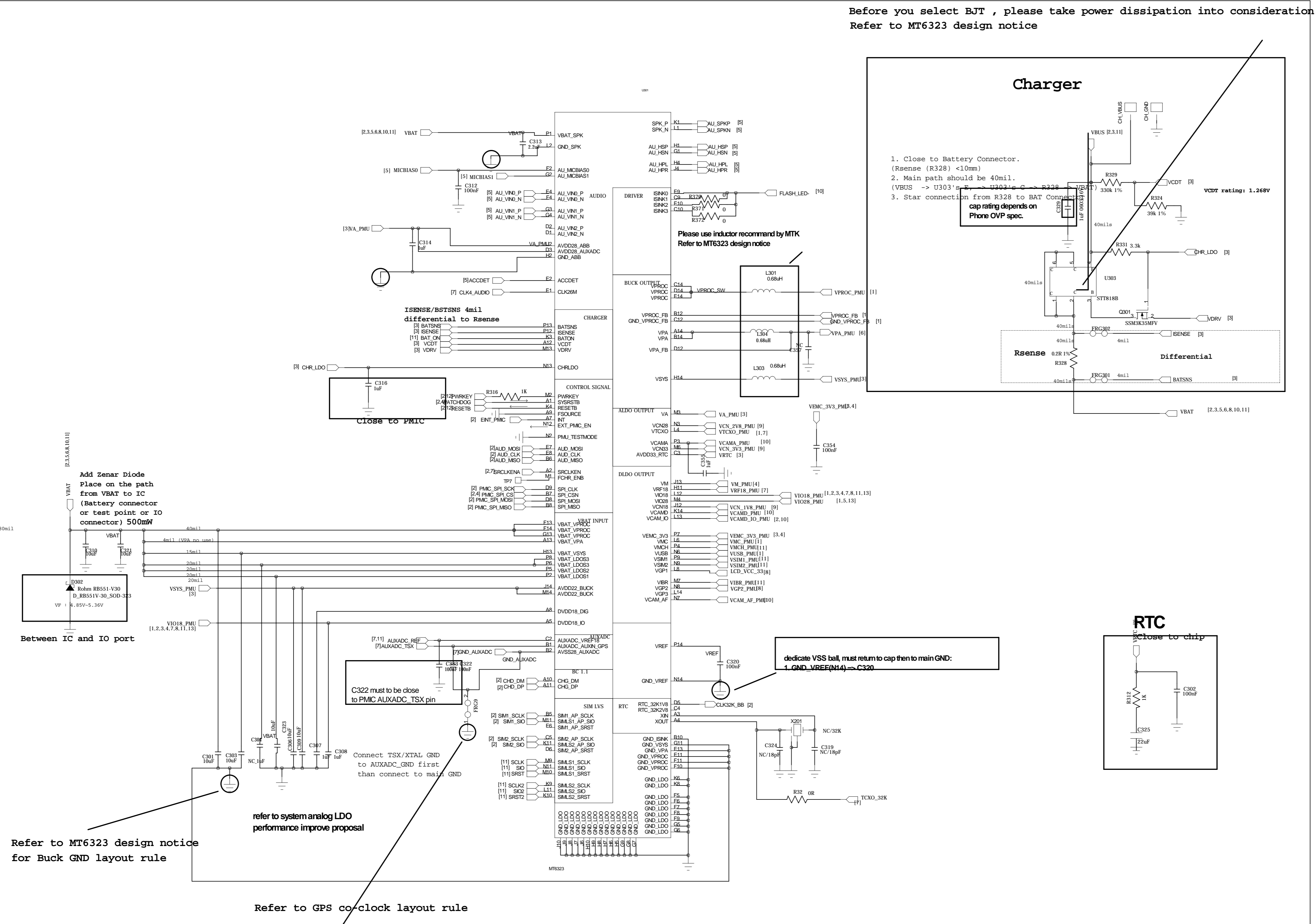


MT6572 support JTAG from below:
 1. KP (recommand)
 2. MC1
 3. CAM
 for JTAG pin out from MC1/CAM, refer to HW design notice





Before you select BJT , please take power dissipation into consideration.
Refer to MT6323 design notice



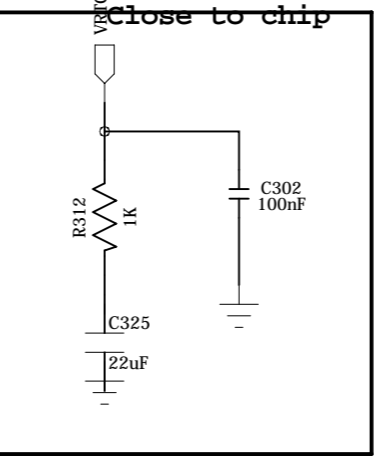
Charger

1. Close to Battery Connector.
(Rsense (R328) <10mm).
2. Main path should be 40mil.
(VBUS -> U303's EN -> U202's C -> R328)
3. Star connection from R328 to BAT Connector

cap rating depends on Phone OVP spec.

vccr rating: 1.268V

RTC

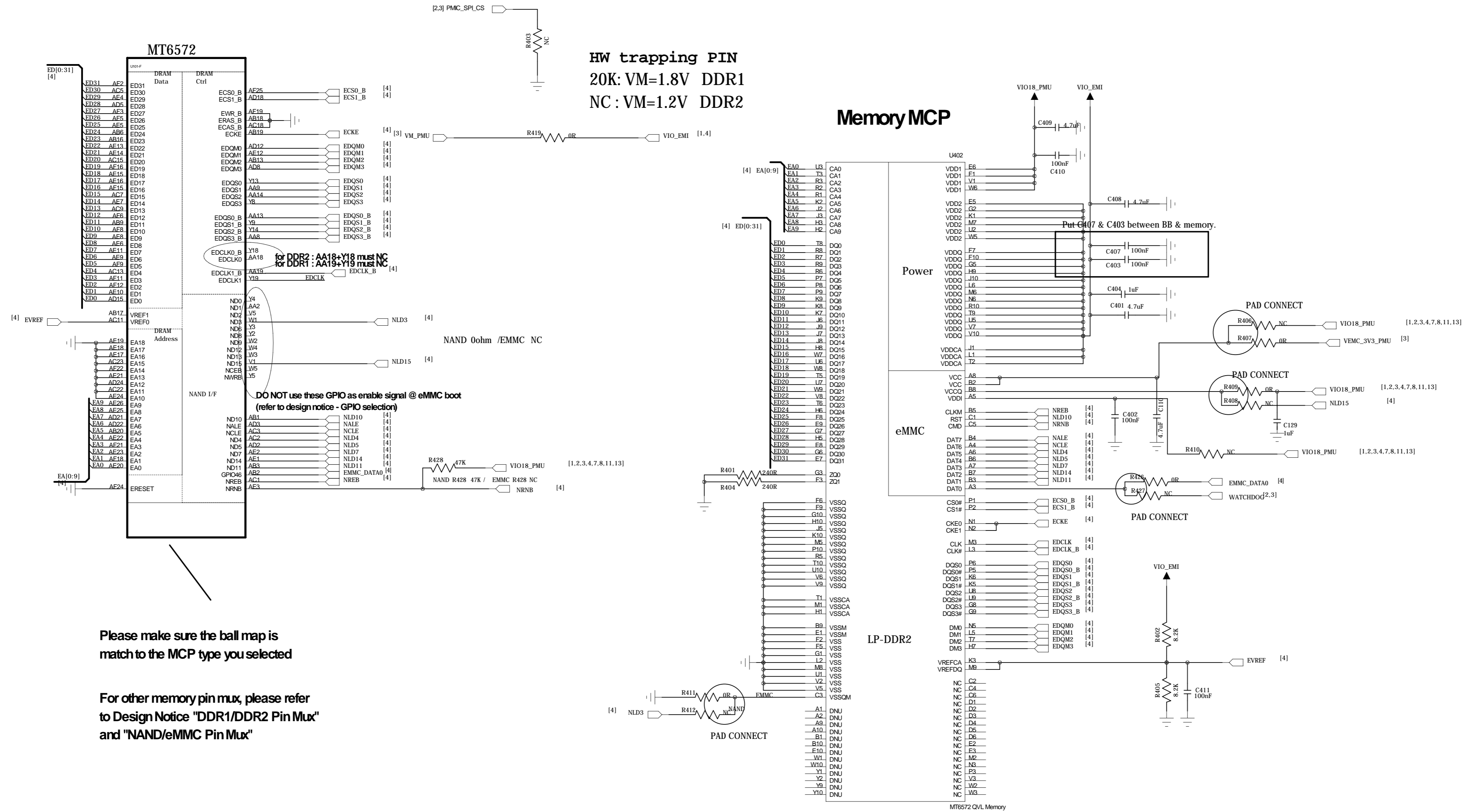


dedicate VSS ball, must return to cap then to main GND:
1 GND_VREF(N14) -> C320

Refer to MT6323 design notice for Buck GND layout rule

refer to system analog LDO performance improve proposal

Refer to GPS co-clock layout rule

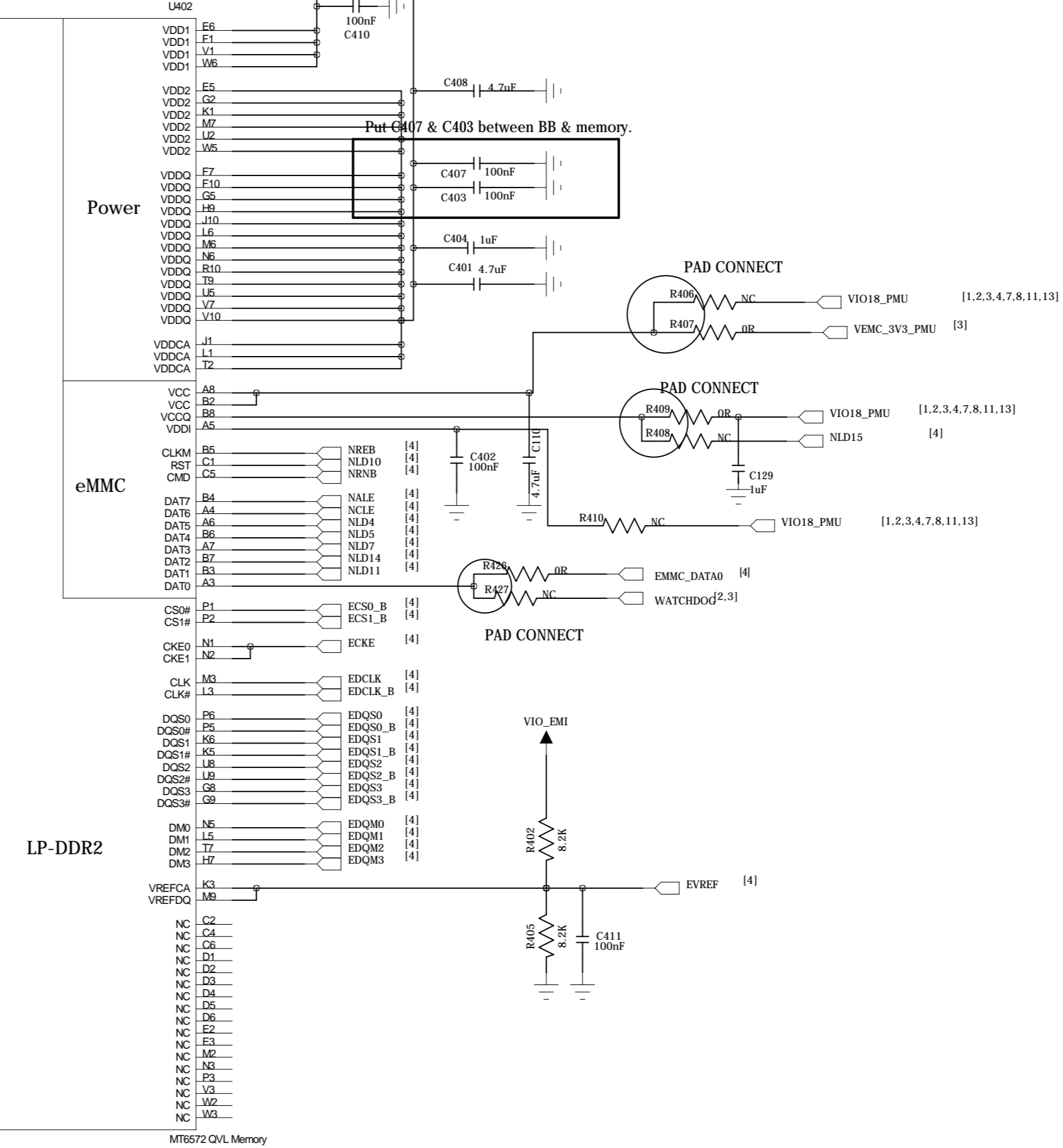


Please make sure the ball map is match to the MCP type you selected

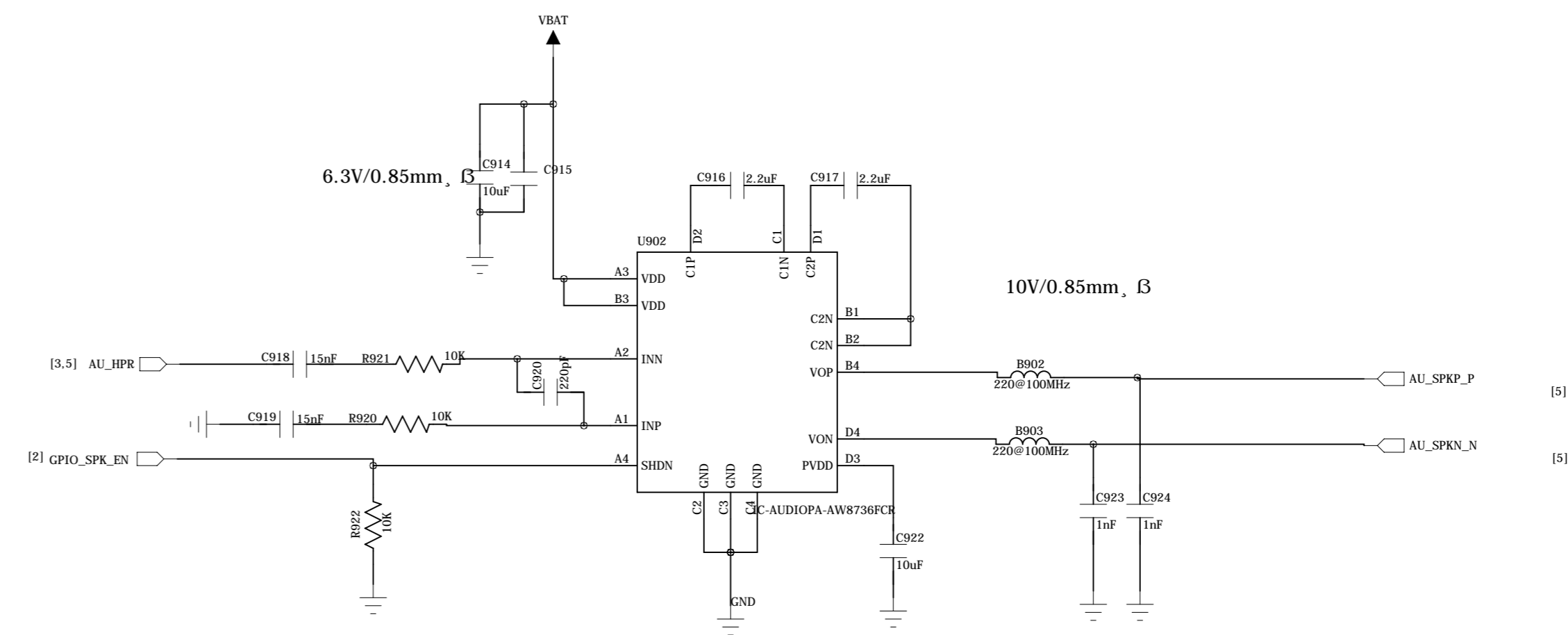
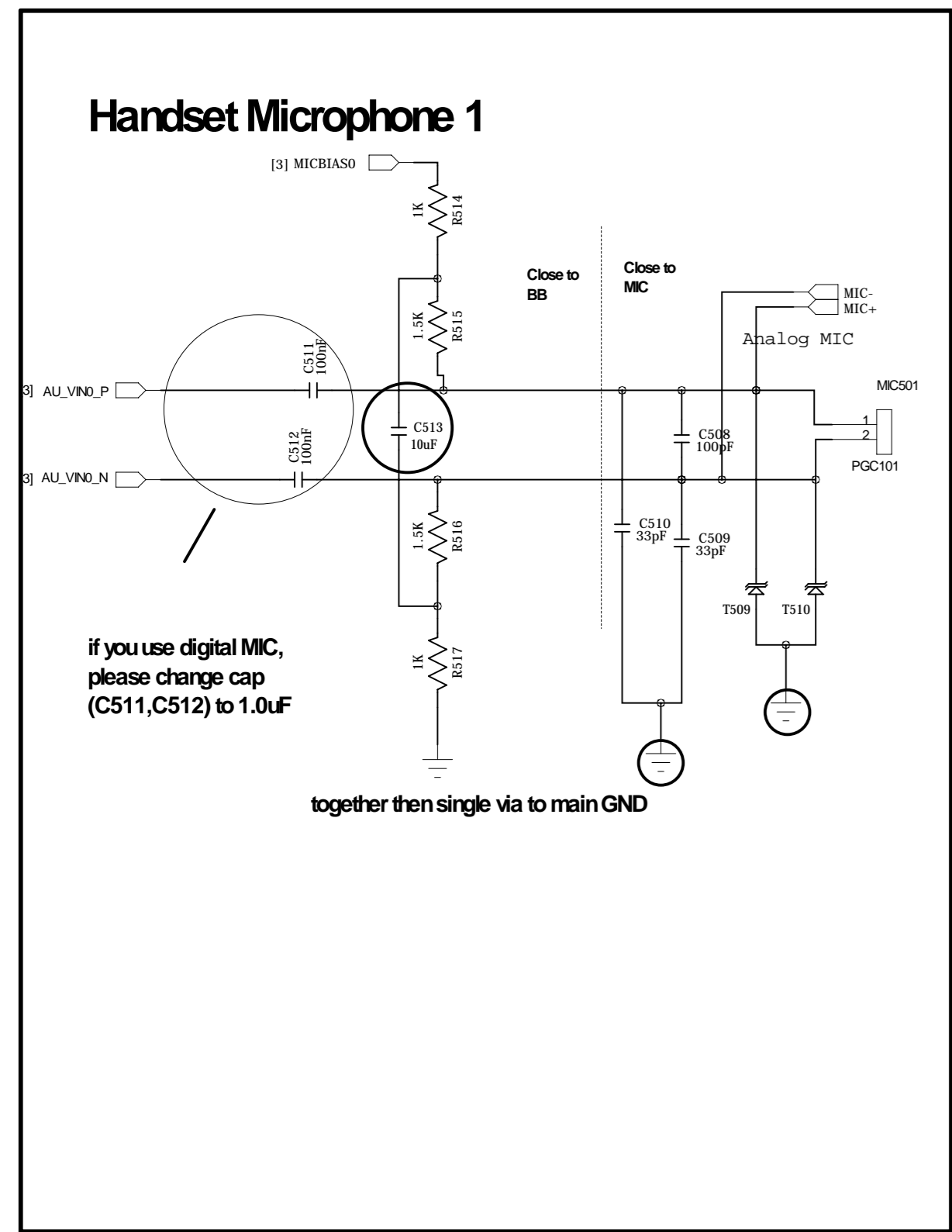
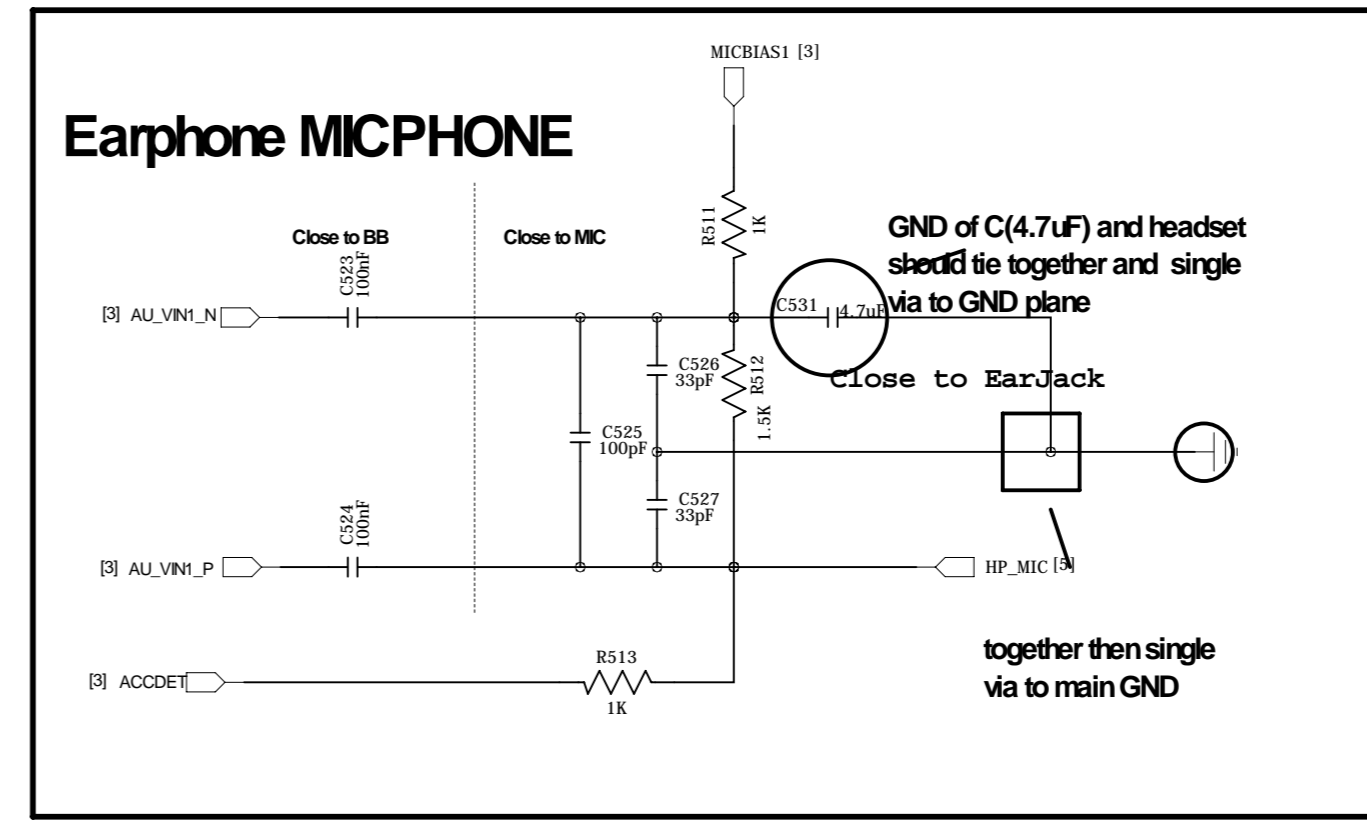
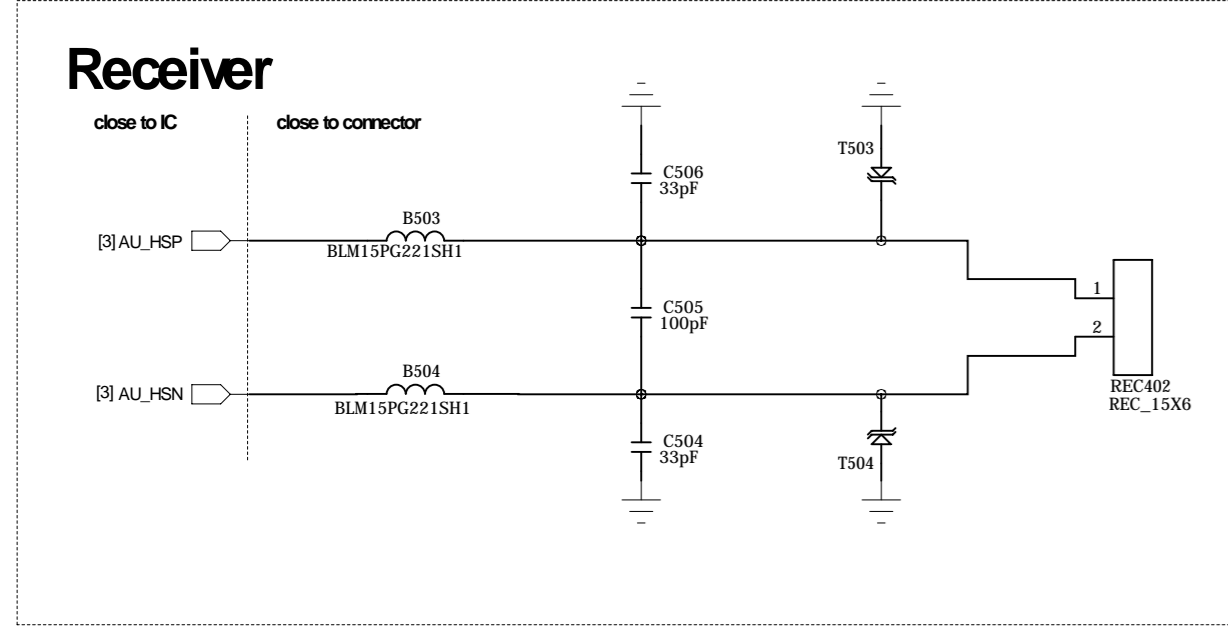
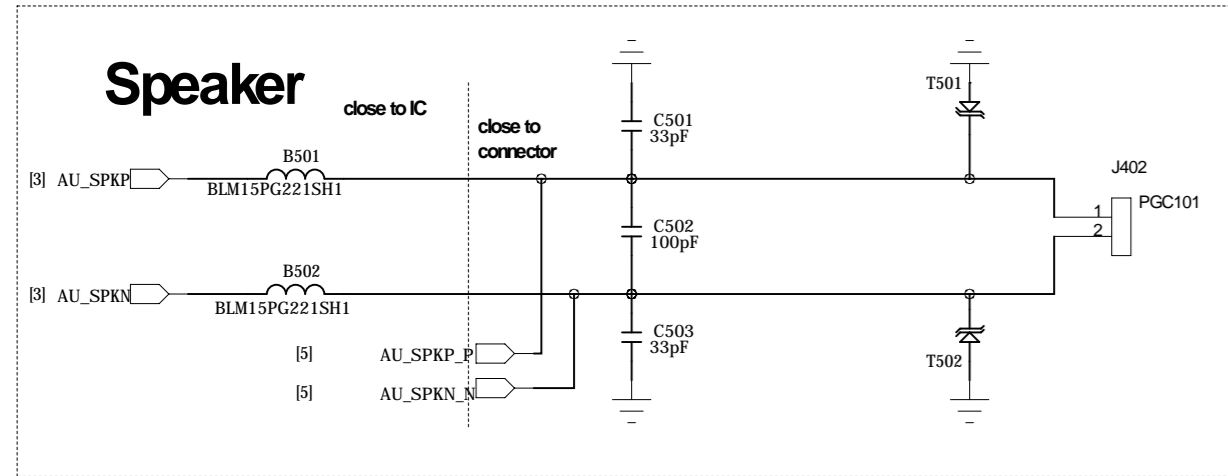
For other memory pin mux, please refer to Design Notice "DDR1/DDR2 Pin Mux" and "NAND/eMMC Pin Mux"

HW trapping PIN
20K: VM=1.8V DDR1
NC : VM=1.2V DDR2

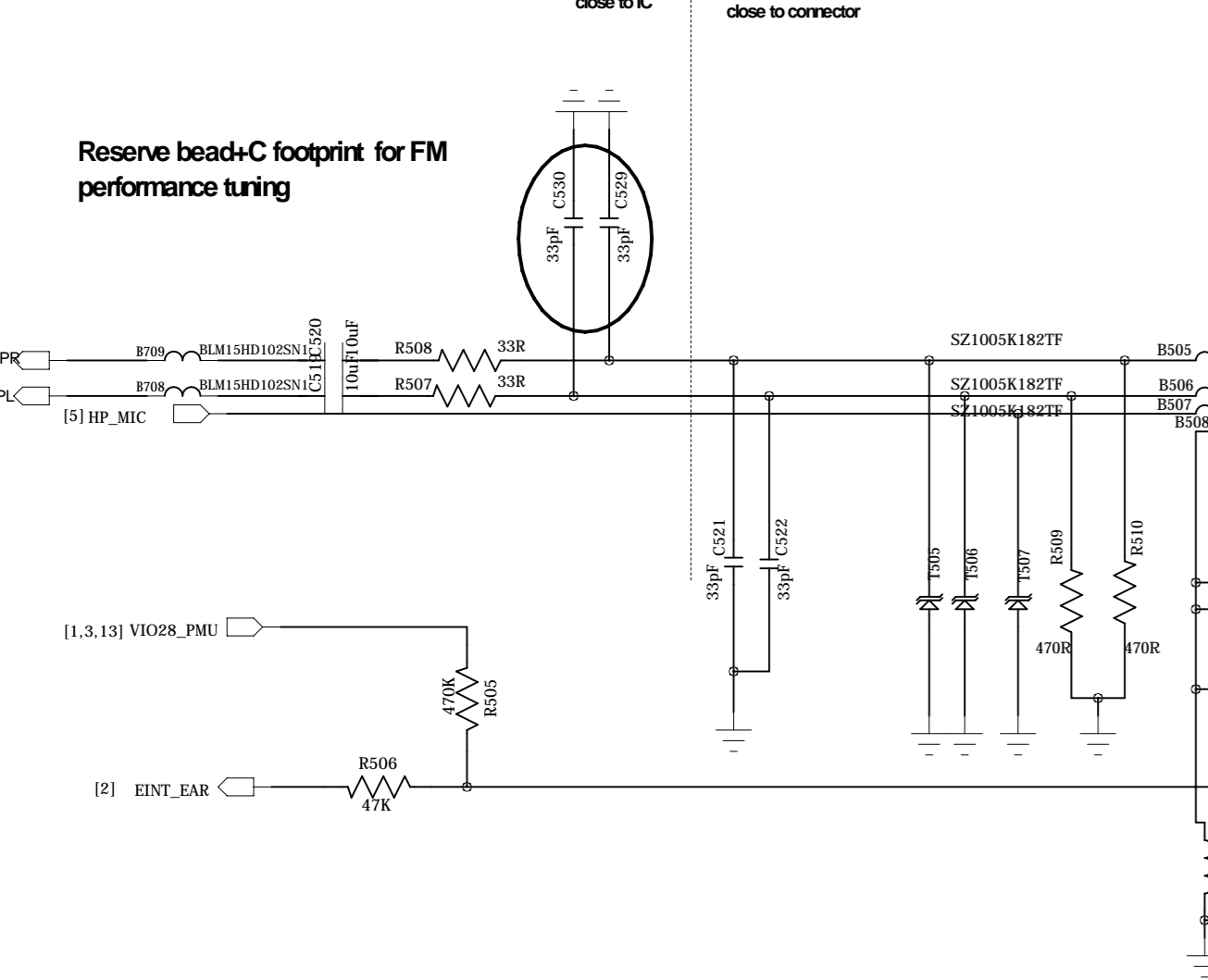
Memory MCP



LP-DDR2

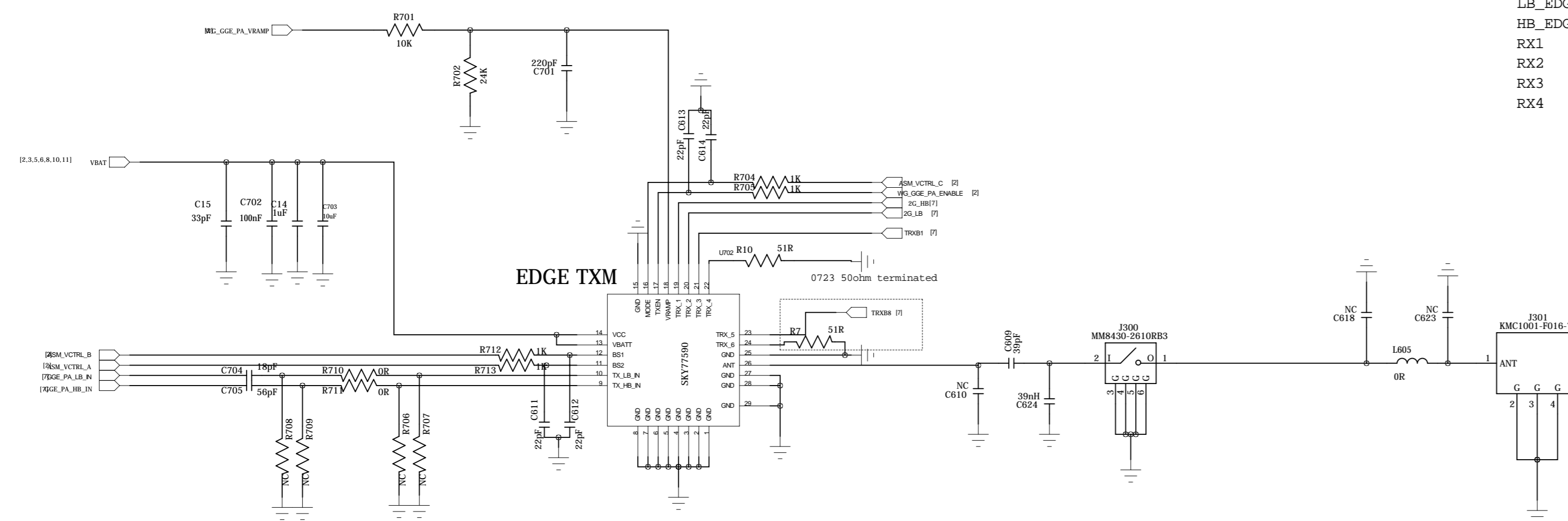


Earphone Audio



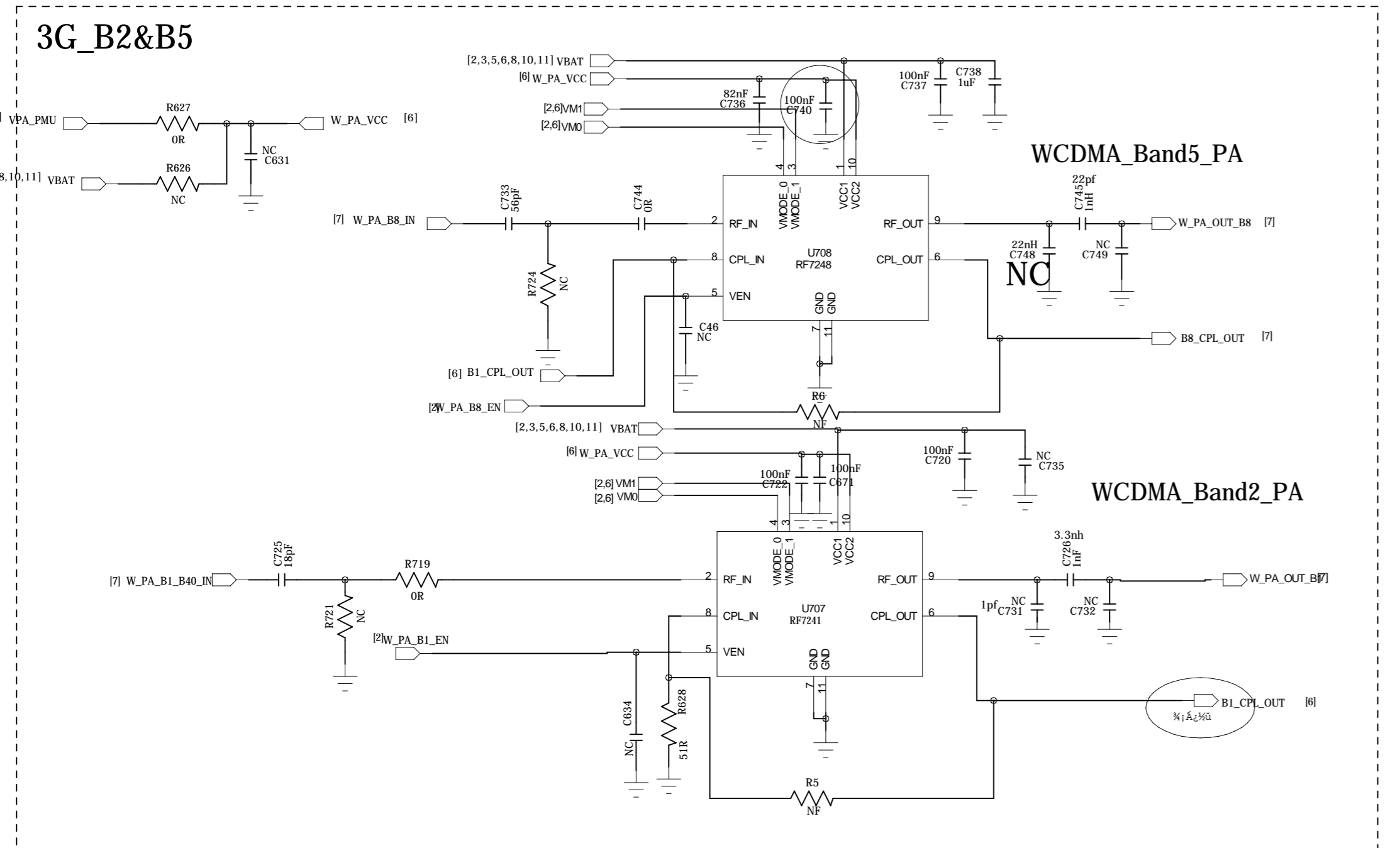
	B507	B508	R501	R502
NOKIA	Ìù	NC	Ìù	NC
SAMSUNG	NC	Ìù	NC	Ìù

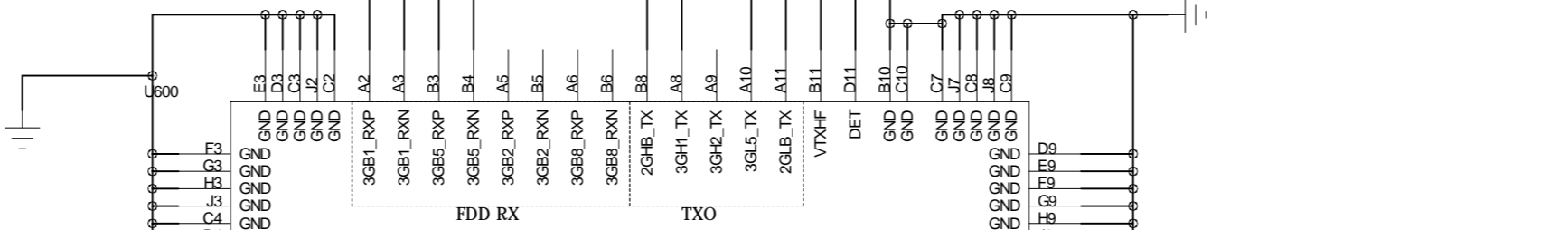
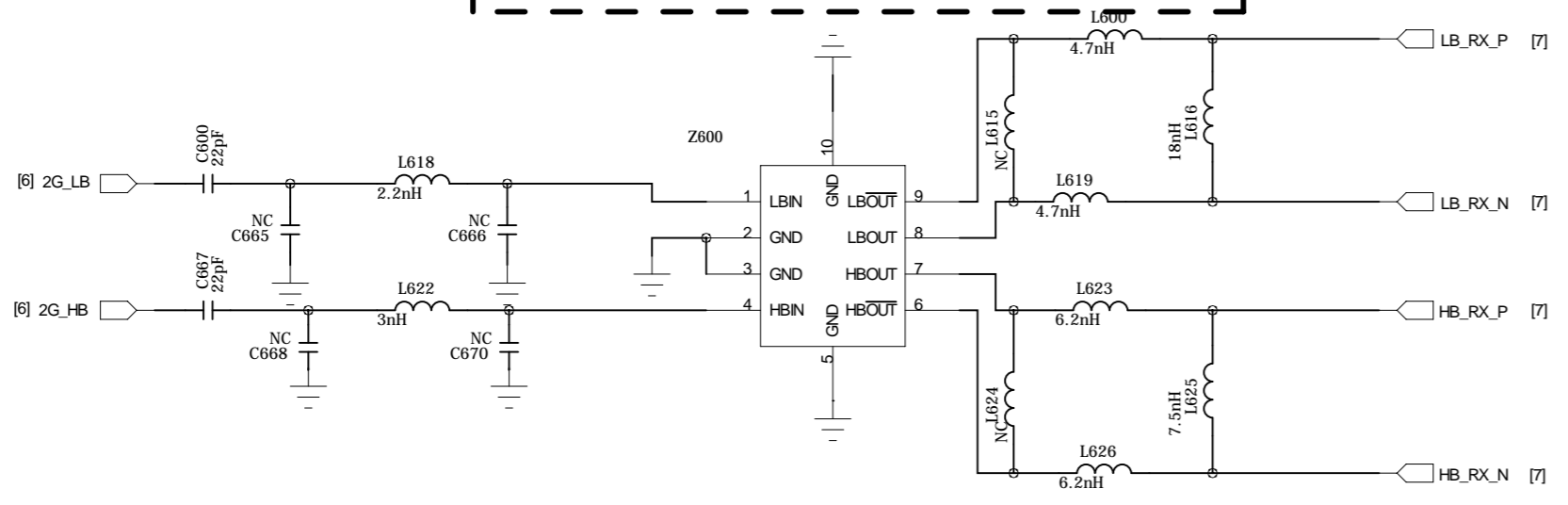
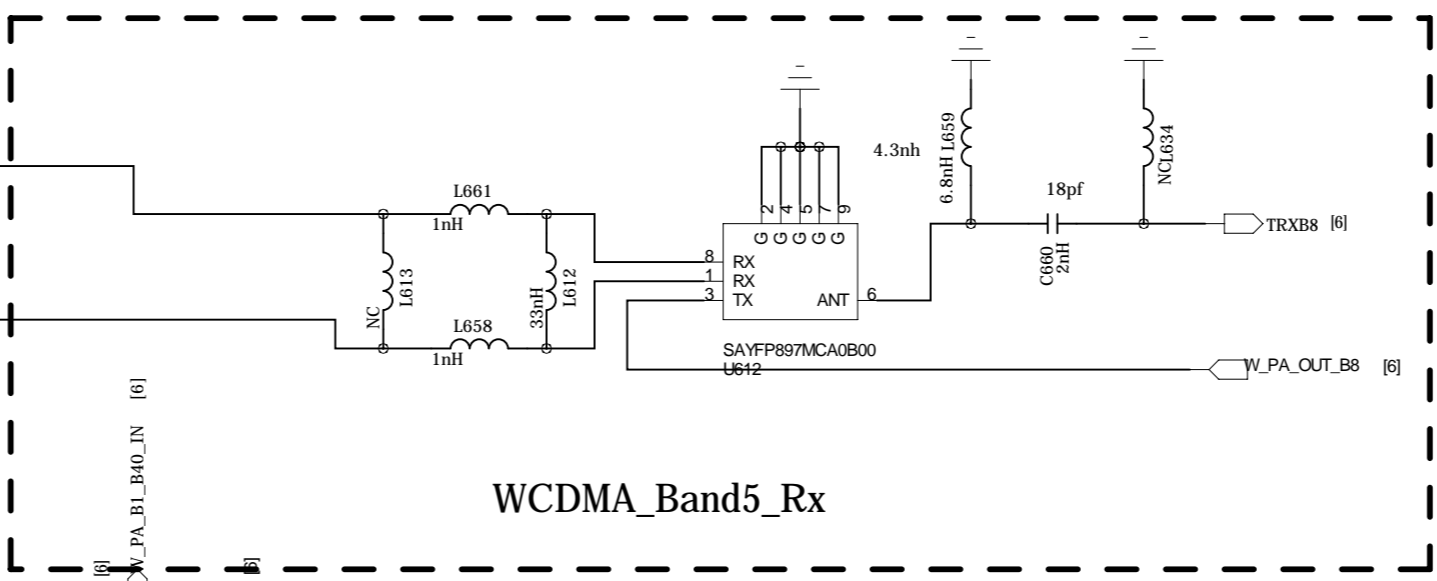
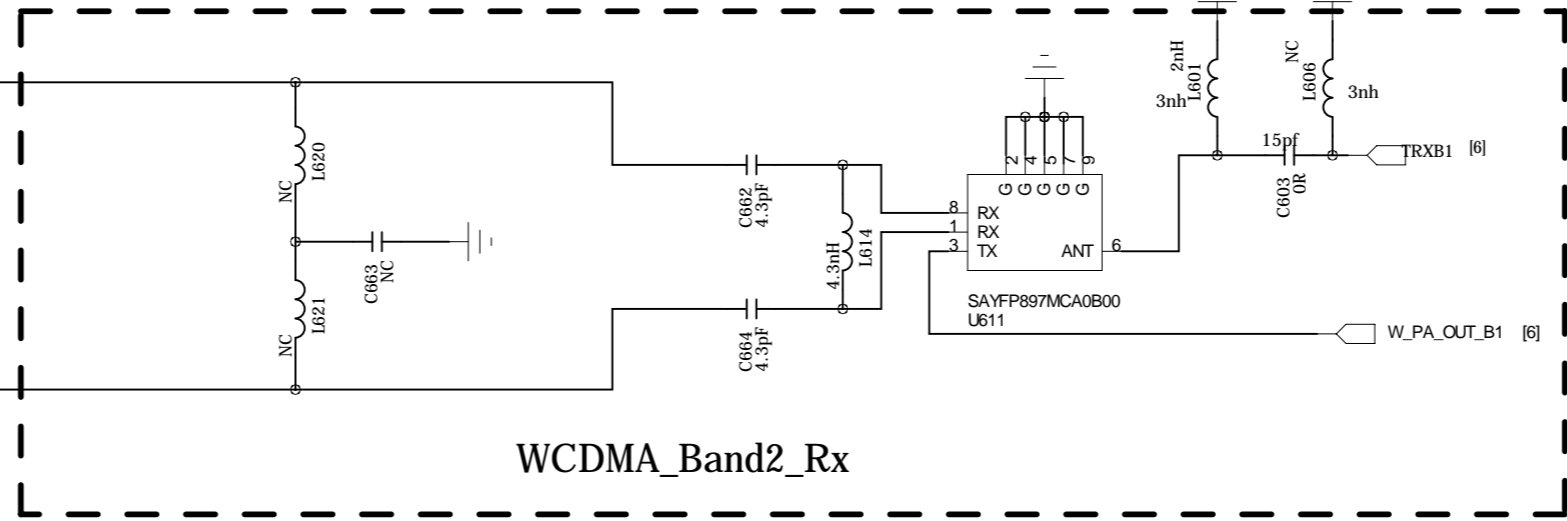
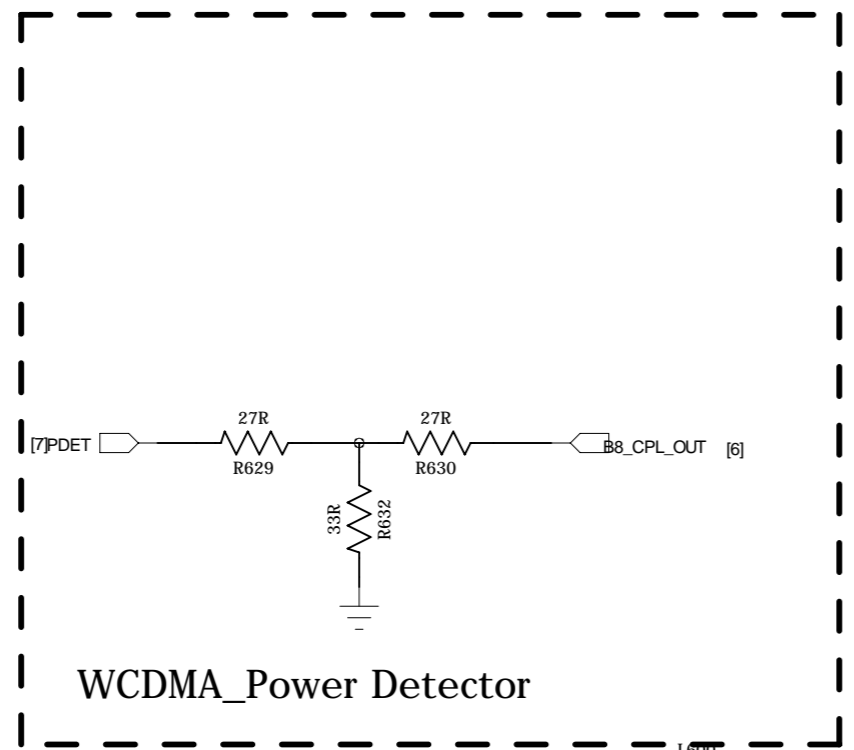
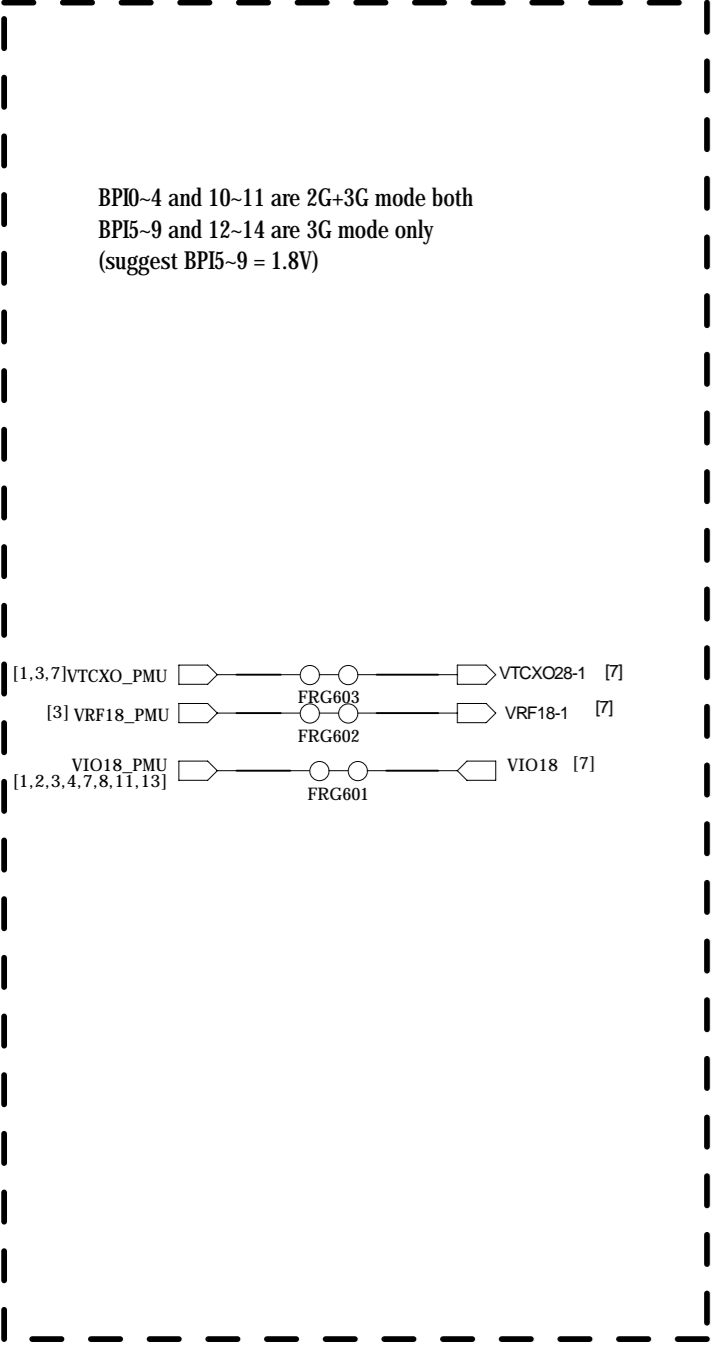
R503 R504



RF9810 control logic table

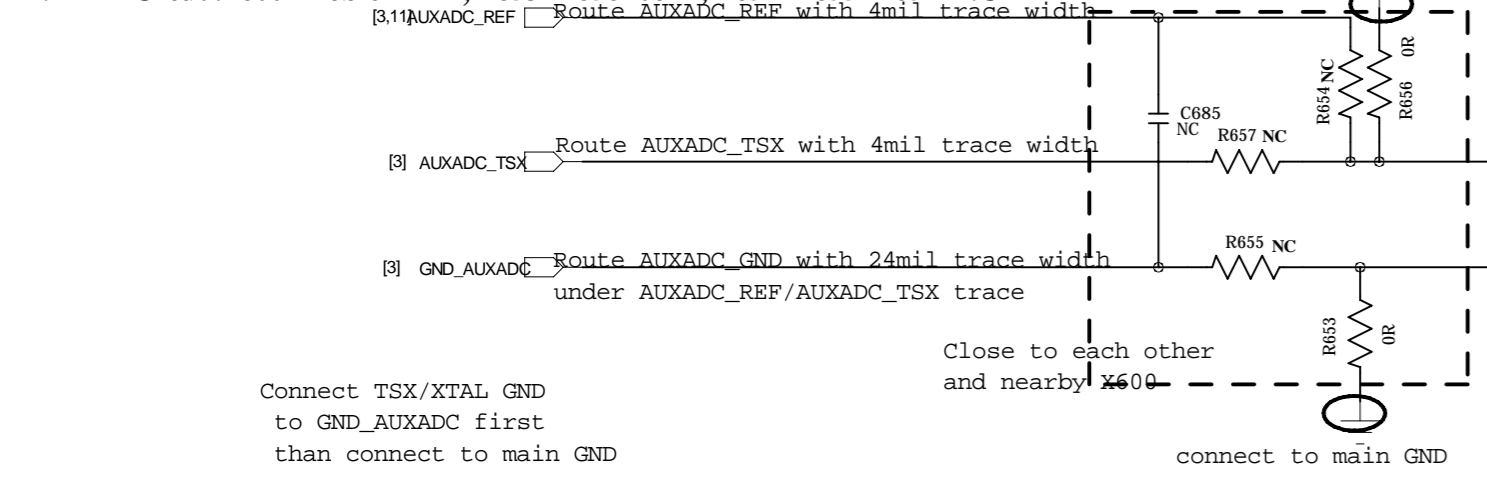
	Enable	VetC	VetB	VetA
LB_GMSK_TX	H	L	H	L
HB_GMSK_TX	H	L	H	H
LB_EDGE_TX	H	L	L	L
HB_EDGE_TX	H	L	L	H
RX1	L	H	L	L
RX2	L	L	H	L
RX3	L	L	H	H
RX4	L	L	L	H



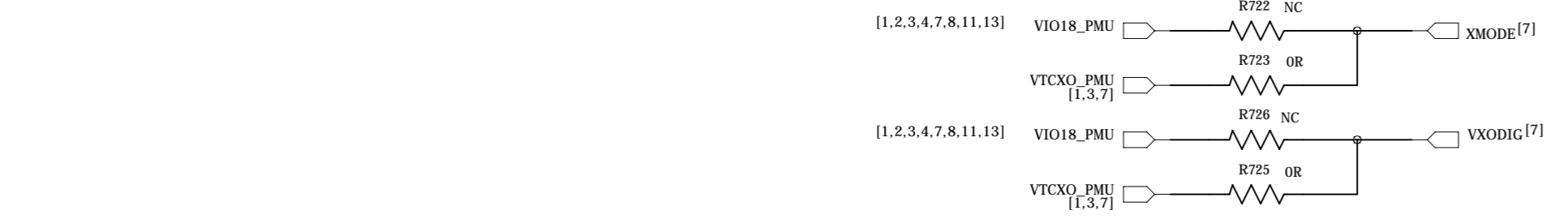


Two Application Circuit Conditions.

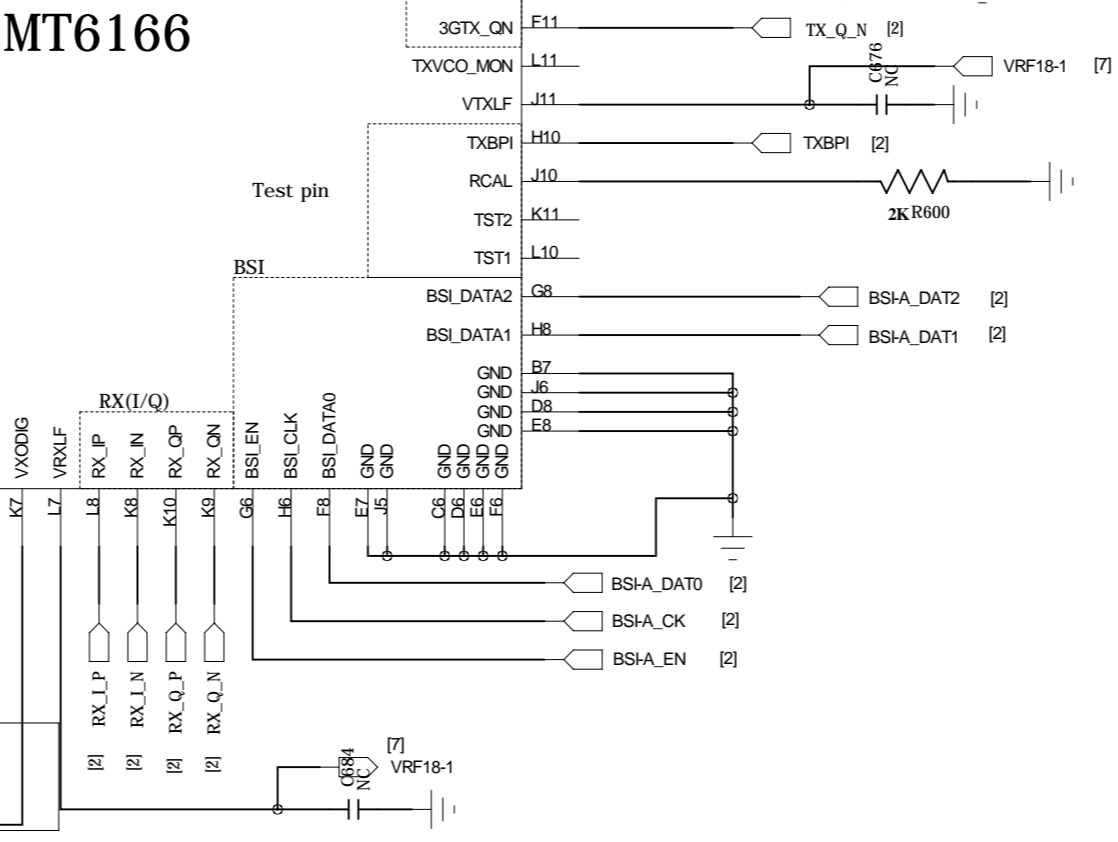
- 1.TSX Circuit : X600=ISX, R653=R656=NC, R654=100K+1%, R655=R657=0ohm
- 2.XTAL Circuit : X600=Mobile XTAL, R653=R656=0ohm, R654=R655=R657=NC



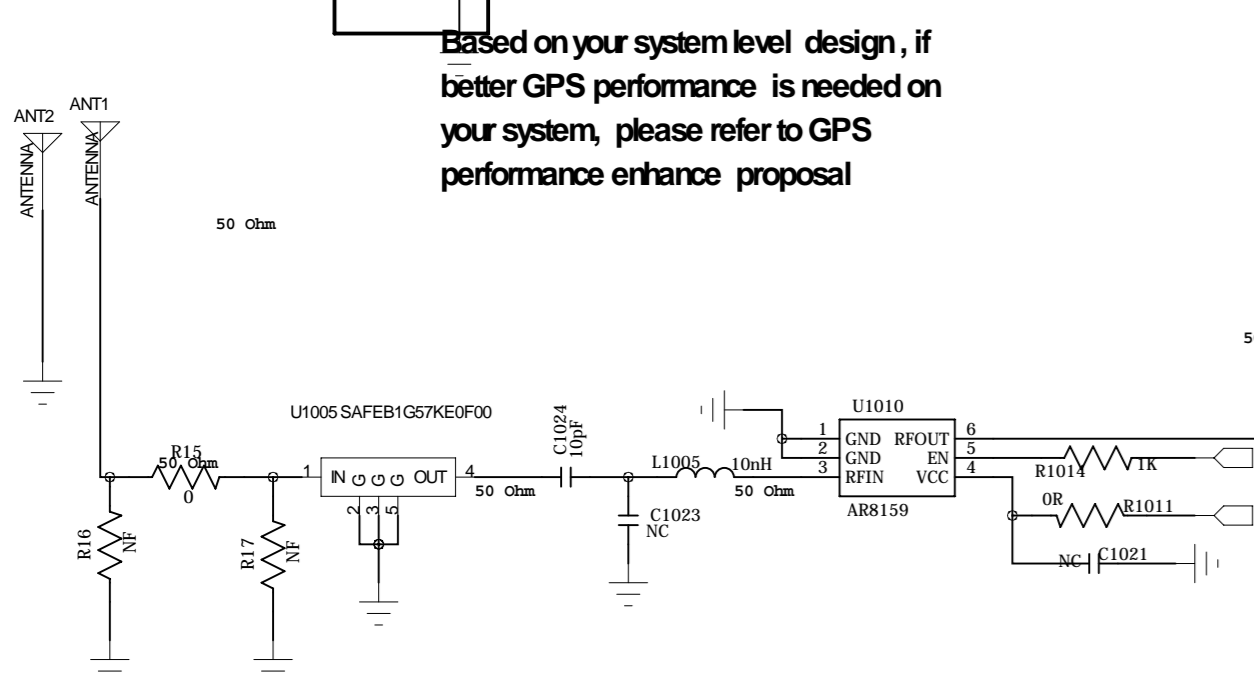
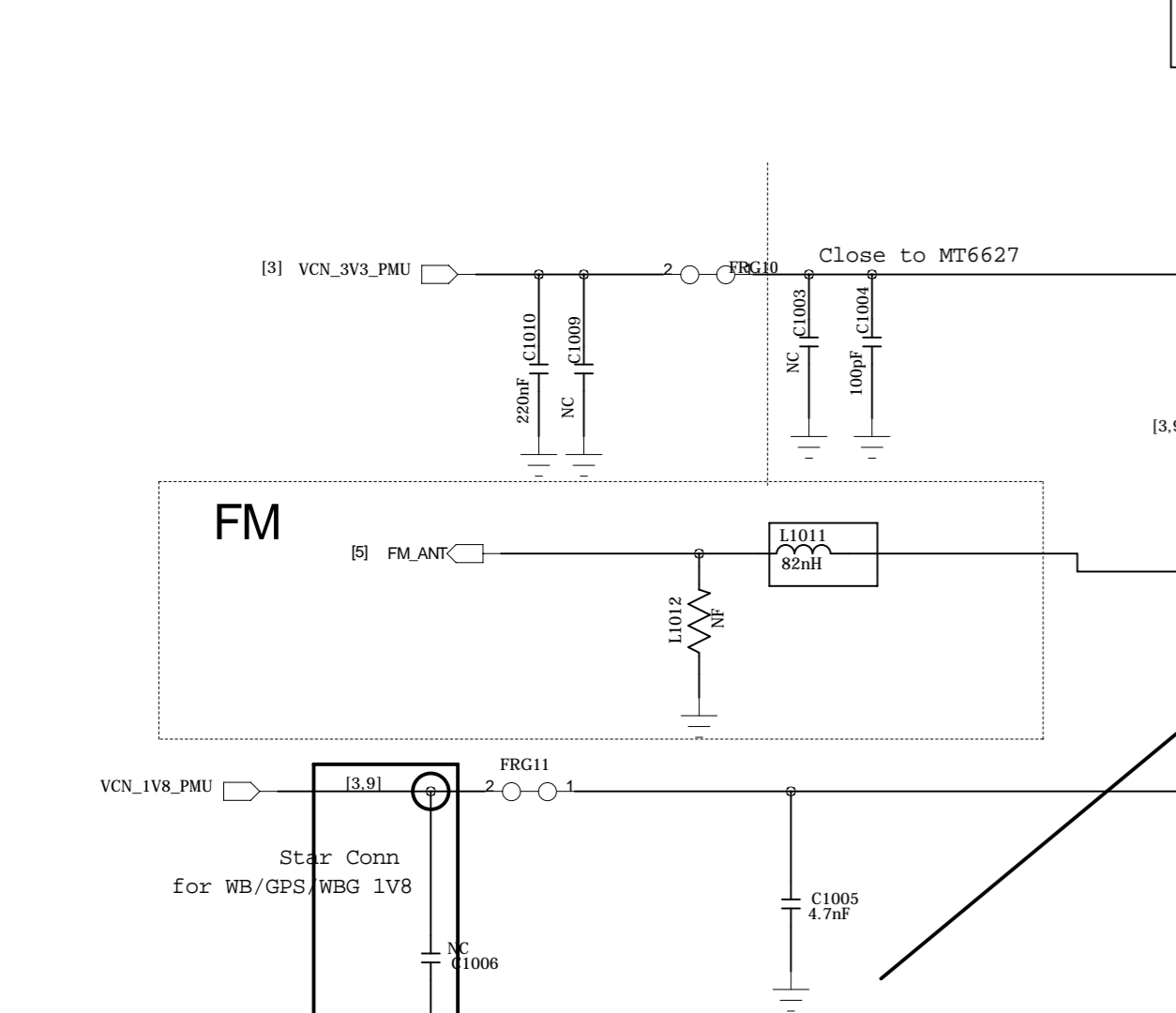
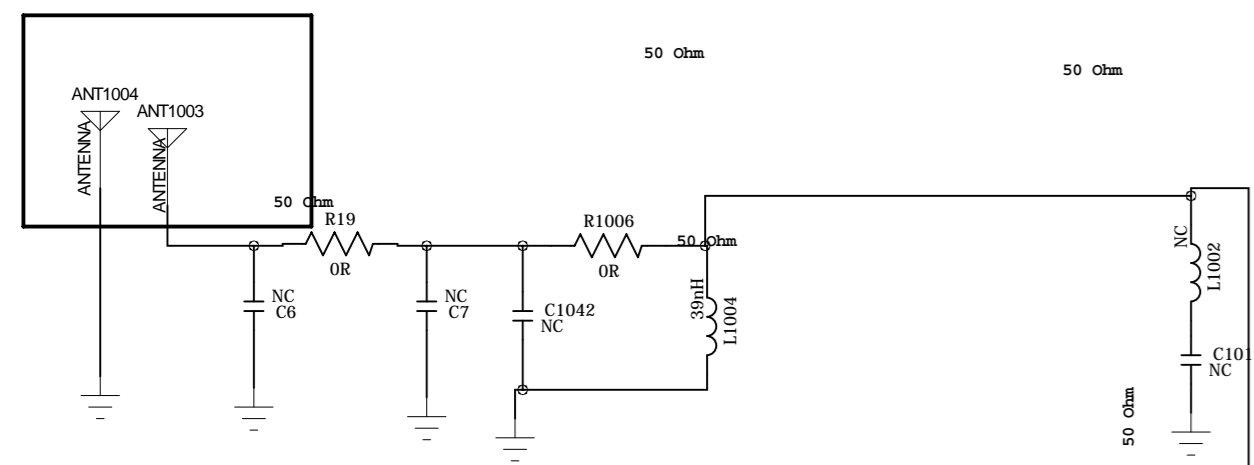
Route AUXADC_REF/AUXADC_TSX as differential trace with well GND shielding
Route AUXADC_GND with 24mil trace width under
Route AUXADC_TSX/AUXADC_REF trace to provide return current path.



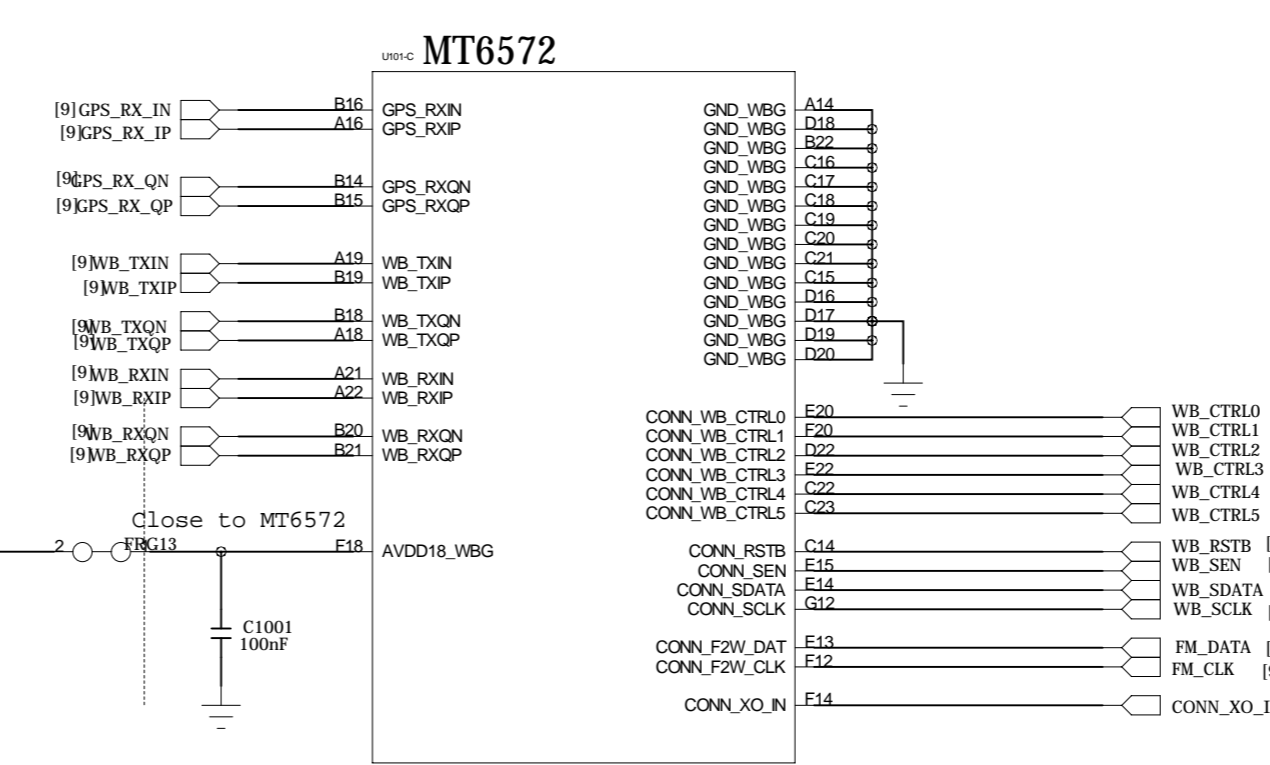
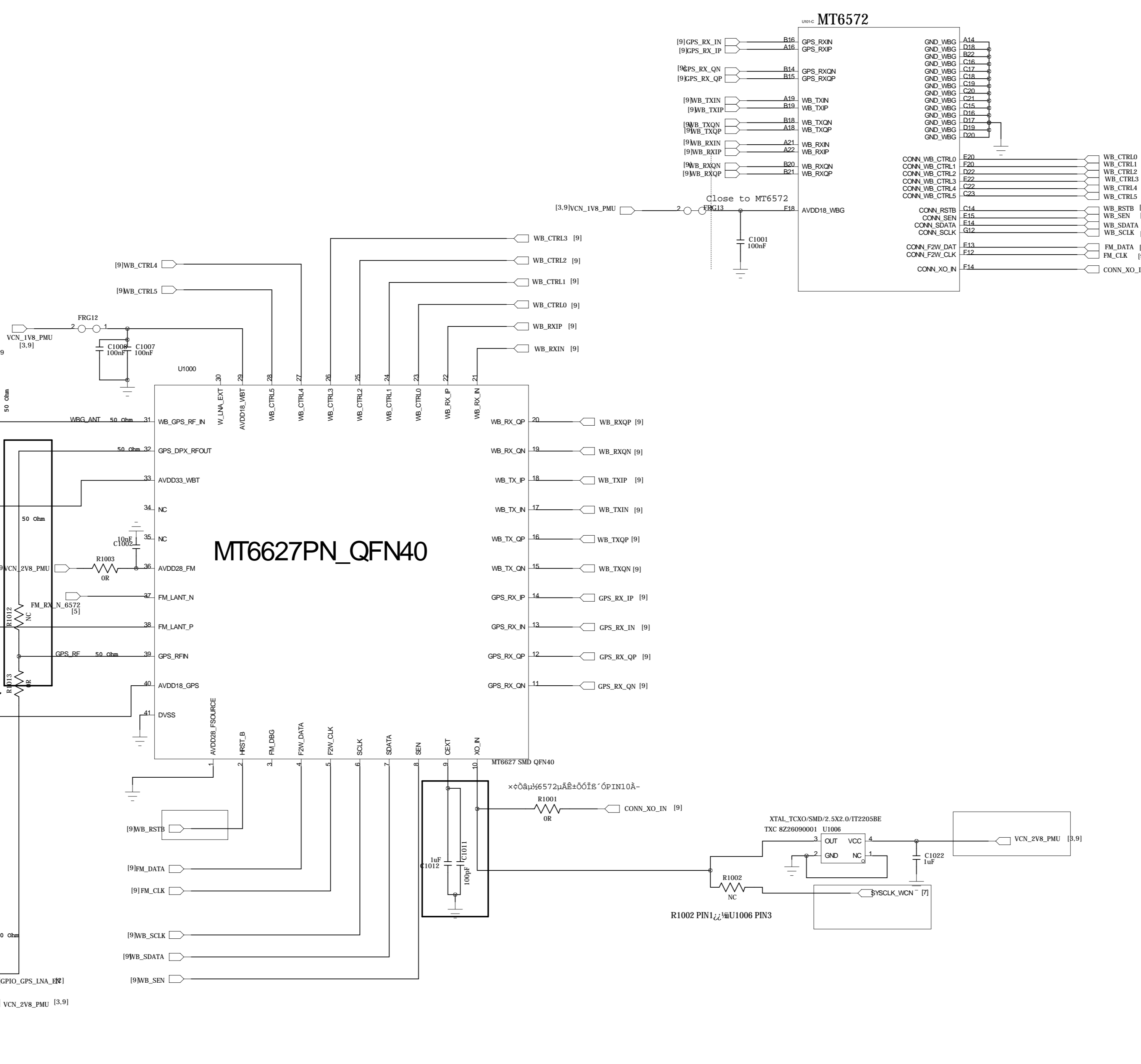
Reserved LC filter



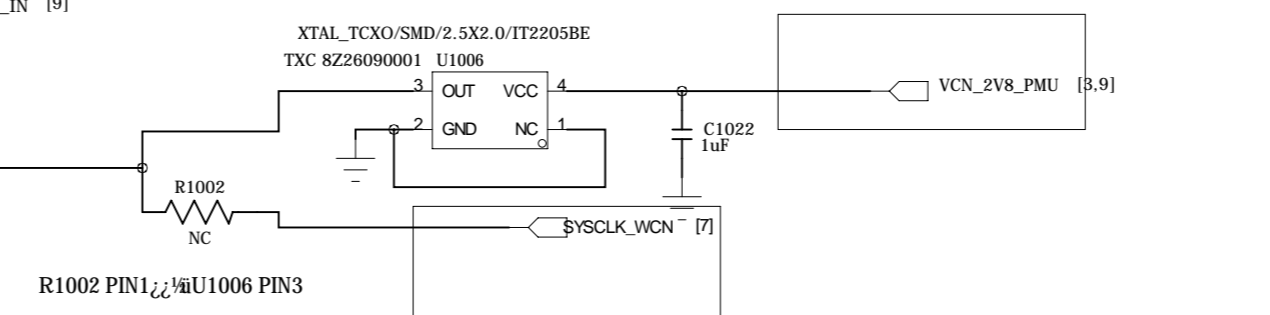
WiFi/BT/Single ANT Ref.



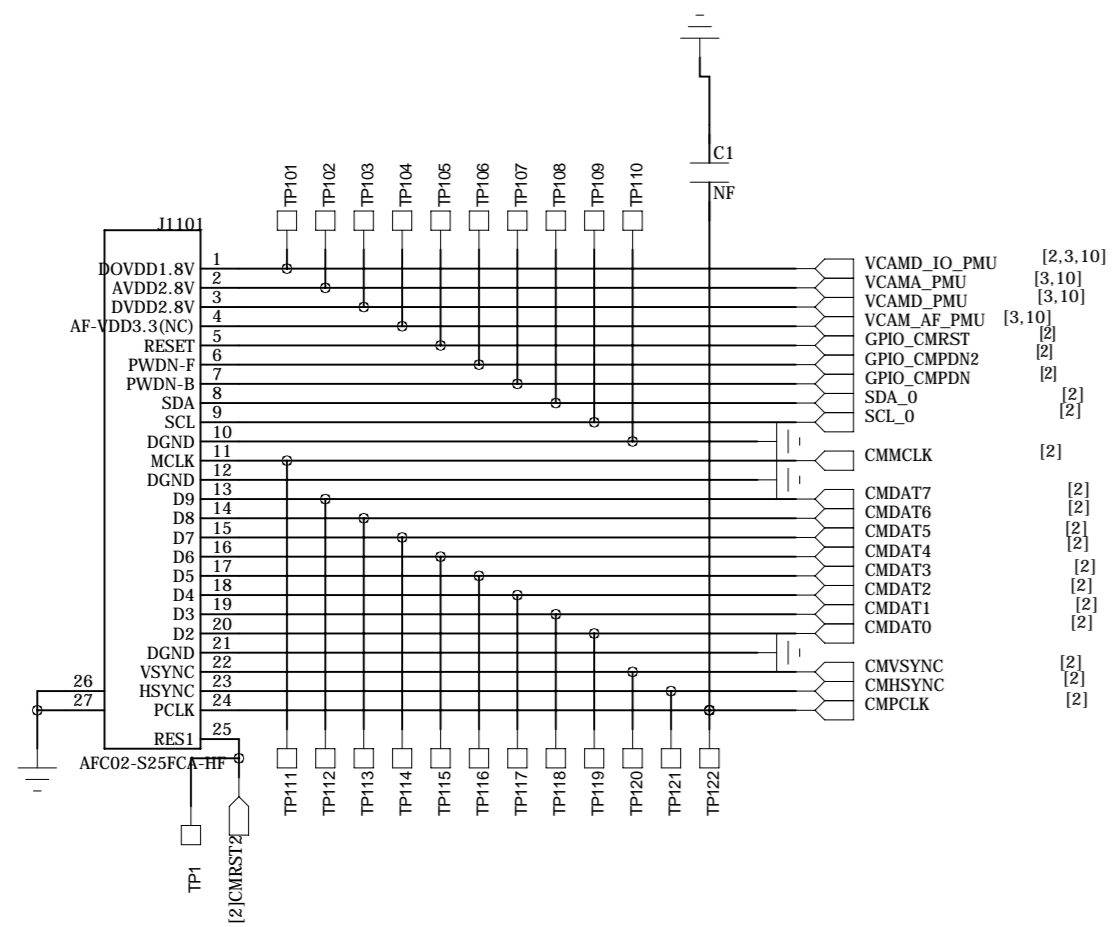
MT6627PN_QFN40



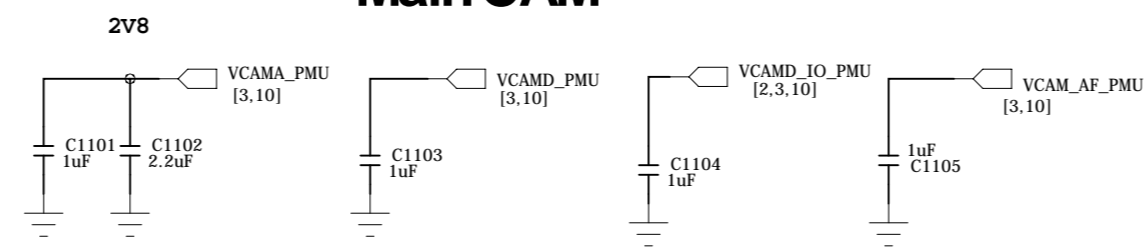
Based on your system level design, if better GPS performance is needed on your system, please refer to GPS performance enhance proposal



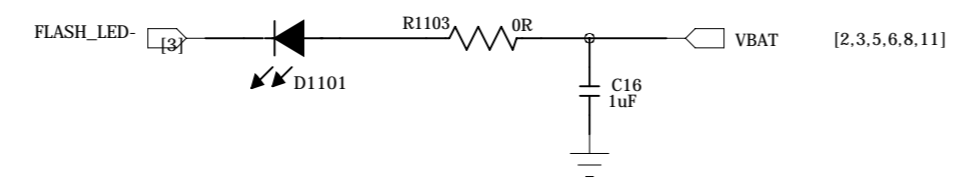
Main CAM

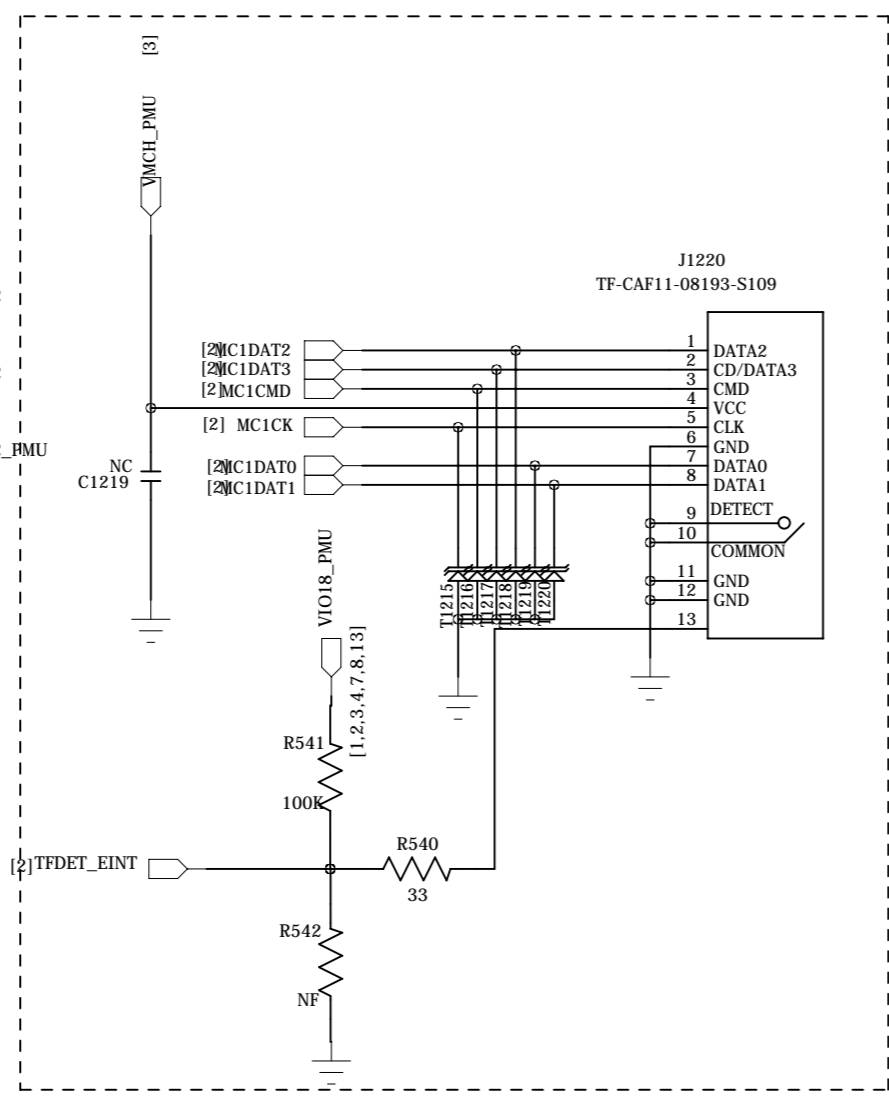
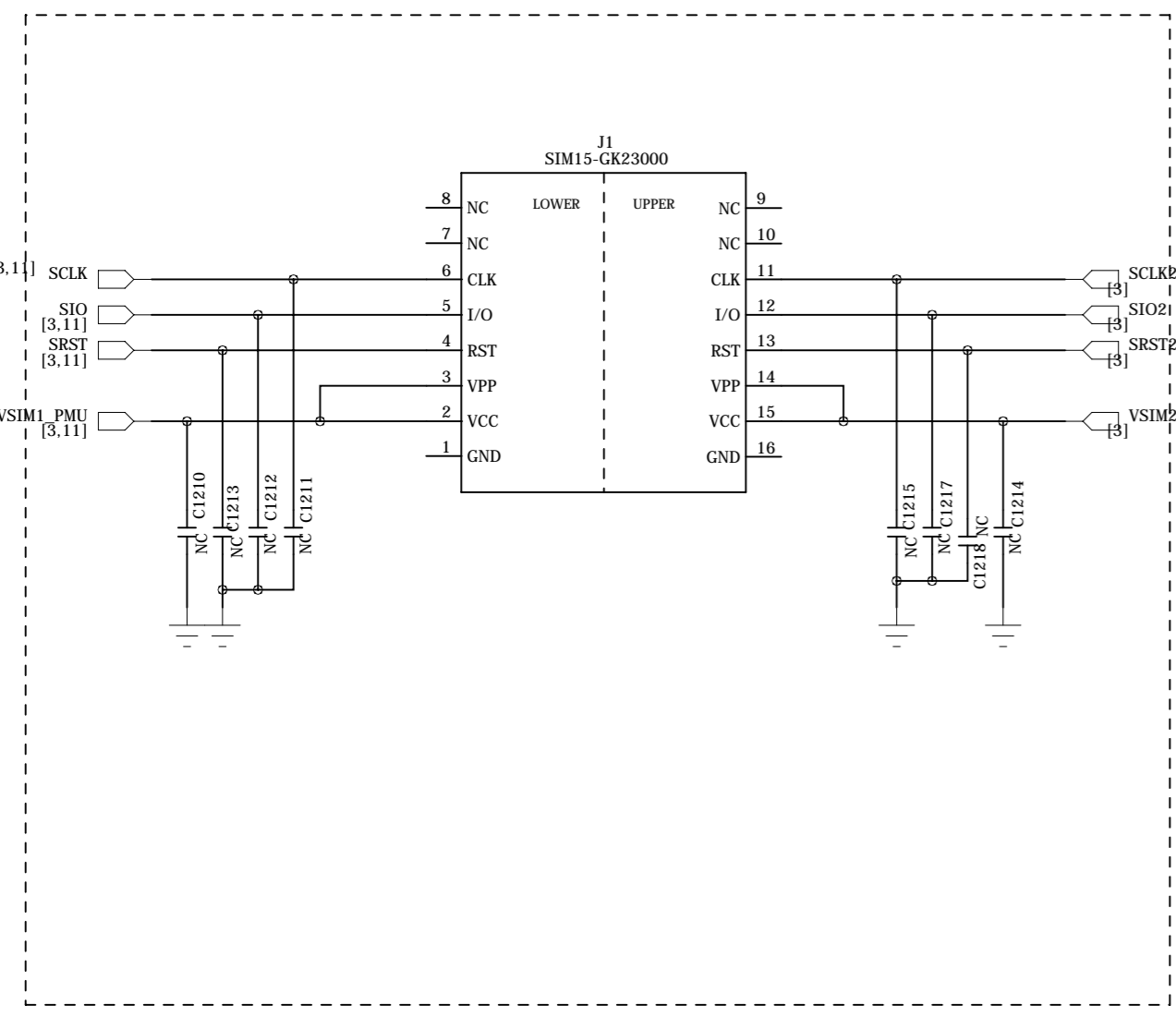
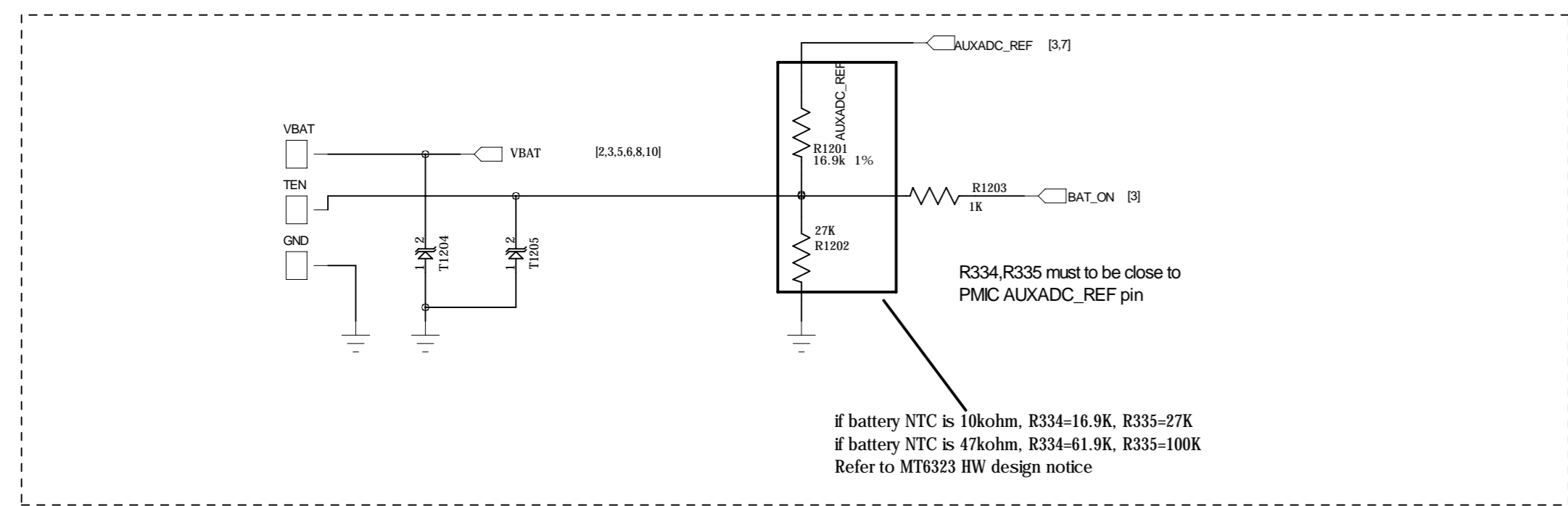
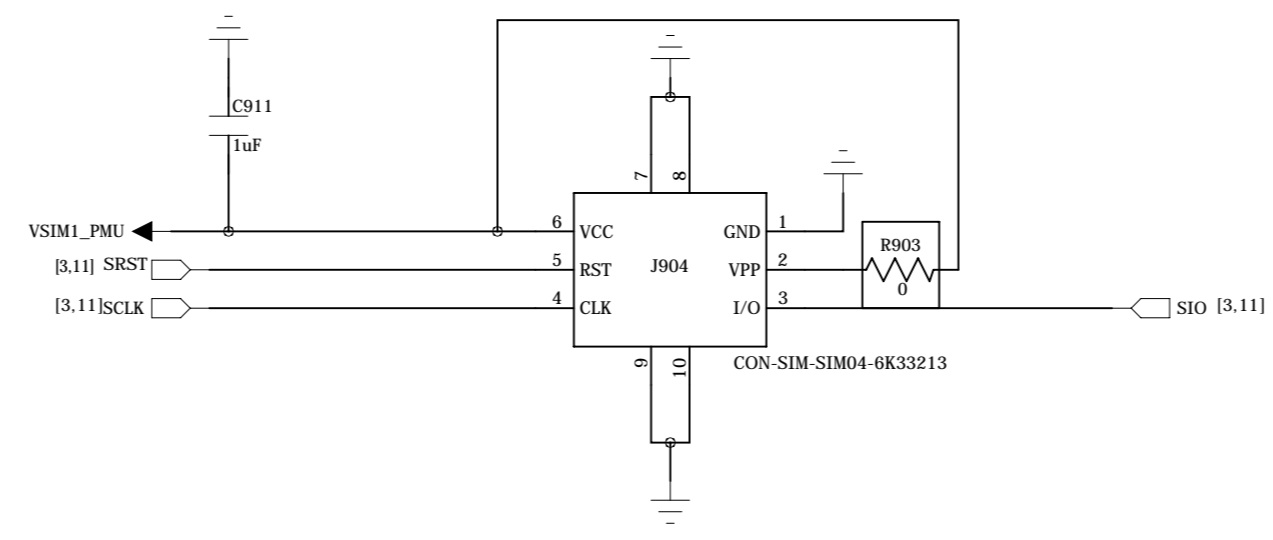
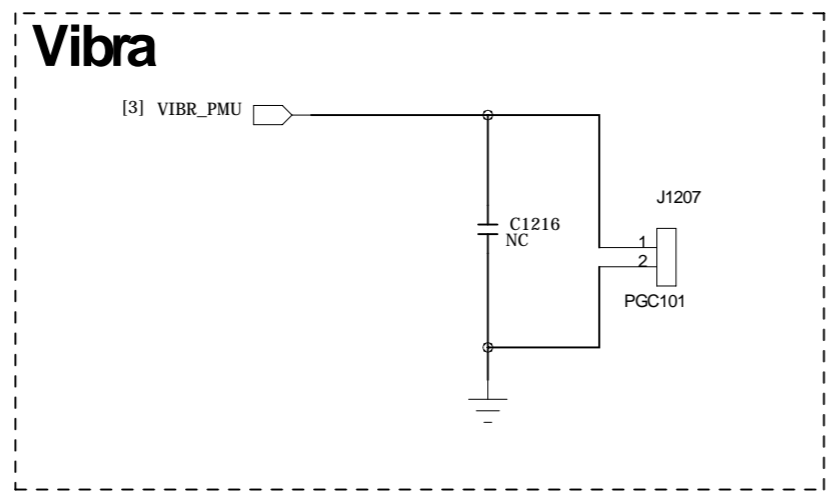
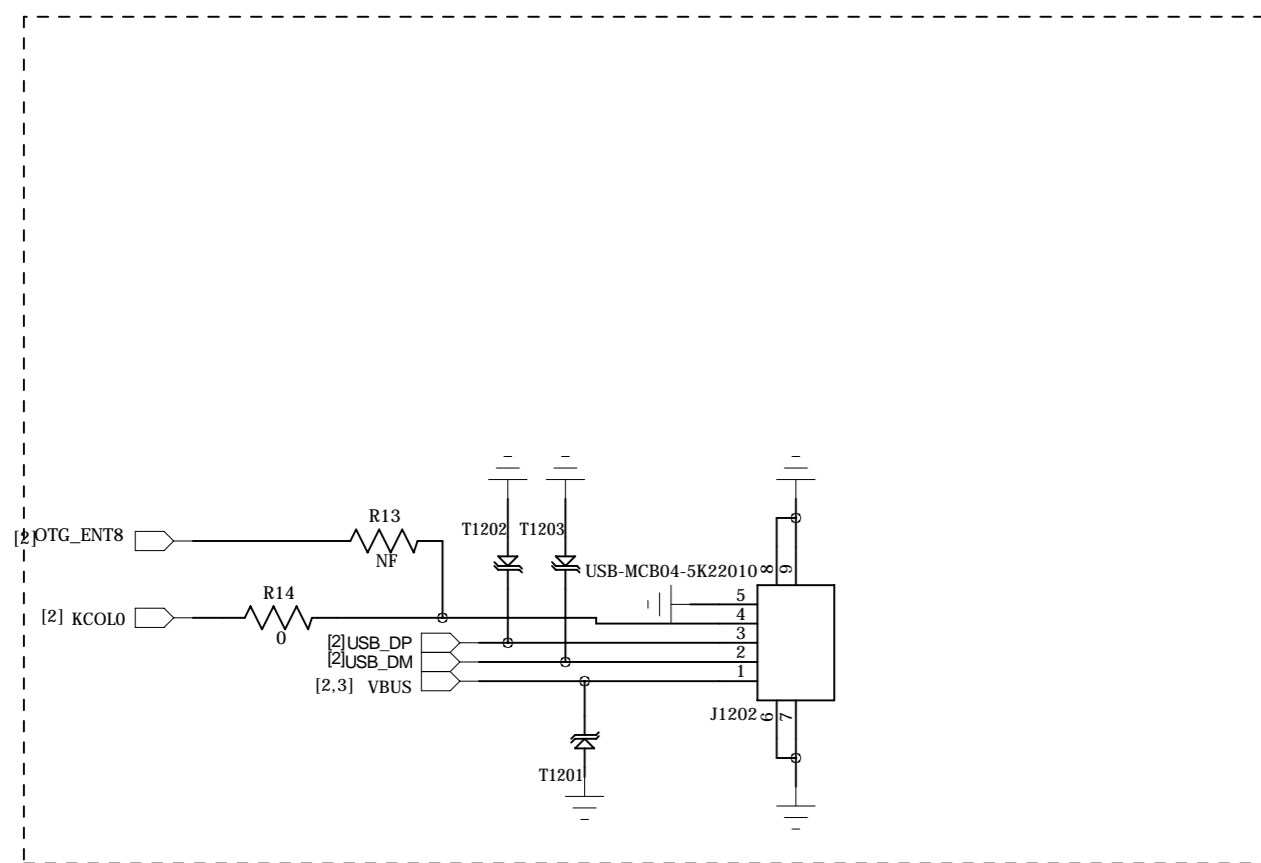


Main CAM

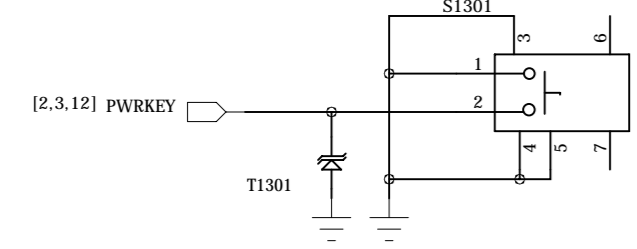


only 150mA from VCAMD
 please check your CAM module DVDD current
 external LDO is required for DVDD current > 150mA

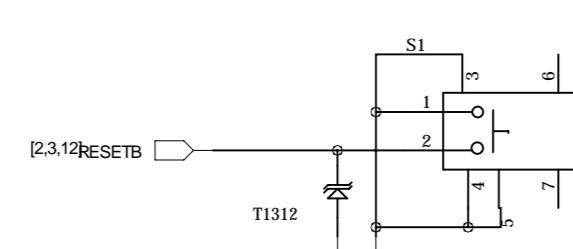




Power Key

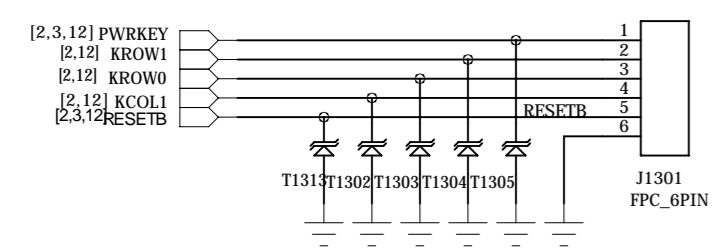
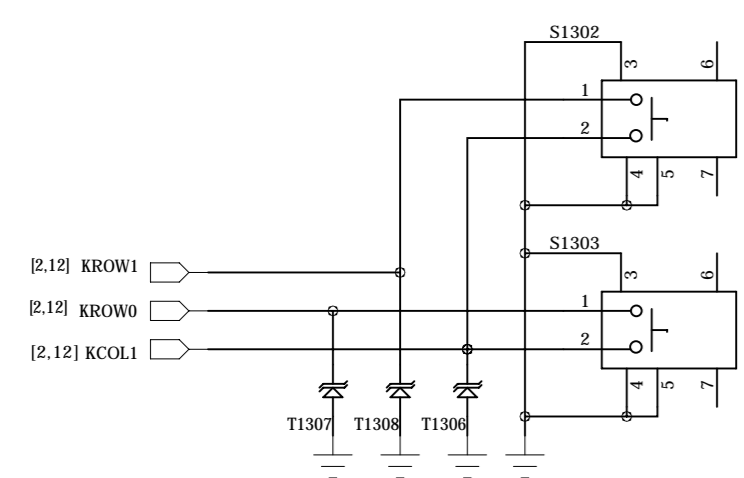


Reset Key



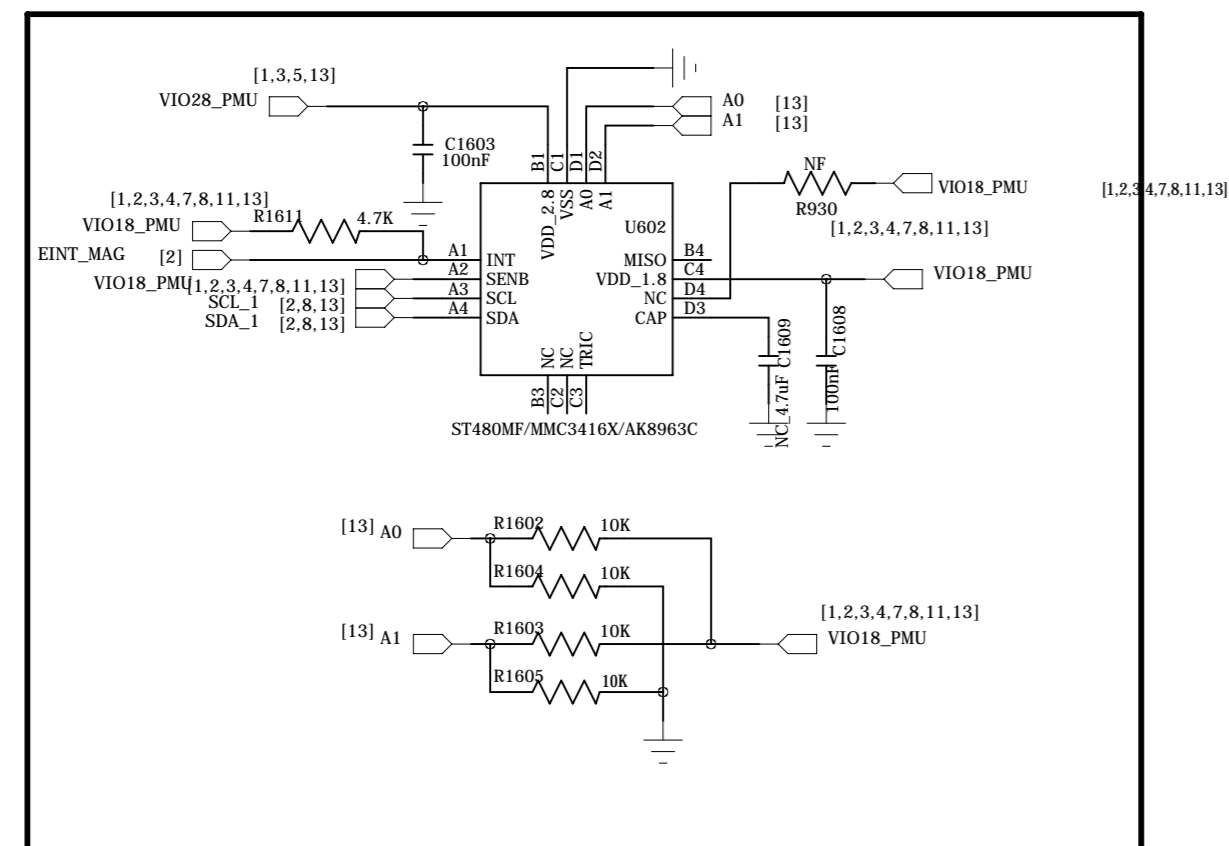
Vol +

Vol -

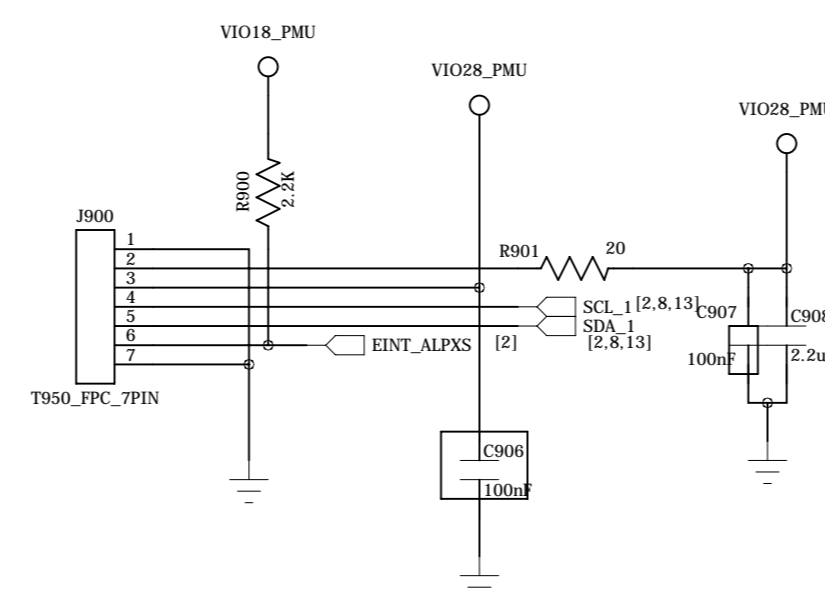


	KROW0	KROW1
KCOL0	VOL-	VOL+

M-Sensor

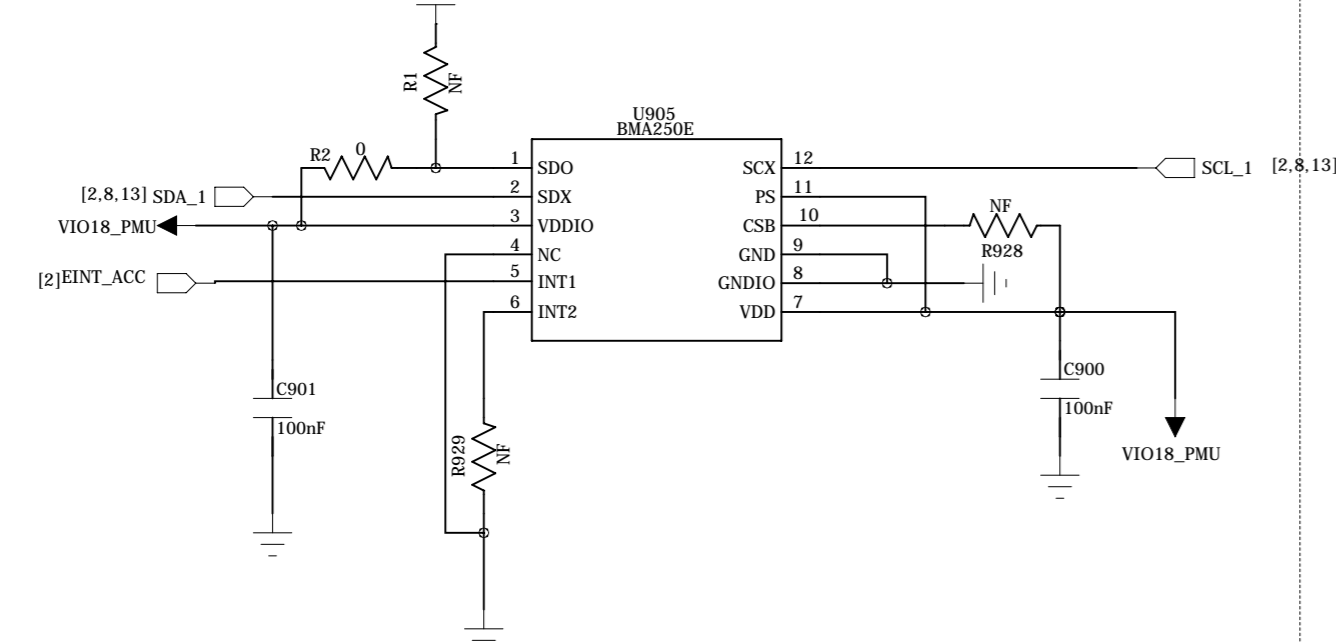


P-snesor



G-Sensor

G Sensor I2C address : 0x18
M Sensor I2C address : 0x10



Gyro Sensor

I2C Address: 0x68 (Write:0xD0, Read:0xD1)

