Operational Description

1.U1
32-bit cmos microcontroller and is developed with ARM920T
MPLL generates the clock to operate MCU at maximum 400Mhz.
Support programmable 8/16/32-bit data bus width for each bank
LCD controller(up to 4K color STN and 256k color TFT)with LCD-dedicated DMA
3-ch UARTs(IrDA1.0,64-Byte Tx FIFO, AND 64-Byte Rx FIFO)
Support 2-ch SPIs, ISS Audio CODEC interface, SD Host controller,2-ch v1.1USB Hos
And so on

2.U7(SAMSUNG NAND FLASH)
K9K8G08U0A,1GX8Bit nand flash memory,48pin TSOP,standard type 12mm x 20mm
voltage supply: 2.7v-3.6v
Data Register: (2k+64)x8bit
page program:(2k+64)Byte
page size: (2k+64)Byte

3.U3,U4(Winbond SDRAM)
W9825G6EH, 4M x 4BANKS X 16BITS SDRAM, TSOPII54PIN, 400mil-0.80, lead free
It is a high-speed synchronous dynamic random access memory(SDRAM),
It delivers a data bandwidth of up to 166M wordsper second(-6)
To fully comply with the personal computer industrial standard
W9825G6EH IS sorted into the following speed grades:-6/-6C and -75.

4.U20(WIFI-USI-WMGMR09)
Wireless LAN Module
High speed for wireless LAN connection: IEEE803.11b/g up to 54Mbps data
rate by incorporating Direct Sequence Spread Spectrum(DSSS)and OFDM data
modulation
Provide seamless roaming within the IEEE802.11b/g WLAN infrastructure.
Auto fallback:54M,48M,36M,24M,18M,12M,9M,6M(802.11G)
11M,5.5M,2M,1M(802.11b)data rate with auto fallback
WPA(WiFi Protected Access)
Support 802.11i Security standard through implementation of AES/CCMP
and WEP with TKIP security mechanism