

## **Appendix for N41**

Product Name: Smart Phone

Model No: N3

## Appendix A: Average Power Output

### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-QPSK	L	Outer_Full	23.77	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	21.41	PC2	PASS
N41	30	10	DFT-QPSK	L	Inner_Full	24.69	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Right	21.13	PC2	PASS
N41	30	10	CP-QPSK	L	Outer_Full	21.75	PC2	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	21.50	PC2	PASS
N41	30	10	CP-QPSK	L	Inner_Full	23.39	PC2	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Right	21.17	PC2	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	24.70	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	22.21	PC2	PASS
N41	30	10	DFT-QPSK	M	Inner_Full	25.51	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Right	22.20	PC2	PASS
N41	30	10	CP-QPSK	M	Outer_Full	22.72	PC2	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	22.47	PC2	PASS
N41	30	10	CP-QPSK	M	Inner_Full	24.37	PC2	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Right	22.48	PC2	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	24.38	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	21.96	PC2	PASS
N41	30	10	DFT-QPSK	H	Inner_Full	25.50	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Right	21.86	PC2	PASS
N41	30	10	CP-QPSK	H	Outer_Full	22.62	PC2	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	22.28	PC2	PASS
N41	30	10	CP-QPSK	H	Inner_Full	23.99	PC2	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Right	22.14	PC2	PASS
N41	30	15	DFT-QPSK	L	Outer_Full	23.71	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	21.29	PC2	PASS
N41	30	15	DFT-QPSK	L	Inner_Full	24.53	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Right	20.96	PC2	PASS
N41	30	15	CP-QPSK	L	Outer_Full	21.76	PC2	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	21.33	PC2	PASS
N41	30	15	CP-QPSK	L	Inner_Full	23.11	PC2	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Right	21.09	PC2	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	24.74	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	21.86	PC2	PASS

N41	30	15	DFT-QPSK	M	Inner_Full	25.50	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Right	22.04	PC2	PASS
N41	30	15	CP-QPSK	M	Outer_Full	22.76	PC2	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	22.02	PC2	PASS
N41	30	15	CP-QPSK	M	Inner_Full	24.13	PC2	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Right	22.13	PC2	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	24.40	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	21.77	PC2	PASS
N41	30	15	DFT-QPSK	H	Inner_Full	25.32	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Right	21.76	PC2	PASS
N41	30	15	CP-QPSK	H	Outer_Full	22.57	PC2	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	21.84	PC2	PASS
N41	30	15	CP-QPSK	H	Inner_Full	23.90	PC2	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Right	21.82	PC2	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	23.68	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	21.33	PC2	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	24.56	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	21.08	PC2	PASS
N41	30	20	CP-QPSK	L	Outer_Full	21.67	PC2	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	21.57	PC2	PASS
N41	30	20	CP-QPSK	L	Inner_Full	23.12	PC2	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	21.35	PC2	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	24.68	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	21.77	PC2	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	25.68	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	22.05	PC2	PASS
N41	30	20	CP-QPSK	M	Outer_Full	22.67	PC2	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	22.06	PC2	PASS
N41	30	20	CP-QPSK	M	Inner_Full	24.06	PC2	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	22.25	PC2	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	24.40	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	21.79	PC2	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	25.30	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	21.90	PC2	PASS
N41	30	20	CP-QPSK	H	Outer_Full	22.49	PC2	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	21.98	PC2	PASS
N41	30	20	CP-QPSK	H	Inner_Full	23.89	PC2	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	21.98	PC2	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	23.59	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	20.83	PC2	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	24.61	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	20.70	PC2	PASS

N41	30	40	CP-QPSK	L	Outer_Full	21.55	PC2	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	21.33	PC2	PASS
N41	30	40	CP-QPSK	L	Inner_Full	23.17	PC2	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	20.96	PC2	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	24.49	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	21.33	PC2	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	25.61	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	21.75	PC2	PASS
N41	30	40	CP-QPSK	M	Outer_Full	22.51	PC2	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	21.45	PC2	PASS
N41	30	40	CP-QPSK	M	Inner_Full	24.07	PC2	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	21.93	PC2	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	24.48	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	22.03	PC2	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	25.46	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	21.52	PC2	PASS
N41	30	40	CP-QPSK	H	Outer_Full	22.51	PC2	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	22.36	PC2	PASS
N41	30	40	CP-QPSK	H	Inner_Full	23.90	PC2	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	21.78	PC2	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	23.61	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	21.13	PC2	PASS
N41	30	50	DFT-QPSK	L	Inner_Full	24.66	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Right	21.03	PC2	PASS
N41	30	50	CP-QPSK	L	Outer_Full	21.67	PC2	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	21.30	PC2	PASS
N41	30	50	CP-QPSK	L	Inner_Full	23.28	PC2	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	21.25	PC2	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	24.50	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	21.25	PC2	PASS
N41	30	50	DFT-QPSK	M	Inner_Full	25.59	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Right	22.08	PC2	PASS
N41	30	50	CP-QPSK	M	Outer_Full	22.56	PC2	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	21.73	PC2	PASS
N41	30	50	CP-QPSK	M	Inner_Full	24.19	PC2	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Right	22.25	PC2	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	24.60	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	22.23	PC2	PASS
N41	30	50	DFT-QPSK	H	Inner_Full	25.62	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	21.68	PC2	PASS
N41	30	50	CP-QPSK	H	Outer_Full	22.74	PC2	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	22.31	PC2	PASS

N41	30	50	CP-QPSK	H	Inner_Full	24.06	PC2	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Right	21.89	PC2	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	23.62	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	21.07	PC2	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	24.65	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	21.24	PC2	PASS
N41	30	60	CP-QPSK	L	Outer_Full	21.58	PC2	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	21.16	PC2	PASS
N41	30	60	CP-QPSK	L	Inner_Full	23.15	PC2	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	21.58	PC2	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	24.44	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	21.35	PC2	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	25.56	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	22.04	PC2	PASS
N41	30	60	CP-QPSK	M	Outer_Full	22.45	PC2	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	21.82	PC2	PASS
N41	30	60	CP-QPSK	M	Inner_Full	24.05	PC2	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	22.65	PC2	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	24.62	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	22.11	PC2	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	25.68	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	21.57	PC2	PASS
N41	30	60	CP-QPSK	H	Outer_Full	22.66	PC2	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	22.25	PC2	PASS
N41	30	60	CP-QPSK	H	Inner_Full	24.14	PC2	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	21.80	PC2	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	23.71	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	20.88	PC2	PASS
N41	30	80	DFT-QPSK	L	Inner_Full	24.59	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Right	21.16	PC2	PASS
N41	30	80	CP-QPSK	L	Outer_Full	21.68	PC2	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	21.31	PC2	PASS
N41	30	80	CP-QPSK	L	Inner_Full	23.27	PC2	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	21.59	PC2	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	24.38	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	21.06	PC2	PASS
N41	30	80	DFT-QPSK	M	Inner_Full	25.44	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Right	21.90	PC2	PASS
N41	30	80	CP-QPSK	M	Outer_Full	22.42	PC2	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	21.30	PC2	PASS
N41	30	80	CP-QPSK	M	Inner_Full	24.04	PC2	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	22.11	PC2	PASS

N41	30	80	DFT-QPSK	H	Outer_Full	24.52	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	21.69	PC2	PASS
N41	30	80	DFT-QPSK	H	Inner_Full	25.65	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Right	21.37	PC2	PASS
N41	30	80	CP-QPSK	H	Outer_Full	22.62	PC2	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	21.83	PC2	PASS
N41	30	80	CP-QPSK	H	Inner_Full	24.19	PC2	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	21.66	PC2	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	23.77	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	20.58	PC2	PASS
N41	30	90	DFT-QPSK	L	Inner_Full	24.59	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Right	21.26	PC2	PASS
N41	30	90	CP-QPSK	L	Outer_Full	21.74	PC2	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	20.76	PC2	PASS
N41	30	90	CP-QPSK	L	Inner_Full	23.22	PC2	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Right	21.37	PC2	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	24.44	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	20.49	PC2	PASS
N41	30	90	DFT-QPSK	M	Inner_Full	25.35	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Right	21.61	PC2	PASS
N41	30	90	CP-QPSK	M	Outer_Full	22.37	PC2	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	20.74	PC2	PASS
N41	30	90	CP-QPSK	M	Inner_Full	23.97	PC2	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Right	21.71	PC2	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	24.50	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	21.37	PC2	PASS
N41	30	90	DFT-QPSK	H	Inner_Full	25.73	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Right	21.23	PC2	PASS
N41	30	90	CP-QPSK	H	Outer_Full	22.63	PC2	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	21.51	PC2	PASS
N41	30	90	CP-QPSK	H	Inner_Full	24.15	PC2	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	21.43	PC2	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	23.79	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	20.53	PC2	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	24.68	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	21.41	PC2	PASS
N41	30	100	CP-QPSK	L	Outer_Full	21.77	PC2	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	20.66	PC2	PASS
N41	30	100	CP-QPSK	L	Inner_Full	23.30	PC2	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	21.47	PC2	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	24.31	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	20.27	PC2	PASS

N41	30	100	DFT-QPSK	M	Inner_Full	25.31	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	21.48	PC2	PASS
N41	30	100	CP-QPSK	M	Outer_Full	22.32	PC2	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	20.53	PC2	PASS
N41	30	100	CP-QPSK	M	Inner_Full	23.99	PC2	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	21.49	PC2	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	24.42	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	21.30	PC2	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	25.74	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	21.13	PC2	PASS
N41	30	100	CP-QPSK	H	Outer_Full	22.59	PC2	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	21.58	PC2	PASS
N41	30	100	CP-QPSK	H	Inner_Full	24.21	PC2	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	21.11	PC2	PASS

## Sensor Mode

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-QPSK	L	Outer_Full	18.62	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	18.68	PC2	PASS
N41	30	10	DFT-QPSK	L	Inner_Full	18.65	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Right	19.18	PC2	PASS
N41	30	10	CP-QPSK	L	Outer_Full	19.06	PC2	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	18.62	PC2	PASS
N41	30	10	CP-QPSK	L	Inner_Full	18.64	PC2	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Right	18.27	PC2	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	19.67	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	19.51	PC2	PASS
N41	30	10	DFT-QPSK	M	Inner_Full	19.77	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Right	19.49	PC2	PASS
N41	30	10	CP-QPSK	M	Outer_Full	19.69	PC2	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	19.58	PC2	PASS
N41	30	10	CP-QPSK	M	Inner_Full	19.60	PC2	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Right	19.43	PC2	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	18.65	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	18.66	PC2	PASS
N41	30	10	DFT-QPSK	H	Inner_Full	18.64	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Right	18.49	PC2	PASS
N41	30	10	CP-QPSK	H	Outer_Full	18.57	PC2	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	18.94	PC2	PASS
N41	30	10	CP-QPSK	H	Inner_Full	18.63	PC2	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Right	18.47	PC2	PASS

N41	30	15	DFT-QPSK	L	Outer_Full	19.52	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	19.16	PC2	PASS
N41	30	15	DFT-QPSK	L	Inner_Full	19.55	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Right	19.28	PC2	PASS
N41	30	15	CP-QPSK	L	Outer_Full	19.54	PC2	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	19.46	PC2	PASS
N41	30	15	CP-QPSK	L	Inner_Full	19.66	PC2	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Right	19.84	PC2	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	19.59	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	19.41	PC2	PASS
N41	30	15	DFT-QPSK	M	Inner_Full	19.64	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Right	19.36	PC2	PASS
N41	30	15	CP-QPSK	M	Outer_Full	19.52	PC2	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	19.86	PC2	PASS
N41	30	15	CP-QPSK	M	Inner_Full	19.61	PC2	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Right	19.81	PC2	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	18.62	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	18.65	PC2	PASS
N41	30	15	DFT-QPSK	H	Inner_Full	18.63	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Right	18.51	PC2	PASS
N41	30	15	CP-QPSK	H	Outer_Full	18.54	PC2	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	18.74	PC2	PASS
N41	30	15	CP-QPSK	H	Inner_Full	18.55	PC2	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Right	18.51	PC2	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	19.50	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	19.20	PC2	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	19.54	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	19.42	PC2	PASS
N41	30	20	CP-QPSK	L	Outer_Full	19.48	PC2	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	19.13	PC2	PASS
N41	30	20	CP-QPSK	L	Inner_Full	19.52	PC2	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	19.43	PC2	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	19.49	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	19.33	PC2	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	19.59	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	19.39	PC2	PASS
N41	30	20	CP-QPSK	M	Outer_Full	19.47	PC2	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	19.67	PC2	PASS
N41	30	20	CP-QPSK	M	Inner_Full	19.51	PC2	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	19.77	PC2	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	19.49	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	19.69	PC2	PASS



N41	30	20	DFT-QPSK	H	Inner_Full	19.58	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	19.60	PC2	PASS
N41	30	20	CP-QPSK	H	Outer_Full	19.43	PC2	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	18.65	PC2	PASS
N41	30	20	CP-QPSK	H	Inner_Full	18.66	PC2	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	18.64	PC2	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	18.46	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	18.25	PC2	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	18.60	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	17.91	PC2	PASS
N41	30	40	CP-QPSK	L	Outer_Full	18.41	PC2	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	18.04	PC2	PASS
N41	30	40	CP-QPSK	L	Inner_Full	18.56	PC2	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	17.74	PC2	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	19.23	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	18.29	PC2	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	19.42	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	18.85	PC2	PASS
N41	30	40	CP-QPSK	M	Outer_Full	19.19	PC2	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	18.17	PC2	PASS
N41	30	40	CP-QPSK	M	Inner_Full	19.38	PC2	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	18.80	PC2	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	19.54	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	19.27	PC2	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	19.60	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	19.00	PC2	PASS
N41	30	40	CP-QPSK	H	Outer_Full	19.45	PC2	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	19.15	PC2	PASS
N41	30	40	CP-QPSK	H	Inner_Full	19.54	PC2	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	18.74	PC2	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	18.57	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	18.46	PC2	PASS
N41	30	50	DFT-QPSK	L	Inner_Full	18.66	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Right	18.17	PC2	PASS
N41	30	50	CP-QPSK	L	Outer_Full	18.49	PC2	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	18.33	PC2	PASS
N41	30	50	CP-QPSK	L	Inner_Full	18.60	PC2	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	18.18	PC2	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	19.25	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	18.63	PC2	PASS
N41	30	50	DFT-QPSK	M	Inner_Full	19.37	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Right	19.43	PC2	PASS

N41	30	50	CP-QPSK	M	Outer_Full	19.26	PC2	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	18.39	PC2	PASS
N41	30	50	CP-QPSK	M	Inner_Full	19.39	PC2	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Right	19.18	PC2	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	19.66	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	19.43	PC2	PASS
N41	30	50	DFT-QPSK	H	Inner_Full	19.73	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	19.19	PC2	PASS
N41	30	50	CP-QPSK	H	Outer_Full	19.59	PC2	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	19.53	PC2	PASS
N41	30	50	CP-QPSK	H	Inner_Full	19.66	PC2	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Right	19.23	PC2	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	18.52	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	18.26	PC2	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	18.59	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	18.41	PC2	PASS
N41	30	60	CP-QPSK	L	Outer_Full	18.36	PC2	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	18.35	PC2	PASS
N41	30	60	CP-QPSK	L	Inner_Full	18.54	PC2	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	18.50	PC2	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	19.25	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	18.52	PC2	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	19.40	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	19.41	PC2	PASS
N41	30	60	CP-QPSK	M	Outer_Full	19.17	PC2	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	18.46	PC2	PASS
N41	30	60	CP-QPSK	M	Inner_Full	19.29	PC2	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	19.39	PC2	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	19.62	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	19.43	PC2	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	19.74	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	19.05	PC2	PASS
N41	30	60	CP-QPSK	H	Outer_Full	19.54	PC2	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	19.42	PC2	PASS
N41	30	60	CP-QPSK	H	Inner_Full	19.71	PC2	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	19.04	PC2	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	18.61	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	18.24	PC2	PASS
N41	30	80	DFT-QPSK	L	Inner_Full	18.70	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Right	18.40	PC2	PASS
N41	30	80	CP-QPSK	L	Outer_Full	18.59	PC2	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	18.21	PC2	PASS

N41	30	80	CP-QPSK	L	Inner_Full	18.62	PC2	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	18.34	PC2	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	19.30	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	18.29	PC2	PASS
N41	30	80	DFT-QPSK	M	Inner_Full	19.40	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Right	19.24	PC2	PASS
N41	30	80	CP-QPSK	M	Outer_Full	19.22	PC2	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	18.11	PC2	PASS
N41	30	80	CP-QPSK	M	Inner_Full	19.36	PC2	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	19.06	PC2	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	19.70	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	18.91	PC2	PASS
N41	30	80	DFT-QPSK	H	Inner_Full	19.87	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Right	19.02	PC2	PASS
N41	30	80	CP-QPSK	H	Outer_Full	19.65	PC2	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	18.92	PC2	PASS
N41	30	80	CP-QPSK	H	Inner_Full	19.81	PC2	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	18.92	PC2	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	18.70	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	18.63	PC2	PASS
N41	30	90	DFT-QPSK	L	Inner_Full	17.94	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Right	18.70	PC2	PASS
N41	30	90	CP-QPSK	L	Outer_Full	18.42	PC2	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	18.62	PC2	PASS
N41	30	90	CP-QPSK	L	Inner_Full	18.21	PC2	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Right	18.69	PC2	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	18.96	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	18.35	PC2	PASS
N41	30	90	DFT-QPSK	M	Inner_Full	19.16	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Right	19.32	PC2	PASS
N41	30	90	CP-QPSK	M	Outer_Full	20.14	PC2	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	19.70	PC2	PASS
N41	30	90	CP-QPSK	M	Inner_Full	19.77	PC2	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Right	18.87	PC2	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	19.39	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	19.32	PC2	PASS
N41	30	90	DFT-QPSK	H	Inner_Full	19.67	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Right	20.40	PC2	PASS
N41	30	90	CP-QPSK	H	Outer_Full	19.53	PC2	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	19.50	PC2	PASS
N41	30	90	CP-QPSK	H	Inner_Full	19.02	PC2	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	20.59	PC2	PASS

N41	30	100	DFT-QPSK	L	Outer_Full	20.88	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	20.40	PC2	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	19.23	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	19.10	PC2	PASS
N41	30	100	CP-QPSK	L	Outer_Full	18.44	PC2	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	19.30	PC2	PASS
N41	30	100	CP-QPSK	L	Inner_Full	18.50	PC2	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	18.99	PC2	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	18.89	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	17.94	PC2	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	19.12	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	19.42	PC2	PASS
N41	30	100	CP-QPSK	M	Outer_Full	18.92	PC2	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	19.81	PC2	PASS
N41	30	100	CP-QPSK	M	Inner_Full	18.92	PC2	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	18.95	PC2	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	18.35	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	17.94	PC2	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	19.02	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	19.65	PC2	PASS
N41	30	100	CP-QPSK	H	Outer_Full	18.92	PC2	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	19.58	PC2	PASS
N41	30	100	CP-QPSK	H	Inner_Full	18.84	PC2	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	18.57	PC2	PASS

## Hotspot Mode

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-QPSK	L	Outer_Full	18.27	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	18.63	PC2	PASS
N41	30	10	DFT-QPSK	L	Inner_Full	18.58	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Right	18.59	PC2	PASS
N41	30	10	CP-QPSK	L	Outer_Full	18.50	PC2	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	18.98	PC2	PASS
N41	30	10	CP-QPSK	L	Inner_Full	18.39	PC2	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Right	18.66	PC2	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	19.33	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	19.18	PC2	PASS
N41	30	10	DFT-QPSK	M	Inner_Full	19.33	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Right	19.23	PC2	PASS
N41	30	10	CP-QPSK	M	Outer_Full	19.30	PC2	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	19.59	PC2	PASS

N41	30	10	CP-QPSK	M	Inner_Full	19.26	PC2	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Right	19.63	PC2	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	19.42	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	19.29	PC2	PASS
N41	30	10	DFT-QPSK	H	Inner_Full	19.50	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Right	19.28	PC2	PASS
N41	30	10	CP-QPSK	H	Outer_Full	19.41	PC2	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	19.67	PC2	PASS
N41	30	10	CP-QPSK	H	Inner_Full	19.37	PC2	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Right	19.74	PC2	PASS
N41	30	15	DFT-QPSK	L	Outer_Full	18.41	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	18.41	PC2	PASS
N41	30	15	DFT-QPSK	L	Inner_Full	18.41	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Right	18.14	PC2	PASS
N41	30	15	CP-QPSK	L	Outer_Full	18.37	PC2	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	18.72	PC2	PASS
N41	30	15	CP-QPSK	L	Inner_Full	18.40	PC2	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Right	18.47	PC2	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	19.29	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	19.06	PC2	PASS
N41	30	15	DFT-QPSK	M	Inner_Full	19.34	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Right	19.19	PC2	PASS
N41	30	15	CP-QPSK	M	Outer_Full	19.28	PC2	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	19.23	PC2	PASS
N41	30	15	CP-QPSK	M	Inner_Full	19.28	PC2	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Right	19.43	PC2	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	19.40	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	19.30	PC2	PASS
N41	30	15	DFT-QPSK	H	Inner_Full	19.39	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Right	19.23	PC2	PASS
N41	30	15	CP-QPSK	H	Outer_Full	19.44	PC2	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	19.65	PC2	PASS
N41	30	15	CP-QPSK	H	Inner_Full	19.42	PC2	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Right	19.60	PC2	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	18.35	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	18.49	PC2	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	18.43	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	18.28	PC2	PASS
N41	30	20	CP-QPSK	L	Outer_Full	18.36	PC2	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	18.65	PC2	PASS
N41	30	20	CP-QPSK	L	Inner_Full	18.40	PC2	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	18.43	PC2	PASS

N41	30	20	DFT-QPSK	M	Outer_Full	19.28	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	18.99	PC2	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	19.39	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	19.26	PC2	PASS
N41	30	20	CP-QPSK	M	Outer_Full	19.32	PC2	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	19.10	PC2	PASS
N41	30	20	CP-QPSK	M	Inner_Full	19.36	PC2	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	19.36	PC2	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	19.39	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	19.13	PC2	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	19.42	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	19.16	PC2	PASS
N41	30	20	CP-QPSK	H	Outer_Full	19.38	PC2	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	19.28	PC2	PASS
N41	30	20	CP-QPSK	H	Inner_Full	19.44	PC2	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	19.28	PC2	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	18.35	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	18.22	PC2	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	18.50	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	17.79	PC2	PASS
N41	30	40	CP-QPSK	L	Outer_Full	18.25	PC2	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	18.25	PC2	PASS
N41	30	40	CP-QPSK	L	Inner_Full	18.42	PC2	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	17.95	PC2	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	19.12	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	18.30	PC2	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	19.30	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	18.92	PC2	PASS
N41	30	40	CP-QPSK	M	Outer_Full	19.02	PC2	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	18.61	PC2	PASS
N41	30	40	CP-QPSK	M	Inner_Full	19.21	PC2	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	19.39	PC2	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	19.40	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	19.31	PC2	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	19.48	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	18.92	PC2	PASS
N41	30	40	CP-QPSK	H	Outer_Full	19.26	PC2	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	19.75	PC2	PASS
N41	30	40	CP-QPSK	H	Inner_Full	19.40	PC2	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	19.24	PC2	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	18.46	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	18.37	PC2	PASS

N41	30	50	DFT-QPSK	L	Inner_Full	18.59	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Right	18.15	PC2	PASS
N41	30	50	CP-QPSK	L	Outer_Full	18.38	PC2	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	18.85	PC2	PASS
N41	30	50	CP-QPSK	L	Inner_Full	18.55	PC2	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	18.60	PC2	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	19.19	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	18.60	PC2	PASS
N41	30	50	DFT-QPSK	M	Inner_Full	19.32	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Right	19.39	PC2	PASS
N41	30	50	CP-QPSK	M	Outer_Full	19.10	PC2	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	18.64	PC2	PASS
N41	30	50	CP-QPSK	M	Inner_Full	19.27	PC2	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Right	19.52	PC2	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	19.59	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	19.43	PC2	PASS
N41	30	50	DFT-QPSK	H	Inner_Full	19.68	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	19.16	PC2	PASS
N41	30	50	CP-QPSK	H	Outer_Full	19.52	PC2	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	19.55	PC2	PASS
N41	30	50	CP-QPSK	H	Inner_Full	19.56	PC2	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Right	19.27	PC2	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	18.38	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	18.25	PC2	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	18.53	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	18.43	PC2	PASS
N41	30	60	CP-QPSK	L	Outer_Full	18.28	PC2	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	18.65	PC2	PASS
N41	30	60	CP-QPSK	L	Inner_Full	18.49	PC2	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	18.83	PC2	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	19.16	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	18.48	PC2	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	19.24	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	19.38	PC2	PASS
N41	30	60	CP-QPSK	M	Outer_Full	19.13	PC2	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	18.56	PC2	PASS
N41	30	60	CP-QPSK	M	Inner_Full	19.25	PC2	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	19.52	PC2	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	19.53	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	19.36	PC2	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	19.64	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	19.06	PC2	PASS

N41	30	60	CP-QPSK	H	Outer_Full	19.47	PC2	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	19.70	PC2	PASS
N41	30	60	CP-QPSK	H	Inner_Full	19.61	PC2	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	19.29	PC2	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	18.57	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	18.11	PC2	PASS
N41	30	80	DFT-QPSK	L	Inner_Full	18.57	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Right	18.44	PC2	PASS
N41	30	80	CP-QPSK	L	Outer_Full	18.49	PC2	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	18.47	PC2	PASS
N41	30	80	CP-QPSK	L	Inner_Full	18.52	PC2	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	18.65	PC2	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	19.21	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	18.22	PC2	PASS
N41	30	80	DFT-QPSK	M	Inner_Full	19.31	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Right	19.15	PC2	PASS
N41	30	80	CP-QPSK	M	Outer_Full	19.15	PC2	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	18.47	PC2	PASS
N41	30	80	CP-QPSK	M	Inner_Full	19.24	PC2	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	19.46	PC2	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	19.60	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	18.69	PC2	PASS
N41	30	80	DFT-QPSK	H	Inner_Full	19.77	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Right	18.77	PC2	PASS
N41	30	80	CP-QPSK	H	Outer_Full	19.51	PC2	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	19.24	PC2	PASS
N41	30	80	CP-QPSK	H	Inner_Full	19.66	PC2	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	19.30	PC2	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	18.58	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	18.01	PC2	PASS
N41	30	90	DFT-QPSK	L	Inner_Full	18.63	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Right	18.55	PC2	PASS
N41	30	90	CP-QPSK	L	Outer_Full	18.56	PC2	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	18.07	PC2	PASS
N41	30	90	CP-QPSK	L	Inner_Full	18.62	PC2	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Right	18.65	PC2	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	19.14	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	17.84	PC2	PASS
N41	30	90	DFT-QPSK	M	Inner_Full	19.23	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Right	18.91	PC2	PASS
N41	30	90	CP-QPSK	M	Outer_Full	19.08	PC2	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	18.28	PC2	PASS



N41	30	90	CP-QPSK	M	Inner_Full	19.21	PC2	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Right	19.26	PC2	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	19.51	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	18.52	PC2	PASS
N41	30	90	DFT-QPSK	H	Inner_Full	19.81	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Right	18.68	PC2	PASS
N41	30	90	CP-QPSK	H	Outer_Full	19.43	PC2	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	18.87	PC2	PASS
N41	30	90	CP-QPSK	H	Inner_Full	19.75	PC2	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	18.91	PC2	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	18.61	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	17.81	PC2	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	18.64	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	18.52	PC2	PASS
N41	30	100	CP-QPSK	L	Outer_Full	18.60	PC2	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	17.83	PC2	PASS
N41	30	100	CP-QPSK	L	Inner_Full	18.65	PC2	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	18.60	PC2	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	19.11	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	17.52	PC2	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	19.25	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	18.81	PC2	PASS
N41	30	100	CP-QPSK	M	Outer_Full	19.10	PC2	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	17.51	PC2	PASS
N41	30	100	CP-QPSK	M	Inner_Full	19.24	PC2	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	18.82	PC2	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	19.48	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	18.28	PC2	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	19.82	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	18.45	PC2	PASS
N41	30	100	CP-QPSK	H	Outer_Full	19.40	PC2	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	18.84	PC2	PASS
N41	30	100	CP-QPSK	H	Inner_Full	19.75	PC2	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	18.78	PC2	PASS

## Appendix B: Peak-to-Average Ratio for SA

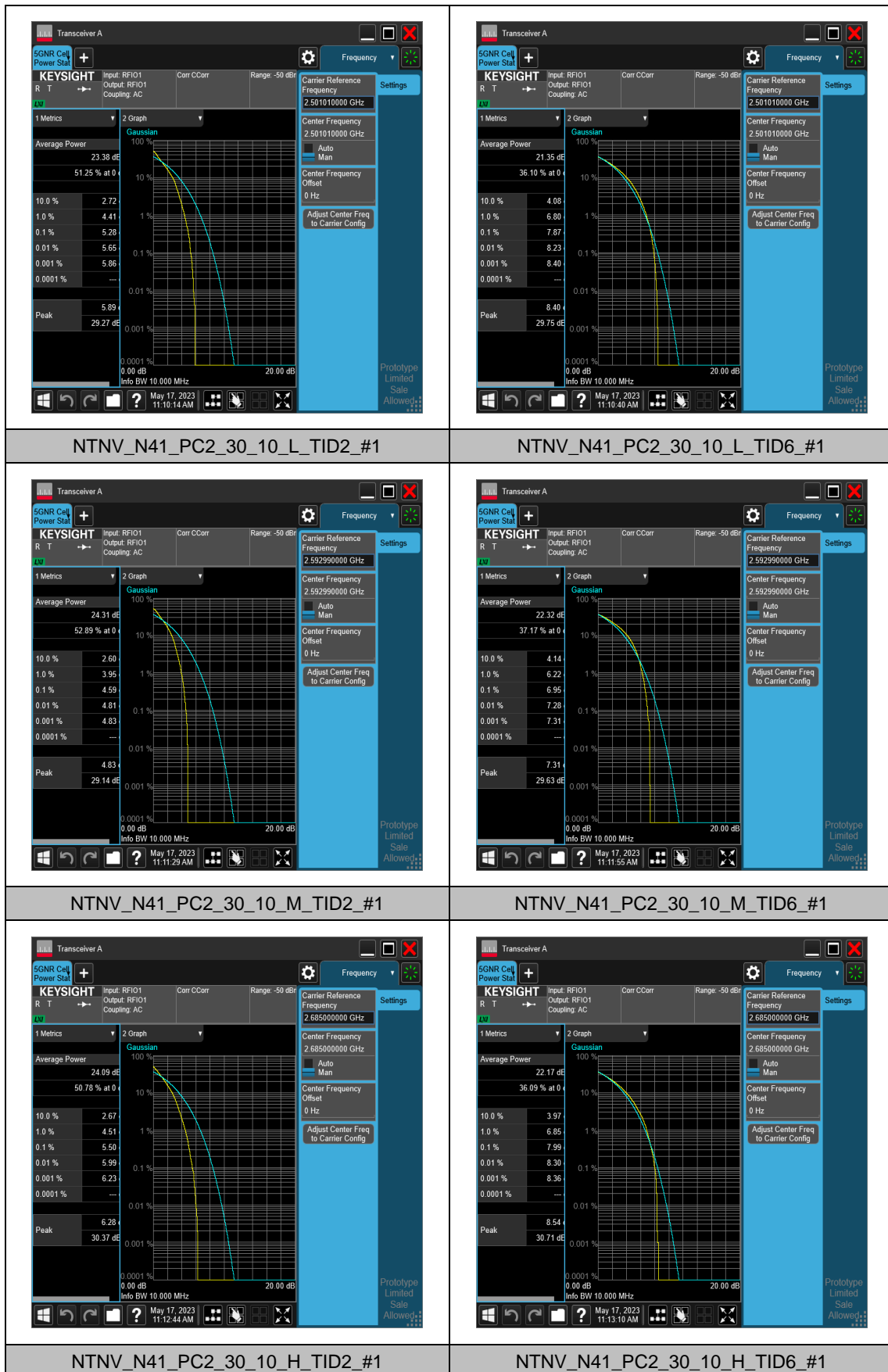
### Peak-to-Average Ratio(CCDF)

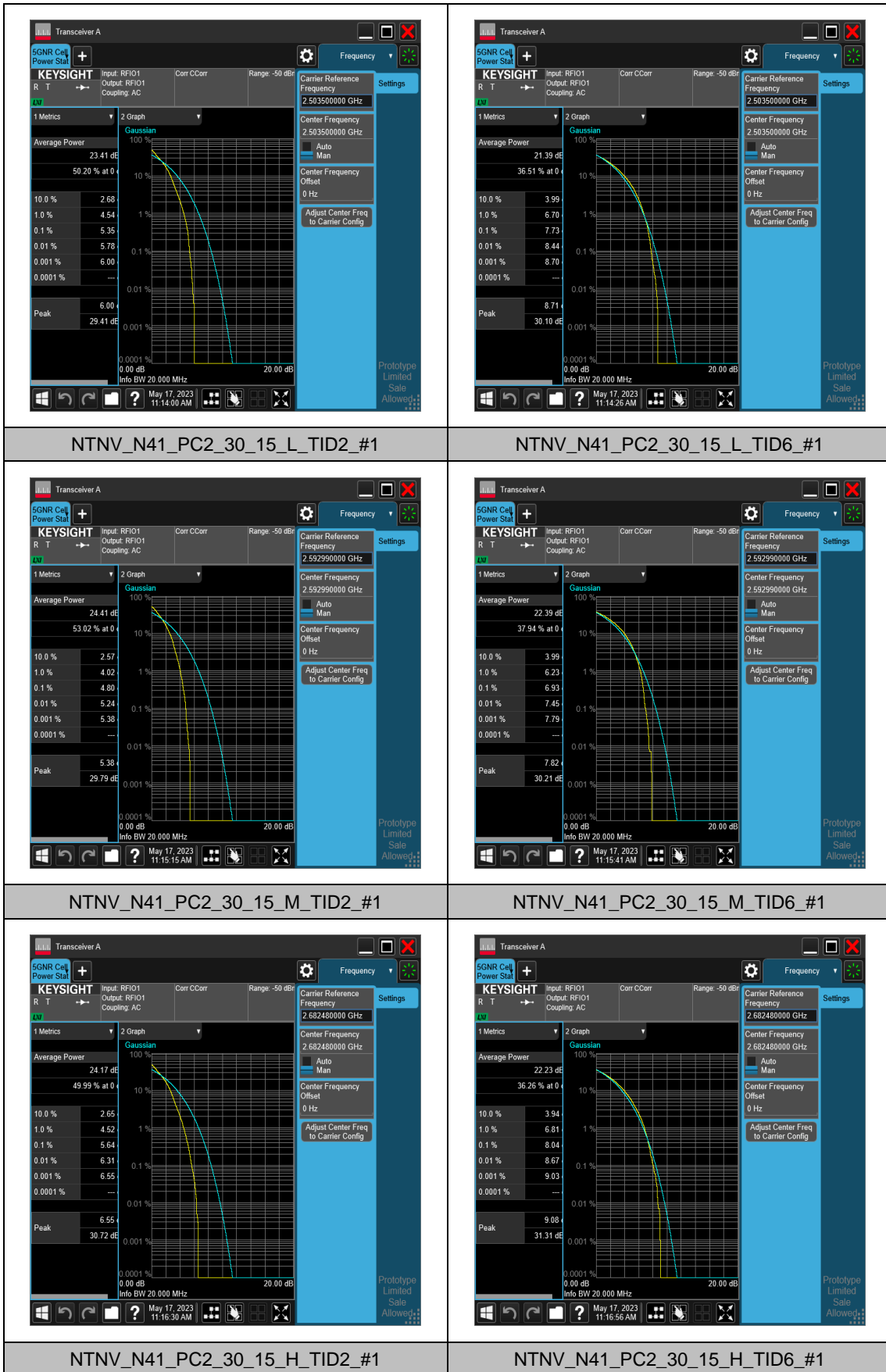
#### Test Result

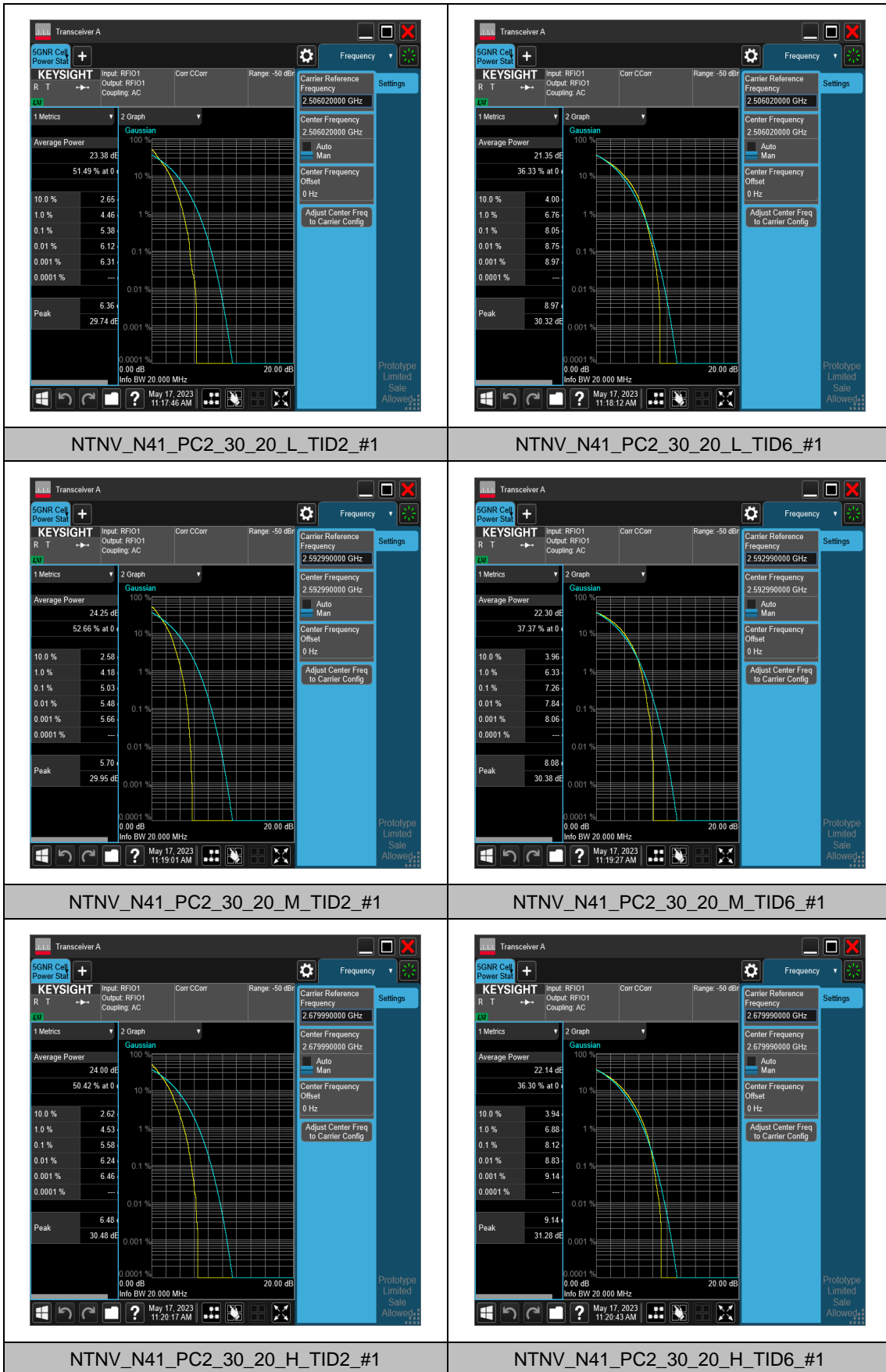
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N41	30	10	DFT-QPSK	L	Outer_Full	5.28	≤13	PASS
N41	30	10	CP-QPSK	L	Outer_Full	7.87	≤13	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	4.59	≤13	PASS
N41	30	10	CP-QPSK	M	Outer_Full	6.95	≤13	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	5.50	≤13	PASS
N41	30	10	CP-QPSK	H	Outer_Full	7.99	≤13	PASS
N41	30	15	DFT-QPSK	L	Outer_Full	5.35	≤13	PASS
N41	30	15	CP-QPSK	L	Outer_Full	7.73	≤13	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	4.80	≤13	PASS
N41	30	15	CP-QPSK	M	Outer_Full	6.93	≤13	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	5.64	≤13	PASS
N41	30	15	CP-QPSK	H	Outer_Full	8.04	≤13	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	5.38	≤13	PASS
N41	30	20	CP-QPSK	L	Outer_Full	8.05	≤13	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	5.03	≤13	PASS
N41	30	20	CP-QPSK	M	Outer_Full	7.26	≤13	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	5.58	≤13	PASS
N41	30	20	CP-QPSK	H	Outer_Full	8.12	≤13	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	5.63	≤13	PASS
N41	30	40	CP-QPSK	L	Outer_Full	8.13	≤13	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	5.15	≤13	PASS
N41	30	40	CP-QPSK	M	Outer_Full	7.43	≤13	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	5.60	≤13	PASS
N41	30	40	CP-QPSK	H	Outer_Full	8.13	≤13	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	5.65	≤13	PASS
N41	30	50	CP-QPSK	L	Outer_Full	8.23	≤13	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	5.29	≤13	PASS
N41	30	50	CP-QPSK	M	Outer_Full	7.60	≤13	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	5.64	≤13	PASS
N41	30	50	CP-QPSK	H	Outer_Full	7.99	≤13	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	5.64	≤13	PASS
N41	30	60	CP-QPSK	L	Outer_Full	8.05	≤13	PASS

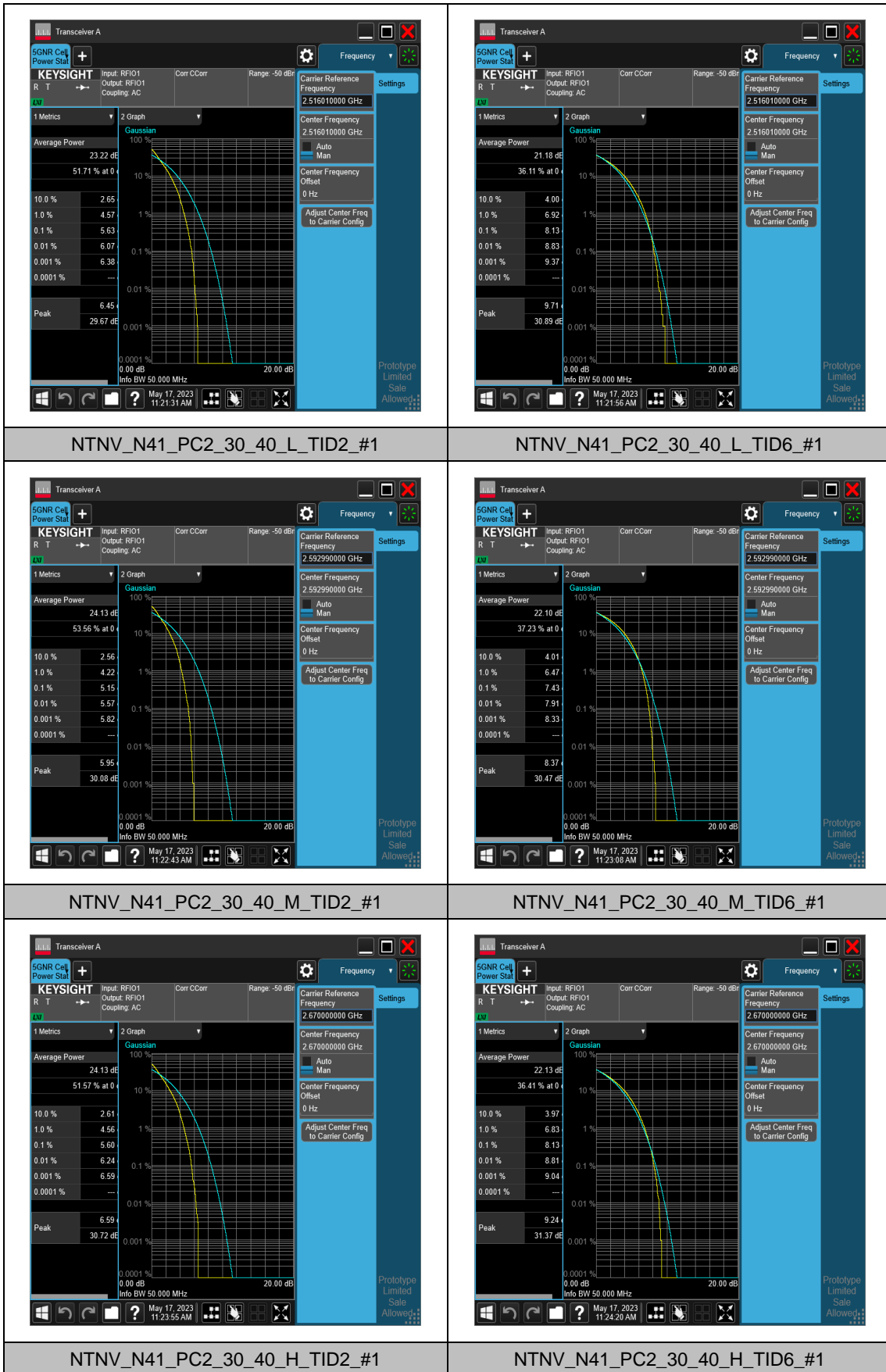
N41	30	60	DFT-QPSK	M	Outer_Full	5.07	≤13	PASS
N41	30	60	CP-QPSK	M	Outer_Full	7.32	≤13	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	5.50	≤13	PASS
N41	30	60	CP-QPSK	H	Outer_Full	7.85	≤13	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	5.62	≤13	PASS
N41	30	80	CP-QPSK	L	Outer_Full	8.06	≤13	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	5.33	≤13	PASS
N41	30	80	CP-QPSK	M	Outer_Full	7.55	≤13	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	5.58	≤13	PASS
N41	30	80	CP-QPSK	H	Outer_Full	7.79	≤13	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	5.63	≤13	PASS
N41	30	90	CP-QPSK	L	Outer_Full	8.09	≤13	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	5.36	≤13	PASS
N41	30	90	CP-QPSK	M	Outer_Full	7.61	≤13	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	5.61	≤13	PASS
N41	30	90	CP-QPSK	H	Outer_Full	7.92	≤13	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	5.68	≤13	PASS
N41	30	100	CP-QPSK	L	Outer_Full	8.19	≤13	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	5.52	≤13	PASS
N41	30	100	CP-QPSK	M	Outer_Full	7.91	≤13	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	5.61	≤13	PASS
N41	30	100	CP-QPSK	H	Outer_Full	7.93	≤13	PASS

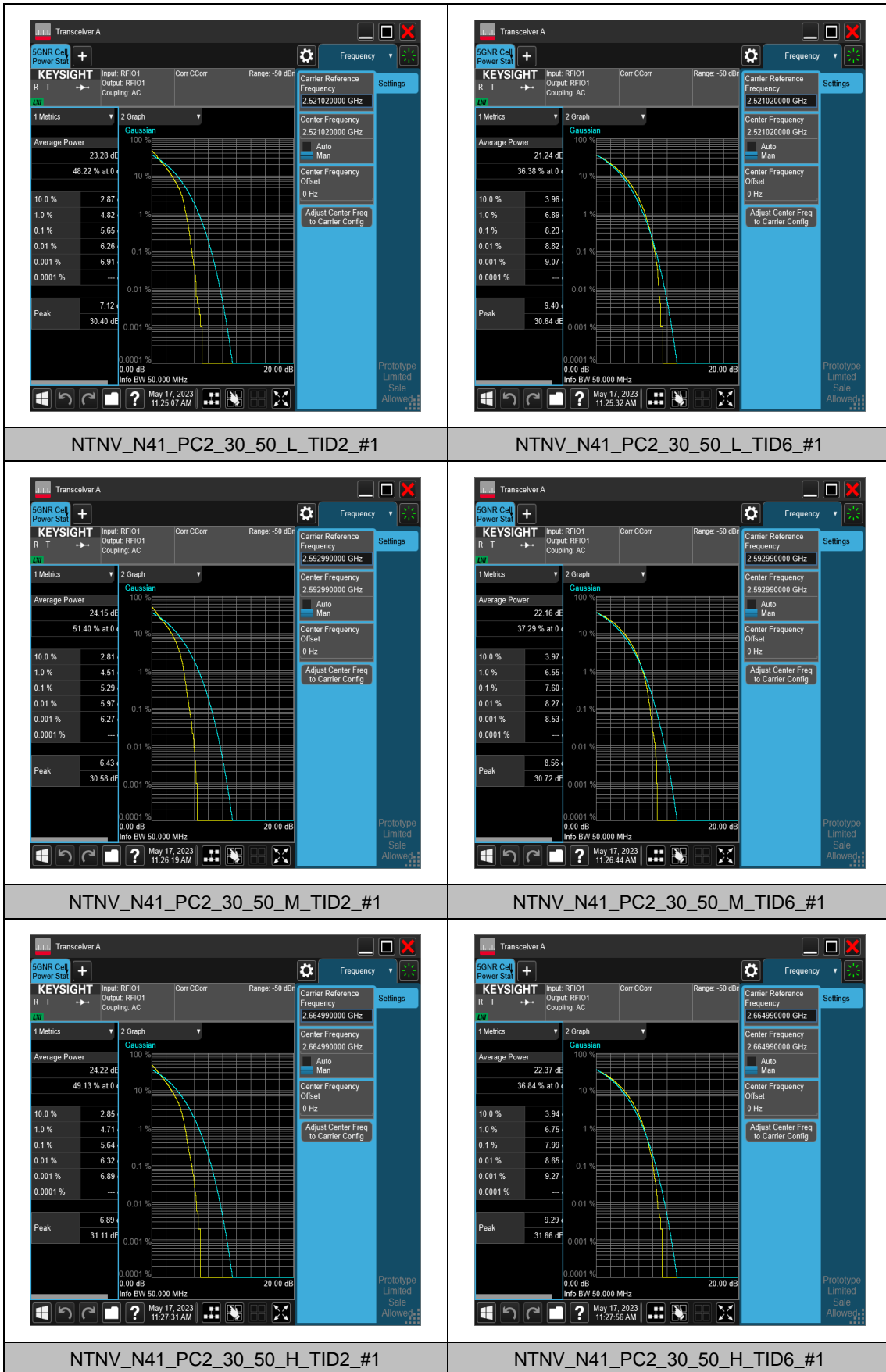
### Test Graphs



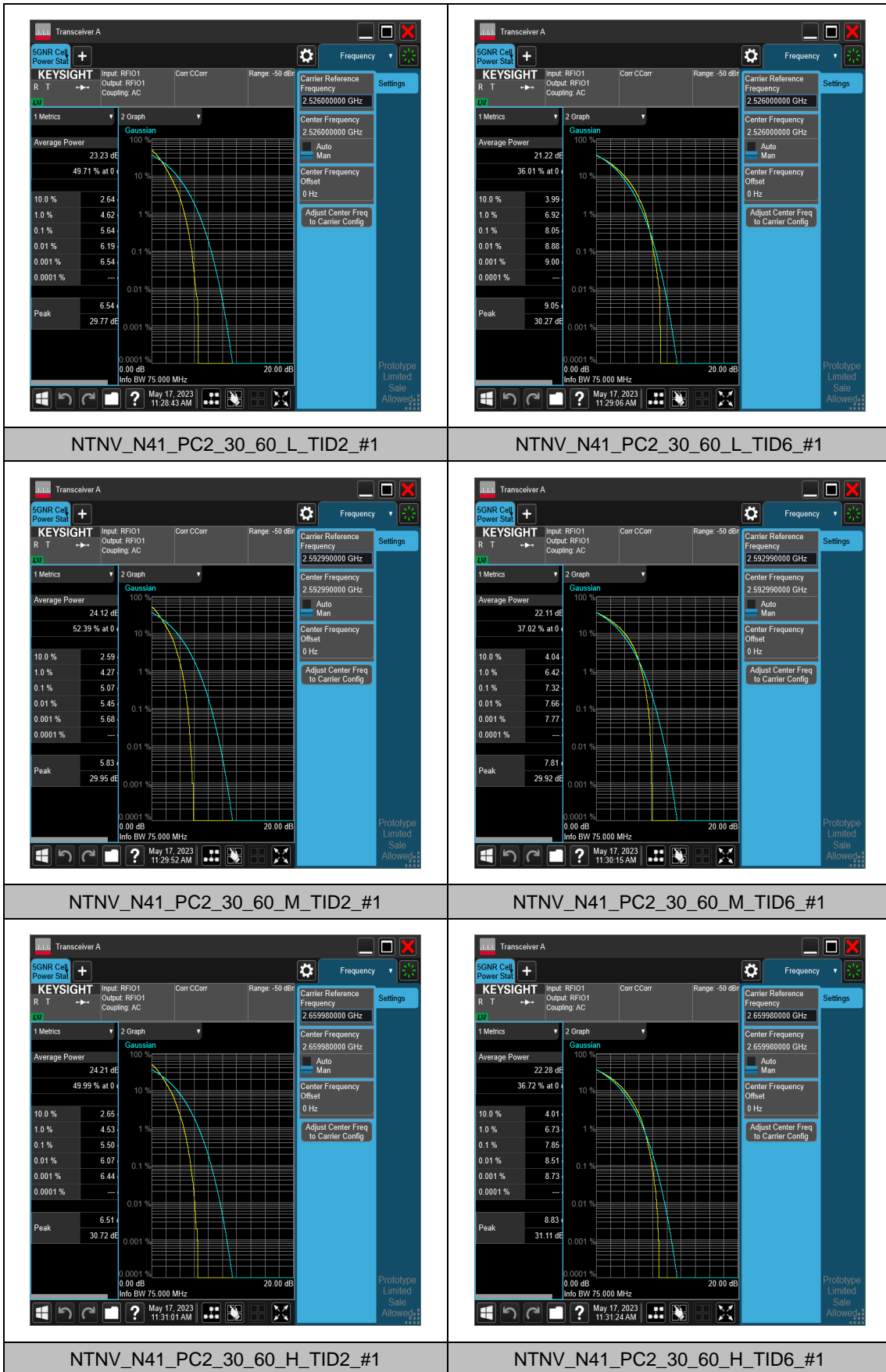


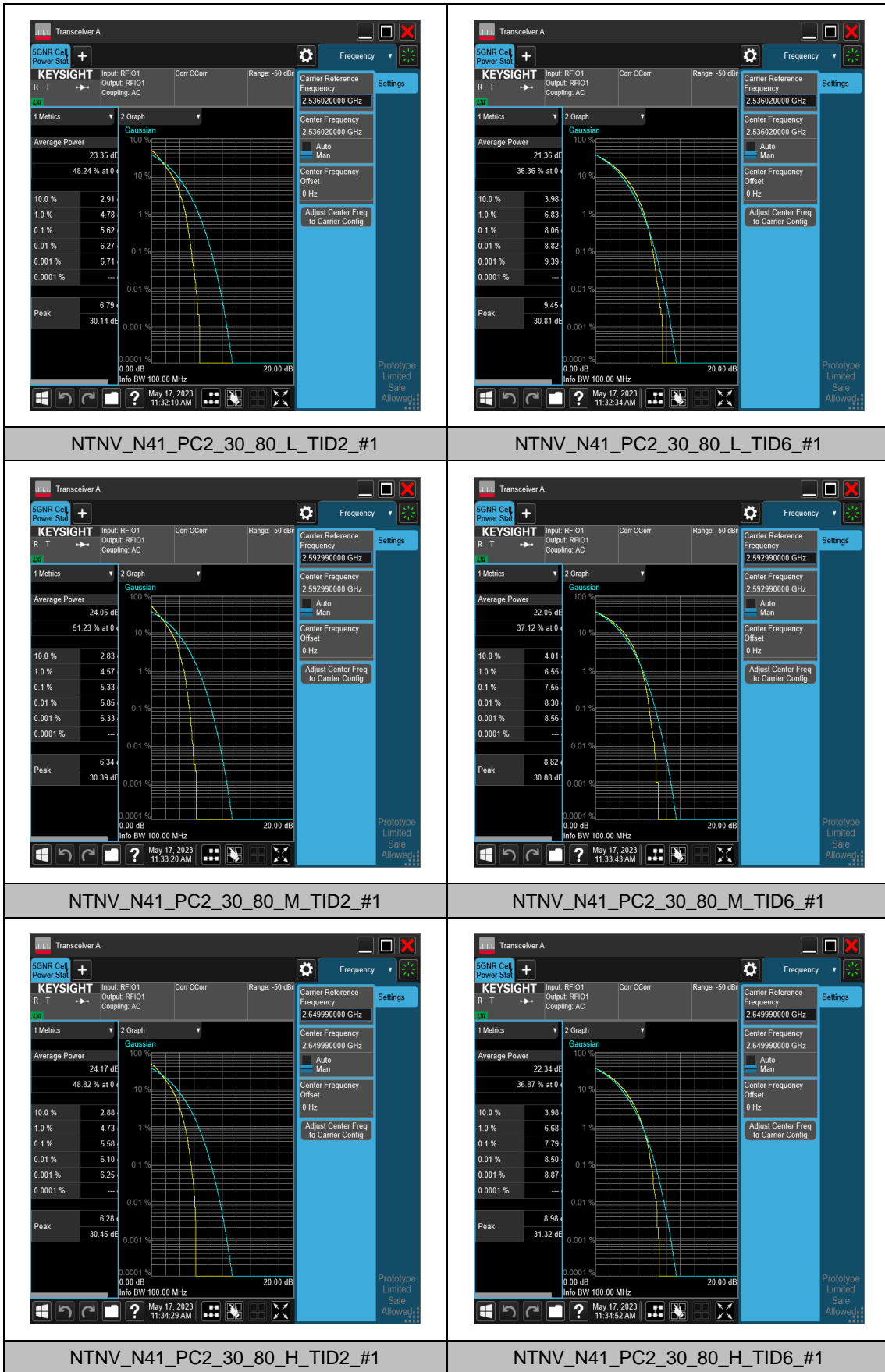


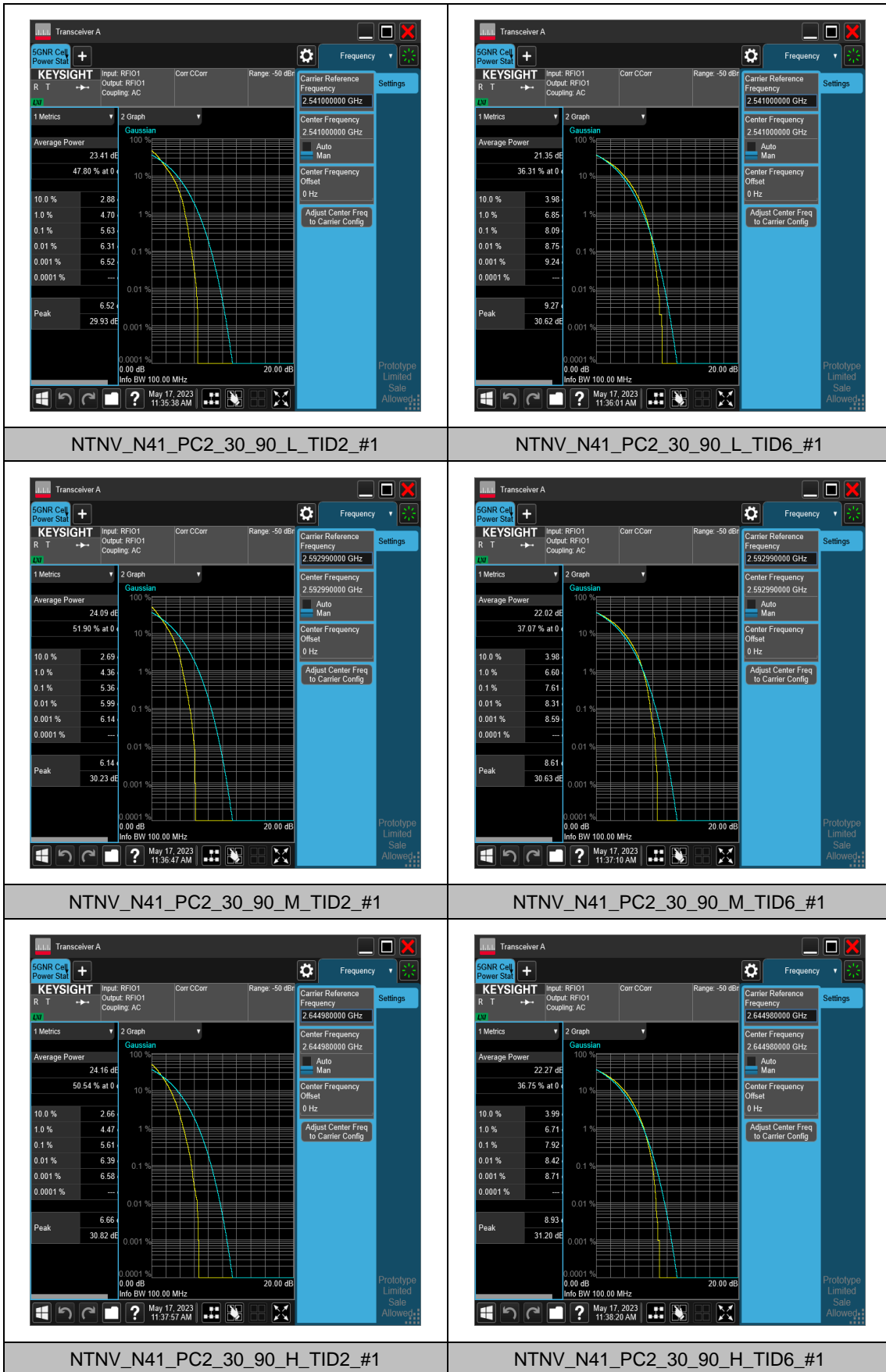


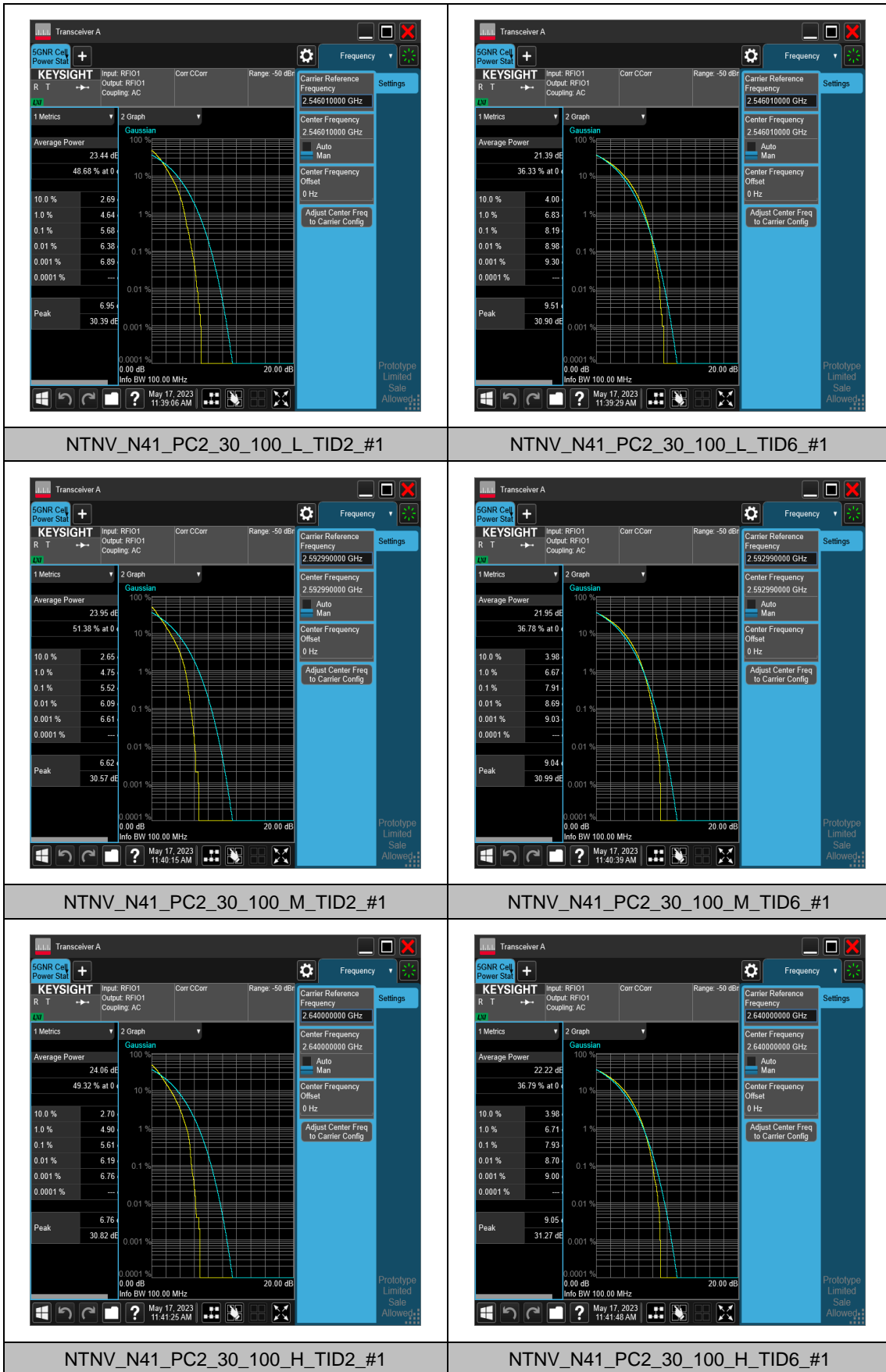












## Appendix C: 26dB Bandwidth and Occupied Bandwidth for SA

### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
N41	30	10	DFT-QPSK	L	Outer_Full	8.5696	9.370	PASS
N41	30	10	CP-QPSK	L	Outer_Full	8.5768	9.447	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	8.5873	9.371	PASS
N41	30	10	CP-QPSK	M	Outer_Full	8.5804	9.454	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	8.6113	9.390	PASS
N41	30	10	CP-QPSK	H	Outer_Full	8.6065	9.413	PASS
N41	30	15	DFT-QPSK	L	Outer_Full	12.824	13.88	PASS
N41	30	15	CP-QPSK	L	Outer_Full	13.550	14.70	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	12.864	14.04	PASS
N41	30	15	CP-QPSK	M	Outer_Full	13.563	14.91	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	12.865	13.94	PASS
N41	30	15	CP-QPSK	H	Outer_Full	13.563	15.04	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	17.834	19.03	PASS
N41	30	20	CP-QPSK	L	Outer_Full	18.181	19.51	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	17.847	19.03	PASS
N41	30	20	CP-QPSK	M	Outer_Full	18.179	19.58	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	17.832	19.09	PASS
N41	30	20	CP-QPSK	H	Outer_Full	18.177	19.70	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	35.633	37.67	PASS
N41	30	40	CP-QPSK	L	Outer_Full	37.770	39.53	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	35.746	37.63	PASS
N41	30	40	CP-QPSK	M	Outer_Full	37.827	39.86	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	35.691	37.57	PASS
N41	30	40	CP-QPSK	H	Outer_Full	37.827	40.28	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	45.696	48.11	PASS
N41	30	50	CP-QPSK	L	Outer_Full	47.434	49.63	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	45.732	48.28	PASS
N41	30	50	CP-QPSK	M	Outer_Full	47.492	49.87	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	45.724	47.84	PASS
N41	30	50	CP-QPSK	H	Outer_Full	47.499	50.02	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	57.746	60.41	PASS
N41	30	60	CP-QPSK	L	Outer_Full	57.763	60.70	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	57.792	60.35	PASS
N41	30	60	CP-QPSK	M	Outer_Full	57.900	60.55	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	57.741	60.21	PASS
N41	30	60	CP-QPSK	H	Outer_Full	57.834	61.42	PASS

N41	30	80	DFT-QPSK	L	Outer_Full	77.016	80.12	PASS
N41	30	80	CP-QPSK	L	Outer_Full	77.381	80.37	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	77.062	80.13	PASS
N41	30	80	CP-QPSK	M	Outer_Full	77.408	80.21	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	77.001	80.01	PASS
N41	30	80	CP-QPSK	H	Outer_Full	77.222	80.30	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	86.583	89.76	PASS
N41	30	90	CP-QPSK	L	Outer_Full	87.249	90.70	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	86.647	89.78	PASS
N41	30	90	CP-QPSK	M	Outer_Full	87.495	90.29	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	86.348	89.73	PASS
N41	30	90	CP-QPSK	H	Outer_Full	87.321	90.46	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	96.241	99.92	PASS
N41	30	100	CP-QPSK	L	Outer_Full	97.210	101.3	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	96.263	99.93	PASS
N41	30	100	CP-QPSK	M	Outer_Full	97.432	101.0	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	96.346	99.84	PASS
N41	30	100	CP-QPSK	H	Outer_Full	97.410	100.7	PASS

### Test Graphs

