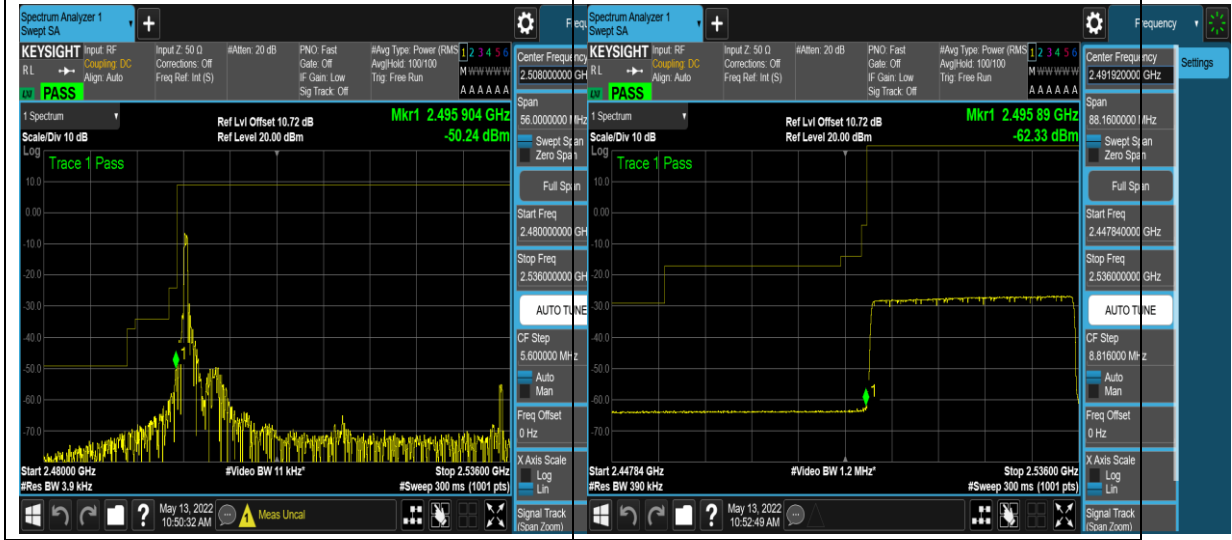


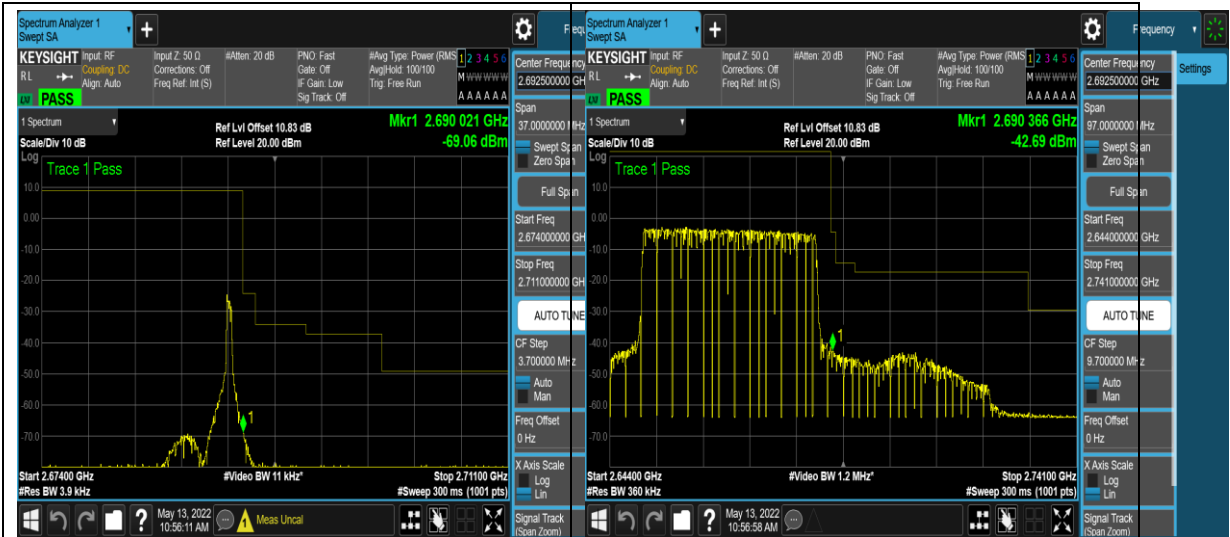
1-N41-PC3-30-40-L-1-DFT-PI2BPSK-Edge_1 RB_Left-1@0--46.92,Margin[21.92]-PASS-see graph

1-N41-PC3-30-40-L-3-DFT-PI2BPSK-Outer_Full-100@0--62.11,Margin[37.11]-PASS-see graph



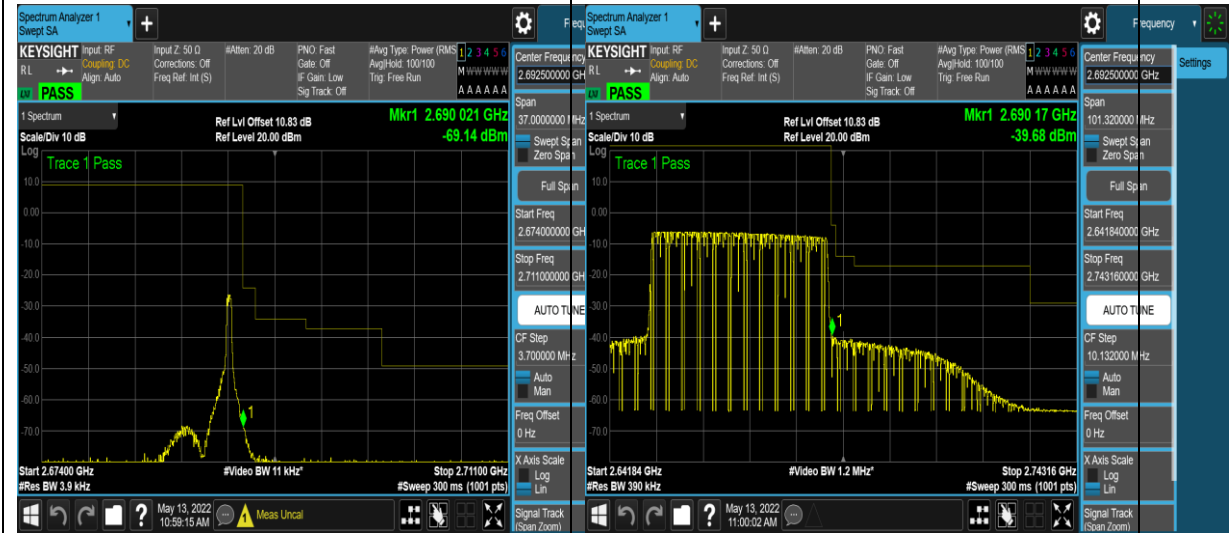
1-N41-PC3-30-40-L-4-CP-QPSK-Edge_1RB_Left-1@0--50.24,Margin[25.24]-PASS-see graph

1-N41-PC3-30-40-L-6-CP-QPSK-Outer_Full-106@0--62.33,Margin[37.33]-PASS-see graph



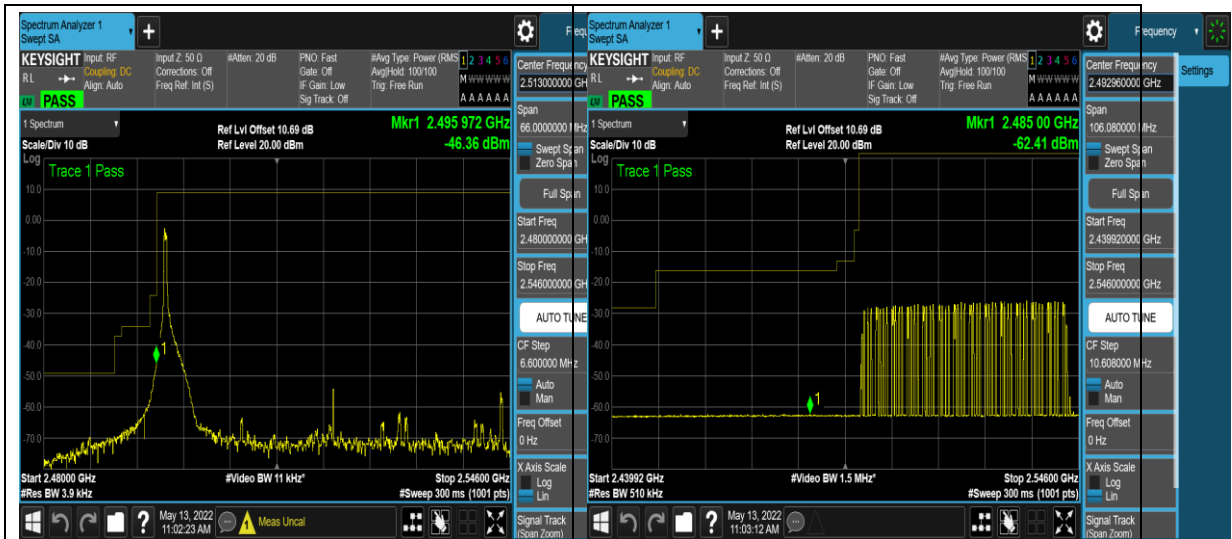
1-N41-PC3-30-40-H-2-DFT-PI2BPSK-Edge_1RB_Right-1@105--69.06,Margin[44.06]-PA SS-see graph

1-N41-PC3-30-40-H-3-DFT-PI2BPSK-Outer_Full-100@0--42.69,Margin[17.69]-PASS-see graph



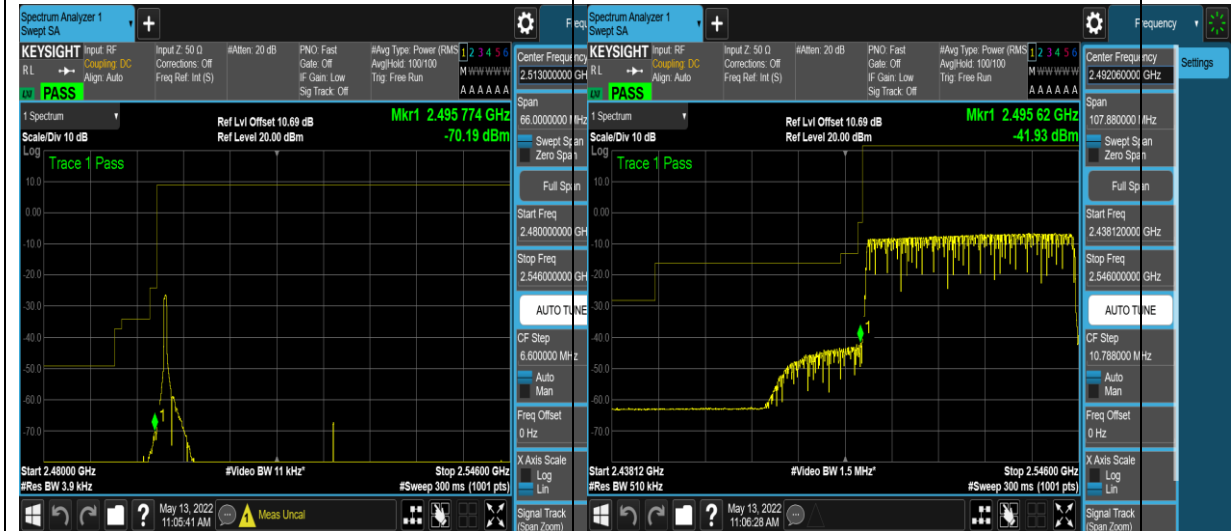
1-N41-PC3-30-40-H-5-CP-QPSK-Edge_1RB_Right-1@105--69.14,Margin[44.14]-PASS-see graph

1-N41-PC3-30-40-H-6-CP-QPSK-Outer_Full-106@0--39.68,Margin[14.68]-PASS-see graph



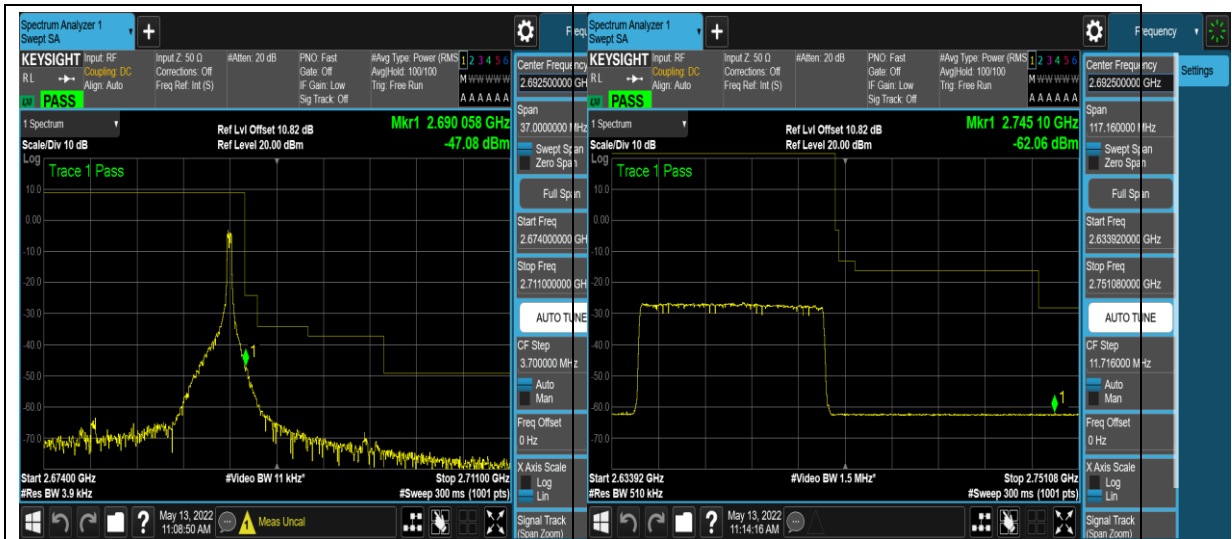
1-N41-PC3-30-50-L-1-DFT-PI2BPSK-Edge_1
RB_Left-1@0--46.36,Margin[21.36]-PASS-se
e graph

1-N41-PC3-30-50-L-3-DFT-PI2BPSK-Outer_
Full-128@0--62.41,Margin[37.41]-PASS-see
graph



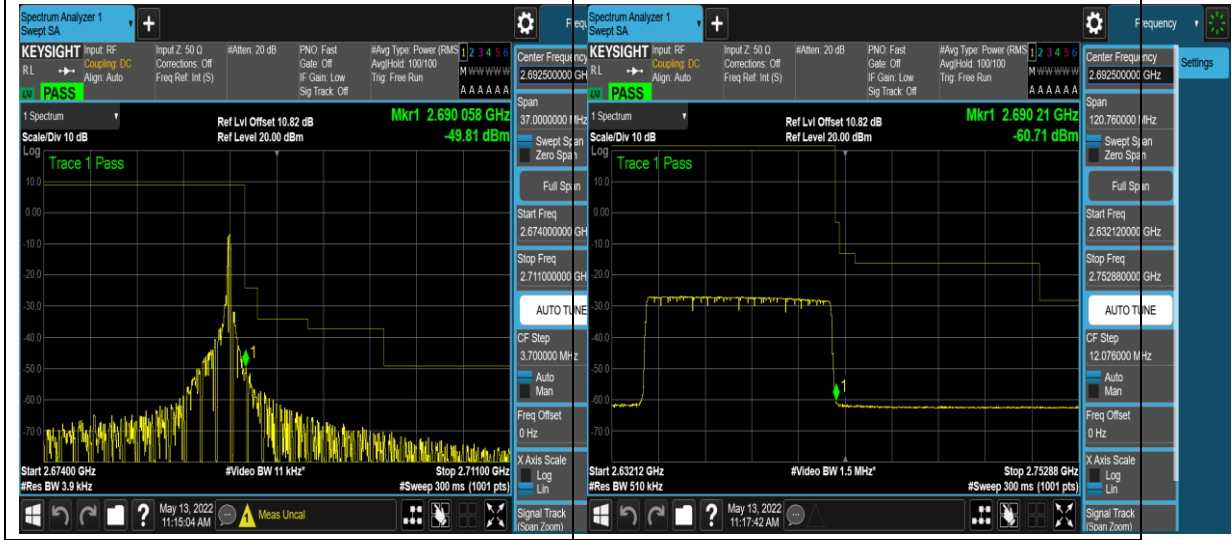
1-N41-PC3-30-50-L-4-CP-QPSK-Edge_1RB
_Left-1@0--70.19,Margin[45.19]-PASS-see
graph

1-N41-PC3-30-50-L-6-CP-QPSK-Outer_Full-
133@0--41.93,Margin[16.93]-PASS-see
graph



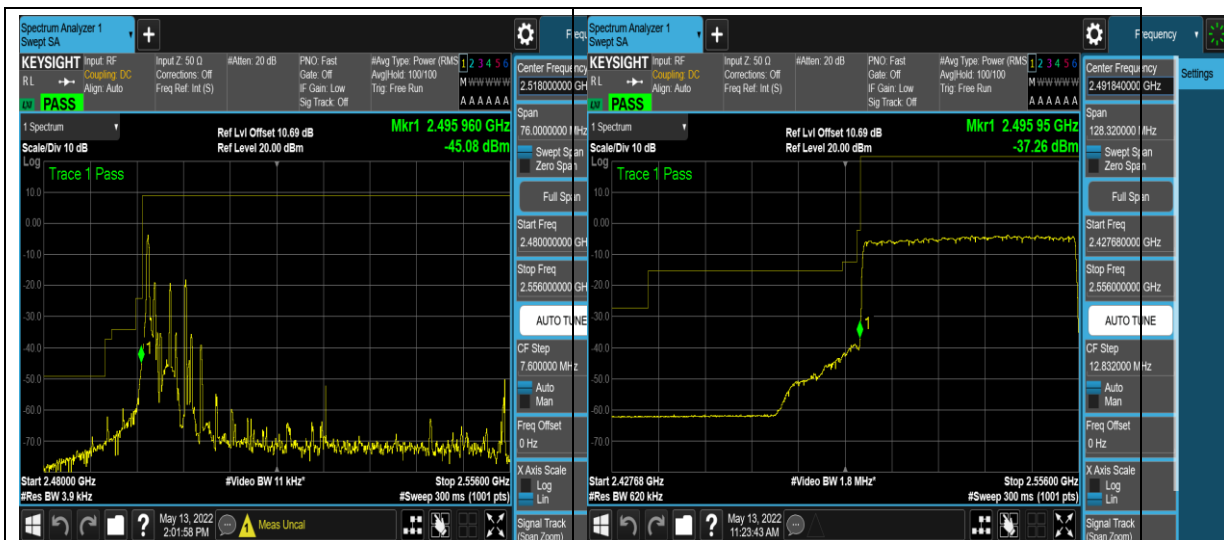
1-N41-PC3-30-50-H-2-DFT-PI2BPSK-Edge_1RB_Right-1@132--47.08,Margin[22.08]-PA SS-see graph

1-N41-PC3-30-50-H-3-DFT-PI2BPSK-Outer_Full-128@0--62.06,Margin[37.06]-PASS-see graph



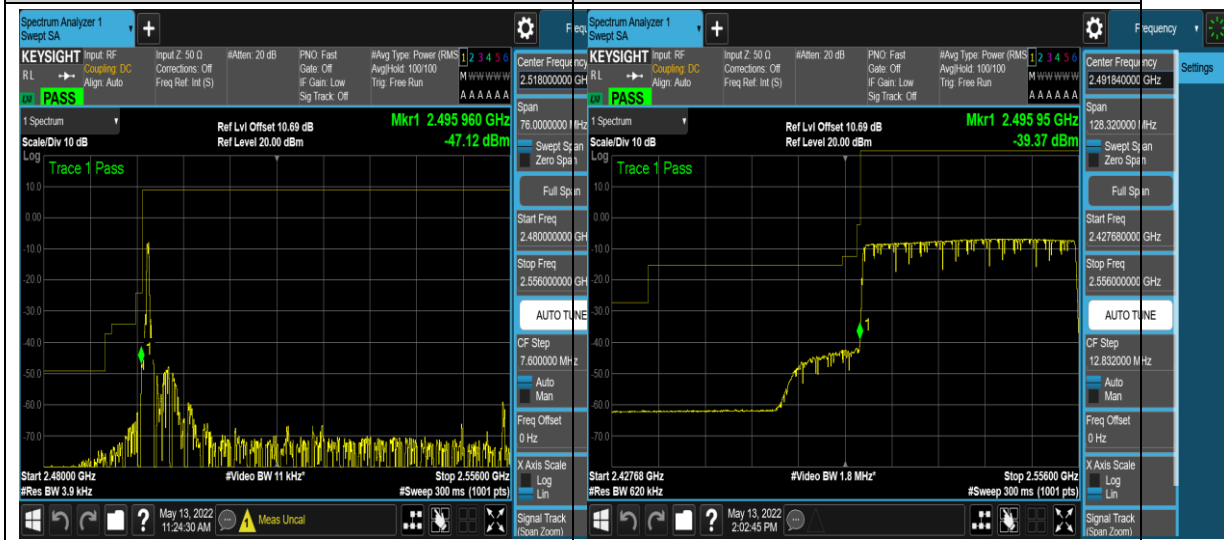
1-N41-PC3-30-50-H-5-CP-QPSK-Edge_1RB_Right-1@132--49.81,Margin[24.81]-PASS-see graph

1-N41-PC3-30-50-H-6-CP-QPSK-Outer_Full-133@0--60.71,Margin[35.71]-PASS-see graph



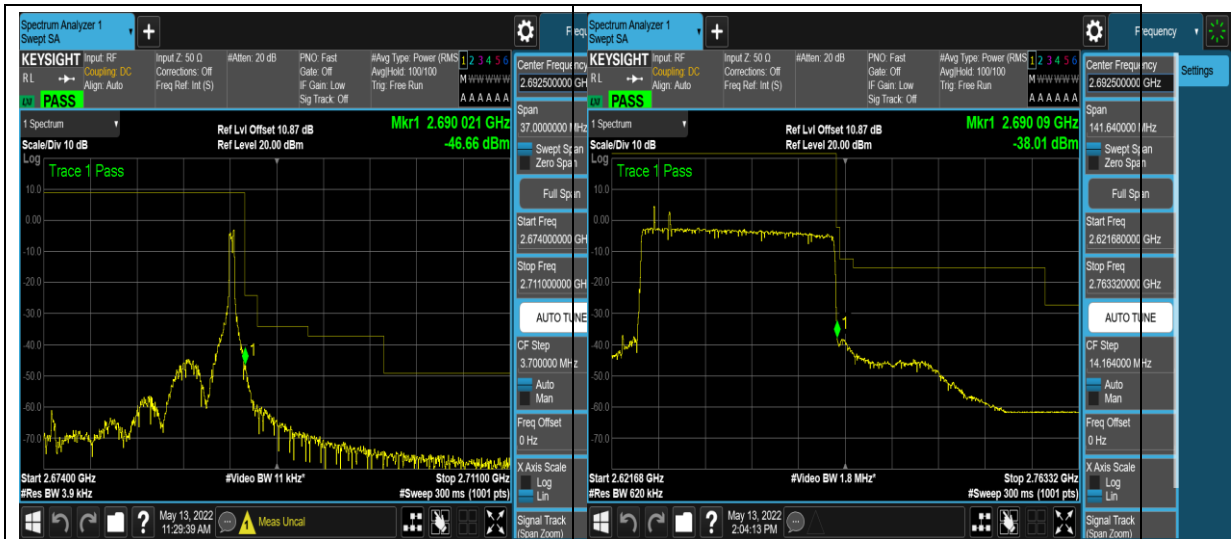
1-N41-PC3-30-60-L-1-DFT-PI2BPSK-Edge_1 RB_Left-1@0--45.08,Margin[20.08]-PASS-se e graph

1-N41-PC3-30-60-L-3-DFT-PI2BPSK-Outer_Full-162@0--37.26,Margin[12.26]-PASS-see graph



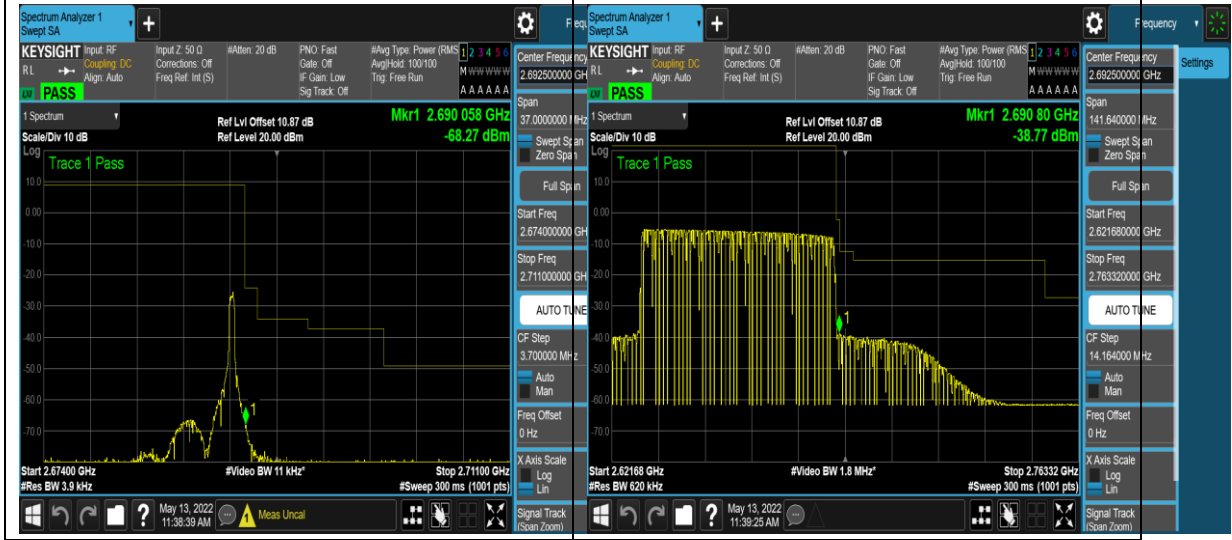
1-N41-PC3-30-60-L-4-CP-QPSK-Edge_1RB_Left-1@0--47.12,Margin[22.12]-PASS-see graph

1-N41-PC3-30-60-L-6-CP-QPSK-Outer_Full-162@0--39.37,Margin[14.37]-PASS-see graph



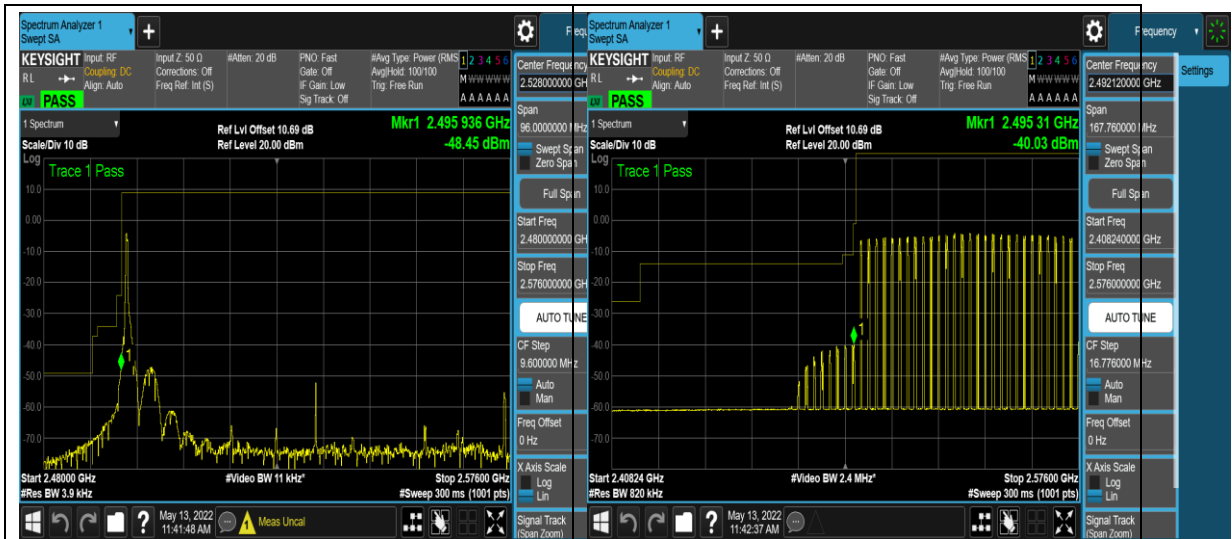
1-N41-PC3-30-60-H-2-DFT-PI2BPSK-Edge_1RB_Right-1@161--46.66,Margin[21.66]-PA SS-see graph

1-N41-PC3-30-60-H-3-DFT-PI2BPSK-Outer_Full-162@0--38.01,Margin[13.01]-PASS-see graph



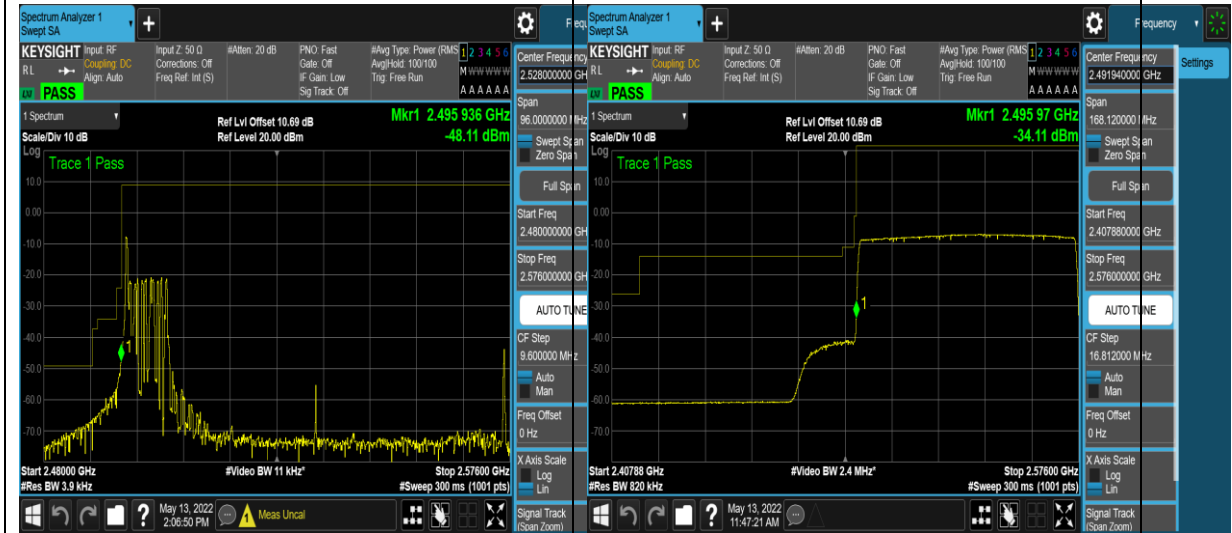
1-N41-PC3-30-60-H-5-CP-QPSK-Edge_1RB_Right-1@161--68.27,Margin[43.27]-PASS-see graph

1-N41-PC3-30-60-H-6-CP-QPSK-Outer_Full-162@0--38.77,Margin[13.77]-PASS-see graph



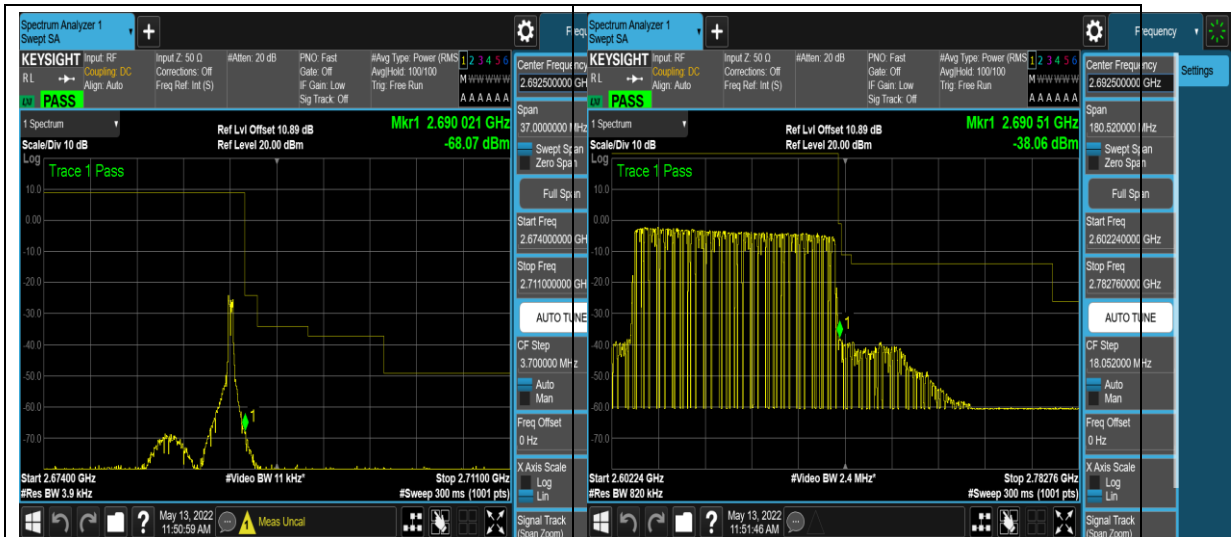
1-N41-PC3-30-80-L-1-DFT-PI2BPSK-Edge_1 RB_Left-1 @0--48.45,Margin[23.45]-PASS-se e graph

1-N41-PC3-30-80-L-3-DFT-PI2BPSK-Outer_Full-216 @0--40.03,Margin[15.03]-PASS-see graph



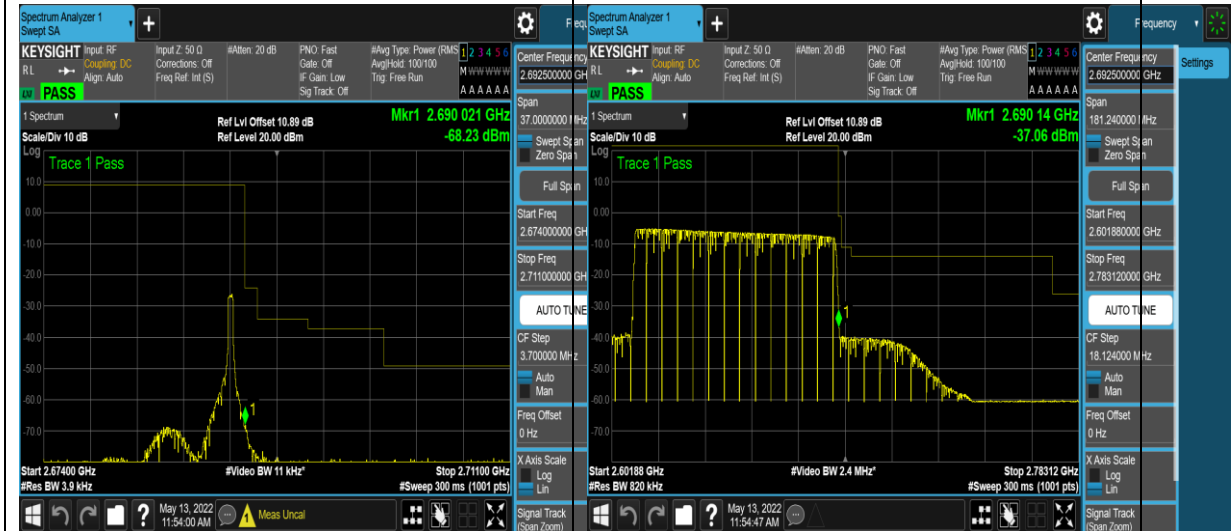
1-N41-PC3-30-80-L-4-CP-QPSK-Edge_1RB_Left-1 @0--48.11,Margin[23.11]-PASS-see graph

1-N41-PC3-30-80-L-6-CP-QPSK-Outer_Full-217 @0--34.11,Margin[9.11]-PASS-see graph



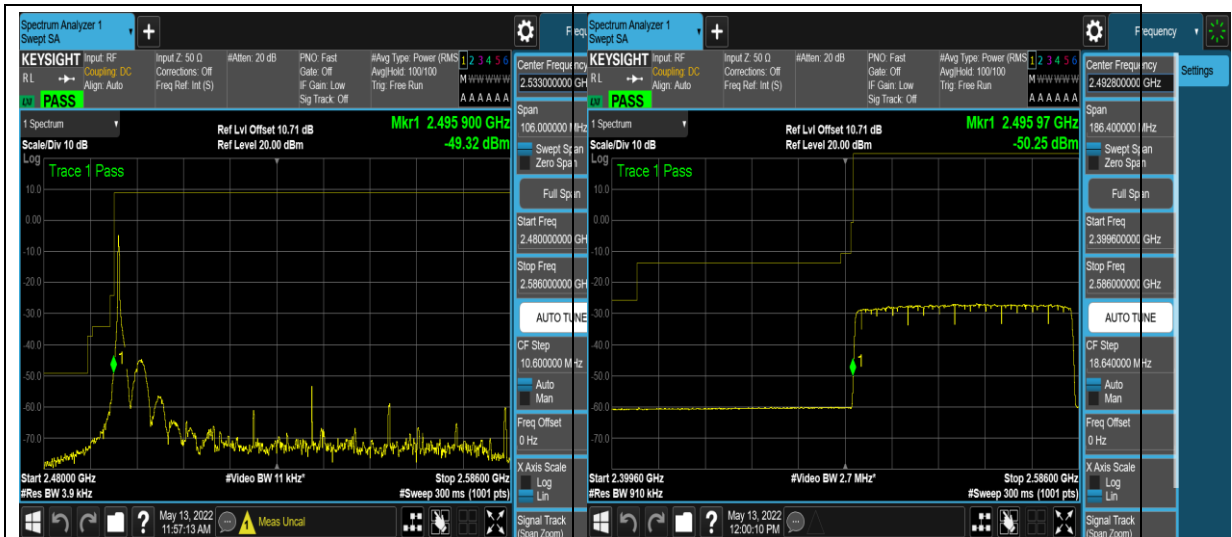
1-N41-PC3-30-80-H-2-DFT-PI2BPSK-Edge_1RB_Right-1@216--68.07,Margin[43.07]-PA SS-see graph

1-N41-PC3-30-80-H-3-DFT-PI2BPSK-Outer_Full-216@0--38.06,Margin[13.06]-PASS-see graph



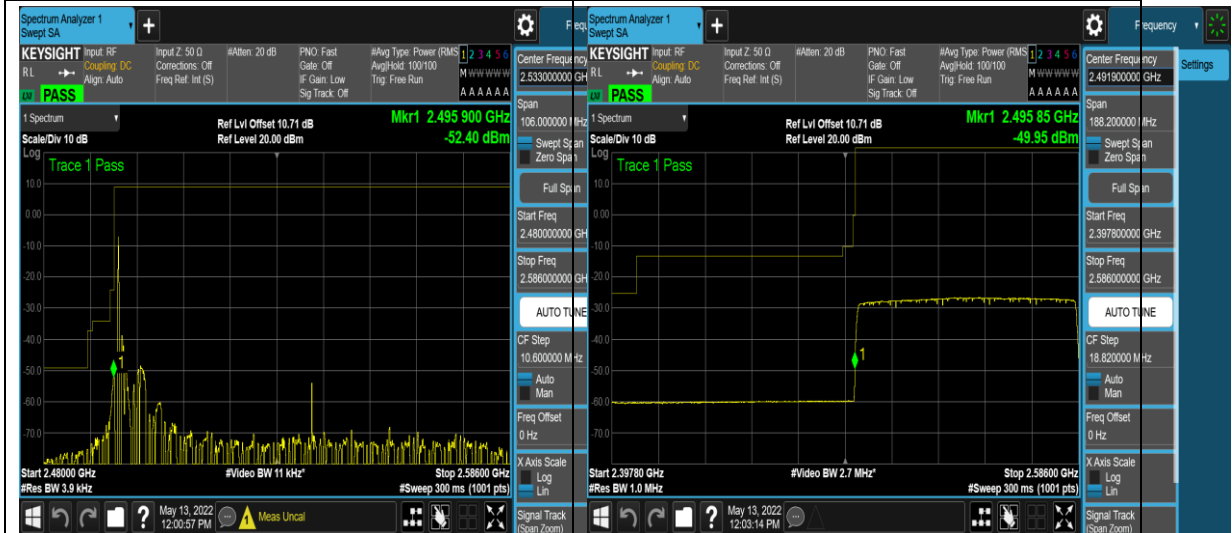
1-N41-PC3-30-80-H-5-CP-QPSK-Edge_1RB_Right-1@216--68.23,Margin[43.23]-PASS-see graph

1-N41-PC3-30-80-H-6-CP-QPSK-Outer_Full-217@0--37.06,Margin[12.06]-PASS-see graph



1-N41-PC3-30-90-L-1-DFT-PI2BPSK-Edge_1
RB_Left-1@0--49.32,Margin[24.32]-PASS-se
e graph

1-N41-PC3-30-90-L-3-DFT-PI2BPSK-Outer_
Full-240@0--50.25,Margin[25.25]-PASS-see
graph



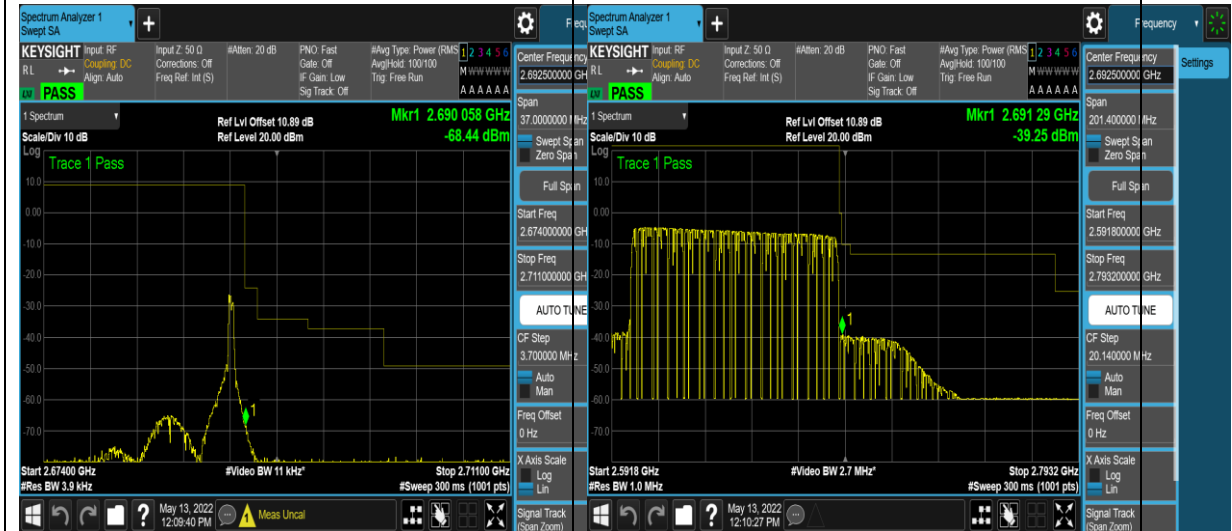
1-N41-PC3-30-90-L-4-CP-QPSK-Edge_1RB
_Left-1@0--52.40,Margin[27.40]-PASS-see
graph

1-N41-PC3-30-90-L-6-CP-QPSK-Outer_Full-
245@0--49.95,Margin[24.95]-PASS-see
graph



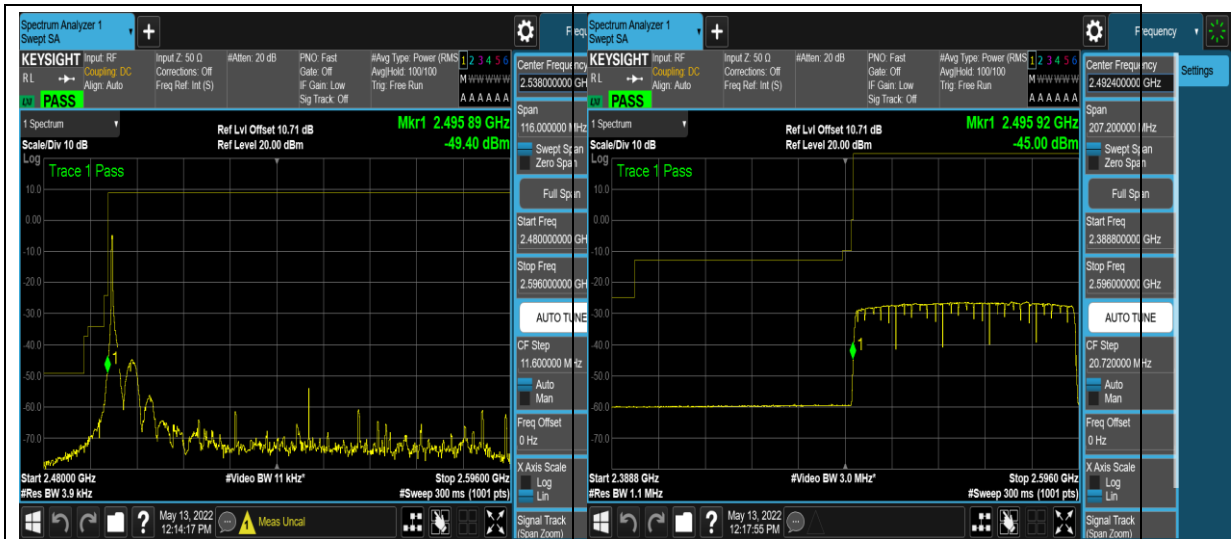
1-N41-PC3-30-90-H-2-DFT-PI2BPSK-Edge_1RB_Right-1@244--69.55,Margin[44.55]-PA SS-see graph

1-N41-PC3-30-90-H-3-DFT-PI2BPSK-Outer_Full-240@0--37.40,Margin[12.40]-PASS-see graph



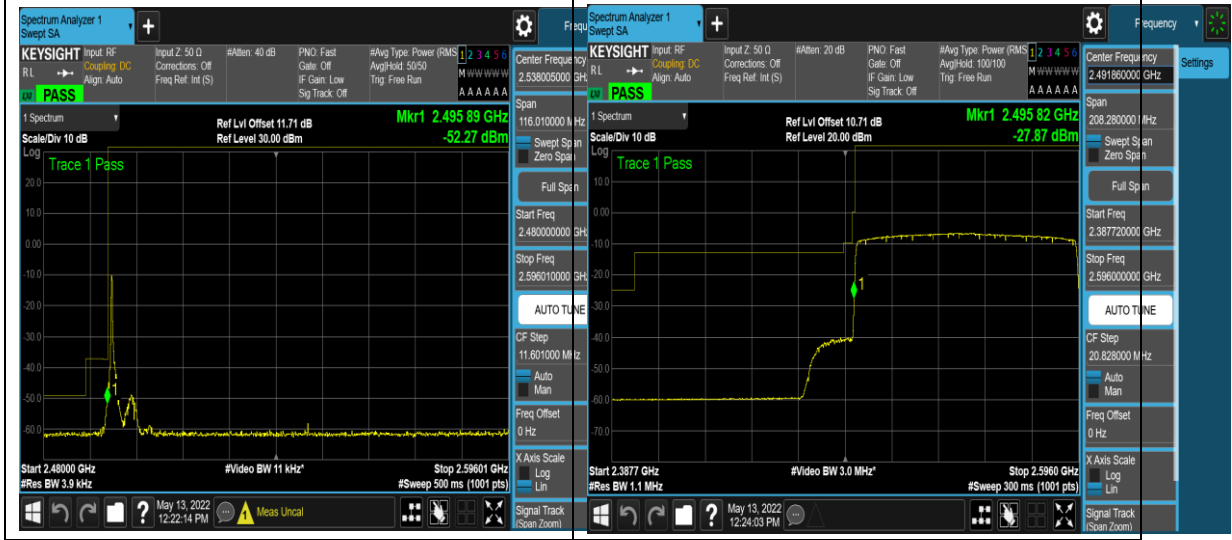
1-N41-PC3-30-90-H-5-CP-QPSK-Edge_1RB_Right-1@244--68.44,Margin[43.44]-PASS-see graph

1-N41-PC3-30-90-H-6-CP-QPSK-Outer_Full-245@0--39.25,Margin[14.25]-PASS-see graph



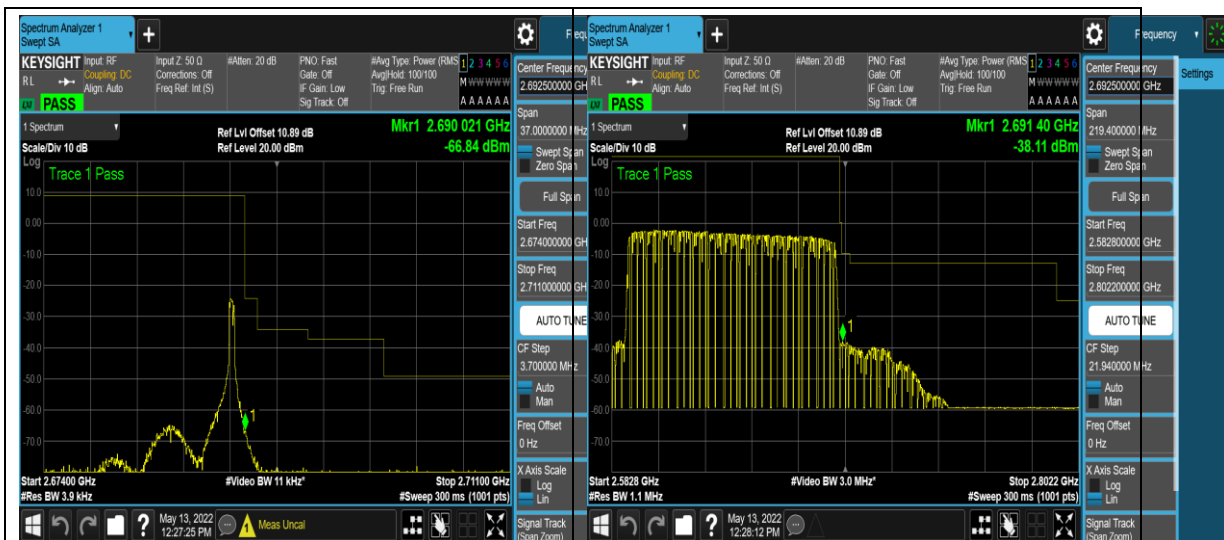
1-N41-PC3-30-100-L-1-DFT-PI2BPSK-Edge_1RB_Left-1@0--49.40,Margin[24.40]-PASS-see graph

1-N41-PC3-30-100-L-3-DFT-PI2BPSK-Outer_Full-270@0--45.00,Margin[20.00]-PASS-see graph



n41-PC3-30-100-L-4-CP-QPSK-Edge_1RB_Left-1@0--52.27,Margin[27.27]-PASS-see graph

1-N41-PC3-30-100-L-6-CP-QPSK-Outer_Full-273@0--27.87,Margin[2.87]-PASS-see graph



1-N41-PC3-30-100-H-2-DFT-PI2BPSK-Edge
_1RB_Right-1@272--66.84,Margin[41.84]-PA
SS-see graph

1-N41-PC3-30-100-H-3-DFT-PI2BPSK-Outer
_Full-270@0--38.11,Margin[13.11]-PASS-see
graph



1-N41-PC3-30-100-H-5-CP-QPSK-Edge_1R
B_Right-1@272--65.23,Margin[40.23]-PASS-
see graph

1-N41-PC3-30-100-H-6-CP-QPSK-Outer_Ful
l-273@0--25.39,Margin[0.39]-PASS-see
graph

Appendix E: Conducted Spurious Emission for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	StartFreq	StopFreq	Verdict
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS

N41	30	15	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS

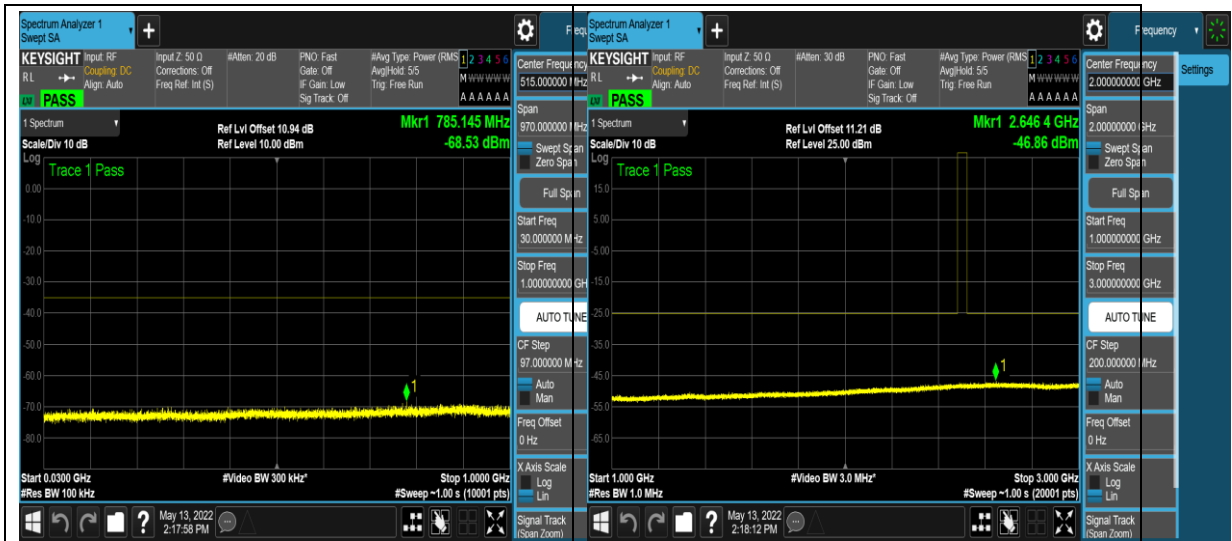
N41	30	30	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS

N41	30	50	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS

N41	30	60	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS

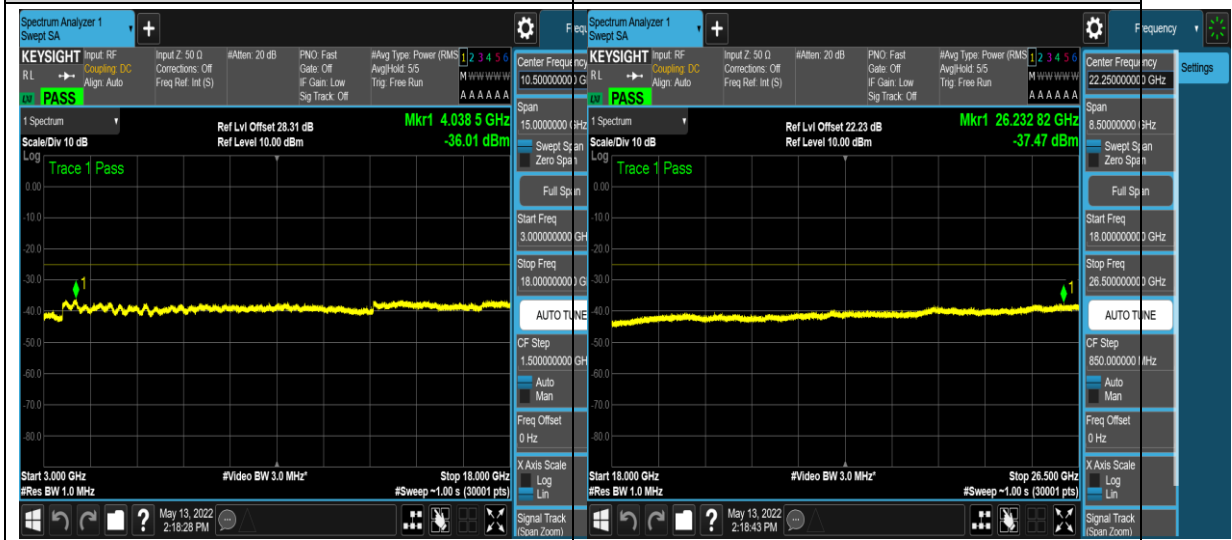
N41	30	90	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	30	1000	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	1000	3000	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	3000	18000	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	18000	26500	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	30	1000	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	1000	3000	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	3000	18000	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	18000	26500	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	18000	26500	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	30	1000	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	1000	3000	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	3000	18000	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	18000	26500	PASS

Test Graphs



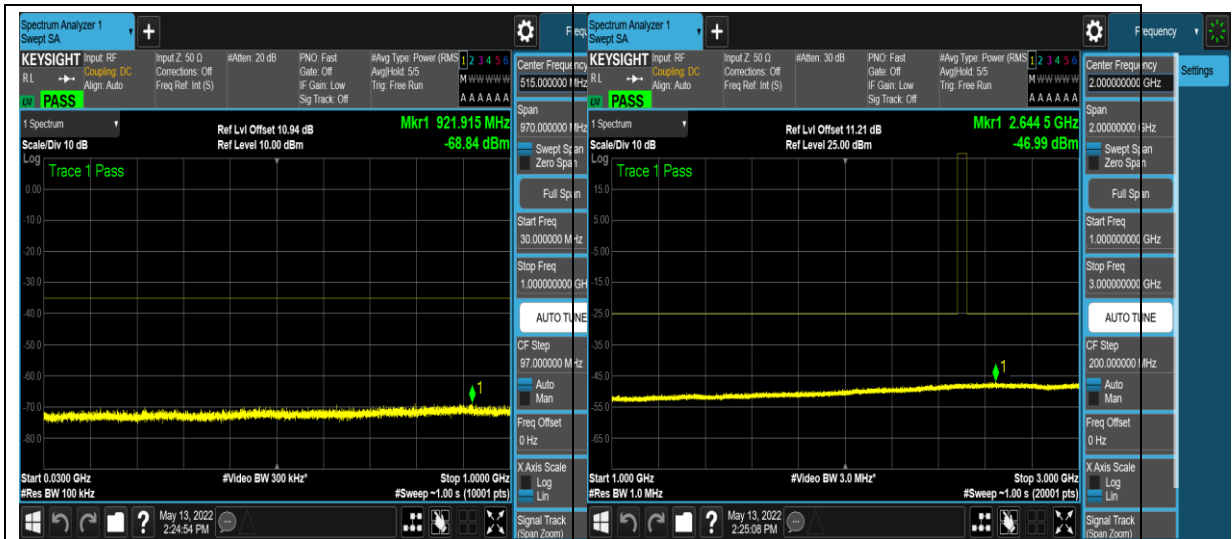
1-N41-PC3-30-10-L-4-DFT-QPSK-Edge_1RB
_Left-1 @ 0-30-1000--68.53--35-PASS

1-N41-PC3-30-10-L-4-DFT-QPSK-Edge_1RB
_Left-1 @ 0-1000-3000--46.86--25-PASS



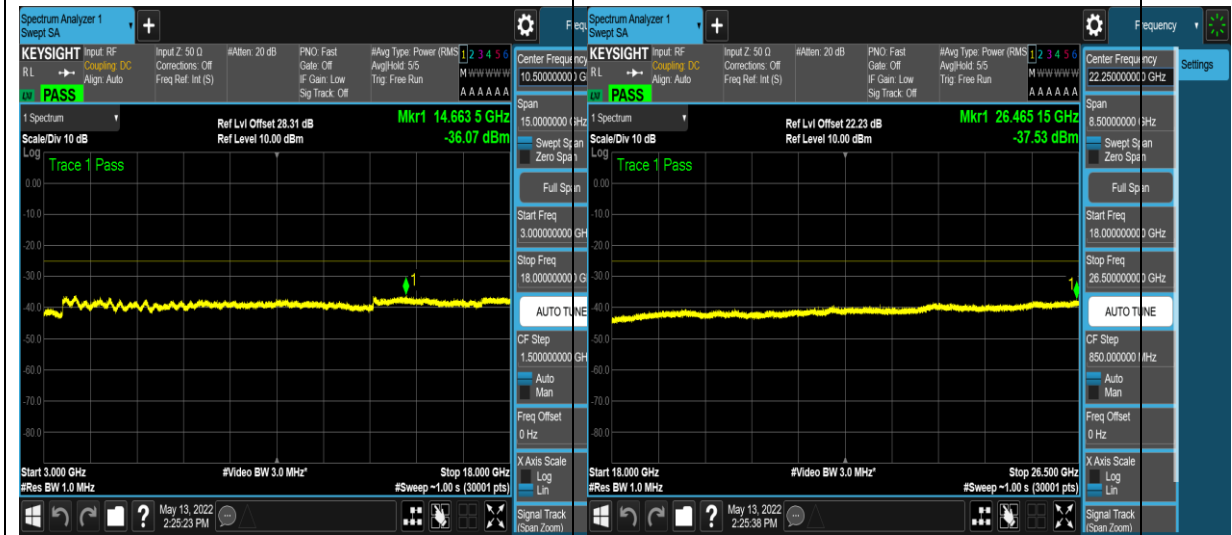
1-N41-PC3-30-10-L-4-DFT-QPSK-Edge_1RB
_Left-1 @ 0-3000-18000--36.01--25-PASS

1-N41-PC3-30-10-L-4-DFT-QPSK-Edge_1RB
_Left-1 @ 0-18000-26500--37.47--25-PASS



1-N41-PC3-30-10-L-16-CP-QPSK-Edge_1R
B_Left-1@0-30-1000--68.84--35-PASS

1-N41-PC3-30-10-L-16-CP-QPSK-Edge_1R
B_Left-1@0-1000-3000--46.99--25-PASS



1-N41-PC3-30-10-L-16-CP-QPSK-Edge_1R
B_Left-1@0-3000-18000--36.07--25-PASS

1-N41-PC3-30-10-L-16-CP-QPSK-Edge_1R
B_Left-1@0-18000-26500--37.53--25-PASS

