

N41	30	10	CP-QPSK	M	Edge_1RB_Left	18.28	PC3	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Right	18.74	PC3	PASS
N41	30	10	CP-QPSK	M	Outer_Full	19.36	PC3	PASS
N41	30	10	CP-QPSK	M	Inner_Full	19.25	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	19.36	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	19.54	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Outer_Full	19.43	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Inner_Full	19.28	PC3	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	18.42	PC3	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Right	18.62	PC3	PASS
N41	30	10	CP-QPSK	H	Outer_Full	18.16	PC3	PASS
N41	30	10	CP-QPSK	H	Inner_Full	18.21	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	18.15	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	18.27	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Outer_Full	18.15	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Inner_Full	18.16	PC3	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	18.26	PC3	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Right	18.28	PC3	PASS
N41	30	15	CP-QPSK	L	Outer_Full	18.41	PC3	PASS
N41	30	15	CP-QPSK	L	Inner_Full	18.23	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	18.21	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	19.16	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Outer_Full	19.27	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Inner_Full	19.36	PC3	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	18.27	PC3	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Right	18.71	PC3	PASS
N41	30	15	CP-QPSK	M	Outer_Full	18.63	PC3	PASS
N41	30	15	CP-QPSK	M	Inner_Full	18.25	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	18.42	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Outer_Full	18.63	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Inner_Full	19.24	PC3	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	18.75	PC3	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Right	18.26	PC3	PASS
N41	30	15	CP-QPSK	H	Outer_Full	18.36	PC3	PASS
N41	30	15	CP-QPSK	H	Inner_Full	18.85	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	18.41	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	18.21	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	18.25	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Inner_Full	18.98	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	18.74	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	18.89	PC3	PASS
N41	30	20	CP-QPSK	L	Outer_Full	18.62	PC3	PASS

N41	30	20	CP-QPSK	L	Inner_Full	18.24	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	19.46	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	19.35	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	19.41	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Inner_Full	19.62	PC3	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	18.41	PC3	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	19.27	PC3	PASS
N41	30	20	CP-QPSK	M	Outer_Full	18.68	PC3	PASS
N41	30	20	CP-QPSK	M	Inner_Full	19.41	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	18.25	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	19.36	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	19.54	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Inner_Full	18.78	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	18.64	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	18.45	PC3	PASS
N41	30	20	CP-QPSK	H	Outer_Full	18.56	PC3	PASS
N41	30	20	CP-QPSK	H	Inner_Full	18.59	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	18.75	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	18.32	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Outer_Full	18.65	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Inner_Full	19.74	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	19.25	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Right	19.36	PC3	PASS
N41	30	30	CP-QPSK	L	Outer_Full	19.87	PC3	PASS
N41	30	30	CP-QPSK	L	Inner_Full	19.75	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	19.52	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	19.36	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Outer_Full	19.21	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Inner_Full	19.18	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	19.16	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Right	19.87	PC3	PASS
N41	30	30	CP-QPSK	M	Outer_Full	19.87	PC3	PASS
N41	30	30	CP-QPSK	M	Inner_Full	19.64	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	19.71	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	19.26	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Outer_Full	19.21	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Inner_Full	19.98	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	19.41	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Right	19.26	PC3	PASS
N41	30	30	CP-QPSK	H	Outer_Full	19.28	PC3	PASS
N41	30	30	CP-QPSK	H	Inner_Full	19.47	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	19.62	PC3	PASS

N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	19.54	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	19.54	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Inner_Full	19.65	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	19.20	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	19.13	PC3	PASS
N41	30	40	CP-QPSK	L	Outer_Full	19.78	PC3	PASS
N41	30	40	CP-QPSK	L	Inner_Full	19.56	PC3	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	19.98	PC3	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	19.74	PC3	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	19.21	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	19.36	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	19.36	PC3	PASS
N41	30	40	CP-QPSK	M	Outer_Full	19.45	PC3	PASS
N41	30	40	CP-QPSK	M	Inner_Full	19.31	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	19.23	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	19.45	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	19.65	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Inner_Full	19.45	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	19.39	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	19.54	PC3	PASS
N41	30	40	CP-QPSK	H	Outer_Full	19.12	PC3	PASS
N41	30	40	CP-QPSK	H	Inner_Full	19.35	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	19.41	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	19.26	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Outer_Full	19.25	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Inner_Full	19.42	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	19.69	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	19.78	PC3	PASS
N41	30	50	CP-QPSK	L	Outer_Full	19.54	PC3	PASS
N41	30	50	CP-QPSK	L	Inner_Full	19.26	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	19.36	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Outer_Full	19.25	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Inner_Full	19.36	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	19.87	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Right	19.45	PC3	PASS
N41	30	50	CP-QPSK	M	Outer_Full	19.69	PC3	PASS
N41	30	50	CP-QPSK	M	Inner_Full	19.87	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	19.65	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	19.54	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Outer_Full	19.52	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Inner_Full	19.36	PC3	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	19.54	PC3	PASS

N41	30	50	CP-QPSK	H	Edge_1RB_Right	19.24	PC3	PASS
N41	30	50	CP-QPSK	H	Outer_Full	19.32	PC3	PASS
N41	30	50	CP-QPSK	H	Inner_Full	19.17	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	19.26	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	19.68	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	19.52	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Inner_Full	19.87	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	19.75	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	19.35	PC3	PASS
N41	30	60	CP-QPSK	L	Outer_Full	19.65	PC3	PASS
N41	30	60	CP-QPSK	L	Inner_Full	19.45	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	19.28	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	19.64	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Inner_Full	19.41	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	19.26	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	19.58	PC3	PASS
N41	30	60	CP-QPSK	M	Outer_Full	19.45	PC3	PASS
N41	30	60	CP-QPSK	M	Inner_Full	19.24	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	19.36	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	19.54	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	19.46	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Inner_Full	19.62	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	19.21	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	19.42	PC3	PASS
N41	30	60	CP-QPSK	H	Outer_Full	19.36	PC3	PASS
N41	30	60	CP-QPSK	H	Inner_Full	19.54	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	19.45	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	19.29	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Outer_Full	19.45	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Inner_Full	19.32	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	19.25	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	19.54	PC3	PASS
N41	30	80	CP-QPSK	L	Outer_Full	19.69	PC3	PASS
N41	30	80	CP-QPSK	L	Inner_Full	19.45	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	19.25	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	19.36	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Outer_Full	19.57	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Inner_Full	19.46	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	19.74	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	19.36	PC3	PASS
N41	30	80	CP-QPSK	M	Outer_Full	19.54	PC3	PASS
N41	30	80	CP-QPSK	M	Inner_Full	19.25	PC3	PASS

N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	19.36	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	19.24	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Outer_Full	19.78	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Inner_Full	19.64	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	19.26	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	19.26	PC3	PASS
N41	30	80	CP-QPSK	H	Outer_Full	19.87	PC3	PASS
N41	30	80	CP-QPSK	H	Inner_Full	19.12	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	19.36	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Right	19.54	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Outer_Full	19.87	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Inner_Full	19.45	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	19.36	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Right	19.54	PC3	PASS
N41	30	90	CP-QPSK	L	Outer_Full	19.12	PC3	PASS
N41	30	90	CP-QPSK	L	Inner_Full	19.65	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Left	19.87	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Right	19.74	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Outer_Full	19.25	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Inner_Full	19.37	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	19.26	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Right	19.54	PC3	PASS
N41	30	90	CP-QPSK	M	Outer_Full	19.15	PC3	PASS
N41	30	90	CP-QPSK	M	Inner_Full	19.15	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	19.36	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Outer_Full	19.15	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Inner_Full	19.65	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	19.15	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	19.48	PC3	PASS
N41	30	90	CP-QPSK	H	Outer_Full	19.15	PC3	PASS
N41	30	90	CP-QPSK	H	Inner_Full	19.16	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	19.25	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	19.18	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	19.10	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Inner_Full	19.05	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	19.35	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	19.05	PC3	PASS
N41	30	100	CP-QPSK	L	Outer_Full	19.24	PC3	PASS
N41	30	100	CP-QPSK	L	Inner_Full	19.28	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	19.26	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	19.36	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Inner_Full	19.54	PC3	PASS

N41	30	100	DFT-QPSK	M	Edge_1RB_Left	19.45	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	19.45	PC3	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	19.28	PC3	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	19.26	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	19.28	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	19.42	PC3	PASS
N41	30	100	CP-QPSK	M	Outer_Full	19.36	PC3	PASS
N41	30	100	CP-QPSK	M	Inner_Full	19.64	PC3	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	19.45	PC3	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	19.23	PC3	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	19.62	PC3	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	19.61	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	<b>19.99</b>	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	19.23	PC3	PASS
N41	30	100	CP-QPSK	H	Outer_Full	19.54	PC3	PASS
N41	30	100	CP-QPSK	H	Inner_Full	19.21	PC3	PASS

**5G NR(P3):**

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	20.52	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	20.17	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Outer_Full	20.30	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Inner_Full	20.16	PC3	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	19.54	PC3	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Right	19.41	PC3	PASS
N41	30	10	CP-QPSK	L	Outer_Full	19.23	PC3	PASS
N41	30	10	CP-QPSK	L	Inner_Full	19.29	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	19.46	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	20.21	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Outer_Full	20.23	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Inner_Full	20.02	PC3	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	19.58	PC3	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Right	19.43	PC3	PASS
N41	30	10	CP-QPSK	M	Outer_Full	20.16	PC3	PASS
N41	30	10	CP-QPSK	M	Inner_Full	20.16	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	20.14	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	19.06	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Outer_Full	19.23	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Inner_Full	20.27	PC3	PASS

N41	30	10	CP-QPSK	H	Edge_1RB_Left	19.46	PC3	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Right	19.62	PC3	PASS
N41	30	10	CP-QPSK	H	Outer_Full	19.42	PC3	PASS
N41	30	10	CP-QPSK	H	Inner_Full	19.25	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	19.62	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	19.45	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Outer_Full	19.36	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Inner_Full	19.41	PC3	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	19.23	PC3	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Right	19.48	PC3	PASS
N41	30	15	CP-QPSK	L	Outer_Full	19.71	PC3	PASS
N41	30	15	CP-QPSK	L	Inner_Full	19.62	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	19.46	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	20.25	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Outer_Full	20.46	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Inner_Full	20.23	PC3	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	19.51	PC3	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Right	19.63	PC3	PASS
N41	30	15	CP-QPSK	M	Outer_Full	19.41	PC3	PASS
N41	30	15	CP-QPSK	M	Inner_Full	19.23	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	19.25	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Outer_Full	19.42	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Inner_Full	20.46	PC3	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	19.28	PC3	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Right	19.41	PC3	PASS
N41	30	15	CP-QPSK	H	Outer_Full	19.26	PC3	PASS
N41	30	15	CP-QPSK	H	Inner_Full	19.30	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	19.37	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	19.34	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	19.74	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Inner_Full	19.62	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	19.51	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	19.23	PC3	PASS
N41	30	20	CP-QPSK	L	Outer_Full	19.24	PC3	PASS
N41	30	20	CP-QPSK	L	Inner_Full	19.64	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	20.45	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	20.15	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	20.25	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Inner_Full	20.68	PC3	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	19.74	PC3	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	19.74	PC3	PASS
N41	30	20	CP-QPSK	M	Outer_Full	19.65	PC3	PASS

N41	30	20	CP-QPSK	M	Inner_Full	20.12	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	19.39	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	20.81	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	20.24	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Inner_Full	19.35	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	19.62	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	19.87	PC3	PASS
N41	30	20	CP-QPSK	H	Outer_Full	19.15	PC3	PASS
N41	30	20	CP-QPSK	H	Inner_Full	19.46	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	19.25	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	19.46	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Outer_Full	19.23	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Inner_Full	20.25	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	20.46	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Right	20.41	PC3	PASS
N41	30	30	CP-QPSK	L	Outer_Full	20.20	PC3	PASS
N41	30	30	CP-QPSK	L	Inner_Full	20.15	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	20.15	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	20.45	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Outer_Full	20.32	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Inner_Full	20.54	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	20.42	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Right	20.62	PC3	PASS
N41	30	30	CP-QPSK	M	Outer_Full	20.41	PC3	PASS
N41	30	30	CP-QPSK	M	Inner_Full	20.23	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	20.24	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	20.46	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Outer_Full	20.48	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Inner_Full	20.46	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	20.16	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Right	20.13	PC3	PASS
N41	30	30	CP-QPSK	H	Outer_Full	20.25	PC3	PASS
N41	30	30	CP-QPSK	H	Inner_Full	20.35	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	20.42	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	20.25	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	20.36	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Inner_Full	20.42	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	20.42	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	20.23	PC3	PASS
N41	30	40	CP-QPSK	L	Outer_Full	20.25	PC3	PASS
N41	30	40	CP-QPSK	L	Inner_Full	20.34	PC3	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	20.78	PC3	PASS



N41	30	40	DFT-QPSK	M	Outer_Full	20.31	PC3	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	20.24	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	20.45	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	20.64	PC3	PASS
N41	30	40	CP-QPSK	M	Outer_Full	20.15	PC3	PASS
N41	30	40	CP-QPSK	M	Inner_Full	20.16	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	20.19	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	20.41	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	20.16	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Inner_Full	20.23	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	20.26	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	20.27	PC3	PASS
N41	30	40	CP-QPSK	H	Outer_Full	20.75	PC3	PASS
N41	30	40	CP-QPSK	H	Inner_Full	20.66	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	20.54	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	20.12	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Outer_Full	20.35	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Inner_Full	20.47	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	20.46	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	20.26	PC3	PASS
N41	30	50	CP-QPSK	L	Outer_Full	20.15	PC3	PASS
N41	30	50	CP-QPSK	L	Inner_Full	20.64	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	20.75	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Outer_Full	20.65	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Inner_Full	20.41	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	20.23	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Right	20.25	PC3	PASS
N41	30	50	CP-QPSK	M	Outer_Full	20.74	PC3	PASS
N41	30	50	CP-QPSK	M	Inner_Full	20.26	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	20.24	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	20.12	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Outer_Full	20.31	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Inner_Full	20.15	PC3	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	20.15	PC3	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Right	20.36	PC3	PASS
N41	30	50	CP-QPSK	H	Outer_Full	20.57	PC3	PASS
N41	30	50	CP-QPSK	H	Inner_Full	20.16	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	20.71	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	20.13	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	20.25	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Inner_Full	20.16	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	20.54	PC3	PASS

N41	30	60	CP-QPSK	L	Edge_1RB_Right	20.13	PC3	PASS
N41	30	60	CP-QPSK	L	Outer_Full	20.23	PC3	PASS
N41	30	60	CP-QPSK	L	Inner_Full	20.27	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	20.16	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	20.21	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Inner_Full	20.15	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	20.15	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	20.16	PC3	PASS
N41	30	60	CP-QPSK	M	Outer_Full	20.87	PC3	PASS
N41	30	60	CP-QPSK	M	Inner_Full	20.36	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	20.21	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	20.16	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	20.25	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Inner_Full	20.14	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	20.45	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	20.13	PC3	PASS
N41	30	60	CP-QPSK	H	Outer_Full	20.25	PC3	PASS
N41	30	60	CP-QPSK	H	Inner_Full	20.18	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	20.16	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	20.12	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Outer_Full	20.41	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Inner_Full	20.21	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	20.16	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	20.18	PC3	PASS
N41	30	80	CP-QPSK	L	Outer_Full	20.19	PC3	PASS
N41	30	80	CP-QPSK	L	Inner_Full	20.56	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	20.16	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	20.16	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Outer_Full	20.28	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Inner_Full	20.18	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	20.36	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	20.18	PC3	PASS
N41	30	80	CP-QPSK	M	Outer_Full	20.41	PC3	PASS
N41	30	80	CP-QPSK	M	Inner_Full	20.25	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	20.31	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	20.45	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Outer_Full	20.58	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Inner_Full	20.46	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	20.19	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	20.19	PC3	PASS
N41	30	80	CP-QPSK	H	Outer_Full	20.24	PC3	PASS
N41	30	80	CP-QPSK	H	Inner_Full	20.16	PC3	PASS

N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	20.87	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Right	20.24	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Outer_Full	20.16	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Inner_Full	20.25	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	20.16	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Right	20.87	PC3	PASS
N41	30	90	CP-QPSK	L	Outer_Full	20.16	PC3	PASS
N41	30	90	CP-QPSK	L	Inner_Full	20.12	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Left	20.36	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Right	20.46	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Outer_Full	20.18	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Inner_Full	20.18	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	20.13	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Right	20.41	PC3	PASS
N41	30	90	CP-QPSK	M	Outer_Full	20.16	PC3	PASS
N41	30	90	CP-QPSK	M	Inner_Full	20.23	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	20.54	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Outer_Full	20.46	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Inner_Full	20.19	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	20.78	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	20.15	PC3	PASS
N41	30	90	CP-QPSK	H	Outer_Full	20.12	PC3	PASS
N41	30	90	CP-QPSK	H	Inner_Full	20.05	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	20.18	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	20.19	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	20.21	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Inner_Full	20.41	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	20.26	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	20.25	PC3	PASS
N41	30	100	CP-QPSK	L	Outer_Full	20.16	PC3	PASS
N41	30	100	CP-QPSK	L	Inner_Full	20.25	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	20.34	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	20.47	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Inner_Full	20.15	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	<b>20.89</b>	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	20.78	PC3	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	20.76	PC3	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	20.53	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	20.54	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	20.13	PC3	PASS
N41	30	100	CP-QPSK	M	Outer_Full	20.51	PC3	PASS
N41	30	100	CP-QPSK	M	Inner_Full	20.15	PC3	PASS

N41	30	100	DFT-QPSK	H	Edge_1RB_Left	20.18	PC3	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	20.16	PC3	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	20.64	PC3	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	20.13	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	20.21	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	20.24	PC3	PASS
N41	30	100	CP-QPSK	H	Outer_Full	20.25	PC3	PASS
N41	30	100	CP-QPSK	H	Inner_Full	20.16	PC3	PASS

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N71	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	22.98	PC3	PASS
N71	15	5	DFT-PI2BPSK	L	Edge_1RB_Right	22.78	PC3	PASS
N71	15	5	DFT-PI2BPSK	L	Outer_Full	22.54	PC3	PASS
N71	15	5	DFT-PI2BPSK	L	Inner_Full	22.65	PC3	PASS
N71	15	5	DFT-QPSK	L	Edge_1RB_Left	22.58	PC3	PASS
N71	15	5	DFT-QPSK	L	Edge_1RB_Right	22.69	PC3	PASS
N71	15	5	DFT-QPSK	L	Outer_Full	22.78	PC3	PASS
N71	15	5	DFT-QPSK	L	Inner_Full	21.65	PC3	PASS
N71	15	5	DFT-16QAM	L	Edge_1RB_Left	22.90	PC3	PASS
N71	15	5	DFT-16QAM	L	Edge_1RB_Right	23.05	PC3	PASS
N71	15	5	DFT-16QAM	L	Outer_Full	22.36	PC3	PASS
N71	15	5	DFT-16QAM	L	Inner_Full	23.87	PC3	PASS
N71	15	5	DFT-64QAM	L	Edge_1RB_Left	22.45	PC3	PASS
N71	15	5	DFT-64QAM	L	Edge_1RB_Right	22.63	PC3	PASS
N71	15	5	DFT-64QAM	L	Outer_Full	22.98	PC3	PASS
N71	15	5	DFT-64QAM	L	Inner_Full	22.78	PC3	PASS
N71	15	5	DFT-256QAM	L	Edge_1RB_Left	21.97	PC3	PASS
N71	15	5	DFT-256QAM	L	Edge_1RB_Right	21.99	PC3	PASS
N71	15	5	DFT-256QAM	L	Outer_Full	21.63	PC3	PASS
N71	15	5	DFT-256QAM	L	Inner_Full	21.81	PC3	PASS
N71	15	5	CP-QPSK	L	Edge_1RB_Left	22.36	PC3	PASS
N71	15	5	CP-QPSK	L	Edge_1RB_Right	22.65	PC3	PASS
N71	15	5	CP-QPSK	L	Outer_Full	21.66	PC3	PASS
N71	15	5	CP-QPSK	L	Inner_Full	22.29	PC3	PASS
N71	15	5	CP-16QAM	L	Edge_1RB_Left	21.77	PC3	PASS
N71	15	5	CP-16QAM	L	Edge_1RB_Right	21.58	PC3	PASS
N71	15	5	CP-16QAM	L	Outer_Full	21.69	PC3	PASS
N71	15	5	CP-16QAM	L	Inner_Full	21.97	PC3	PASS
N71	15	5	CP-64QAM	L	Edge_1RB_Left	21.95	PC3	PASS
N71	15	5	CP-64QAM	L	Edge_1RB_Right	21.96	PC3	PASS
N71	15	5	CP-64QAM	L	Outer_Full	22.28	PC3	PASS

N71	15	5	CP-64QAM	L	Inner_Full	22.98	PC3	PASS
N71	15	5	CP-256QAM	L	Edge_1RB_Left	22.73	PC3	PASS
N71	15	5	CP-256QAM	L	Edge_1RB_Right	22.25	PC3	PASS
N71	15	5	CP-256QAM	L	Outer_Full	22.26	PC3	PASS
N71	15	5	CP-256QAM	L	Inner_Full	21.69	PC3	PASS
N71	15	5	DFT-PI2BPSK	M	Edge_1RB_Left	23.41	PC3	PASS
N71	15	5	DFT-PI2BPSK	M	Edge_1RB_Right	23.25	PC3	PASS
N71	15	5	DFT-PI2BPSK	M	Outer_Full	23.36	PC3	PASS
N71	15	5	DFT-PI2BPSK	M	Inner_Full	23.54	PC3	PASS
N71	15	5	DFT-QPSK	M	Edge_1RB_Left	22.41	PC3	PASS
N71	15	5	DFT-QPSK	M	Edge_1RB_Right	22.25	PC3	PASS
N71	15	5	DFT-QPSK	M	Outer_Full	22.36	PC3	PASS
N71	15	5	DFT-QPSK	M	Inner_Full	23.57	PC3	PASS
N71	15	5	DFT-16QAM	M	Edge_1RB_Left	22.41	PC3	PASS
N71	15	5	DFT-16QAM	M	Edge_1RB_Right	22.74	PC3	PASS
N71	15	5	DFT-16QAM	M	Outer_Full	22.26	PC3	PASS
N71	15	5	DFT-16QAM	M	Inner_Full	22.36	PC3	PASS
N71	15	5	DFT-64QAM	M	Edge_1RB_Left	22.24	PC3	PASS
N71	15	5	DFT-64QAM	M	Edge_1RB_Right	21.95	PC3	PASS
N71	15	5	DFT-64QAM	M	Outer_Full	22.28	PC3	PASS
N71	15	5	DFT-64QAM	M	Inner_Full	22.13	PC3	PASS
N71	15	5	DFT-256QAM	M	Edge_1RB_Left	21.95	PC3	PASS
N71	15	5	DFT-256QAM	M	Edge_1RB_Right	22.19	PC3	PASS
N71	15	5	DFT-256QAM	M	Outer_Full	22.56	PC3	PASS
N71	15	5	DFT-256QAM	M	Inner_Full	22.25	PC3	PASS
N71	15	5	CP-QPSK	M	Edge_1RB_Left	22.36	PC3	PASS
N71	15	5	CP-QPSK	M	Edge_1RB_Right	22.74	PC3	PASS
N71	15	5	CP-QPSK	M	Outer_Full	22.25	PC3	PASS
N71	15	5	CP-QPSK	M	Inner_Full	22.36	PC3	PASS
N71	15	5	CP-16QAM	M	Edge_1RB_Left	21.97	PC3	PASS
N71	15	5	CP-16QAM	M	Edge_1RB_Right	21.95	PC3	PASS
N71	15	5	CP-16QAM	M	Outer_Full	21.89	PC3	PASS
N71	15	5	CP-16QAM	M	Inner_Full	21.89	PC3	PASS
N71	15	5	CP-64QAM	M	Edge_1RB_Left	21.91	PC3	PASS
N71	15	5	CP-64QAM	M	Edge_1RB_Right	21.90	PC3	PASS
N71	15	5	CP-64QAM	M	Outer_Full	21.92	PC3	PASS
N71	15	5	CP-64QAM	M	Inner_Full	21.93	PC3	PASS
N71	15	5	CP-256QAM	M	Edge_1RB_Left	21.97	PC3	PASS
N71	15	5	CP-256QAM	M	Edge_1RB_Right	21.87	PC3	PASS
N71	15	5	CP-256QAM	M	Outer_Full	21.99	PC3	PASS
N71	15	5	CP-256QAM	M	Inner_Full	21.97	PC3	PASS
N71	15	5	DFT-PI2BPSK	H	Edge_1RB_Right	22.98	PC3	PASS

N71	15	5	DFT-PI2BPSK	H	Outer_Full	23.16	PC3	PASS
N71	15	5	DFT-PI2BPSK	H	Inner_Full	23.05	PC3	PASS
N71	15	5	DFT-QPSK	H	Edge_1RB_Left	22.61	PC3	PASS
N71	15	5	DFT-QPSK	H	Edge_1RB_Right	22.52	PC3	PASS
N71	15	5	DFT-QPSK	H	Outer_Full	22.65	PC3	PASS
N71	15	5	DFT-QPSK	H	Inner_Full	23.28	PC3	PASS
N71	15	5	DFT-16QAM	H	Edge_1RB_Left	22.78	PC3	PASS
N71	15	5	DFT-16QAM	H	Edge_1RB_Right	21.88	PC3	PASS
N71	15	5	DFT-16QAM	H	Outer_Full	21.79	PC3	PASS
N71	15	5	DFT-16QAM	H	Inner_Full	22.35	PC3	PASS
N71	15	5	DFT-64QAM	H	Edge_1RB_Left	22.45	PC3	PASS
N71	15	5	DFT-64QAM	H	Edge_1RB_Right	21.69	PC3	PASS
N71	15	5	DFT-64QAM	H	Outer_Full	21.78	PC3	PASS
N71	15	5	DFT-64QAM	H	Inner_Full	21.98	PC3	PASS
N71	15	5	DFT-256QAM	H	Edge_1RB_Left	21.78	PC3	PASS
N71	15	5	DFT-256QAM	H	Edge_1RB_Right	21.88	PC3	PASS
N71	15	5	DFT-256QAM	H	Outer_Full	21.89	PC3	PASS
N71	15	5	DFT-256QAM	H	Inner_Full	21.92	PC3	PASS
N71	15	5	CP-QPSK	H	Edge_1RB_Left	22.49	PC3	PASS
N71	15	5	CP-QPSK	H	Edge_1RB_Right	22.78	PC3	PASS
N71	15	5	CP-QPSK	H	Outer_Full	22.65	PC3	PASS
N71	15	5	CP-QPSK	H	Inner_Full	22.65	PC3	PASS
N71	15	5	CP-16QAM	H	Edge_1RB_Left	22.78	PC3	PASS
N71	15	5	CP-16QAM	H	Edge_1RB_Right	22.73	PC3	PASS
N71	15	5	CP-16QAM	H	Outer_Full	22.64	PC3	PASS
N71	15	5	CP-16QAM	H	Inner_Full	22.71	PC3	PASS
N71	15	5	CP-64QAM	H	Edge_1RB_Left	22.44	PC3	PASS
N71	15	5	CP-64QAM	H	Edge_1RB_Right	22.27	PC3	PASS
N71	15	5	CP-64QAM	H	Outer_Full	22.13	PC3	PASS
N71	15	5	CP-64QAM	H	Inner_Full	22.25	PC3	PASS
N71	15	5	CP-256QAM	H	Edge_1RB_Left	22.39	PC3	PASS
N71	15	5	CP-256QAM	H	Edge_1RB_Right	22.34	PC3	PASS
N71	15	5	CP-256QAM	H	Outer_Full	21.98	PC3	PASS
N71	15	5	CP-256QAM	H	Inner_Full	21.78	PC3	PASS
N71	15	10	DFT-PI2BPSK	L	Edge_1RB_Left	22.94	PC3	PASS
N71	15	10	DFT-PI2BPSK	L	Edge_1RB_Right	22.92	PC3	PASS
N71	15	10	DFT-PI2BPSK	L	Outer_Full	22.26	PC3	PASS
N71	15	10	DFT-PI2BPSK	L	Inner_Full	23.35	PC3	PASS
N71	15	10	DFT-QPSK	L	Edge_1RB_Left	22.78	PC3	PASS
N71	15	10	DFT-QPSK	L	Edge_1RB_Right	22.43	PC3	PASS
N71	15	10	DFT-QPSK	L	Outer_Full	22.36	PC3	PASS
N71	15	10	DFT-QPSK	L	Inner_Full	23.54	PC3	PASS

N71	15	10	DFT-16QAM	L	Edge_1RB_Left	21.75	PC3	PASS
N71	15	10	DFT-16QAM	L	Edge_1RB_Right	21.78	PC3	PASS
N71	15	10	DFT-16QAM	L	Outer_Full	22.98	PC3	PASS
N71	15	10	DFT-16QAM	L	Inner_Full	22.42	PC3	PASS
N71	15	10	DFT-64QAM	L	Edge_1RB_Left	21.98	PC3	PASS
N71	15	10	DFT-64QAM	L	Edge_1RB_Right	21.79	PC3	PASS
N71	15	10	DFT-64QAM	L	Outer_Full	22.04	PC3	PASS
N71	15	10	DFT-64QAM	L	Inner_Full	22.36	PC3	PASS
N71	15	10	DFT-256QAM	L	Edge_1RB_Left	22.25	PC3	PASS
N71	15	10	DFT-256QAM	L	Edge_1RB_Right	22.62	PC3	PASS
N71	15	10	DFT-256QAM	L	Outer_Full	21.98	PC3	PASS
N71	15	10	DFT-256QAM	L	Inner_Full	21.78	PC3	PASS
N71	15	10	CP-QPSK	L	Edge_1RB_Left	22.56	PC3	PASS
N71	15	10	CP-QPSK	L	Edge_1RB_Right	22.05	PC3	PASS
N71	15	10	CP-QPSK	L	Outer_Full	22.42	PC3	PASS
N71	15	10	CP-QPSK	L	Inner_Full	22.12	PC3	PASS
N71	15	10	CP-16QAM	L	Edge_1RB_Left	22.53	PC3	PASS
N71	15	10	CP-16QAM	L	Edge_1RB_Right	22.19	PC3	PASS
N71	15	10	CP-16QAM	L	Outer_Full	22.26	PC3	PASS
N71	15	10	CP-16QAM	L	Inner_Full	22.41	PC3	PASS
N71	15	10	CP-64QAM	L	Edge_1RB_Left	21.99	PC3	PASS
N71	15	10	CP-64QAM	L	Edge_1RB_Right	21.98	PC3	PASS
N71	15	10	CP-64QAM	L	Outer_Full	21.89	PC3	PASS
N71	15	10	CP-64QAM	L	Inner_Full	21.78	PC3	PASS
N71	15	10	CP-256QAM	L	Edge_1RB_Left	21.93	PC3	PASS
N71	15	10	CP-256QAM	L	Edge_1RB_Right	21.98	PC3	PASS
N71	15	10	CP-256QAM	L	Outer_Full	22.92	PC3	PASS
N71	15	10	CP-256QAM	L	Inner_Full	22.02	PC3	PASS
N71	15	10	DFT-PI2BPSK	M	Edge_1RB_Left	23.03	PC3	PASS
N71	15	10	DFT-PI2BPSK	M	Edge_1RB_Right	23.11	PC3	PASS
N71	15	10	DFT-PI2BPSK	M	Outer_Full	22.92	PC3	PASS
N71	15	10	DFT-PI2BPSK	M	Inner_Full	22.88	PC3	PASS
N71	15	10	DFT-QPSK	M	Edge_1RB_Left	22.44	PC3	PASS
N71	15	10	DFT-QPSK	M	Edge_1RB_Right	22.43	PC3	PASS
N71	15	10	DFT-QPSK	M	Outer_Full	22.54	PC3	PASS
N71	15	10	DFT-QPSK	M	Inner_Full	23.52	PC3	PASS
N71	15	10	DFT-16QAM	M	Edge_1RB_Left	21.99	PC3	PASS
N71	15	10	DFT-16QAM	M	Edge_1RB_Right	21.97	PC3	PASS
N71	15	10	DFT-16QAM	M	Outer_Full	22.50	PC3	PASS
N71	15	10	DFT-16QAM	M	Inner_Full	22.52	PC3	PASS
N71	15	10	DFT-64QAM	M	Edge_1RB_Left	21.98	PC3	PASS
N71	15	10	DFT-64QAM	M	Edge_1RB_Right	22.07	PC3	PASS

N71	15	10	DFT-64QAM	M	Outer_Full	22.07	PC3	PASS
N71	15	10	DFT-64QAM	M	Inner_Full	21.89	PC3	PASS
N71	15	10	DFT-256QAM	M	Edge_1RB_Left	21.97	PC3	PASS
N71	15	10	DFT-256QAM	M	Edge_1RB_Right	21.87	PC3	PASS
N71	15	10	DFT-256QAM	M	Outer_Full	21.99	PC3	PASS
N71	15	10	DFT-256QAM	M	Inner_Full	22.00	PC3	PASS
N71	15	10	CP-QPSK	M	Edge_1RB_Left	22.44	PC3	PASS
N71	15	10	CP-QPSK	M	Edge_1RB_Right	22.12	PC3	PASS
N71	15	10	CP-QPSK	M	Outer_Full	22.35	PC3	PASS
N71	15	10	CP-QPSK	M	Inner_Full	22.45	PC3	PASS
N71	15	10	CP-16QAM	M	Edge_1RB_Left	22.65	PC3	PASS
N71	15	10	CP-16QAM	M	Edge_1RB_Right	22.89	PC3	PASS
N71	15	10	CP-16QAM	M	Outer_Full	22.54	PC3	PASS
N71	15	10	CP-16QAM	M	Inner_Full	22.45	PC3	PASS
N71	15	10	CP-64QAM	M	Edge_1RB_Left	22.25	PC3	PASS
N71	15	10	CP-64QAM	M	Edge_1RB_Right	22.65	PC3	PASS
N71	15	10	CP-64QAM	M	Outer_Full	22.58	PC3	PASS
N71	15	10	CP-64QAM	M	Inner_Full	22.69	PC3	PASS
N71	15	10	CP-256QAM	M	Edge_1RB_Left	22.78	PC3	PASS
N71	15	10	CP-256QAM	M	Edge_1RB_Right	22.25	PC3	PASS
N71	15	10	CP-256QAM	M	Outer_Full	22.36	PC3	PASS
N71	15	10	CP-256QAM	M	Inner_Full	22.57	PC3	PASS
N71	15	10	DFT-PI2BPSK	H	Edge_1RB_Left	22.46	PC3	PASS
N71	15	10	DFT-PI2BPSK	H	Edge_1RB_Right	22.28	PC3	PASS
N71	15	10	DFT-PI2BPSK	H	Outer_Full	23.39	PC3	PASS
N71	15	10	DFT-PI2BPSK	H	Inner_Full	23.79	PC3	PASS
N71	15	10	DFT-QPSK	H	Edge_1RB_Left	23.12	PC3	PASS
N71	15	10	DFT-QPSK	H	Edge_1RB_Right	23.28	PC3	PASS
N71	15	10	DFT-QPSK	H	Outer_Full	23.10	PC3	PASS
N71	15	10	DFT-QPSK	H	Inner_Full	23.13	PC3	PASS
N71	15	10	DFT-16QAM	H	Edge_1RB_Left	22.28	PC3	PASS
N71	15	10	DFT-16QAM	H	Edge_1RB_Right	22.35	PC3	PASS
N71	15	10	DFT-16QAM	H	Outer_Full	22.48	PC3	PASS
N71	15	10	DFT-16QAM	H	Inner_Full	22.74	PC3	PASS
N71	15	10	DFT-64QAM	H	Edge_1RB_Left	21.97	PC3	PASS
N71	15	10	DFT-64QAM	H	Edge_1RB_Right	21.99	PC3	PASS
N71	15	10	DFT-64QAM	H	Outer_Full	22.05	PC3	PASS
N71	15	10	DFT-64QAM	H	Inner_Full	21.68	PC3	PASS
N71	15	10	DFT-256QAM	H	Edge_1RB_Left	21.79	PC3	PASS
N71	15	10	DFT-256QAM	H	Edge_1RB_Right	21.86	PC3	PASS
N71	15	10	DFT-256QAM	H	Outer_Full	21.72	PC3	PASS
N71	15	10	DFT-256QAM	H	Inner_Full	21.36	PC3	PASS



N71	15	10	CP-QPSK	H	Edge_1RB_Left	21.89	PC3	PASS
N71	15	10	CP-QPSK	H	Edge_1RB_Right	21.89	PC3	PASS
N71	15	10	CP-QPSK	H	Outer_Full	21.75	PC3	PASS
N71	15	10	CP-QPSK	H	Inner_Full	21.84	PC3	PASS
N71	15	10	CP-16QAM	H	Edge_1RB_Left	21.97	PC3	PASS
N71	15	10	CP-16QAM	H	Edge_1RB_Right	21.98	PC3	PASS
N71	15	10	CP-16QAM	H	Outer_Full	21.97	PC3	PASS
N71	15	10	CP-16QAM	H	Inner_Full	22.98	PC3	PASS
N71	15	10	CP-64QAM	H	Edge_1RB_Left	22.46	PC3	PASS
N71	15	10	CP-64QAM	H	Edge_1RB_Right	21.99	PC3	PASS
N71	15	10	CP-64QAM	H	Outer_Full	21.98	PC3	PASS
N71	15	10	CP-64QAM	H	Inner_Full	21.79	PC3	PASS
N71	15	10	CP-256QAM	H	Edge_1RB_Left	21.99	PC3	PASS
N71	15	10	CP-256QAM	H	Edge_1RB_Right	21.97	PC3	PASS
N71	15	10	CP-256QAM	H	Outer_Full	22.98	PC3	PASS
N71	15	10	CP-256QAM	H	Inner_Full	22.05	PC3	PASS
N71	15	15	DFT-PI2BPSK	L	Edge_1RB_Left	23.09	PC3	PASS
N71	15	15	DFT-PI2BPSK	L	Edge_1RB_Right	23.07	PC3	PASS
N71	15	15	DFT-PI2BPSK	L	Outer_Full	23.14	PC3	PASS
N71	15	15	DFT-PI2BPSK	L	Inner_Full	23.63	PC3	PASS
N71	15	15	DFT-QPSK	L	Edge_1RB_Left	22.60	PC3	PASS
N71	15	15	DFT-QPSK	L	Edge_1RB_Right	22.52	PC3	PASS
N71	15	15	DFT-QPSK	L	Outer_Full	22.66	PC3	PASS
N71	15	15	DFT-QPSK	L	Inner_Full	22.67	PC3	PASS
N71	15	15	DFT-16QAM	L	Edge_1RB_Left	22.96	PC3	PASS
N71	15	15	DFT-16QAM	L	Edge_1RB_Right	22.41	PC3	PASS
N71	15	15	DFT-16QAM	L	Outer_Full	22.07	PC3	PASS
N71	15	15	DFT-16QAM	L	Inner_Full	23.71	PC3	PASS
N71	15	15	DFT-64QAM	L	Edge_1RB_Left	22.93	PC3	PASS
N71	15	15	DFT-64QAM	L	Edge_1RB_Right	22.16	PC3	PASS
N71	15	15	DFT-64QAM	L	Outer_Full	22.14	PC3	PASS
N71	15	15	DFT-64QAM	L	Inner_Full	22.14	PC3	PASS
N71	15	15	DFT-256QAM	L	Edge_1RB_Left	22.87	PC3	PASS
N71	15	15	DFT-256QAM	L	Edge_1RB_Right	22.12	PC3	PASS
N71	15	15	DFT-256QAM	L	Outer_Full	22.23	PC3	PASS
N71	15	15	DFT-256QAM	L	Inner_Full	22.15	PC3	PASS
N71	15	15	CP-QPSK	L	Edge_1RB_Left	22.76	PC3	PASS
N71	15	15	CP-QPSK	L	Edge_1RB_Right	23.05	PC3	PASS
N71	15	15	CP-QPSK	L	Outer_Full	23.05	PC3	PASS
N71	15	15	CP-QPSK	L	Inner_Full	23.07	PC3	PASS
N71	15	15	CP-16QAM	L	Edge_1RB_Left	23.09	PC3	PASS
N71	15	15	CP-16QAM	L	Edge_1RB_Right	23.07	PC3	PASS

N71	15	15	CP-16QAM	L	Outer_Full	22.98	PC3	PASS
N71	15	15	CP-16QAM	L	Inner_Full	22.97	PC3	PASS
N71	15	15	CP-64QAM	L	Edge_1RB_Left	22.98	PC3	PASS
N71	15	15	CP-64QAM	L	Edge_1RB_Right	22.24	PC3	PASS
N71	15	15	CP-64QAM	L	Outer_Full	22.20	PC3	PASS
N71	15	15	CP-64QAM	L	Inner_Full	22.14	PC3	PASS
N71	15	15	CP-256QAM	L	Edge_1RB_Left	22.08	PC3	PASS
N71	15	15	CP-256QAM	L	Edge_1RB_Right	22.99	PC3	PASS
N71	15	15	CP-256QAM	L	Outer_Full	22.16	PC3	PASS
N71	15	15	CP-256QAM	L	Inner_Full	22.12	PC3	PASS
N71	15	15	DFT-PI2BPSK	M	Edge_1RB_Right	22.51	PC3	PASS
N71	15	15	DFT-PI2BPSK	M	Outer_Full	22.12	PC3	PASS
N71	15	15	DFT-PI2BPSK	M	Inner_Full	22.64	PC3	PASS
N71	15	15	DFT-QPSK	M	Edge_1RB_Left	23.12	PC3	PASS
N71	15	15	DFT-QPSK	M	Edge_1RB_Right	22.51	PC3	PASS
N71	15	15	DFT-QPSK	M	Outer_Full	22.66	PC3	PASS
N71	15	15	DFT-QPSK	M	Inner_Full	23.13	PC3	PASS
N71	15	15	DFT-16QAM	M	Edge_1RB_Left	22.40	PC3	PASS
N71	15	15	DFT-16QAM	M	Edge_1RB_Right	22.44	PC3	PASS
N71	15	15	DFT-16QAM	M	Outer_Full	22.69	PC3	PASS
N71	15	15	DFT-16QAM	M	Inner_Full	22.65	PC3	PASS
N71	15	15	DFT-64QAM	M	Edge_1RB_Left	22.66	PC3	PASS
N71	15	15	DFT-64QAM	M	Edge_1RB_Right	22.74	PC3	PASS
N71	15	15	DFT-64QAM	M	Outer_Full	22.16	PC3	PASS
N71	15	15	DFT-64QAM	M	Inner_Full	22.14	PC3	PASS
N71	15	15	DFT-256QAM	M	Edge_1RB_Left	22.71	PC3	PASS
N71	15	15	DFT-256QAM	M	Edge_1RB_Right	21.98	PC3	PASS
N71	15	15	DFT-256QAM	M	Outer_Full	21.87	PC3	PASS
N71	15	15	DFT-256QAM	M	Inner_Full	21.97	PC3	PASS
N71	15	15	CP-QPSK	M	Edge_1RB_Left	21.93	PC3	PASS
N71	15	15	CP-QPSK	M	Edge_1RB_Right	21.67	PC3	PASS
N71	15	15	CP-QPSK	M	Outer_Full	21.79	PC3	PASS
N71	15	15	CP-QPSK	M	Inner_Full	21.89	PC3	PASS
N71	15	15	CP-16QAM	M	Edge_1RB_Right	21.92	PC3	PASS
N71	15	15	CP-16QAM	M	Outer_Full	21.68	PC3	PASS
N71	15	15	CP-16QAM	M	Inner_Full	22.67	PC3	PASS
N71	15	15	CP-64QAM	M	Edge_1RB_Left	21.98	PC3	PASS
N71	15	15	CP-64QAM	M	Edge_1RB_Right	21.79	PC3	PASS
N71	15	15	CP-64QAM	M	Outer_Full	21.99	PC3	PASS
N71	15	15	CP-64QAM	M	Inner_Full	21.97	PC3	PASS
N71	15	15	CP-256QAM	M	Edge_1RB_Left	21.89	PC3	PASS
N71	15	15	CP-256QAM	M	Edge_1RB_Right	21.89	PC3	PASS

N71	15	15	CP-256QAM	M	Outer_Full	21.99	PC3	PASS
N71	15	15	CP-256QAM	M	Inner_Full	21.98	PC3	PASS
N71	15	15	DFT-PI2BPSK	H	Edge_1RB_Left	22.01	PC3	PASS
N71	15	15	DFT-PI2BPSK	H	Edge_1RB_Right	22.91	PC3	PASS
N71	15	15	DFT-PI2BPSK	H	Outer_Full	22.36	PC3	PASS
N71	15	15	DFT-PI2BPSK	H	Inner_Full	22.65	PC3	PASS
N71	15	15	DFT-QPSK	H	Edge_1RB_Left	22.35	PC3	PASS
N71	15	15	DFT-QPSK	H	Edge_1RB_Right	22.41	PC3	PASS
N71	15	15	DFT-QPSK	H	Outer_Full	22.02	PC3	PASS
N71	15	15	DFT-QPSK	H	Inner_Full	22.16	PC3	PASS
N71	15	15	DFT-16QAM	H	Edge_1RB_Left	22.69	PC3	PASS
N71	15	15	DFT-16QAM	H	Edge_1RB_Right	22.47	PC3	PASS
N71	15	15	DFT-16QAM	H	Outer_Full	22.35	PC3	PASS
N71	15	15	DFT-16QAM	H	Inner_Full	23.64	PC3	PASS
N71	15	15	DFT-64QAM	H	Edge_1RB_Left	22.15	PC3	PASS
N71	15	15	DFT-64QAM	H	Edge_1RB_Right	22.25	PC3	PASS
N71	15	15	DFT-64QAM	H	Outer_Full	22.36	PC3	PASS
N71	15	15	DFT-64QAM	H	Inner_Full	22.57	PC3	PASS
N71	15	15	DFT-256QAM	H	Edge_1RB_Left	22.76	PC3	PASS
N71	15	15	DFT-256QAM	H	Edge_1RB_Right	22.25	PC3	PASS
N71	15	15	DFT-256QAM	H	Outer_Full	22.36	PC3	PASS
N71	15	15	DFT-256QAM	H	Inner_Full	22.57	PC3	PASS
N71	15	15	CP-QPSK	H	Edge_1RB_Left	22.69	PC3	PASS
N71	15	15	CP-QPSK	H	Edge_1RB_Right	22.12	PC3	PASS
N71	15	15	CP-QPSK	H	Outer_Full	21.97	PC3	PASS
N71	15	15	CP-QPSK	H	Inner_Full	21.98	PC3	PASS
N71	15	15	CP-16QAM	H	Edge_1RB_Left	21.78	PC3	PASS
N71	15	15	CP-16QAM	H	Edge_1RB_Right	22.01	PC3	PASS
N71	15	15	CP-16QAM	H	Outer_Full	22.05	PC3	PASS
N71	15	15	CP-16QAM	H	Inner_Full	22.06	PC3	PASS
N71	15	15	CP-64QAM	H	Edge_1RB_Left	22.12	PC3	PASS
N71	15	15	CP-64QAM	H	Edge_1RB_Right	22.14	PC3	PASS
N71	15	15	CP-64QAM	H	Outer_Full	22.16	PC3	PASS
N71	15	15	CP-64QAM	H	Inner_Full	22.17	PC3	PASS
N71	15	15	CP-256QAM	H	Edge_1RB_Left	22.21	PC3	PASS
N71	15	15	CP-256QAM	H	Edge_1RB_Right	22.20	PC3	PASS
N71	15	15	CP-256QAM	H	Outer_Full	22.05	PC3	PASS
N71	15	15	CP-256QAM	H	Inner_Full	22.99	PC3	PASS
N71	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	22.02	PC3	PASS
N71	15	20	DFT-PI2BPSK	L	Edge_1RB_Right	22.16	PC3	PASS
N71	15	20	DFT-PI2BPSK	L	Outer_Full	22.08	PC3	PASS
N71	15	20	DFT-PI2BPSK	L	Inner_Full	22.52	PC3	PASS

N71	15	20	DFT-QPSK	L	Edge_1RB_Left	22.15	PC3	PASS
N71	15	20	DFT-QPSK	L	Edge_1RB_Right	22.39	PC3	PASS
N71	15	20	DFT-QPSK	L	Outer_Full	22.15	PC3	PASS
N71	15	20	DFT-QPSK	L	Inner_Full	22.02	PC3	PASS
N71	15	20	DFT-16QAM	L	Edge_1RB_Left	22.71	PC3	PASS
N71	15	20	DFT-16QAM	L	Edge_1RB_Right	22.02	PC3	PASS
N71	15	20	DFT-16QAM	L	Outer_Full	22.15	PC3	PASS
N71	15	20	DFT-16QAM	L	Inner_Full	22.15	PC3	PASS
N71	15	20	DFT-64QAM	L	Edge_1RB_Left	22.16	PC3	PASS
N71	15	20	DFT-64QAM	L	Edge_1RB_Right	22.19	PC3	PASS
N71	15	20	DFT-64QAM	L	Outer_Full	22.19	PC3	PASS
N71	15	20	DFT-64QAM	L	Inner_Full	22.47	PC3	PASS
N71	15	20	DFT-256QAM	L	Edge_1RB_Left	22.16	PC3	PASS
N71	15	20	DFT-256QAM	L	Edge_1RB_Right	21.98	PC3	PASS
N71	15	20	DFT-256QAM	L	Outer_Full	21.76	PC3	PASS
N71	15	20	DFT-256QAM	L	Inner_Full	21.78	PC3	PASS
N71	15	20	CP-QPSK	L	Edge_1RB_Left	21.79	PC3	PASS
N71	15	20	CP-QPSK	L	Edge_1RB_Right	21.79	PC3	PASS
N71	15	20	CP-QPSK	L	Outer_Full	21.98	PC3	PASS
N71	15	20	CP-QPSK	L	Inner_Full	21.97	PC3	PASS
N71	15	20	CP-16QAM	L	Edge_1RB_Left	22.28	PC3	PASS
N71	15	20	CP-16QAM	L	Edge_1RB_Right	22.45	PC3	PASS
N71	15	20	CP-16QAM	L	Outer_Full	22.35	PC3	PASS
N71	15	20	CP-16QAM	L	Inner_Full	22.05	PC3	PASS
N71	15	20	CP-64QAM	L	Edge_1RB_Left	22.74	PC3	PASS
N71	15	20	CP-64QAM	L	Edge_1RB_Right	22.02	PC3	PASS
N71	15	20	CP-64QAM	L	Outer_Full	22.35	PC3	PASS
N71	15	20	CP-64QAM	L	Inner_Full	22.71	PC3	PASS
N71	15	20	CP-256QAM	L	Edge_1RB_Left	22.65	PC3	PASS
N71	15	20	CP-256QAM	L	Edge_1RB_Right	22.42	PC3	PASS
N71	15	20	CP-256QAM	L	Outer_Full	22.59	PC3	PASS
N71	15	20	CP-256QAM	L	Inner_Full	22.06	PC3	PASS
N71	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	22.12	PC3	PASS
N71	15	20	DFT-PI2BPSK	M	Edge_1RB_Right	22.36	PC3	PASS
N71	15	20	DFT-PI2BPSK	M	Outer_Full	22.89	PC3	PASS
N71	15	20	DFT-PI2BPSK	M	Inner_Full	22.46	PC3	PASS
N71	15	20	DFT-QPSK	M	Edge_1RB_Left	22.87	PC3	PASS
N71	15	20	DFT-QPSK	M	Edge_1RB_Right	22.42	PC3	PASS
N71	15	20	DFT-QPSK	M	Outer_Full	22.47	PC3	PASS
N71	15	20	DFT-QPSK	M	Inner_Full	22.32	PC3	PASS
N71	15	20	DFT-16QAM	M	Edge_1RB_Left	22.04	PC3	PASS
N71	15	20	DFT-16QAM	M	Edge_1RB_Right	22.02	PC3	PASS

N71	15	20	DFT-16QAM	M	Outer_Full	22.03	PC3	PASS
N71	15	20	DFT-16QAM	M	Inner_Full	22.47	PC3	PASS
N71	15	20	DFT-64QAM	M	Edge_1RB_Left	22.05	PC3	PASS
N71	15	20	DFT-64QAM	M	Edge_1RB_Right	22.58	PC3	PASS
N71	15	20	DFT-64QAM	M	Outer_Full	22.03	PC3	PASS
N71	15	20	DFT-64QAM	M	Inner_Full	22.87	PC3	PASS
N71	15	20	DFT-256QAM	M	Edge_1RB_Left	22.13	PC3	PASS
N71	15	20	DFT-256QAM	M	Edge_1RB_Right	22.02	PC3	PASS
N71	15	20	DFT-256QAM	M	Outer_Full	22.28	PC3	PASS
N71	15	20	DFT-256QAM	M	Inner_Full	22.62	PC3	PASS
N71	15	20	CP-QPSK	M	Edge_1RB_Left	<b>23.68</b>	PC3	PASS
N71	15	20	CP-QPSK	M	Edge_1RB_Right	23.18	PC3	PASS
N71	15	20	CP-QPSK	M	Outer_Full	22.97	PC3	PASS
N71	15	20	CP-QPSK	M	Inner_Full	23.08	PC3	PASS
N71	15	20	CP-16QAM	M	Edge_1RB_Left	22.78	PC3	PASS
N71	15	20	CP-16QAM	M	Edge_1RB_Right	22.86	PC3	PASS
N71	15	20	CP-16QAM	M	Outer_Full	22.79	PC3	PASS
N71	15	20	CP-16QAM	M	Inner_Full	22.98	PC3	PASS
N71	15	20	CP-64QAM	M	Edge_1RB_Left	22.94	PC3	PASS
N71	15	20	CP-64QAM	M	Edge_1RB_Right	22.95	PC3	PASS
N71	15	20	CP-64QAM	M	Outer_Full	22.95	PC3	PASS
N71	15	20	CP-64QAM	M	Inner_Full	22.99	PC3	PASS
N71	15	20	CP-256QAM	M	Edge_1RB_Left	22.99	PC3	PASS
N71	15	20	CP-256QAM	M	Edge_1RB_Right	22.10	PC3	PASS
N71	15	20	CP-256QAM	M	Outer_Full	22.02	PC3	PASS
N71	15	20	CP-256QAM	M	Inner_Full	22.04	PC3	PASS
N71	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	22.18	PC3	PASS
N71	15	20	DFT-PI2BPSK	H	Edge_1RB_Right	22.03	PC3	PASS
N71	15	20	DFT-PI2BPSK	H	Outer_Full	22.02	PC3	PASS
N71	15	20	DFT-PI2BPSK	H	Inner_Full	22.41	PC3	PASS
N71	15	20	DFT-QPSK	H	Edge_1RB_Left	22.58	PC3	PASS
N71	15	20	DFT-QPSK	H	Edge_1RB_Right	22.69	PC3	PASS
N71	15	20	DFT-QPSK	H	Outer_Full	21.98	PC3	PASS
N71	15	20	DFT-QPSK	H	Inner_Full	22.02	PC3	PASS
N71	15	20	DFT-16QAM	H	Edge_1RB_Left	22.05	PC3	PASS
N71	15	20	DFT-16QAM	H	Edge_1RB_Right	22.35	PC3	PASS
N71	15	20	DFT-16QAM	H	Outer_Full	22.07	PC3	PASS
N71	15	20	DFT-16QAM	H	Inner_Full	22.98	PC3	PASS
N71	15	20	DFT-64QAM	H	Edge_1RB_Left	22.73	PC3	PASS
N71	15	20	DFT-64QAM	H	Edge_1RB_Right	22.82	PC3	PASS
N71	15	20	DFT-64QAM	H	Outer_Full	22.65	PC3	PASS
N71	15	20	DFT-64QAM	H	Inner_Full	22.36	PC3	PASS

N71	15	20	DFT-256QAM	H	Edge_1RB_Left	22.78	PC3	PASS
N71	15	20	DFT-256QAM	H	Edge_1RB_Right	22.65	PC3	PASS
N71	15	20	DFT-256QAM	H	Outer_Full	22.35	PC3	PASS
N71	15	20	DFT-256QAM	H	Inner_Full	22.79	PC3	PASS
N71	15	20	CP-QPSK	H	Edge_1RB_Left	22.58	PC3	PASS
N71	15	20	CP-QPSK	H	Edge_1RB_Right	22.77	PC3	PASS
N71	15	20	CP-QPSK	H	Outer_Full	22.92	PC3	PASS
N71	15	20	CP-QPSK	H	Inner_Full	22.79	PC3	PASS
N71	15	20	CP-16QAM	H	Edge_1RB_Left	22.56	PC3	PASS
N71	15	20	CP-16QAM	H	Edge_1RB_Right	22.79	PC3	PASS
N71	15	20	CP-16QAM	H	Outer_Full	22.65	PC3	PASS
N71	15	20	CP-16QAM	H	Inner_Full	22.79	PC3	PASS
N71	15	20	CP-64QAM	H	Edge_1RB_Left	22.36	PC3	PASS
N71	15	20	CP-64QAM	H	Edge_1RB_Right	22.78	PC3	PASS
N71	15	20	CP-64QAM	H	Outer_Full	22.69	PC3	PASS
N71	15	20	CP-64QAM	H	Inner_Full	22.78	PC3	PASS
N71	15	20	CP-256QAM	H	Edge_1RB_Left	22.56	PC3	PASS
N71	15	20	CP-256QAM	H	Edge_1RB_Right	22.32	PC3	PASS
N71	15	20	CP-256QAM	H	Outer_Full	22.74	PC3	PASS
N71	15	20	CP-256QAM	H	Inner_Full	22.98	PC3	PASS

**Remark:**

1. Per KDB941225 D05 v02r05, Start with the largest channel bandwidth then measure SAR for QPSK with 1 RB allocation, using the RB offset and required test channel combination with the highest maximum output power among RB offsets at the upper edge, middle, and lower edge of each required test channel. When the reported SAR is  $\leq 0.8$  W/kg, testing of the remaining RB offset configurations and required test channels is not required for 1 RB allocation; otherwise, SAR is required for the remaining required test channels and only for the RB offset configuration with the highest output power for that channel. 6 When the reported SAR of a required test channel is  $> 1.45$  W/kg, SAR is required for all three RB offset configurations for that required test channel.
2. Per KDB941225 D05 v02r05, The procedures required for 1 RB allocation in 5.2.1 are applied to measure the SAR for QPSK with 50% RB allocation.
3. Per KDB941225 D05 v02r05, For QPSK with 100% RB allocation, SAR is not required when the highest maximum output power for 100 % RB allocation is less than the highest maximum output power in 50% and 1 RB allocations, and the highest reported SAR for 1 RB and 50% RB allocation in 5.2.1 and 5.2.2 are  $\leq 0.8$  W/kg. Otherwise, SAR is measured for the highest output power channel; and if the reported SAR is  $> 1.45$  W/kg, the remaining required test channels must also be tested.
4. Per KDB941225 D05 v02r05, For each modulation besides QPSK; e.g., 16-QAM, 64-QAM, apply the QPSK procedures in 5.2.1, 5.2.2, and 5.2.3 to determine the QAM configurations that may need SAR measurement. For each configuration