

N41	30	40	CP-16QAM	M	Inner_Full	21.52	PC3	PASS
N41	30	40	CP-64QAM	M	Edge_1RB_Left	21.58	PC3	PASS
N41	30	40	CP-64QAM	M	Edge_1RB_Right	21.73	PC3	PASS
N41	30	40	CP-64QAM	M	Outer_Full	21.64	PC3	PASS
N41	30	40	CP-64QAM	M	Inner_Full	21.92	PC3	PASS
N41	30	40	CP-256QAM	M	Edge_1RB_Left	21.51	PC3	PASS
N41	30	40	CP-256QAM	M	Edge_1RB_Right	21.55	PC3	PASS
N41	30	40	CP-256QAM	M	Outer_Full	21.73	PC3	PASS
N41	30	40	CP-256QAM	M	Inner_Full	21.67	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	22.82	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	21.95	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	23.10	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Inner_Full	23.80	PC3	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	21.50	PC3	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	21.10	PC3	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	22.56	PC3	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	23.80	PC3	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Left	21.50	PC3	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Right	21.57	PC3	PASS
N41	30	40	DFT-16QAM	H	Outer_Full	21.62	PC3	PASS
N41	30	40	DFT-16QAM	H	Inner_Full	22.72	PC3	PASS
N41	30	40	DFT-64QAM	H	Edge_1RB_Left	21.71	PC3	PASS
N41	30	40	DFT-64QAM	H	Edge_1RB_Right	21.75	PC3	PASS
N41	30	40	DFT-64QAM	H	Outer_Full	21.06	PC3	PASS
N41	30	40	DFT-64QAM	H	Inner_Full	21.16	PC3	PASS
N41	30	40	DFT-256QAM	H	Edge_1RB_Left	21.96	PC3	PASS
N41	30	40	DFT-256QAM	H	Edge_1RB_Right	21.19	PC3	PASS
N41	30	40	DFT-256QAM	H	Outer_Full	21.13	PC3	PASS
N41	30	40	DFT-256QAM	H	Inner_Full	21.32	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	21.56	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	21.69	PC3	PASS
N41	30	40	CP-QPSK	H	Outer_Full	21.54	PC3	PASS
N41	30	40	CP-QPSK	H	Inner_Full	21.53	PC3	PASS
N41	30	40	CP-16QAM	H	Edge_1RB_Left	21.63	PC3	PASS
N41	30	40	CP-16QAM	H	Edge_1RB_Right	21.58	PC3	PASS
N41	30	40	CP-16QAM	H	Outer_Full	21.57	PC3	PASS
N41	30	40	CP-16QAM	H	Inner_Full	21.81	PC3	PASS
N41	30	40	CP-64QAM	H	Edge_1RB_Left	21.59	PC3	PASS
N41	30	40	CP-64QAM	H	Edge_1RB_Right	21.69	PC3	PASS
N41	30	40	CP-64QAM	H	Outer_Full	21.52	PC3	PASS
N41	30	40	CP-64QAM	H	Inner_Full	21.77	PC3	PASS
N41	30	40	CP-256QAM	H	Edge_1RB_Left	21.96	PC3	PASS

N41	30	40	CP-256QAM	H	Edge_1RB_Right	21.50	PC3	PASS
N41	30	40	CP-256QAM	H	Outer_Full	21.79	PC3	PASS
N41	30	40	CP-256QAM	H	Inner_Full	21.69	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	21.75	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	22.03	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Outer_Full	21.87	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Inner_Full	22.43	PC3	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	21.31	PC3	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Right	21.70	PC3	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	21.38	PC3	PASS
N41	30	50	DFT-QPSK	L	Inner_Full	22.43	PC3	PASS
N41	30	50	DFT-16QAM	L	Edge_1RB_Left	21.51	PC3	PASS
N41	30	50	DFT-16QAM	L	Edge_1RB_Right	21.70	PC3	PASS
N41	30	50	DFT-16QAM	L	Outer_Full	21.51	PC3	PASS
N41	30	50	DFT-16QAM	L	Inner_Full	21.47	PC3	PASS
N41	30	50	DFT-64QAM	L	Edge_1RB_Left	21.96	PC3	PASS
N41	30	50	DFT-64QAM	L	Edge_1RB_Right	21.92	PC3	PASS
N41	30	50	DFT-64QAM	L	Outer_Full	21.95	PC3	PASS
N41	30	50	DFT-64QAM	L	Inner_Full	21.98	PC3	PASS
N41	30	50	DFT-256QAM	L	Edge_1RB_Left	21.19	PC3	PASS
N41	30	50	DFT-256QAM	L	Edge_1RB_Right	21.70	PC3	PASS
N41	30	50	DFT-256QAM	L	Outer_Full	21.69	PC3	PASS
N41	30	50	DFT-256QAM	L	Inner_Full	21.03	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	21.24	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	21.66	PC3	PASS
N41	30	50	CP-QPSK	L	Outer_Full	21.42	PC3	PASS
N41	30	50	CP-QPSK	L	Inner_Full	21.86	PC3	PASS
N41	30	50	CP-16QAM	L	Edge_1RB_Left	21.64	PC3	PASS
N41	30	50	CP-16QAM	L	Edge_1RB_Right	21.94	PC3	PASS
N41	30	50	CP-16QAM	L	Outer_Full	21.37	PC3	PASS
N41	30	50	CP-16QAM	L	Inner_Full	21.44	PC3	PASS
N41	30	50	CP-64QAM	L	Edge_1RB_Left	21.51	PC3	PASS
N41	30	50	CP-64QAM	L	Edge_1RB_Right	21.78	PC3	PASS
N41	30	50	CP-64QAM	L	Outer_Full	21.84	PC3	PASS
N41	30	50	CP-64QAM	L	Inner_Full	21.87	PC3	PASS
N41	30	50	CP-256QAM	L	Edge_1RB_Left	21.79	PC3	PASS
N41	30	50	CP-256QAM	L	Edge_1RB_Right	21.41	PC3	PASS
N41	30	50	CP-256QAM	L	Outer_Full	21.86	PC3	PASS
N41	30	50	CP-256QAM	L	Inner_Full	21.86	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	21.80	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Outer_Full	23.26	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Inner_Full	23.78	PC3	PASS

N41	30	50	DFT-QPSK	M	Edge_1RB_Left	21.70	PC3	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Right	23.00	PC3	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	22.74	PC3	PASS
N41	30	50	DFT-QPSK	M	Inner_Full	23.67	PC3	PASS
N41	30	50	DFT-16QAM	M	Edge_1RB_Left	21.81	PC3	PASS
N41	30	50	DFT-16QAM	M	Edge_1RB_Right	21.23	PC3	PASS
N41	30	50	DFT-16QAM	M	Outer_Full	21.72	PC3	PASS
N41	30	50	DFT-16QAM	M	Inner_Full	22.72	PC3	PASS
N41	30	50	DFT-64QAM	M	Edge_1RB_Left	21.83	PC3	PASS
N41	30	50	DFT-64QAM	M	Edge_1RB_Right	21.36	PC3	PASS
N41	30	50	DFT-64QAM	M	Outer_Full	21.25	PC3	PASS
N41	30	50	DFT-64QAM	M	Inner_Full	21.26	PC3	PASS
N41	30	50	DFT-256QAM	M	Edge_1RB_Left	21.67	PC3	PASS
N41	30	50	DFT-256QAM	M	Edge_1RB_Right	21.41	PC3	PASS
N41	30	50	DFT-256QAM	M	Outer_Full	21.93	PC3	PASS
N41	30	50	DFT-256QAM	M	Inner_Full	21.82	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	21.98	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Right	21.18	PC3	PASS
N41	30	50	CP-QPSK	M	Outer_Full	21.69	PC3	PASS
N41	30	50	CP-QPSK	M	Inner_Full	22.24	PC3	PASS
N41	30	50	CP-16QAM	M	Edge_1RB_Left	21.92	PC3	PASS
N41	30	50	CP-16QAM	M	Edge_1RB_Right	21.02	PC3	PASS
N41	30	50	CP-16QAM	M	Outer_Full	21.75	PC3	PASS
N41	30	50	CP-16QAM	M	Inner_Full	21.77	PC3	PASS
N41	30	50	CP-64QAM	M	Edge_1RB_Left	21.05	PC3	PASS
N41	30	50	CP-64QAM	M	Edge_1RB_Right	21.00	PC3	PASS
N41	30	50	CP-64QAM	M	Outer_Full	21.98	PC3	PASS
N41	30	50	CP-64QAM	M	Inner_Full	21.87	PC3	PASS
N41	30	50	CP-256QAM	M	Edge_1RB_Left	21.78	PC3	PASS
N41	30	50	CP-256QAM	M	Edge_1RB_Right	21.94	PC3	PASS
N41	30	50	CP-256QAM	M	Outer_Full	21.66	PC3	PASS
N41	30	50	CP-256QAM	M	Inner_Full	21.52	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	21.55	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	22.20	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Outer_Full	23.26	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Inner_Full	23.78	PC3	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	22.98	PC3	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	21.66	PC3	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	22.77	PC3	PASS
N41	30	50	DFT-QPSK	H	Inner_Full	23.85	PC3	PASS
N41	30	50	DFT-16QAM	H	Edge_1RB_Left	22.45	PC3	PASS
N41	30	50	DFT-16QAM	H	Edge_1RB_Right	21.98	PC3	PASS

N41	30	50	DFT-16QAM	H	Outer_Full	21.74	PC3	PASS
N41	30	50	DFT-16QAM	H	Inner_Full	22.84	PC3	PASS
N41	30	50	DFT-64QAM	H	Edge_1RB_Left	21.86	PC3	PASS
N41	30	50	DFT-64QAM	H	Edge_1RB_Right	21.56	PC3	PASS
N41	30	50	DFT-64QAM	H	Outer_Full	21.21	PC3	PASS
N41	30	50	DFT-64QAM	H	Inner_Full	21.00	PC3	PASS
N41	30	50	DFT-256QAM	H	Edge_1RB_Left	21.68	PC3	PASS
N41	30	50	DFT-256QAM	H	Edge_1RB_Right	21.73	PC3	PASS
N41	30	50	DFT-256QAM	H	Outer_Full	21.71	PC3	PASS
N41	30	50	DFT-256QAM	H	Inner_Full	21.42	PC3	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	21.21	PC3	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Right	21.87	PC3	PASS
N41	30	50	CP-QPSK	H	Outer_Full	21.68	PC3	PASS
N41	30	50	CP-QPSK	H	Inner_Full	22.29	PC3	PASS
N41	30	50	CP-16QAM	H	Edge_1RB_Left	21.52	PC3	PASS
N41	30	50	CP-16QAM	H	Edge_1RB_Right	21.44	PC3	PASS
N41	30	50	CP-16QAM	H	Outer_Full	21.64	PC3	PASS
N41	30	50	CP-16QAM	H	Inner_Full	21.79	PC3	PASS
N41	30	50	CP-64QAM	H	Edge_1RB_Left	21.56	PC3	PASS
N41	30	50	CP-64QAM	H	Edge_1RB_Right	21.85	PC3	PASS
N41	30	50	CP-64QAM	H	Outer_Full	21.92	PC3	PASS
N41	30	50	CP-64QAM	H	Inner_Full	21.73	PC3	PASS
N41	30	50	CP-256QAM	H	Edge_1RB_Left	21.72	PC3	PASS
N41	30	50	CP-256QAM	H	Edge_1RB_Right	21.65	PC3	PASS
N41	30	50	CP-256QAM	H	Outer_Full	21.71	PC3	PASS
N41	30	50	CP-256QAM	H	Inner_Full	21.61	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	21.75	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	21.90	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	21.87	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Inner_Full	22.36	PC3	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	21.05	PC3	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	21.49	PC3	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	21.33	PC3	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	22.40	PC3	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Left	21.43	PC3	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Right	21.72	PC3	PASS
N41	30	60	DFT-16QAM	L	Outer_Full	21.39	PC3	PASS
N41	30	60	DFT-16QAM	L	Inner_Full	21.80	PC3	PASS
N41	30	60	DFT-64QAM	L	Edge_1RB_Left	21.98	PC3	PASS
N41	30	60	DFT-64QAM	L	Edge_1RB_Right	21.36	PC3	PASS
N41	30	60	DFT-64QAM	L	Outer_Full	21.89	PC3	PASS
N41	30	60	DFT-64QAM	L	Inner_Full	21.85	PC3	PASS

N41	30	60	DFT-256QAM	L	Edge_1RB_Left	21.81	PC3	PASS
N41	30	60	DFT-256QAM	L	Edge_1RB_Right	21.77	PC3	PASS
N41	30	60	DFT-256QAM	L	Outer_Full	21.59	PC3	PASS
N41	30	60	DFT-256QAM	L	Inner_Full	21.91	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	21.31	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	21.47	PC3	PASS
N41	30	60	CP-QPSK	L	Outer_Full	21.29	PC3	PASS
N41	30	60	CP-QPSK	L	Inner_Full	21.85	PC3	PASS
N41	30	60	CP-16QAM	L	Edge_1RB_Left	21.58	PC3	PASS
N41	30	60	CP-16QAM	L	Edge_1RB_Right	21.70	PC3	PASS
N41	30	60	CP-16QAM	L	Outer_Full	21.32	PC3	PASS
N41	30	60	CP-16QAM	L	Inner_Full	21.75	PC3	PASS
N41	30	60	CP-64QAM	L	Edge_1RB_Left	21.64	PC3	PASS
N41	30	60	CP-64QAM	L	Edge_1RB_Right	21.71	PC3	PASS
N41	30	60	CP-64QAM	L	Outer_Full	21.84	PC3	PASS
N41	30	60	CP-64QAM	L	Inner_Full	21.85	PC3	PASS
N41	30	60	CP-256QAM	L	Edge_1RB_Left	21.38	PC3	PASS
N41	30	60	CP-256QAM	L	Edge_1RB_Right	21.75	PC3	PASS
N41	30	60	CP-256QAM	L	Outer_Full	21.87	PC3	PASS
N41	30	60	CP-256QAM	L	Inner_Full	21.85	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	23.26	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	23.39	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Inner_Full	23.95	PC3	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	21.53	PC3	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	22.78	PC3	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	22.80	PC3	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	23.87	PC3	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Left	21.76	PC3	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Right	21.97	PC3	PASS
N41	30	60	DFT-16QAM	M	Outer_Full	21.78	PC3	PASS
N41	30	60	DFT-16QAM	M	Inner_Full	21.34	PC3	PASS
N41	30	60	DFT-64QAM	M	Edge_1RB_Right	21.74	PC3	PASS
N41	30	60	DFT-64QAM	M	Outer_Full	21.38	PC3	PASS
N41	30	60	DFT-64QAM	M	Inner_Full	21.45	PC3	PASS
N41	30	60	DFT-256QAM	M	Edge_1RB_Left	21.92	PC3	PASS
N41	30	60	DFT-256QAM	M	Edge_1RB_Right	21.45	PC3	PASS
N41	30	60	DFT-256QAM	M	Outer_Full	21.37	PC3	PASS
N41	30	60	DFT-256QAM	M	Inner_Full	21.49	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	21.55	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	21.88	PC3	PASS
N41	30	60	CP-QPSK	M	Outer_Full	21.87	PC3	PASS
N41	30	60	CP-QPSK	M	Inner_Full	22.44	PC3	PASS

N41	30	60	CP-16QAM	M	Edge_1RB_Left	21.75	PC3	PASS
N41	30	60	CP-16QAM	M	Edge_1RB_Right	21.11	PC3	PASS
N41	30	60	CP-16QAM	M	Outer_Full	21.79	PC3	PASS
N41	30	60	CP-16QAM	M	Inner_Full	21.89	PC3	PASS
N41	30	60	CP-64QAM	M	Edge_1RB_Left	21.36	PC3	PASS
N41	30	60	CP-64QAM	M	Edge_1RB_Right	21.53	PC3	PASS
N41	30	60	CP-64QAM	M	Outer_Full	21.35	PC3	PASS
N41	30	60	CP-64QAM	M	Inner_Full	21.36	PC3	PASS
N41	30	60	CP-256QAM	M	Edge_1RB_Left	21.98	PC3	PASS
N41	30	60	CP-256QAM	M	Edge_1RB_Right	21.51	PC3	PASS
N41	30	60	CP-256QAM	M	Outer_Full	21.35	PC3	PASS
N41	30	60	CP-256QAM	M	Inner_Full	21.43	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	21.19	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	22.13	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	23.34	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Inner_Full	23.87	PC3	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	22.96	PC3	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	21.68	PC3	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	22.90	PC3	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	23.90	PC3	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Left	22.08	PC3	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Right	21.85	PC3	PASS
N41	30	60	DFT-16QAM	H	Outer_Full	21.16	PC3	PASS
N41	30	60	DFT-16QAM	H	Inner_Full	22.92	PC3	PASS
N41	30	60	DFT-64QAM	H	Edge_1RB_Left	21.72	PC3	PASS
N41	30	60	DFT-64QAM	H	Edge_1RB_Right	21.48	PC3	PASS
N41	30	60	DFT-64QAM	H	Outer_Full	21.42	PC3	PASS
N41	30	60	DFT-64QAM	H	Inner_Full	21.39	PC3	PASS
N41	30	60	DFT-256QAM	H	Edge_1RB_Left	21.56	PC3	PASS
N41	30	60	DFT-256QAM	H	Edge_1RB_Right	21.76	PC3	PASS
N41	30	60	DFT-256QAM	H	Outer_Full	21.19	PC3	PASS
N41	30	60	DFT-256QAM	H	Inner_Full	21.48	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	21.33	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	21.89	PC3	PASS
N41	30	60	CP-QPSK	H	Outer_Full	21.87	PC3	PASS
N41	30	60	CP-QPSK	H	Inner_Full	22.38	PC3	PASS
N41	30	60	CP-16QAM	H	Edge_1RB_Left	21.26	PC3	PASS
N41	30	60	CP-16QAM	H	Edge_1RB_Right	21.99	PC3	PASS
N41	30	60	CP-16QAM	H	Outer_Full	21.90	PC3	PASS
N41	30	60	CP-16QAM	H	Inner_Full	21.85	PC3	PASS
N41	30	60	CP-64QAM	H	Edge_1RB_Left	21.57	PC3	PASS
N41	30	60	CP-64QAM	H	Edge_1RB_Right	21.95	PC3	PASS

N41	30	60	CP-64QAM	H	Outer_Full	21.14	PC3	PASS
N41	30	60	CP-64QAM	H	Inner_Full	21.70	PC3	PASS
N41	30	60	CP-256QAM	H	Edge_1RB_Left	21.69	PC3	PASS
N41	30	60	CP-256QAM	H	Edge_1RB_Right	21.00	PC3	PASS
N41	30	60	CP-256QAM	H	Outer_Full	21.84	PC3	PASS
N41	30	60	CP-256QAM	H	Inner_Full	21.36	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	21.59	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	22.18	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Outer_Full	21.90	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Inner_Full	22.40	PC3	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	21.81	PC3	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Right	21.85	PC3	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	21.36	PC3	PASS
N41	30	80	DFT-QPSK	L	Inner_Full	22.56	PC3	PASS
N41	30	80	DFT-16QAM	L	Edge_1RB_Left	21.82	PC3	PASS
N41	30	80	DFT-16QAM	L	Edge_1RB_Right	21.12	PC3	PASS
N41	30	80	DFT-16QAM	L	Outer_Full	21.64	PC3	PASS
N41	30	80	DFT-16QAM	L	Inner_Full	21.40	PC3	PASS
N41	30	80	DFT-64QAM	L	Edge_1RB_Left	21.77	PC3	PASS
N41	30	80	DFT-64QAM	L	Edge_1RB_Right	21.75	PC3	PASS
N41	30	80	DFT-64QAM	L	Outer_Full	21.83	PC3	PASS
N41	30	80	DFT-64QAM	L	Inner_Full	21.85	PC3	PASS
N41	30	80	DFT-256QAM	L	Edge_1RB_Left	21.68	PC3	PASS
N41	30	80	DFT-256QAM	L	Edge_1RB_Right	21.71	PC3	PASS
N41	30	80	DFT-256QAM	L	Outer_Full	21.64	PC3	PASS
N41	30	80	DFT-256QAM	L	Inner_Full	21.89	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	21.98	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	21.58	PC3	PASS
N41	30	80	CP-QPSK	L	Outer_Full	21.39	PC3	PASS
N41	30	80	CP-QPSK	L	Inner_Full	21.84	PC3	PASS
N41	30	80	CP-16QAM	L	Edge_1RB_Left	21.69	PC3	PASS
N41	30	80	CP-16QAM	L	Edge_1RB_Right	21.57	PC3	PASS
N41	30	80	CP-16QAM	L	Outer_Full	21.37	PC3	PASS
N41	30	80	CP-16QAM	L	Inner_Full	21.42	PC3	PASS
N41	30	80	CP-64QAM	L	Edge_1RB_Left	21.40	PC3	PASS
N41	30	80	CP-64QAM	L	Edge_1RB_Right	21.51	PC3	PASS
N41	30	80	CP-64QAM	L	Outer_Full	21.79	PC3	PASS
N41	30	80	CP-64QAM	L	Inner_Full	21.87	PC3	PASS
N41	30	80	CP-256QAM	L	Edge_1RB_Left	21.90	PC3	PASS
N41	30	80	CP-256QAM	L	Edge_1RB_Right	21.50	PC3	PASS
N41	30	80	CP-256QAM	L	Outer_Full	21.83	PC3	PASS
N41	30	80	CP-256QAM	L	Inner_Full	21.97	PC3	PASS

N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	21.14	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	23.11	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Outer_Full	23.07	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Inner_Full	23.88	PC3	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	21.27	PC3	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Right	22.75	PC3	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	22.64	PC3	PASS
N41	30	80	DFT-QPSK	M	Inner_Full	23.86	PC3	PASS
N41	30	80	DFT-16QAM	M	Edge_1RB_Left	21.44	PC3	PASS
N41	30	80	DFT-16QAM	M	Edge_1RB_Right	22.16	PC3	PASS
N41	30	80	DFT-16QAM	M	Outer_Full	21.60	PC3	PASS
N41	30	80	DFT-16QAM	M	Inner_Full	21.11	PC3	PASS
N41	30	80	DFT-64QAM	M	Edge_1RB_Left	21.53	PC3	PASS
N41	30	80	DFT-64QAM	M	Edge_1RB_Right	21.55	PC3	PASS
N41	30	80	DFT-64QAM	M	Outer_Full	21.16	PC3	PASS
N41	30	80	DFT-64QAM	M	Inner_Full	21.34	PC3	PASS
N41	30	80	DFT-256QAM	M	Edge_1RB_Left	21.12	PC3	PASS
N41	30	80	DFT-256QAM	M	Edge_1RB_Right	21.96	PC3	PASS
N41	30	80	DFT-256QAM	M	Outer_Full	21.10	PC3	PASS
N41	30	80	DFT-256QAM	M	Inner_Full	21.59	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	21.65	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	21.78	PC3	PASS
N41	30	80	CP-QPSK	M	Outer_Full	21.58	PC3	PASS
N41	30	80	CP-QPSK	M	Inner_Full	22.29	PC3	PASS
N41	30	80	CP-16QAM	M	Edge_1RB_Left	21.37	PC3	PASS
N41	30	80	CP-16QAM	M	Edge_1RB_Right	21.96	PC3	PASS
N41	30	80	CP-16QAM	M	Outer_Full	21.53	PC3	PASS
N41	30	80	CP-16QAM	M	Inner_Full	21.86	PC3	PASS
N41	30	80	CP-64QAM	M	Edge_1RB_Left	21.10	PC3	PASS
N41	30	80	CP-64QAM	M	Edge_1RB_Right	21.88	PC3	PASS
N41	30	80	CP-64QAM	M	Outer_Full	21.54	PC3	PASS
N41	30	80	CP-64QAM	M	Inner_Full	21.63	PC3	PASS
N41	30	80	CP-256QAM	M	Edge_1RB_Left	21.77	PC3	PASS
N41	30	80	CP-256QAM	M	Edge_1RB_Right	21.59	PC3	PASS
N41	30	80	CP-256QAM	M	Outer_Full	21.64	PC3	PASS
N41	30	80	CP-256QAM	M	Inner_Full	21.41	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	23.31	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	21.91	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Outer_Full	23.31	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Inner_Full	23.97	PC3	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	21.94	PC3	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Right	21.34	PC3	PASS

N41	30	80	DFT-QPSK	H	Outer_Full	22.88	PC3	PASS
N41	30	80	DFT-QPSK	H	Inner_Full	24.02	PC3	PASS
N41	30	80	DFT-16QAM	H	Edge_1RB_Left	21.76	PC3	PASS
N41	30	80	DFT-16QAM	H	Edge_1RB_Right	21.39	PC3	PASS
N41	30	80	DFT-16QAM	H	Outer_Full	21.90	PC3	PASS
N41	30	80	DFT-16QAM	H	Inner_Full	23.05	PC3	PASS
N41	30	80	DFT-64QAM	H	Edge_1RB_Left	21.70	PC3	PASS
N41	30	80	DFT-64QAM	H	Edge_1RB_Right	21.80	PC3	PASS
N41	30	80	DFT-64QAM	H	Outer_Full	21.85	PC3	PASS
N41	30	80	DFT-64QAM	H	Inner_Full	21.53	PC3	PASS
N41	30	80	DFT-256QAM	H	Edge_1RB_Left	21.32	PC3	PASS
N41	30	80	DFT-256QAM	H	Edge_1RB_Right	21.95	PC3	PASS
N41	30	80	DFT-256QAM	H	Outer_Full	21.54	PC3	PASS
N41	30	80	DFT-256QAM	H	Inner_Full	21.56	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	21.49	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	21.51	PC3	PASS
N41	30	80	CP-QPSK	H	Outer_Full	21.80	PC3	PASS
N41	30	80	CP-QPSK	H	Inner_Full	22.49	PC3	PASS
N41	30	80	CP-16QAM	H	Edge_1RB_Left	21.32	PC3	PASS
N41	30	80	CP-16QAM	H	Edge_1RB_Right	21.85	PC3	PASS
N41	30	80	CP-16QAM	H	Outer_Full	21.85	PC3	PASS
N41	30	80	CP-16QAM	H	Inner_Full	21.98	PC3	PASS
N41	30	80	CP-64QAM	H	Edge_1RB_Left	21.28	PC3	PASS
N41	30	80	CP-64QAM	H	Edge_1RB_Right	21.52	PC3	PASS
N41	30	80	CP-64QAM	H	Outer_Full	21.93	PC3	PASS
N41	30	80	CP-64QAM	H	Inner_Full	21.59	PC3	PASS
N41	30	80	CP-256QAM	H	Edge_1RB_Left	21.62	PC3	PASS
N41	30	80	CP-256QAM	H	Edge_1RB_Right	21.77	PC3	PASS
N41	30	80	CP-256QAM	H	Outer_Full	21.98	PC3	PASS
N41	30	80	CP-256QAM	H	Inner_Full	21.56	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	21.85	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Right	22.40	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Outer_Full	21.97	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Inner_Full	22.51	PC3	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	21.65	PC3	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Right	21.96	PC3	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	21.48	PC3	PASS
N41	30	90	DFT-QPSK	L	Inner_Full	22.48	PC3	PASS
N41	30	90	DFT-16QAM	L	Edge_1RB_Left	21.91	PC3	PASS
N41	30	90	DFT-16QAM	L	Edge_1RB_Right	21.26	PC3	PASS
N41	30	90	DFT-16QAM	L	Outer_Full	21.45	PC3	PASS
N41	30	90	DFT-16QAM	L	Inner_Full	21.32	PC3	PASS

N41	30	90	DFT-64QAM	L	Edge_1RB_Left	21.64	PC3	PASS
N41	30	90	DFT-64QAM	L	Edge_1RB_Right	21.78	PC3	PASS
N41	30	90	DFT-64QAM	L	Outer_Full	21.92	PC3	PASS
N41	30	90	DFT-64QAM	L	Inner_Full	21.94	PC3	PASS
N41	30	90	DFT-256QAM	L	Edge_1RB_Left	21.42	PC3	PASS
N41	30	90	DFT-256QAM	L	Edge_1RB_Right	21.45	PC3	PASS
N41	30	90	DFT-256QAM	L	Outer_Full	21.92	PC3	PASS
N41	30	90	DFT-256QAM	L	Inner_Full	21.00	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	21.64	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Right	21.01	PC3	PASS
N41	30	90	CP-QPSK	L	Outer_Full	21.52	PC3	PASS
N41	30	90	CP-QPSK	L	Inner_Full	21.90	PC3	PASS
N41	30	90	CP-16QAM	L	Edge_1RB_Left	21.95	PC3	PASS
N41	30	90	CP-16QAM	L	Edge_1RB_Right	21.97	PC3	PASS
N41	30	90	CP-16QAM	L	Outer_Full	21.40	PC3	PASS
N41	30	90	CP-16QAM	L	Inner_Full	21.43	PC3	PASS
N41	30	90	CP-64QAM	L	Edge_1RB_Left	21.95	PC3	PASS
N41	30	90	CP-64QAM	L	Edge_1RB_Right	21.47	PC3	PASS
N41	30	90	CP-64QAM	L	Outer_Full	21.92	PC3	PASS
N41	30	90	CP-64QAM	L	Inner_Full	21.88	PC3	PASS
N41	30	90	CP-256QAM	L	Edge_1RB_Left	21.87	PC3	PASS
N41	30	90	CP-256QAM	L	Edge_1RB_Right	21.10	PC3	PASS
N41	30	90	CP-256QAM	L	Outer_Full	21.86	PC3	PASS
N41	30	90	CP-256QAM	L	Inner_Full	21.90	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Left	21.94	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Right	22.98	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Outer_Full	23.00	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Inner_Full	23.83	PC3	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	21.97	PC3	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Right	22.52	PC3	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	22.57	PC3	PASS
N41	30	90	DFT-QPSK	M	Inner_Full	23.82	PC3	PASS
N41	30	90	DFT-16QAM	M	Edge_1RB_Left	21.51	PC3	PASS
N41	30	90	DFT-16QAM	M	Edge_1RB_Right	21.84	PC3	PASS
N41	30	90	DFT-16QAM	M	Outer_Full	21.56	PC3	PASS
N41	30	90	DFT-16QAM	M	Inner_Full	21.50	PC3	PASS
N41	30	90	DFT-64QAM	M	Edge_1RB_Left	21.79	PC3	PASS
N41	30	90	DFT-64QAM	M	Edge_1RB_Right	21.25	PC3	PASS
N41	30	90	DFT-64QAM	M	Outer_Full	21.99	PC3	PASS
N41	30	90	DFT-64QAM	M	Inner_Full	21.26	PC3	PASS
N41	30	90	DFT-256QAM	M	Edge_1RB_Left	21.44	PC3	PASS
N41	30	90	DFT-256QAM	M	Edge_1RB_Right	21.83	PC3	PASS

N41	30	90	DFT-256QAM	M	Outer_Full	21.73	PC3	PASS
N41	30	90	DFT-256QAM	M	Inner_Full	21.33	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	21.61	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Right	21.50	PC3	PASS
N41	30	90	CP-QPSK	M	Outer_Full	21.53	PC3	PASS
N41	30	90	CP-QPSK	M	Inner_Full	22.29	PC3	PASS
N41	30	90	CP-16QAM	M	Edge_1RB_Left	21.51	PC3	PASS
N41	30	90	CP-16QAM	M	Edge_1RB_Right	21.81	PC3	PASS
N41	30	90	CP-16QAM	M	Outer_Full	21.47	PC3	PASS
N41	30	90	CP-16QAM	M	Inner_Full	21.83	PC3	PASS
N41	30	90	CP-64QAM	M	Edge_1RB_Left	21.78	PC3	PASS
N41	30	90	CP-64QAM	M	Edge_1RB_Right	21.82	PC3	PASS
N41	30	90	CP-64QAM	M	Outer_Full	21.95	PC3	PASS
N41	30	90	CP-64QAM	M	Inner_Full	21.62	PC3	PASS
N41	30	90	CP-256QAM	M	Edge_1RB_Left	21.45	PC3	PASS
N41	30	90	CP-256QAM	M	Edge_1RB_Right	21.86	PC3	PASS
N41	30	90	CP-256QAM	M	Outer_Full	21.93	PC3	PASS
N41	30	90	CP-256QAM	M	Inner_Full	21.75	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	21.03	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Outer_Full	23.46	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Inner_Full	23.08	PC3	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	22.16	PC3	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Right	21.23	PC3	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	22.97	PC3	PASS
N41	30	90	DFT-QPSK	H	Inner_Full	23.05	PC3	PASS
N41	30	90	DFT-16QAM	H	Edge_1RB_Left	21.35	PC3	PASS
N41	30	90	DFT-16QAM	H	Edge_1RB_Right	21.35	PC3	PASS
N41	30	90	DFT-16QAM	H	Outer_Full	21.91	PC3	PASS
N41	30	90	DFT-16QAM	H	Inner_Full	21.02	PC3	PASS
N41	30	90	DFT-64QAM	H	Edge_1RB_Left	21.01	PC3	PASS
N41	30	90	DFT-64QAM	H	Edge_1RB_Right	21.04	PC3	PASS
N41	30	90	DFT-64QAM	H	Outer_Full	21.49	PC3	PASS
N41	30	90	DFT-64QAM	H	Inner_Full	21.55	PC3	PASS
N41	30	90	DFT-256QAM	H	Edge_1RB_Left	21.58	PC3	PASS
N41	30	90	DFT-256QAM	H	Edge_1RB_Right	21.59	PC3	PASS
N41	30	90	DFT-256QAM	H	Outer_Full	21.50	PC3	PASS
N41	30	90	DFT-256QAM	H	Inner_Full	21.55	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	21.71	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	21.45	PC3	PASS
N41	30	90	CP-QPSK	H	Outer_Full	21.50	PC3	PASS
N41	30	90	CP-QPSK	H	Inner_Full	22.52	PC3	PASS
N41	30	90	CP-16QAM	H	Edge_1RB_Left	21.37	PC3	PASS

N41	30	90	CP-16QAM	H	Edge_1RB_Right	21.53	PC3	PASS
N41	30	90	CP-16QAM	H	Outer_Full	21.83	PC3	PASS
N41	30	90	CP-16QAM	H	Inner_Full	21.02	PC3	PASS
N41	30	90	CP-64QAM	H	Edge_1RB_Left	21.69	PC3	PASS
N41	30	90	CP-64QAM	H	Edge_1RB_Right	21.69	PC3	PASS
N41	30	90	CP-64QAM	H	Outer_Full	21.35	PC3	PASS
N41	30	90	CP-64QAM	H	Inner_Full	21.46	PC3	PASS
N41	30	90	CP-256QAM	H	Edge_1RB_Left	21.42	PC3	PASS
N41	30	90	CP-256QAM	H	Edge_1RB_Right	21.66	PC3	PASS
N41	30	90	CP-256QAM	H	Outer_Full	21.33	PC3	PASS
N41	30	90	CP-256QAM	H	Inner_Full	21.50	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	21.34	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	22.18	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	22.02	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Inner_Full	22.56	PC3	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	21.40	PC3	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	21.74	PC3	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	21.44	PC3	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	22.53	PC3	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Left	21.65	PC3	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Right	21.01	PC3	PASS
N41	30	100	DFT-16QAM	L	Outer_Full	21.48	PC3	PASS
N41	30	100	DFT-16QAM	L	Inner_Full	21.34	PC3	PASS
N41	30	100	DFT-64QAM	L	Edge_1RB_Left	21.68	PC3	PASS
N41	30	100	DFT-64QAM	L	Edge_1RB_Right	21.57	PC3	PASS
N41	30	100	DFT-64QAM	L	Outer_Full	21.98	PC3	PASS
N41	30	100	DFT-64QAM	L	Inner_Full	21.50	PC3	PASS
N41	30	100	DFT-256QAM	L	Edge_1RB_Left	21.46	PC3	PASS
N41	30	100	DFT-256QAM	L	Edge_1RB_Right	21.63	PC3	PASS
N41	30	100	DFT-256QAM	L	Outer_Full	21.94	PC3	PASS
N41	30	100	DFT-256QAM	L	Inner_Full	21.64	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	21.31	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	21.58	PC3	PASS
N41	30	100	CP-QPSK	L	Outer_Full	21.87	PC3	PASS
N41	30	100	CP-QPSK	L	Inner_Full	21.99	PC3	PASS
N41	30	100	CP-16QAM	L	Edge_1RB_Left	21.64	PC3	PASS
N41	30	100	CP-16QAM	L	Edge_1RB_Right	21.51	PC3	PASS
N41	30	100	CP-16QAM	L	Outer_Full	21.48	PC3	PASS
N41	30	100	CP-16QAM	L	Inner_Full	21.54	PC3	PASS
N41	30	100	CP-64QAM	L	Edge_1RB_Left	21.54	PC3	PASS
N41	30	100	CP-64QAM	L	Edge_1RB_Right	21.97	PC3	PASS
N41	30	100	CP-64QAM	L	Outer_Full	21.92	PC3	PASS

N41	30	100	CP-64QAM	L	Inner_Full	21.62	PC3	PASS
N41	30	100	CP-256QAM	L	Edge_1RB_Left	21.37	PC3	PASS
N41	30	100	CP-256QAM	L	Edge_1RB_Right	21.94	PC3	PASS
N41	30	100	CP-256QAM	L	Outer_Full	21.93	PC3	PASS
N41	30	100	CP-256QAM	L	Inner_Full	21.56	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	22.69	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	22.96	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Inner_Full	23.74	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	21.44	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	22.14	PC3	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	22.45	PC3	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	23.98	PC3	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Left	21.96	PC3	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Right	21.56	PC3	PASS
N41	30	100	DFT-16QAM	M	Outer_Full	21.46	PC3	PASS
N41	30	100	DFT-16QAM	M	Inner_Full	21.06	PC3	PASS
N41	30	100	DFT-64QAM	M	Edge_1RB_Left	21.35	PC3	PASS
N41	30	100	DFT-64QAM	M	Edge_1RB_Right	21.93	PC3	PASS
N41	30	100	DFT-64QAM	M	Outer_Full	21.00	PC3	PASS
N41	30	100	DFT-64QAM	M	Inner_Full	21.25	PC3	PASS
N41	30	100	DFT-256QAM	M	Edge_1RB_Left	21.66	PC3	PASS
N41	30	100	DFT-256QAM	M	Edge_1RB_Right	21.78	PC3	PASS
N41	30	100	DFT-256QAM	M	Outer_Full	21.32	PC3	PASS
N41	30	100	DFT-256QAM	M	Inner_Full	21.32	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	21.82	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	21.12	PC3	PASS
N41	30	100	CP-QPSK	M	Outer_Full	21.41	PC3	PASS
N41	30	100	CP-QPSK	M	Inner_Full	22.30	PC3	PASS
N41	30	100	CP-16QAM	M	Edge_1RB_Left	21.08	PC3	PASS
N41	30	100	CP-16QAM	M	Edge_1RB_Right	21.67	PC3	PASS
N41	30	100	CP-16QAM	M	Outer_Full	21.40	PC3	PASS
N41	30	100	CP-16QAM	M	Inner_Full	21.87	PC3	PASS
N41	30	100	CP-64QAM	M	Edge_1RB_Left	21.08	PC3	PASS
N41	30	100	CP-64QAM	M	Edge_1RB_Right	21.67	PC3	PASS
N41	30	100	CP-64QAM	M	Outer_Full	21.91	PC3	PASS
N41	30	100	CP-64QAM	M	Inner_Full	21.64	PC3	PASS
N41	30	100	CP-256QAM	M	Edge_1RB_Left	21.83	PC3	PASS
N41	30	100	CP-256QAM	M	Edge_1RB_Right	21.80	PC3	PASS
N41	30	100	CP-256QAM	M	Outer_Full	21.97	PC3	PASS
N41	30	100	CP-256QAM	M	Inner_Full	21.35	PC3	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	21.38	PC3	PASS
N41	30	100	DFT-PI2BPSK	H	Inner_Full	23.08	PC3	PASS

N41	30	100	DFT-QPSK	H	Edge_1RB_Left	21.66	PC3	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	21.93	PC3	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	22.80	PC3	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	23.10	PC3	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Left	21.88	PC3	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Right	21.61	PC3	PASS
N41	30	100	DFT-16QAM	H	Outer_Full	21.87	PC3	PASS
N41	30	100	DFT-16QAM	H	Inner_Full	23.10	PC3	PASS
N41	30	100	DFT-64QAM	H	Edge_1RB_Left	21.95	PC3	PASS
N41	30	100	DFT-64QAM	H	Edge_1RB_Right	21.70	PC3	PASS
N41	30	100	DFT-64QAM	H	Outer_Full	21.40	PC3	PASS
N41	30	100	DFT-64QAM	H	Inner_Full	21.53	PC3	PASS
N41	30	100	DFT-256QAM	H	Edge_1RB_Left	21.60	PC3	PASS
N41	30	100	DFT-256QAM	H	Edge_1RB_Right	21.42	PC3	PASS
N41	30	100	DFT-256QAM	H	Outer_Full	21.41	PC3	PASS
N41	30	100	DFT-256QAM	H	Inner_Full	21.60	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	21.81	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	21.01	PC3	PASS
N41	30	100	CP-QPSK	H	Outer_Full	21.77	PC3	PASS
N41	30	100	CP-QPSK	H	Inner_Full	22.52	PC3	PASS
N41	30	100	CP-16QAM	H	Edge_1RB_Left	21.61	PC3	PASS
N41	30	100	CP-16QAM	H	Edge_1RB_Right	21.51	PC3	PASS
N41	30	100	CP-16QAM	H	Outer_Full	21.83	PC3	PASS
N41	30	100	CP-16QAM	H	Inner_Full	22.15	PC3	PASS
N41	30	100	CP-64QAM	H	Edge_1RB_Left	21.63	PC3	PASS
N41	30	100	CP-64QAM	H	Edge_1RB_Right	21.74	PC3	PASS
N41	30	100	CP-64QAM	H	Outer_Full	21.52	PC3	PASS
N41	30	100	CP-64QAM	H	Inner_Full	21.64	PC3	PASS
N41	30	100	CP-256QAM	H	Edge_1RB_Left	21.98	PC3	PASS
N41	30	100	CP-256QAM	H	Edge_1RB_Right	21.74	PC3	PASS
N41	30	100	CP-256QAM	H	Outer_Full	21.51	PC3	PASS
N41	30	100	CP-256QAM	H	Inner_Full	21.59	PC3	PASS

5G NR(P4):

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	20.12	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	20.25	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Outer_Full	20.36	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Inner_Full	20.45	PC3	PASS

N41	30	10	CP-QPSK	L	Edge_1RB_Left	19.28	PC3	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Right	19.98	PC3	PASS
N41	30	10	CP-QPSK	L	Outer_Full	19.75	PC3	PASS
N41	30	10	CP-QPSK	L	Inner_Full	19.25	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	19.36	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	20.54	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Outer_Full	20.54	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Inner_Full	20.16	PC3	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	19.19	PC3	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Right	19.18	PC3	PASS
N41	30	10	CP-QPSK	M	Outer_Full	20.18	PC3	PASS
N41	30	10	CP-QPSK	M	Inner_Full	20.13	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	20.24	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	19.27	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Outer_Full	19.41	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Inner_Full	20.12	PC3	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	19.15	PC3	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Right	19.16	PC3	PASS
N41	30	10	CP-QPSK	H	Outer_Full	19.19	PC3	PASS
N41	30	10	CP-QPSK	H	Inner_Full	19.25	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	19.74	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	19.25	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Outer_Full	19.36	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Inner_Full	19.54	PC3	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	19.47	PC3	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Right	19.25	PC3	PASS
N41	30	15	CP-QPSK	L	Outer_Full	19.36	PC3	PASS
N41	30	15	CP-QPSK	L	Inner_Full	19.42	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	19.28	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	20.65	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Outer_Full	20.41	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Inner_Full	20.26	PC3	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	19.28	PC3	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Right	19.54	PC3	PASS
N41	30	15	CP-QPSK	M	Outer_Full	19.54	PC3	PASS
N41	30	15	CP-QPSK	M	Inner_Full	19.25	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	19.36	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Outer_Full	19.41	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Inner_Full	20.25	PC3	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	19.36	PC3	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Right	19.87	PC3	PASS
N41	30	15	CP-QPSK	H	Outer_Full	19.41	PC3	PASS

N41	30	15	CP-QPSK	H	Inner_Full	19.25	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	19.36	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	19.57	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	19.45	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Inner_Full	19.25	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	19.36	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	19.87	PC3	PASS
N41	30	20	CP-QPSK	L	Outer_Full	19.14	PC3	PASS
N41	30	20	CP-QPSK	L	Inner_Full	19.25	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	20.36	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	20.24	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	20.74	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Inner_Full	20.25	PC3	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	19.26	PC3	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	19.28	PC3	PASS
N41	30	20	CP-QPSK	M	Outer_Full	19.14	PC3	PASS
N41	30	20	CP-QPSK	M	Inner_Full	20.18	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	19.23	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	20.28	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	20.45	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Inner_Full	19.16	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	19.29	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	19.71	PC3	PASS
N41	30	20	CP-QPSK	H	Outer_Full	19.65	PC3	PASS
N41	30	20	CP-QPSK	H	Inner_Full	19.62	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	19.28	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	19.74	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Outer_Full	19.36	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Inner_Full	20.54	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	20.17	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Right	20.26	PC3	PASS
N41	30	30	CP-QPSK	L	Outer_Full	20.24	PC3	PASS
N41	30	30	CP-QPSK	L	Inner_Full	20.41	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	20.62	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	20.45	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Outer_Full	20.36	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Inner_Full	20.25	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	20.64	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Right	20.12	PC3	PASS
N41	30	30	CP-QPSK	M	Outer_Full	20.25	PC3	PASS
N41	30	30	CP-QPSK	M	Inner_Full	20.36	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	20.28	PC3	PASS

N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	20.24	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Outer_Full	20.25	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Inner_Full	20.21	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	20.13	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Right	20.15	PC3	PASS
N41	30	30	CP-QPSK	H	Outer_Full	20.87	PC3	PASS
N41	30	30	CP-QPSK	H	Inner_Full	20.36	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	20.41	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	20.62	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	20.57	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Inner_Full	20.71	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	20.25	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	20.65	PC3	PASS
N41	30	40	CP-QPSK	L	Outer_Full	20.74	PC3	PASS
N41	30	40	CP-QPSK	L	Inner_Full	20.25	PC3	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	20.65	PC3	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	20.42	PC3	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	20.25	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	20.62	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	20.24	PC3	PASS
N41	30	40	CP-QPSK	M	Outer_Full	20.28	PC3	PASS
N41	30	40	CP-QPSK	M	Inner_Full	20.25	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	20.65	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	20.74	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	20.52	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Inner_Full	20.36	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	20.57	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	20.47	PC3	PASS
N41	30	40	CP-QPSK	H	Outer_Full	20.65	PC3	PASS
N41	30	40	CP-QPSK	H	Inner_Full	20.78	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	20.65	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	20.25	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Outer_Full	20.21	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Inner_Full	20.58	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	20.48	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	20.14	PC3	PASS
N41	30	50	CP-QPSK	L	Outer_Full	20.48	PC3	PASS
N41	30	50	CP-QPSK	L	Inner_Full	20.19	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	20.16	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Outer_Full	20.16	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Inner_Full	20.15	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	20.25	PC3	PASS

N41	30	50	CP-QPSK	M	Edge_1RB_Right	20.35	PC3	PASS
N41	30	50	CP-QPSK	M	Outer_Full	20.51	PC3	PASS
N41	30	50	CP-QPSK	M	Inner_Full	20.54	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	20.26	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	20.28	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Outer_Full	20.64	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Inner_Full	20.41	PC3	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	20.25	PC3	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Right	20.36	PC3	PASS
N41	30	50	CP-QPSK	H	Outer_Full	20.54	PC3	PASS
N41	30	50	CP-QPSK	H	Inner_Full	20.15	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	20.25	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	20.36	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	20.58	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Inner_Full	20.45	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	20.25	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	20.36	PC3	PASS
N41	30	60	CP-QPSK	L	Outer_Full	20.87	PC3	PASS
N41	30	60	CP-QPSK	L	Inner_Full	20.41	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	20.25	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	20.36	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Inner_Full	20.54	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	20.12	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	20.28	PC3	PASS
N41	30	60	CP-QPSK	M	Outer_Full	20.41	PC3	PASS
N41	30	60	CP-QPSK	M	Inner_Full	20.25	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	20.36	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	20.81	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	20.41	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Inner_Full	20.23	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	20.02	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	20.12	PC3	PASS
N41	30	60	CP-QPSK	H	Outer_Full	20.13	PC3	PASS
N41	30	60	CP-QPSK	H	Inner_Full	20.17	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	20.25	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	20.23	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Outer_Full	20.36	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Inner_Full	20.44	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	20.25	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	20.24	PC3	PASS
N41	30	80	CP-QPSK	L	Outer_Full	20.16	PC3	PASS
N41	30	80	CP-QPSK	L	Inner_Full	20.28	PC3	PASS

N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	20.16	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	20.15	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Outer_Full	20.14	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Inner_Full	20.27	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	20.44	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	20.13	PC3	PASS
N41	30	80	CP-QPSK	M	Outer_Full	20.35	PC3	PASS
N41	30	80	CP-QPSK	M	Inner_Full	20.39	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	20.71	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	20.12	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Outer_Full	20.32	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Inner_Full	20.34	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	20.58	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	20.78	PC3	PASS
N41	30	80	CP-QPSK	H	Outer_Full	20.79	PC3	PASS
N41	30	80	CP-QPSK	H	Inner_Full	20.25	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	20.36	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Right	20.54	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Outer_Full	20.15	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Inner_Full	20.12	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	20.36	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Right	20.54	PC3	PASS
N41	30	90	CP-QPSK	L	Outer_Full	20.45	PC3	PASS
N41	30	90	CP-QPSK	L	Inner_Full	20.12	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Left	20.12	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Right	20.47	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Outer_Full	20.15	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Inner_Full	20.25	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	20.36	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Right	20.54	PC3	PASS
N41	30	90	CP-QPSK	M	Outer_Full	20.10	PC3	PASS
N41	30	90	CP-QPSK	M	Inner_Full	20.05	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	20.21	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Outer_Full	20.21	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Inner_Full	20.28	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	20.47	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	20.23	PC3	PASS
N41	30	90	CP-QPSK	H	Outer_Full	20.25	PC3	PASS
N41	30	90	CP-QPSK	H	Inner_Full	20.16	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	20.21	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	20.91	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	20.55	PC3	PASS

N41	30	100	DFT-PI2BPSK	L	Inner_Full	20.65	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	20.42	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	20.51	PC3	PASS
N41	30	100	CP-QPSK	L	Outer_Full	20.35	PC3	PASS
N41	30	100	CP-QPSK	L	Inner_Full	20.36	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	20.54	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	20.15	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Inner_Full	20.25	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	20.36	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	20.57	PC3	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	20.46	PC3	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	20.23	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	20.24	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	20.15	PC3	PASS
N41	30	100	CP-QPSK	M	Outer_Full	20.25	PC3	PASS
N41	30	100	CP-QPSK	M	Inner_Full	20.36	PC3	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	20.42	PC3	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	20.52	PC3	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	20.36	PC3	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	20.54	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	20.26	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	20.54	PC3	PASS
N41	30	100	CP-QPSK	H	Outer_Full	20.15	PC3	PASS
N41	30	100	CP-QPSK	H	Inner_Full	20.25	PC3	PASS

5G NR(PC2):

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	19.21	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	19.41	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Outer_Full	19.74	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Inner_Full	19.32	PC3	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	19.26	PC3	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Right	19.21	PC3	PASS
N41	30	10	CP-QPSK	L	Outer_Full	18.16	PC3	PASS
N41	30	10	CP-QPSK	L	Inner_Full	18.17	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	18.87	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	19.15	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Outer_Full	19.36	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Inner_Full	19.41	PC3	PASS