

## Appendix for N41

Product Name: Smart Phone

Model No: F91 5G

## Appendix A: Conducted Power & Effective Radiated Power

### Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
n41	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	21.54	PC3	PASS
n41	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	21.47	PC3	PASS
n41	30	10	DFT-PI2BPSK	L	Outer_Full	21.02	PC3	PASS
n41	30	10	DFT-PI2BPSK	L	Inner_Full	21.36	PC3	PASS
n41	30	10	DFT-QPSK	L	Edge_1RB_Left	21.45	PC3	PASS
n41	30	10	DFT-QPSK	L	Edge_1RB_Right	21.02	PC3	PASS
n41	30	10	DFT-QPSK	L	Outer_Full	21.69	PC3	PASS
n41	30	10	DFT-QPSK	L	Inner_Full	20.98	PC3	PASS
n41	30	10	DFT-16QAM	L	Edge_1RB_Left	20.99	PC3	PASS
n41	30	10	DFT-16QAM	L	Edge_1RB_Right	21.44	PC3	PASS
n41	30	10	DFT-16QAM	L	Outer_Full	21.26	PC3	PASS
n41	30	10	DFT-16QAM	L	Inner_Full	22.02	PC3	PASS
n41	30	10	DFT-64QAM	L	Edge_1RB_Left	20.68	PC3	PASS
n41	30	10	DFT-64QAM	L	Edge_1RB_Right	21.65	PC3	PASS
n41	30	10	DFT-64QAM	L	Outer_Full	21.87	PC3	PASS
n41	30	10	DFT-64QAM	L	Inner_Full	22.65	PC3	PASS
n41	30	10	DFT-256QAM	L	Edge_1RB_Left	22.17	PC3	PASS
n41	30	10	DFT-256QAM	L	Edge_1RB_Right	21.39	PC3	PASS
n41	30	10	DFT-256QAM	L	Outer_Full	21.47	PC3	PASS
n41	30	10	DFT-256QAM	L	Inner_Full	21.52	PC3	PASS
n41	30	10	CP-QPSK	L	Edge_1RB_Left	22.39	PC3	PASS
n41	30	10	CP-QPSK	L	Edge_1RB_Right	22.12	PC3	PASS
n41	30	10	CP-QPSK	L	Outer_Full	21.64	PC3	PASS
n41	30	10	CP-QPSK	L	Inner_Full	22.48	PC3	PASS
n41	30	10	CP-16QAM	L	Edge_1RB_Left	22.38	PC3	PASS
n41	30	10	CP-16QAM	L	Edge_1RB_Right	22.14	PC3	PASS
n41	30	10	CP-16QAM	L	Outer_Full	22.58	PC3	PASS
n41	30	10	CP-16QAM	L	Inner_Full	22.61	PC3	PASS
n41	30	10	CP-64QAM	L	Edge_1RB_Left	23.31	PC3	PASS
n41	30	10	CP-64QAM	L	Edge_1RB_Right	23.01	PC3	PASS
n41	30	10	CP-64QAM	L	Outer_Full	22.69	PC3	PASS
n41	30	10	CP-64QAM	L	Inner_Full	21.74	PC3	PASS
n41	30	10	CP-256QAM	L	Edge_1RB_Left	21.35	PC3	PASS
n41	30	10	CP-256QAM	L	Edge_1RB_Right	22.45	PC3	PASS
n41	30	10	CP-256QAM	L	Outer_Full	22.36	PC3	PASS
n41	30	10	CP-256QAM	L	Inner_Full	22.41	PC3	PASS

n41	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	21.78	PC3	PASS
n41	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	21.68	PC3	PASS
n41	30	10	DFT-PI2BPSK	M	Outer_Full	21.35	PC3	PASS
n41	30	10	DFT-PI2BPSK	M	Inner_Full	21.89	PC3	PASS
n41	30	10	DFT-QPSK	M	Edge_1RB_Left	21.68	PC3	PASS
n41	30	10	DFT-QPSK	M	Edge_1RB_Right	22.17	PC3	PASS
n41	30	10	DFT-QPSK	M	Outer_Full	22.65	PC3	PASS
n41	30	10	DFT-QPSK	M	Inner_Full	22.17	PC3	PASS
n41	30	10	DFT-16QAM	M	Edge_1RB_Left	22.42	PC3	PASS
n41	30	10	DFT-16QAM	M	Edge_1RB_Right	22.36	PC3	PASS
n41	30	10	DFT-16QAM	M	Outer_Full	22.47	PC3	PASS
n41	30	10	DFT-16QAM	M	Inner_Full	22.02	PC3	PASS
n41	30	10	DFT-64QAM	M	Edge_1RB_Left	22.41	PC3	PASS
n41	30	10	DFT-64QAM	M	Edge_1RB_Right	22.13	PC3	PASS
n41	30	10	DFT-64QAM	M	Outer_Full	22.28	PC3	PASS
n41	30	10	DFT-64QAM	M	Inner_Full	22.49	PC3	PASS
n41	30	10	DFT-256QAM	M	Edge_1RB_Left	21.68	PC3	PASS
n41	30	10	DFT-256QAM	M	Edge_1RB_Right	21.75	PC3	PASS
n41	30	10	DFT-256QAM	M	Outer_Full	21.65	PC3	PASS
n41	30	10	DFT-256QAM	M	Inner_Full	21.42	PC3	PASS
n41	30	10	CP-QPSK	M	Edge_1RB_Left	21.65	PC3	PASS
n41	30	10	CP-QPSK	M	Edge_1RB_Right	21.25	PC3	PASS
n41	30	10	CP-QPSK	M	Outer_Full	21.58	PC3	PASS
n41	30	10	CP-QPSK	M	Inner_Full	21.35	PC3	PASS
n41	30	10	CP-16QAM	M	Edge_1RB_Left	21.74	PC3	PASS
n41	30	10	CP-16QAM	M	Edge_1RB_Right	21.52	PC3	PASS
n41	30	10	CP-16QAM	M	Outer_Full	21.33	PC3	PASS
n41	30	10	CP-16QAM	M	Inner_Full	22.05	PC3	PASS
n41	30	10	CP-64QAM	M	Edge_1RB_Left	22.09	PC3	PASS
n41	30	10	CP-64QAM	M	Edge_1RB_Right	21.78	PC3	PASS
n41	30	10	CP-64QAM	M	Outer_Full	21.08	PC3	PASS
n41	30	10	CP-64QAM	M	Inner_Full	21.42	PC3	PASS
n41	30	10	CP-256QAM	M	Edge_1RB_Left	21.36	PC3	PASS
n41	30	10	CP-256QAM	M	Edge_1RB_Right	21.24	PC3	PASS
n41	30	10	CP-256QAM	M	Outer_Full	22.35	PC3	PASS
n41	30	10	CP-256QAM	M	Inner_Full	22.14	PC3	PASS
n41	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	22.28	PC3	PASS
n41	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	22.21	PC3	PASS
n41	30	10	DFT-PI2BPSK	H	Outer_Full	22.18	PC3	PASS
n41	30	10	DFT-PI2BPSK	H	Inner_Full	22.62	PC3	PASS
n41	30	10	DFT-QPSK	H	Edge_1RB_Left	21.98	PC3	PASS
n41	30	10	DFT-QPSK	H	Edge_1RB_Right	21.42	PC3	PASS

n41	30	10	DFT-QPSK	H	Outer_Full	21.36	PC3	PASS
n41	30	10	DFT-QPSK	H	Inner_Full	22.41	PC3	PASS
n41	30	10	DFT-16QAM	H	Edge_1RB_Left	21.25	PC3	PASS
n41	30	10	DFT-16QAM	H	Edge_1RB_Right	21.68	PC3	PASS
n41	30	10	DFT-16QAM	H	Outer_Full	21.74	PC3	PASS
n41	30	10	DFT-16QAM	H	Inner_Full	21.52	PC3	PASS
n41	30	10	DFT-64QAM	H	Edge_1RB_Left	21.35	PC3	PASS
n41	30	10	DFT-64QAM	H	Edge_1RB_Right	21.24	PC3	PASS
n41	30	10	DFT-64QAM	H	Outer_Full	22.14	PC3	PASS
n41	30	10	DFT-64QAM	H	Inner_Full	22.16	PC3	PASS
n41	30	10	DFT-256QAM	H	Edge_1RB_Left	21.28	PC3	PASS
n41	30	10	DFT-256QAM	H	Edge_1RB_Right	22.36	PC3	PASS
n41	30	10	DFT-256QAM	H	Outer_Full	23.24	PC3	PASS
n41	30	10	DFT-256QAM	H	Inner_Full	23.01	PC3	PASS
n41	30	10	CP-QPSK	H	Edge_1RB_Left	23.28	PC3	PASS
n41	30	10	CP-QPSK	H	Edge_1RB_Right	21.47	PC3	PASS
n41	30	10	CP-QPSK	H	Outer_Full	21.53	PC3	PASS
n41	30	10	CP-QPSK	H	Inner_Full	22.85	PC3	PASS
n41	30	10	CP-16QAM	H	Edge_1RB_Left	23.02	PC3	PASS
n41	30	10	CP-16QAM	H	Edge_1RB_Right	22.67	PC3	PASS
n41	30	10	CP-16QAM	H	Outer_Full	22.93	PC3	PASS
n41	30	10	CP-16QAM	H	Inner_Full	22.47	PC3	PASS
n41	30	10	CP-64QAM	H	Edge_1RB_Left	22.57	PC3	PASS
n41	30	10	CP-64QAM	H	Edge_1RB_Right	21.47	PC3	PASS
n41	30	10	CP-64QAM	H	Outer_Full	21.53	PC3	PASS
n41	30	10	CP-64QAM	H	Inner_Full	22.63	PC3	PASS
n41	30	10	CP-256QAM	H	Edge_1RB_Left	22.68	PC3	PASS
n41	30	10	CP-256QAM	H	Edge_1RB_Right	22.47	PC3	PASS
n41	30	10	CP-256QAM	H	Outer_Full	21.58	PC3	PASS
n41	30	10	CP-256QAM	H	Inner_Full	22.87	PC3	PASS
n41	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	21.46	PC3	PASS
n41	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	22.58	PC3	PASS
n41	30	15	DFT-PI2BPSK	L	Outer_Full	21.46	PC3	PASS
n41	30	15	DFT-PI2BPSK	L	Inner_Full	22.36	PC3	PASS
n41	30	15	DFT-QPSK	L	Edge_1RB_Left	22.57	PC3	PASS
n41	30	15	DFT-QPSK	L	Edge_1RB_Right	22.79	PC3	PASS
n41	30	15	DFT-QPSK	L	Outer_Full	22.62	PC3	PASS
n41	30	15	DFT-QPSK	L	Inner_Full	22.41	PC3	PASS
n41	30	15	DFT-16QAM	L	Edge_1RB_Left	22.69	PC3	PASS
n41	30	15	DFT-16QAM	L	Edge_1RB_Right	22.96	PC3	PASS
n41	30	15	DFT-16QAM	L	Outer_Full	22.42	PC3	PASS
n41	30	15	DFT-16QAM	L	Inner_Full	22.21	PC3	PASS

n41	30	15	DFT-64QAM	L	Edge_1RB_Left	22.32	PC3	PASS
n41	30	15	DFT-64QAM	L	Edge_1RB_Right	22.69	PC3	PASS
n41	30	15	DFT-64QAM	L	Outer_Full	22.87	PC3	PASS
n41	30	15	DFT-64QAM	L	Inner_Full	22.82	PC3	PASS
n41	30	15	DFT-256QAM	L	Edge_1RB_Left	22.12	PC3	PASS
n41	30	15	DFT-256QAM	L	Edge_1RB_Right	22.36	PC3	PASS
n41	30	15	DFT-256QAM	L	Outer_Full	22.17	PC3	PASS
n41	30	15	DFT-256QAM	L	Inner_Full	22.49	PC3	PASS
n41	30	15	CP-QPSK	L	Edge_1RB_Left	22.58	PC3	PASS
n41	30	15	CP-QPSK	L	Edge_1RB_Right	22.46	PC3	PASS
n41	30	15	CP-QPSK	L	Outer_Full	22.32	PC3	PASS
n41	30	15	CP-QPSK	L	Inner_Full	22.14	PC3	PASS
n41	30	15	CP-16QAM	L	Edge_1RB_Left	22.52	PC3	PASS
n41	30	15	CP-16QAM	L	Edge_1RB_Right	22.39	PC3	PASS
n41	30	15	CP-16QAM	L	Outer_Full	22.74	PC3	PASS
n41	30	15	CP-16QAM	L	Inner_Full	22.32	PC3	PASS
n41	30	15	CP-64QAM	L	Edge_1RB_Left	22.74	PC3	PASS
n41	30	15	CP-64QAM	L	Edge_1RB_Right	22.35	PC3	PASS
n41	30	15	CP-64QAM	L	Outer_Full	22.43	PC3	PASS
n41	30	15	CP-64QAM	L	Inner_Full	22.71	PC3	PASS
n41	30	15	CP-256QAM	L	Edge_1RB_Left	22.57	PC3	PASS
n41	30	15	CP-256QAM	L	Edge_1RB_Right	21.36	PC3	PASS
n41	30	15	CP-256QAM	L	Outer_Full	21.57	PC3	PASS
n41	30	15	CP-256QAM	L	Inner_Full	21.98	PC3	PASS
n41	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	21.75	PC3	PASS
n41	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	21.35	PC3	PASS
n41	30	15	DFT-PI2BPSK	M	Outer_Full	21.52	PC3	PASS
n41	30	15	DFT-PI2BPSK	M	Inner_Full	21.35	PC3	PASS
n41	30	15	DFT-QPSK	M	Edge_1RB_Left	21.41	PC3	PASS
n41	30	15	DFT-QPSK	M	Edge_1RB_Right	21.36	PC3	PASS
n41	30	15	DFT-QPSK	M	Outer_Full	21.87	PC3	PASS
n41	30	15	DFT-QPSK	M	Inner_Full	21.02	PC3	PASS
n41	30	15	DFT-16QAM	M	Edge_1RB_Left	21.87	PC3	PASS
n41	30	15	DFT-16QAM	M	Edge_1RB_Right	21.35	PC3	PASS
n41	30	15	DFT-16QAM	M	Outer_Full	21.42	PC3	PASS
n41	30	15	DFT-16QAM	M	Inner_Full	21.28	PC3	PASS
n41	30	15	DFT-64QAM	M	Edge_1RB_Left	21.98	PC3	PASS
n41	30	15	DFT-64QAM	M	Edge_1RB_Right	21.74	PC3	PASS
n41	30	15	DFT-64QAM	M	Outer_Full	21.32	PC3	PASS
n41	30	15	DFT-64QAM	M	Inner_Full	21.25	PC3	PASS
n41	30	15	DFT-256QAM	M	Edge_1RB_Left	21.98	PC3	PASS
n41	30	15	DFT-256QAM	M	Edge_1RB_Right	22.35	PC3	PASS

n41	30	15	DFT-256QAM	M	Outer_Full	22.02	PC3	PASS
n41	30	15	DFT-256QAM	M	Inner_Full	22.52	PC3	PASS
n41	30	15	CP-QPSK	M	Edge_1RB_Left	22.39	PC3	PASS
n41	30	15	CP-QPSK	M	Edge_1RB_Right	22.74	PC3	PASS
n41	30	15	CP-QPSK	M	Outer_Full	22.36	PC3	PASS
n41	30	15	CP-QPSK	M	Inner_Full	22.75	PC3	PASS
n41	30	15	CP-16QAM	M	Edge_1RB_Left	22.36	PC3	PASS
n41	30	15	CP-16QAM	M	Edge_1RB_Right	22.58	PC3	PASS
n41	30	15	CP-16QAM	M	Outer_Full	22.69	PC3	PASS
n41	30	15	CP-16QAM	M	Inner_Full	22.46	PC3	PASS
n41	30	15	CP-64QAM	M	Edge_1RB_Left	22.32	PC3	PASS
n41	30	15	CP-64QAM	M	Edge_1RB_Right	22.98	PC3	PASS
n41	30	15	CP-64QAM	M	Outer_Full	23.01	PC3	PASS
n41	30	15	CP-64QAM	M	Inner_Full	23.21	PC3	PASS
n41	30	15	CP-256QAM	M	Edge_1RB_Left	23.14	PC3	PASS
n41	30	15	CP-256QAM	M	Edge_1RB_Right	23.02	PC3	PASS
n41	30	15	CP-256QAM	M	Outer_Full	23.25	PC3	PASS
n41	30	15	CP-256QAM	M	Inner_Full	23.14	PC3	PASS
n41	30	15	DFT-PI2BPSK	H	Edge_1RB_Left	23.25	PC3	PASS
n41	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	23.41	PC3	PASS
n41	30	15	DFT-PI2BPSK	H	Outer_Full	23.25	PC3	PASS
n41	30	15	DFT-PI2BPSK	H	Inner_Full	22.02	PC3	PASS
n41	30	15	DFT-QPSK	H	Edge_1RB_Left	22.14	PC3	PASS
n41	30	15	DFT-QPSK	H	Edge_1RB_Right	22.36	PC3	PASS
n41	30	15	DFT-QPSK	H	Outer_Full	21.57	PC3	PASS
n41	30	15	DFT-QPSK	H	Inner_Full	22.39	PC3	PASS
n41	30	15	DFT-16QAM	H	Edge_1RB_Left	22.17	PC3	PASS
n41	30	15	DFT-16QAM	H	Edge_1RB_Right	22.35	PC3	PASS
n41	30	15	DFT-16QAM	H	Outer_Full	22.17	PC3	PASS
n41	30	15	DFT-16QAM	H	Inner_Full	22.52	PC3	PASS
n41	30	15	DFT-64QAM	H	Edge_1RB_Left	22.36	PC3	PASS
n41	30	15	DFT-64QAM	H	Edge_1RB_Right	22.47	PC3	PASS
n41	30	15	DFT-64QAM	H	Outer_Full	22.32	PC3	PASS
n41	30	15	DFT-64QAM	H	Inner_Full	22.98	PC3	PASS
n41	30	15	DFT-256QAM	H	Edge_1RB_Left	22.42	PC3	PASS
n41	30	15	DFT-256QAM	H	Edge_1RB_Right	22.35	PC3	PASS
n41	30	15	DFT-256QAM	H	Outer_Full	22.14	PC3	PASS
n41	30	15	DFT-256QAM	H	Inner_Full	22.32	PC3	PASS
n41	30	15	CP-QPSK	H	Edge_1RB_Left	22.58	PC3	PASS
n41	30	15	CP-QPSK	H	Edge_1RB_Right	22.32	PC3	PASS
n41	30	15	CP-QPSK	H	Outer_Full	22.41	PC3	PASS
n41	30	15	CP-QPSK	H	Inner_Full	22.28	PC3	PASS

n41	30	15	CP-16QAM	H	Edge_1RB_Left	22.41	PC3	PASS
n41	30	15	CP-16QAM	H	Edge_1RB_Right	22.32	PC3	PASS
n41	30	15	CP-16QAM	H	Outer_Full	23.01	PC3	PASS
n41	30	15	CP-16QAM	H	Inner_Full	22.27	PC3	PASS
n41	30	15	CP-64QAM	H	Edge_1RB_Left	22.14	PC3	PASS
n41	30	15	CP-64QAM	H	Edge_1RB_Right	22.65	PC3	PASS
n41	30	15	CP-64QAM	H	Outer_Full	22.41	PC3	PASS
n41	30	15	CP-64QAM	H	Inner_Full	22.02	PC3	PASS
n41	30	15	CP-256QAM	H	Edge_1RB_Left	22.69	PC3	PASS
n41	30	15	CP-256QAM	H	Edge_1RB_Right	22.41	PC3	PASS
n41	30	15	CP-256QAM	H	Outer_Full	22.32	PC3	PASS
n41	30	15	CP-256QAM	H	Inner_Full	22.58	PC3	PASS
n41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	22.65	PC3	PASS
n41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	22.17	PC3	PASS
n41	30	20	DFT-PI2BPSK	L	Outer_Full	22.98	PC3	PASS
n41	30	20	DFT-PI2BPSK	L	Inner_Full	22.46	PC3	PASS
n41	30	20	DFT-QPSK	L	Edge_1RB_Left	23.02	PC3	PASS
n41	30	20	DFT-QPSK	L	Edge_1RB_Right	23.14	PC3	PASS
n41	30	20	DFT-QPSK	L	Outer_Full	23.24	PC3	PASS
n41	30	20	DFT-QPSK	L	Inner_Full	23.05	PC3	PASS
n41	30	20	DFT-16QAM	L	Edge_1RB_Left	23.16	PC3	PASS
n41	30	20	DFT-16QAM	L	Edge_1RB_Right	23.18	PC3	PASS
n41	30	20	DFT-16QAM	L	Outer_Full	23.28	PC3	PASS
n41	30	20	DFT-16QAM	L	Inner_Full	23.16	PC3	PASS
n41	30	20	DFT-64QAM	L	Edge_1RB_Left	23.24	PC3	PASS
n41	30	20	DFT-64QAM	L	Edge_1RB_Right	23.19	PC3	PASS
n41	30	20	DFT-64QAM	L	Outer_Full	23.52	PC3	PASS
n41	30	20	DFT-64QAM	L	Inner_Full	23.21	PC3	PASS
n41	30	20	DFT-256QAM	L	Edge_1RB_Left	23.24	PC3	PASS
n41	30	20	DFT-256QAM	L	Edge_1RB_Right	22.58	PC3	PASS
n41	30	20	DFT-256QAM	L	Outer_Full	22.36	PC3	PASS
n41	30	20	DFT-256QAM	L	Inner_Full	22.69	PC3	PASS
n41	30	20	CP-QPSK	L	Edge_1RB_Left	22.74	PC3	PASS
n41	30	20	CP-QPSK	L	Edge_1RB_Right	22.35	PC3	PASS
n41	30	20	CP-QPSK	L	Outer_Full	22.41	PC3	PASS
n41	30	20	CP-QPSK	L	Inner_Full	22.32	PC3	PASS
n41	30	20	CP-16QAM	L	Edge_1RB_Left	21.25	PC3	PASS
n41	30	20	CP-16QAM	L	Edge_1RB_Right	20.98	PC3	PASS
n41	30	20	CP-16QAM	L	Outer_Full	20.41	PC3	PASS
n41	30	20	CP-16QAM	L	Inner_Full	20.69	PC3	PASS
n41	30	20	CP-64QAM	L	Edge_1RB_Left	20.79	PC3	PASS
n41	30	20	CP-64QAM	L	Edge_1RB_Right	20.63	PC3	PASS

n41	30	20	CP-64QAM	L	Outer_Full	20.79	PC3	PASS
n41	30	20	CP-64QAM	L	Inner_Full	20.46	PC3	PASS
n41	30	20	CP-256QAM	L	Edge_1RB_Left	20.35	PC3	PASS
n41	30	20	CP-256QAM	L	Edge_1RB_Right	20.49	PC3	PASS
n41	30	20	CP-256QAM	L	Outer_Full	20.35	PC3	PASS
n41	30	20	CP-256QAM	L	Inner_Full	20.69	PC3	PASS
n41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	20.59	PC3	PASS
n41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	20.36	PC3	PASS
n41	30	20	DFT-PI2BPSK	M	Outer_Full	20.46	PC3	PASS
n41	30	20	DFT-PI2BPSK	M	Inner_Full	20.98	PC3	PASS
n41	30	20	DFT-QPSK	M	Edge_1RB_Left	21.14	PC3	PASS
n41	30	20	DFT-QPSK	M	Edge_1RB_Right	21.24	PC3	PASS
n41	30	20	DFT-QPSK	M	Outer_Full	21.25	PC3	PASS
n41	30	20	DFT-QPSK	M	Inner_Full	21.62	PC3	PASS
n41	30	20	DFT-16QAM	M	Edge_1RB_Left	21.45	PC3	PASS
n41	30	20	DFT-16QAM	M	Edge_1RB_Right	21.58	PC3	PASS
n41	30	20	DFT-16QAM	M	Outer_Full	22.02	PC3	PASS
n41	30	20	DFT-16QAM	M	Inner_Full	22.03	PC3	PASS
n41	30	20	DFT-64QAM	M	Edge_1RB_Left	22.42	PC3	PASS
n41	30	20	DFT-64QAM	M	Edge_1RB_Right	22.32	PC3	PASS
n41	30	20	DFT-64QAM	M	Outer_Full	22.14	PC3	PASS
n41	30	20	DFT-64QAM	M	Inner_Full	22.35	PC3	PASS
n41	30	20	DFT-256QAM	M	Edge_1RB_Left	22.17	PC3	PASS
n41	30	20	DFT-256QAM	M	Edge_1RB_Right	22.56	PC3	PASS
n41	30	20	DFT-256QAM	M	Outer_Full	21.95	PC3	PASS
n41	30	20	DFT-256QAM	M	Inner_Full	21.98	PC3	PASS
n41	30	20	CP-QPSK	M	Edge_1RB_Left	21.75	PC3	PASS
n41	30	20	CP-QPSK	M	Edge_1RB_Right	21.35	PC3	PASS
n41	30	20	CP-QPSK	M	Outer_Full	21.43	PC3	PASS
n41	30	20	CP-QPSK	M	Inner_Full	21.53	PC3	PASS
n41	30	20	CP-16QAM	M	Edge_1RB_Left	21.25	PC3	PASS
n41	30	20	CP-16QAM	M	Edge_1RB_Right	21.35	PC3	PASS
n41	30	20	CP-16QAM	M	Outer_Full	21.49	PC3	PASS
n41	30	20	CP-16QAM	M	Inner_Full	21.38	PC3	PASS
n41	30	20	CP-64QAM	M	Edge_1RB_Left	21.46	PC3	PASS
n41	30	20	CP-64QAM	M	Edge_1RB_Right	21.96	PC3	PASS
n41	30	20	CP-64QAM	M	Outer_Full	21.42	PC3	PASS
n41	30	20	CP-64QAM	M	Inner_Full	21.35	PC3	PASS
n41	30	20	CP-256QAM	M	Edge_1RB_Left	21.58	PC3	PASS
n41	30	20	CP-256QAM	M	Edge_1RB_Right	21.99	PC3	PASS
n41	30	20	CP-256QAM	M	Outer_Full	22.16	PC3	PASS
n41	30	20	CP-256QAM	M	Inner_Full	22.35	PC3	PASS



n41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	21.69	PC3	PASS
n41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	21.98	PC3	PASS
n41	30	20	DFT-PI2BPSK	H	Outer_Full	21.46	PC3	PASS
n41	30	20	DFT-PI2BPSK	H	Inner_Full	21.93	PC3	PASS
n41	30	20	DFT-QPSK	H	Edge_1RB_Left	21.82	PC3	PASS
n41	30	20	DFT-QPSK	H	Edge_1RB_Right	21.73	PC3	PASS
n41	30	20	DFT-QPSK	H	Outer_Full	21.62	PC3	PASS
n41	30	20	DFT-QPSK	H	Inner_Full	21.52	PC3	PASS
n41	30	20	DFT-16QAM	H	Edge_1RB_Left	21.34	PC3	PASS
n41	30	20	DFT-16QAM	H	Edge_1RB_Right	21.46	PC3	PASS
n41	30	20	DFT-16QAM	H	Outer_Full	21.52	PC3	PASS
n41	30	20	DFT-16QAM	H	Inner_Full	21.36	PC3	PASS
n41	30	20	DFT-64QAM	H	Edge_1RB_Left	21.57	PC3	PASS
n41	30	20	DFT-64QAM	H	Edge_1RB_Right	21.56	PC3	PASS
n41	30	20	DFT-64QAM	H	Outer_Full	21.71	PC3	PASS
n41	30	20	DFT-64QAM	H	Inner_Full	21.35	PC3	PASS
n41	30	20	DFT-256QAM	H	Edge_1RB_Left	21.79	PC3	PASS
n41	30	20	DFT-256QAM	H	Edge_1RB_Right	21.53	PC3	PASS
n41	30	20	DFT-256QAM	H	Outer_Full	21.39	PC3	PASS
n41	30	20	DFT-256QAM	H	Inner_Full	21.79	PC3	PASS
n41	30	20	CP-QPSK	H	Edge_1RB_Left	21.52	PC3	PASS
n41	30	20	CP-QPSK	H	Edge_1RB_Right	21.36	PC3	PASS
n41	30	20	CP-QPSK	H	Outer_Full	21.79	PC3	PASS
n41	30	20	CP-QPSK	H	Inner_Full	21.53	PC3	PASS
n41	30	20	CP-16QAM	H	Edge_1RB_Left	21.25	PC3	PASS
n41	30	20	CP-16QAM	H	Edge_1RB_Right	21.79	PC3	PASS
n41	30	20	CP-16QAM	H	Outer_Full	21.52	PC3	PASS
n41	30	20	CP-16QAM	H	Inner_Full	21.39	PC3	PASS
n41	30	20	CP-64QAM	H	Edge_1RB_Left	20.96	PC3	PASS
n41	30	20	CP-64QAM	H	Edge_1RB_Right	20.74	PC3	PASS
n41	30	20	CP-64QAM	H	Outer_Full	20.65	PC3	PASS
n41	30	20	CP-64QAM	H	Inner_Full	20.39	PC3	PASS
n41	30	20	CP-256QAM	H	Edge_1RB_Left	20.79	PC3	PASS
n41	30	20	CP-256QAM	H	Edge_1RB_Right	20.78	PC3	PASS
n41	30	20	CP-256QAM	H	Outer_Full	20.98	PC3	PASS
n41	30	20	CP-256QAM	H	Inner_Full	20.93	PC3	PASS
n41	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	20.87	PC3	PASS
n41	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	20.72	PC3	PASS
n41	30	30	DFT-PI2BPSK	L	Outer_Full	20.65	PC3	PASS
n41	30	30	DFT-PI2BPSK	L	Inner_Full	20.46	PC3	PASS
n41	30	30	DFT-QPSK	L	Edge_1RB_Left	20.58	PC3	PASS
n41	30	30	DFT-QPSK	L	Edge_1RB_Right	20.69	PC3	PASS

n41	30	30	DFT-QPSK	L	Outer_Full	20.71	PC3	PASS
n41	30	30	DFT-QPSK	L	Inner_Full	20.65	PC3	PASS
n41	30	30	DFT-16QAM	L	Edge_1RB_Left	20.35	PC3	PASS
n41	30	30	DFT-16QAM	L	Edge_1RB_Right	20.47	PC3	PASS
n41	30	30	DFT-16QAM	L	Outer_Full	20.65	PC3	PASS
n41	30	30	DFT-16QAM	L	Inner_Full	20.47	PC3	PASS
n41	30	30	DFT-64QAM	L	Edge_1RB_Left	20.65	PC3	PASS
n41	30	30	DFT-64QAM	L	Edge_1RB_Right	21.36	PC3	PASS
n41	30	30	DFT-64QAM	L	Outer_Full	21.78	PC3	PASS
n41	30	30	DFT-64QAM	L	Inner_Full	21.52	PC3	PASS
n41	30	30	DFT-256QAM	L	Edge_1RB_Left	21.69	PC3	PASS
n41	30	30	DFT-256QAM	L	Edge_1RB_Right	21.71	PC3	PASS
n41	30	30	DFT-256QAM	L	Outer_Full	21.35	PC3	PASS
n41	30	30	DFT-256QAM	L	Inner_Full	21.47	PC3	PASS
n41	30	30	CP-QPSK	L	Edge_1RB_Left	21.96	PC3	PASS
n41	30	30	CP-QPSK	L	Edge_1RB_Right	21.74	PC3	PASS
n41	30	30	CP-QPSK	L	Outer_Full	21.87	PC3	PASS
n41	30	30	CP-QPSK	L	Inner_Full	21.41	PC3	PASS
n41	30	30	CP-16QAM	L	Edge_1RB_Left	21.05	PC3	PASS
n41	30	30	CP-16QAM	L	Edge_1RB_Right	21.65	PC3	PASS
n41	30	30	CP-16QAM	L	Outer_Full	21.75	PC3	PASS
n41	30	30	CP-16QAM	L	Inner_Full	22.27	PC3	PASS
n41	30	30	CP-64QAM	L	Edge_1RB_Left	22.35	PC3	PASS
n41	30	30	CP-64QAM	L	Edge_1RB_Right	21.54	PC3	PASS
n41	30	30	CP-64QAM	L	Outer_Full	22.69	PC3	PASS
n41	30	30	CP-64QAM	L	Inner_Full	22.47	PC3	PASS
n41	30	30	CP-256QAM	L	Edge_1RB_Left	22.42	PC3	PASS
n41	30	30	CP-256QAM	L	Edge_1RB_Right	22.36	PC3	PASS
n41	30	30	CP-256QAM	L	Outer_Full	22.47	PC3	PASS
n41	30	30	CP-256QAM	L	Inner_Full	22.13	PC3	PASS
n41	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	22.25	PC3	PASS
n41	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	22.85	PC3	PASS
n41	30	30	DFT-PI2BPSK	M	Outer_Full	22.43	PC3	PASS
n41	30	30	DFT-PI2BPSK	M	Inner_Full	22.32	PC3	PASS
n41	30	30	DFT-QPSK	M	Edge_1RB_Left	22.47	PC3	PASS
n41	30	30	DFT-QPSK	M	Edge_1RB_Right	22.69	PC3	PASS
n41	30	30	DFT-QPSK	M	Outer_Full	22.72	PC3	PASS
n41	30	30	DFT-QPSK	M	Inner_Full	22.02	PC3	PASS
n41	30	30	DFT-16QAM	M	Edge_1RB_Left	22.32	PC3	PASS
n41	30	30	DFT-16QAM	M	Edge_1RB_Right	22.41	PC3	PASS
n41	30	30	DFT-16QAM	M	Outer_Full	22.25	PC3	PASS
n41	30	30	DFT-16QAM	M	Inner_Full	22.35	PC3	PASS

n41	30	30	DFT-64QAM	M	Edge_1RB_Left	22.46	PC3	PASS
n41	30	30	DFT-64QAM	M	Edge_1RB_Right	22.28	PC3	PASS
n41	30	30	DFT-64QAM	M	Outer_Full	22.45	PC3	PASS
n41	30	30	DFT-64QAM	M	Inner_Full	22.96	PC3	PASS
n41	30	30	DFT-256QAM	M	Edge_1RB_Left	21.74	PC3	PASS
n41	30	30	DFT-256QAM	M	Edge_1RB_Right	21.35	PC3	PASS
n41	30	30	DFT-256QAM	M	Outer_Full	21.46	PC3	PASS
n41	30	30	DFT-256QAM	M	Inner_Full	21.52	PC3	PASS
n41	30	30	CP-QPSK	M	Edge_1RB_Left	21.36	PC3	PASS
n41	30	30	CP-QPSK	M	Edge_1RB_Right	21.47	PC3	PASS
n41	30	30	CP-QPSK	M	Outer_Full	21.32	PC3	PASS
n41	30	30	CP-QPSK	M	Inner_Full	21.46	PC3	PASS
n41	30	30	CP-16QAM	M	Edge_1RB_Left	21.25	PC3	PASS
n41	30	30	CP-16QAM	M	Edge_1RB_Right	21.28	PC3	PASS
n41	30	30	CP-16QAM	M	Outer_Full	21.41	PC3	PASS
n41	30	30	CP-16QAM	M	Inner_Full	21.02	PC3	PASS
n41	30	30	CP-64QAM	M	Edge_1RB_Left	21.57	PC3	PASS
n41	30	30	CP-64QAM	M	Edge_1RB_Right	21.42	PC3	PASS
n41	30	30	CP-64QAM	M	Outer_Full	21.02	PC3	PASS
n41	30	30	CP-64QAM	M	Inner_Full	21.35	PC3	PASS
n41	30	30	CP-256QAM	M	Edge_1RB_Left	21.49	PC3	PASS
n41	30	30	CP-256QAM	M	Edge_1RB_Right	21.42	PC3	PASS
n41	30	30	CP-256QAM	M	Outer_Full	21.02	PC3	PASS
n41	30	30	CP-256QAM	M	Inner_Full	21.45	PC3	PASS
n41	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	22.39	PC3	PASS
n41	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	22.87	PC3	PASS
n41	30	30	DFT-PI2BPSK	H	Outer_Full	22.21	PC3	PASS
n41	30	30	DFT-PI2BPSK	H	Inner_Full	22.42	PC3	PASS
n41	30	30	DFT-QPSK	H	Edge_1RB_Left	22.31	PC3	PASS
n41	30	30	DFT-QPSK	H	Edge_1RB_Right	22.17	PC3	PASS
n41	30	30	DFT-QPSK	H	Outer_Full	22.52	PC3	PASS
n41	30	30	DFT-QPSK	H	Inner_Full	22.35	PC3	PASS
n41	30	30	DFT-16QAM	H	Edge_1RB_Left	22.46	PC3	PASS
n41	30	30	DFT-16QAM	H	Edge_1RB_Right	22.17	PC3	PASS
n41	30	30	DFT-16QAM	H	Outer_Full	22.58	PC3	PASS
n41	30	30	DFT-16QAM	H	Inner_Full	21.54	PC3	PASS
n41	30	30	DFT-64QAM	H	Edge_1RB_Left	22.36	PC3	PASS
n41	30	30	DFT-64QAM	H	Edge_1RB_Right	23.64	PC3	PASS
n41	30	30	DFT-64QAM	H	Outer_Full	22.58	PC3	PASS
n41	30	30	DFT-64QAM	H	Inner_Full	22.47	PC3	PASS
n41	30	30	DFT-256QAM	H	Edge_1RB_Left	21.72	PC3	PASS
n41	30	30	DFT-256QAM	H	Edge_1RB_Right	21.39	PC3	PASS

n41	30	30	DFT-256QAM	H	Outer_Full	21.56	PC3	PASS
n41	30	30	DFT-256QAM	H	Inner_Full	21.25	PC3	PASS
n41	30	30	CP-QPSK	H	Edge_1RB_Left	22.35	PC3	PASS
n41	30	30	CP-QPSK	H	Edge_1RB_Right	21.47	PC3	PASS
n41	30	30	CP-QPSK	H	Outer_Full	21.63	PC3	PASS
n41	30	30	CP-QPSK	H	Inner_Full	21.58	PC3	PASS
n41	30	30	CP-16QAM	H	Edge_1RB_Left	21.46	PC3	PASS
n41	30	30	CP-16QAM	H	Edge_1RB_Right	21.87	PC3	PASS
n41	30	30	CP-16QAM	H	Outer_Full	22.69	PC3	PASS
n41	30	30	CP-16QAM	H	Inner_Full	22.41	PC3	PASS
n41	30	30	CP-64QAM	H	Edge_1RB_Left	22.02	PC3	PASS
n41	30	30	CP-64QAM	H	Edge_1RB_Right	22.57	PC3	PASS
n41	30	30	CP-64QAM	H	Outer_Full	22.31	PC3	PASS
n41	30	30	CP-64QAM	H	Inner_Full	22.59	PC3	PASS
n41	30	30	CP-256QAM	H	Edge_1RB_Left	22.41	PC3	PASS
n41	30	30	CP-256QAM	H	Edge_1RB_Right	22.58	PC3	PASS
n41	30	30	CP-256QAM	H	Outer_Full	22.35	PC3	PASS
n41	30	30	CP-256QAM	H	Inner_Full	22.45	PC3	PASS
n41	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	22.32	PC3	PASS
n41	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	22.47	PC3	PASS
n41	30	40	DFT-PI2BPSK	L	Outer_Full	22.13	PC3	PASS
n41	30	40	DFT-PI2BPSK	L	Inner_Full	22.28	PC3	PASS
n41	30	40	DFT-QPSK	L	Edge_1RB_Left	22.78	PC3	PASS
n41	30	40	DFT-QPSK	L	Edge_1RB_Right	22.35	PC3	PASS
n41	30	40	DFT-QPSK	L	Outer_Full	22.42	PC3	PASS
n41	30	40	DFT-QPSK	L	Inner_Full	22.35	PC3	PASS
n41	30	40	DFT-16QAM	L	Edge_1RB_Left	22.46	PC3	PASS
n41	30	40	DFT-16QAM	L	Edge_1RB_Right	22.31	PC3	PASS
n41	30	40	DFT-16QAM	L	Outer_Full	22.42	PC3	PASS
n41	30	40	DFT-16QAM	L	Inner_Full	22.28	PC3	PASS
n41	30	40	DFT-64QAM	L	Edge_1RB_Left	22.19	PC3	PASS
n41	30	40	DFT-64QAM	L	Edge_1RB_Right	21.96	PC3	PASS
n41	30	40	DFT-64QAM	L	Outer_Full	21.85	PC3	PASS
n41	30	40	DFT-64QAM	L	Inner_Full	21.72	PC3	PASS
n41	30	40	DFT-256QAM	L	Edge_1RB_Left	21.62	PC3	PASS
n41	30	40	DFT-256QAM	L	Edge_1RB_Right	21.35	PC3	PASS
n41	30	40	DFT-256QAM	L	Outer_Full	21.42	PC3	PASS
n41	30	40	DFT-256QAM	L	Inner_Full	21.25	PC3	PASS
n41	30	40	CP-QPSK	L	Edge_1RB_Left	21.49	PC3	PASS
n41	30	40	CP-QPSK	L	Edge_1RB_Right	21.28	PC3	PASS
n41	30	40	CP-QPSK	L	Outer_Full	22.41	PC3	PASS
n41	30	40	CP-QPSK	L	Inner_Full	22.35	PC3	PASS

n41	30	40	CP-16QAM	L	Edge_1RB_Left	22.17	PC3	PASS
n41	30	40	CP-16QAM	L	Edge_1RB_Right	21.95	PC3	PASS
n41	30	40	CP-16QAM	L	Outer_Full	22.32	PC3	PASS
n41	30	40	CP-16QAM	L	Inner_Full	22.41	PC3	PASS
n41	30	40	CP-64QAM	L	Edge_1RB_Left	22.47	PC3	PASS
n41	30	40	CP-64QAM	L	Edge_1RB_Right	21.65	PC3	PASS
n41	30	40	CP-64QAM	L	Outer_Full	21.87	PC3	PASS
n41	30	40	CP-64QAM	L	Inner_Full	21.35	PC3	PASS
n41	30	40	CP-256QAM	L	Edge_1RB_Left	21.79	PC3	PASS
n41	30	40	CP-256QAM	L	Edge_1RB_Right	22.05	PC3	PASS
n41	30	40	CP-256QAM	L	Outer_Full	22.32	PC3	PASS
n41	30	40	CP-256QAM	L	Inner_Full	22.42	PC3	PASS
n41	30	40	DFT-PI2BPSK	M	Edge_1RB_Left	22.13	PC3	PASS
n41	30	40	DFT-PI2BPSK	M	Edge_1RB_Right	22.02	PC3	PASS
n41	30	40	DFT-PI2BPSK	M	Outer_Full	21.63	PC3	PASS
n41	30	40	DFT-PI2BPSK	M	Inner_Full	21.78	PC3	PASS
n41	30	40	DFT-QPSK	M	Edge_1RB_Left	21.85	PC3	PASS
n41	30	40	DFT-QPSK	M	Edge_1RB_Right	21.65	PC3	PASS
n41	30	40	DFT-QPSK	M	Outer_Full	21.32	PC3	PASS
n41	30	40	DFT-QPSK	M	Inner_Full	21.42	PC3	PASS
n41	30	40	DFT-16QAM	M	Edge_1RB_Left	21.35	PC3	PASS
n41	30	40	DFT-16QAM	M	Edge_1RB_Right	21.25	PC3	PASS
n41	30	40	DFT-16QAM	M	Outer_Full	21.37	PC3	PASS
n41	30	40	DFT-16QAM	M	Inner_Full	21.24	PC3	PASS
n41	30	40	DFT-64QAM	M	Edge_1RB_Left	21.68	PC3	PASS
n41	30	40	DFT-64QAM	M	Edge_1RB_Right	21.46	PC3	PASS
n41	30	40	DFT-64QAM	M	Outer_Full	21.02	PC3	PASS
n41	30	40	DFT-64QAM	M	Inner_Full	21.57	PC3	PASS
n41	30	40	DFT-256QAM	M	Edge_1RB_Left	21.76	PC3	PASS
n41	30	40	DFT-256QAM	M	Edge_1RB_Right	21.02	PC3	PASS
n41	30	40	DFT-256QAM	M	Outer_Full	22.39	PC3	PASS
n41	30	40	DFT-256QAM	M	Inner_Full	22.45	PC3	PASS
n41	30	40	CP-QPSK	M	Edge_1RB_Left	22.32	PC3	PASS
n41	30	40	CP-QPSK	M	Edge_1RB_Right	22.24	PC3	PASS
n41	30	40	CP-QPSK	M	Outer_Full	22.39	PC3	PASS
n41	30	40	CP-QPSK	M	Inner_Full	22.38	PC3	PASS
n41	30	40	CP-16QAM	M	Edge_1RB_Left	22.94	PC3	PASS
n41	30	40	CP-16QAM	M	Edge_1RB_Right	22.87	PC3	PASS
n41	30	40	CP-16QAM	M	Outer_Full	22.65	PC3	PASS
n41	30	40	CP-16QAM	M	Inner_Full	22.65	PC3	PASS
n41	30	40	CP-64QAM	M	Edge_1RB_Left	22.47	PC3	PASS
n41	30	40	CP-64QAM	M	Edge_1RB_Right	22.65	PC3	PASS

n41	30	40	CP-64QAM	M	Outer_Full	22.35	PC3	PASS
n41	30	40	CP-64QAM	M	Inner_Full	22.18	PC3	PASS
n41	30	40	CP-256QAM	M	Edge_1RB_Left	22.28	PC3	PASS
n41	30	40	CP-256QAM	M	Edge_1RB_Right	22.42	PC3	PASS
n41	30	40	CP-256QAM	M	Outer_Full	22.32	PC3	PASS
n41	30	40	CP-256QAM	M	Inner_Full	22.17	PC3	PASS
n41	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	22.23	PC3	PASS
n41	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	22.28	PC3	PASS
n41	30	40	DFT-PI2BPSK	H	Outer_Full	22.49	PC3	PASS
n41	30	40	DFT-PI2BPSK	H	Inner_Full	22.38	PC3	PASS
n41	30	40	DFT-QPSK	H	Edge_1RB_Left	22.14	PC3	PASS
n41	30	40	DFT-QPSK	H	Edge_1RB_Right	21.93	PC3	PASS
n41	30	40	DFT-QPSK	H	Outer_Full	21.54	PC3	PASS
n41	30	40	DFT-QPSK	H	Inner_Full	21.43	PC3	PASS
n41	30	40	DFT-16QAM	H	Edge_1RB_Left	21.28	PC3	PASS
n41	30	40	DFT-16QAM	H	Edge_1RB_Right	21.35	PC3	PASS
n41	30	40	DFT-16QAM	H	Outer_Full	21.62	PC3	PASS
n41	30	40	DFT-16QAM	H	Inner_Full	21.37	PC3	PASS
n41	30	40	DFT-64QAM	H	Edge_1RB_Left	21.74	PC3	PASS
n41	30	40	DFT-64QAM	H	Edge_1RB_Right	21.57	PC3	PASS
n41	30	40	DFT-64QAM	H	Outer_Full	21.65	PC3	PASS
n41	30	40	DFT-64QAM	H	Inner_Full	21.71	PC3	PASS
n41	30	40	DFT-256QAM	H	Edge_1RB_Left	21.22	PC3	PASS
n41	30	40	DFT-256QAM	H	Edge_1RB_Right	21.28	PC3	PASS
n41	30	40	DFT-256QAM	H	Outer_Full	21.58	PC3	PASS
n41	30	40	DFT-256QAM	H	Inner_Full	21.79	PC3	PASS
n41	30	40	CP-QPSK	H	Edge_1RB_Left	21.25	PC3	PASS
n41	30	40	CP-QPSK	H	Edge_1RB_Right	21.16	PC3	PASS
n41	30	40	CP-QPSK	H	Outer_Full	21.58	PC3	PASS
n41	30	40	CP-QPSK	H	Inner_Full	21.78	PC3	PASS
n41	30	40	CP-16QAM	H	Edge_1RB_Left	21.35	PC3	PASS
n41	30	40	CP-16QAM	H	Edge_1RB_Right	21.52	PC3	PASS
n41	30	40	CP-16QAM	H	Outer_Full	21.35	PC3	PASS
n41	30	40	CP-16QAM	H	Inner_Full	21.49	PC3	PASS
n41	30	40	CP-64QAM	H	Edge_1RB_Left	21.23	PC3	PASS
n41	30	40	CP-64QAM	H	Edge_1RB_Right	21.46	PC3	PASS
n41	30	40	CP-64QAM	H	Outer_Full	21.83	PC3	PASS
n41	30	40	CP-64QAM	H	Inner_Full	21.53	PC3	PASS
n41	30	40	CP-256QAM	H	Edge_1RB_Left	21.36	PC3	PASS
n41	30	40	CP-256QAM	H	Edge_1RB_Right	21.72	PC3	PASS
n41	30	40	CP-256QAM	H	Outer_Full	21.54	PC3	PASS
n41	30	40	CP-256QAM	H	Inner_Full	21.35	PC3	PASS

n41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	21.79	PC3	PASS
n41	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	21.56	PC3	PASS
n41	30	50	DFT-PI2BPSK	L	Outer_Full	21.32	PC3	PASS
n41	30	50	DFT-PI2BPSK	L	Inner_Full	21.47	PC3	PASS
n41	30	50	DFT-QPSK	L	Edge_1RB_Left	21.95	PC3	PASS
n41	30	50	DFT-QPSK	L	Edge_1RB_Right	21.35	PC3	PASS
n41	30	50	DFT-QPSK	L	Outer_Full	21.42	PC3	PASS
n41	30	50	DFT-QPSK	L	Inner_Full	21.52	PC3	PASS
n41	30	50	DFT-16QAM	L	Edge_1RB_Left	21.32	PC3	PASS
n41	30	50	DFT-16QAM	L	Edge_1RB_Right	21.47	PC3	PASS
n41	30	50	DFT-16QAM	L	Outer_Full	21.25	PC3	PASS
n41	30	50	DFT-16QAM	L	Inner_Full	21.69	PC3	PASS
n41	30	50	DFT-64QAM	L	Edge_1RB_Left	22.87	PC3	PASS
n41	30	50	DFT-64QAM	L	Edge_1RB_Right	22.42	PC3	PASS
n41	30	50	DFT-64QAM	L	Outer_Full	21.17	PC3	PASS
n41	30	50	DFT-64QAM	L	Inner_Full	21.87	PC3	PASS
n41	30	50	DFT-256QAM	L	Edge_1RB_Left	22.32	PC3	PASS
n41	30	50	DFT-256QAM	L	Edge_1RB_Right	22.42	PC3	PASS
n41	30	50	DFT-256QAM	L	Outer_Full	22.13	PC3	PASS
n41	30	50	DFT-256QAM	L	Inner_Full	22.20	PC3	PASS
n41	30	50	CP-QPSK	L	Edge_1RB_Left	21.68	PC3	PASS
n41	30	50	CP-QPSK	L	Edge_1RB_Right	21.72	PC3	PASS
n41	30	50	CP-QPSK	L	Outer_Full	21.62	PC3	PASS
n41	30	50	CP-QPSK	L	Inner_Full	21.02	PC3	PASS
n41	30	50	CP-16QAM	L	Edge_1RB_Left	22.05	PC3	PASS
n41	30	50	CP-16QAM	L	Edge_1RB_Right	22.35	PC3	PASS
n41	30	50	CP-16QAM	L	Outer_Full	22.42	PC3	PASS
n41	30	50	CP-16QAM	L	Inner_Full	22.39	PC3	PASS
n41	30	50	CP-64QAM	L	Edge_1RB_Left	22.47	PC3	PASS
n41	30	50	CP-64QAM	L	Edge_1RB_Right	22.25	PC3	PASS
n41	30	50	CP-64QAM	L	Outer_Full	22.39	PC3	PASS
n41	30	50	CP-64QAM	L	Inner_Full	22.79	PC3	PASS
n41	30	50	CP-256QAM	L	Edge_1RB_Left	22.65	PC3	PASS
n41	30	50	CP-256QAM	L	Edge_1RB_Right	22.42	PC3	PASS
n41	30	50	CP-256QAM	L	Outer_Full	22.35	PC3	PASS
n41	30	50	CP-256QAM	L	Inner_Full	22.79	PC3	PASS
n41	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	22.32	PC3	PASS
n41	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	22.45	PC3	PASS
n41	30	50	DFT-PI2BPSK	M	Outer_Full	22.02	PC3	PASS
n41	30	50	DFT-PI2BPSK	M	Inner_Full	22.38	PC3	PASS
n41	30	50	DFT-QPSK	M	Edge_1RB_Left	22.79	PC3	PASS
n41	30	50	DFT-QPSK	M	Edge_1RB_Right	22.32	PC3	PASS

n41	30	50	DFT-QPSK	M	Outer_Full	22.47	PC3	PASS
n41	30	50	DFT-QPSK	M	Inner_Full	22.25	PC3	PASS
n41	30	50	DFT-16QAM	M	Edge_1RB_Left	21.93	PC3	PASS
n41	30	50	DFT-16QAM	M	Edge_1RB_Right	21.72	PC3	PASS
n41	30	50	DFT-16QAM	M	Outer_Full	21.52	PC3	PASS
n41	30	50	DFT-16QAM	M	Inner_Full	21.36	PC3	PASS
n41	30	50	DFT-64QAM	M	Edge_1RB_Left	21.46	PC3	PASS
n41	30	50	DFT-64QAM	M	Edge_1RB_Right	21.58	PC3	PASS
n41	30	50	DFT-64QAM	M	Outer_Full	21.77	PC3	PASS
n41	30	50	DFT-64QAM	M	Inner_Full	21.62	PC3	PASS
n41	30	50	DFT-256QAM	M	Edge_1RB_Left	21.75	PC3	PASS
n41	30	50	DFT-256QAM	M	Edge_1RB_Right	21.36	PC3	PASS
n41	30	50	DFT-256QAM	M	Outer_Full	21.58	PC3	PASS
n41	30	50	DFT-256QAM	M	Inner_Full	21.69	PC3	PASS
n41	30	50	CP-QPSK	M	Edge_1RB_Left	21.41	PC3	PASS
n41	30	50	CP-QPSK	M	Edge_1RB_Right	21.22	PC3	PASS
n41	30	50	CP-QPSK	M	Outer_Full	21.65	PC3	PASS
n41	30	50	CP-QPSK	M	Inner_Full	21.79	PC3	PASS
n41	30	50	CP-16QAM	M	Edge_1RB_Left	21.35	PC3	PASS
n41	30	50	CP-16QAM	M	Edge_1RB_Right	21.02	PC3	PASS
n41	30	50	CP-16QAM	M	Outer_Full	21.24	PC3	PASS
n41	30	50	CP-16QAM	M	Inner_Full	21.89	PC3	PASS
n41	30	50	CP-64QAM	M	Edge_1RB_Left	22.36	PC3	PASS
n41	30	50	CP-64QAM	M	Edge_1RB_Right	22.14	PC3	PASS
n41	30	50	CP-64QAM	M	Outer_Full	22.02	PC3	PASS
n41	30	50	CP-64QAM	M	Inner_Full	22.28	PC3	PASS
n41	30	50	CP-256QAM	M	Edge_1RB_Left	22.38	PC3	PASS
n41	30	50	CP-256QAM	M	Edge_1RB_Right	22.14	PC3	PASS
n41	30	50	CP-256QAM	M	Outer_Full	22.28	PC3	PASS
n41	30	50	CP-256QAM	M	Inner_Full	21.57	PC3	PASS
n41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	21.36	PC3	PASS
n41	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	21.82	PC3	PASS
n41	30	50	DFT-PI2BPSK	H	Outer_Full	21.36	PC3	PASS
n41	30	50	DFT-PI2BPSK	H	Inner_Full	21.74	PC3	PASS
n41	30	50	DFT-QPSK	H	Edge_1RB_Left	21.25	PC3	PASS
n41	30	50	DFT-QPSK	H	Edge_1RB_Right	21.35	PC3	PASS
n41	30	50	DFT-QPSK	H	Outer_Full	21.47	PC3	PASS
n41	30	50	DFT-QPSK	H	Inner_Full	21.35	PC3	PASS
n41	30	50	DFT-16QAM	H	Edge_1RB_Left	21.69	PC3	PASS
n41	30	50	DFT-16QAM	H	Edge_1RB_Right	21.42	PC3	PASS
n41	30	50	DFT-16QAM	H	Outer_Full	20.89	PC3	PASS
n41	30	50	DFT-16QAM	H	Inner_Full	20.36	PC3	PASS



n41	30	50	DFT-64QAM	H	Edge_1RB_Left	20.87	PC3	PASS
n41	30	50	DFT-64QAM	H	Edge_1RB_Right	20.82	PC3	PASS
n41	30	50	DFT-64QAM	H	Outer_Full	20.99	PC3	PASS
n41	30	50	DFT-64QAM	H	Inner_Full	21.04	PC3	PASS
n41	30	50	DFT-256QAM	H	Edge_1RB_Left	21.42	PC3	PASS
n41	30	50	DFT-256QAM	H	Edge_1RB_Right	21.02	PC3	PASS
n41	30	50	DFT-256QAM	H	Outer_Full	21.35	PC3	PASS
n41	30	50	DFT-256QAM	H	Inner_Full	21.41	PC3	PASS
n41	30	50	CP-QPSK	H	Edge_1RB_Left	21.05	PC3	PASS
n41	30	50	CP-QPSK	H	Edge_1RB_Right	21.35	PC3	PASS
n41	30	50	CP-QPSK	H	Outer_Full	21.57	PC3	PASS
n41	30	50	CP-QPSK	H	Inner_Full	21.46	PC3	PASS
n41	30	50	CP-16QAM	H	Edge_1RB_Left	21.25	PC3	PASS
n41	30	50	CP-16QAM	H	Edge_1RB_Right	21.39	PC3	PASS
n41	30	50	CP-16QAM	H	Outer_Full	21.38	PC3	PASS
n41	30	50	CP-16QAM	H	Inner_Full	21.46	PC3	PASS
n41	30	50	CP-64QAM	H	Edge_1RB_Left	21.02	PC3	PASS
n41	30	50	CP-64QAM	H	Edge_1RB_Right	21.28	PC3	PASS
n41	30	50	CP-64QAM	H	Outer_Full	21.32	PC3	PASS
n41	30	50	CP-64QAM	H	Inner_Full	21.41	PC3	PASS
n41	30	50	CP-256QAM	H	Edge_1RB_Left	21.02	PC3	PASS
n41	30	50	CP-256QAM	H	Edge_1RB_Right	21.28	PC3	PASS
n41	30	50	CP-256QAM	H	Outer_Full	22.35	PC3	PASS
n41	30	50	CP-256QAM	H	Inner_Full	22.74	PC3	PASS
n41	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	22.35	PC3	PASS
n41	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	22.69	PC3	PASS
n41	30	60	DFT-PI2BPSK	L	Outer_Full	22.41	PC3	PASS
n41	30	60	DFT-PI2BPSK	L	Inner_Full	22.35	PC3	PASS
n41	30	60	DFT-QPSK	L	Edge_1RB_Left	22.28	PC3	PASS
n41	30	60	DFT-QPSK	L	Edge_1RB_Right	22.49	PC3	PASS
n41	30	60	DFT-QPSK	L	Outer_Full	22.32	PC3	PASS
n41	30	60	DFT-QPSK	L	Inner_Full	22.17	PC3	PASS
n41	30	60	DFT-16QAM	L	Edge_1RB_Left	22.19	PC3	PASS
n41	30	60	DFT-16QAM	L	Edge_1RB_Right	21.36	PC3	PASS
n41	30	60	DFT-16QAM	L	Outer_Full	21.87	PC3	PASS
n41	30	60	DFT-16QAM	L	Inner_Full	21.83	PC3	PASS
n41	30	60	DFT-64QAM	L	Edge_1RB_Left	21.35	PC3	PASS
n41	30	60	DFT-64QAM	L	Edge_1RB_Right	21.47	PC3	PASS
n41	30	60	DFT-64QAM	L	Outer_Full	21.65	PC3	PASS
n41	30	60	DFT-64QAM	L	Inner_Full	21.27	PC3	PASS
n41	30	60	DFT-256QAM	L	Edge_1RB_Left	22.19	PC3	PASS
n41	30	60	DFT-256QAM	L	Edge_1RB_Right	22.42	PC3	PASS

n41	30	60	DFT-256QAM	L	Outer_Full	22.39	PC3	PASS
n41	30	60	DFT-256QAM	L	Inner_Full	22.71	PC3	PASS
n41	30	60	CP-QPSK	L	Edge_1RB_Left	22.35	PC3	PASS
n41	30	60	CP-QPSK	L	Edge_1RB_Right	22.49	PC3	PASS
n41	30	60	CP-QPSK	L	Outer_Full	22.35	PC3	PASS
n41	30	60	CP-QPSK	L	Inner_Full	21.75	PC3	PASS
n41	30	60	CP-16QAM	L	Edge_1RB_Left	21.54	PC3	PASS
n41	30	60	CP-16QAM	L	Edge_1RB_Right	21.98	PC3	PASS
n41	30	60	CP-16QAM	L	Outer_Full	21.36	PC3	PASS
n41	30	60	CP-16QAM	L	Inner_Full	21.74	PC3	PASS
n41	30	60	CP-64QAM	L	Edge_1RB_Left	21.62	PC3	PASS
n41	30	60	CP-64QAM	L	Edge_1RB_Right	21.42	PC3	PASS
n41	30	60	CP-64QAM	L	Outer_Full	21.28	PC3	PASS
n41	30	60	CP-64QAM	L	Inner_Full	21.74	PC3	PASS
n41	30	60	CP-256QAM	L	Edge_1RB_Left	21.35	PC3	PASS
n41	30	60	CP-256QAM	L	Edge_1RB_Right	21.78	PC3	PASS
n41	30	60	CP-256QAM	L	Outer_Full	21.35	PC3	PASS
n41	30	60	CP-256QAM	L	Inner_Full	21.71	PC3	PASS
n41	30	60	DFT-PI2BPSK	M	Edge_1RB_Left	21.62	PC3	PASS
n41	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	21.52	PC3	PASS
n41	30	60	DFT-PI2BPSK	M	Outer_Full	21.36	PC3	PASS
n41	30	60	DFT-PI2BPSK	M	Inner_Full	21.75	PC3	PASS
n41	30	60	DFT-QPSK	M	Edge_1RB_Left	21.32	PC3	PASS
n41	30	60	DFT-QPSK	M	Edge_1RB_Right	21.56	PC3	PASS
n41	30	60	DFT-QPSK	M	Outer_Full	21.75	PC3	PASS
n41	30	60	DFT-QPSK	M	Inner_Full	21.35	PC3	PASS
n41	30	60	DFT-16QAM	M	Edge_1RB_Left	21.49	PC3	PASS
n41	30	60	DFT-16QAM	M	Edge_1RB_Right	21.38	PC3	PASS
n41	30	60	DFT-16QAM	M	Outer_Full	21.42	PC3	PASS
n41	30	60	DFT-16QAM	M	Inner_Full	21.35	PC3	PASS
n41	30	60	DFT-64QAM	M	Edge_1RB_Left	21.71	PC3	PASS
n41	30	60	DFT-64QAM	M	Edge_1RB_Right	21.02	PC3	PASS
n41	30	60	DFT-64QAM	M	Outer_Full	21.02	PC3	PASS
n41	30	60	DFT-64QAM	M	Inner_Full	21.25	PC3	PASS
n41	30	60	DFT-256QAM	M	Edge_1RB_Left	21.36	PC3	PASS
n41	30	60	DFT-256QAM	M	Edge_1RB_Right	21.74	PC3	PASS
n41	30	60	DFT-256QAM	M	Outer_Full	21.24	PC3	PASS
n41	30	60	DFT-256QAM	M	Inner_Full	21.85	PC3	PASS
n41	30	60	CP-QPSK	M	Edge_1RB_Left	21.32	PC3	PASS
n41	30	60	CP-QPSK	M	Edge_1RB_Right	21.41	PC3	PASS
n41	30	60	CP-QPSK	M	Outer_Full	22.02	PC3	PASS
n41	30	60	CP-QPSK	M	Inner_Full	22.31	PC3	PASS

n41	30	60	CP-16QAM	M	Edge_1RB_Left	21.74	PC3	PASS
n41	30	60	CP-16QAM	M	Edge_1RB_Right	21.65	PC3	PASS
n41	30	60	CP-16QAM	M	Outer_Full	21.74	PC3	PASS
n41	30	60	CP-16QAM	M	Inner_Full	21.62	PC3	PASS
n41	30	60	CP-64QAM	M	Edge_1RB_Left	21.02	PC3	PASS
n41	30	60	CP-64QAM	M	Edge_1RB_Right	21.47	PC3	PASS
n41	30	60	CP-64QAM	M	Outer_Full	21.65	PC3	PASS
n41	30	60	CP-64QAM	M	Inner_Full	21.54	PC3	PASS
n41	30	60	CP-256QAM	M	Edge_1RB_Left	21.32	PC3	PASS
n41	30	60	CP-256QAM	M	Edge_1RB_Right	21.47	PC3	PASS
n41	30	60	CP-256QAM	M	Outer_Full	21.25	PC3	PASS
n41	30	60	CP-256QAM	M	Inner_Full	21.46	PC3	PASS
n41	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	21.28	PC3	PASS
n41	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	21.79	PC3	PASS
n41	30	60	DFT-PI2BPSK	H	Outer_Full	21.65	PC3	PASS
n41	30	60	DFT-PI2BPSK	H	Inner_Full	21.35	PC3	PASS
n41	30	60	DFT-QPSK	H	Edge_1RB_Left	21.45	PC3	PASS
n41	30	60	DFT-QPSK	H	Edge_1RB_Right	21.25	PC3	PASS
n41	30	60	DFT-QPSK	H	Outer_Full	21.16	PC3	PASS
n41	30	60	DFT-QPSK	H	Inner_Full	21.58	PC3	PASS
n41	30	60	DFT-16QAM	H	Edge_1RB_Left	21.35	PC3	PASS
n41	30	60	DFT-16QAM	H	Edge_1RB_Right	21.42	PC3	PASS
n41	30	60	DFT-16QAM	H	Outer_Full	21.35	PC3	PASS
n41	30	60	DFT-16QAM	H	Inner_Full	21.42	PC3	PASS
n41	30	60	DFT-64QAM	H	Edge_1RB_Left	21.02	PC3	PASS
n41	30	60	DFT-64QAM	H	Edge_1RB_Right	21.28	PC3	PASS
n41	30	60	DFT-64QAM	H	Outer_Full	21.49	PC3	PASS
n41	30	60	DFT-64QAM	H	Inner_Full	21.65	PC3	PASS
n41	30	60	DFT-256QAM	H	Edge_1RB_Left	21.08	PC3	PASS
n41	30	60	DFT-256QAM	H	Edge_1RB_Right	21.46	PC3	PASS
n41	30	60	DFT-256QAM	H	Outer_Full	21.02	PC3	PASS
n41	30	60	DFT-256QAM	H	Inner_Full	21.28	PC3	PASS
n41	30	60	CP-QPSK	H	Edge_1RB_Left	21.87	PC3	PASS
n41	30	60	CP-QPSK	H	Edge_1RB_Right	21.35	PC3	PASS
n41	30	60	CP-QPSK	H	Outer_Full	22.87	PC3	PASS
n41	30	60	CP-QPSK	H	Inner_Full	22.46	PC3	PASS
n41	30	60	CP-16QAM	H	Edge_1RB_Left	22.36	PC3	PASS
n41	30	60	CP-16QAM	H	Edge_1RB_Right	22.14	PC3	PASS
n41	30	60	CP-16QAM	H	Outer_Full	22.17	PC3	PASS
n41	30	60	CP-16QAM	H	Inner_Full	22.23	PC3	PASS
n41	30	60	CP-64QAM	H	Edge_1RB_Left	22.28	PC3	PASS
n41	30	60	CP-64QAM	H	Edge_1RB_Right	22.29	PC3	PASS

n41	30	60	CP-64QAM	H	Outer_Full	22.46	PC3	PASS
n41	30	60	CP-64QAM	H	Inner_Full	22.87	PC3	PASS
n41	30	60	CP-256QAM	H	Edge_1RB_Left	22.41	PC3	PASS
n41	30	60	CP-256QAM	H	Edge_1RB_Right	22.35	PC3	PASS
n41	30	60	CP-256QAM	H	Outer_Full	22.47	PC3	PASS
n41	30	60	CP-256QAM	H	Inner_Full	22.52	PC3	PASS
n41	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	22.35	PC3	PASS
n41	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	21.96	PC3	PASS
n41	30	80	DFT-PI2BPSK	L	Outer_Full	21.73	PC3	PASS
n41	30	80	DFT-PI2BPSK	L	Inner_Full	21.35	PC3	PASS
n41	30	80	DFT-QPSK	L	Edge_1RB_Left	21.02	PC3	PASS
n41	30	80	DFT-QPSK	L	Edge_1RB_Right	21.41	PC3	PASS
n41	30	80	DFT-QPSK	L	Outer_Full	21.02	PC3	PASS
n41	30	80	DFT-QPSK	L	Inner_Full	21.56	PC3	PASS
n41	30	80	DFT-16QAM	L	Edge_1RB_Left	21.57	PC3	PASS
n41	30	80	DFT-16QAM	L	Edge_1RB_Right	21.79	PC3	PASS
n41	30	80	DFT-16QAM	L	Outer_Full	21.35	PC3	PASS
n41	30	80	DFT-16QAM	L	Inner_Full	21.02	PC3	PASS
n41	30	80	DFT-64QAM	L	Edge_1RB_Left	21.05	PC3	PASS
n41	30	80	DFT-64QAM	L	Edge_1RB_Right	21.17	PC3	PASS
n41	30	80	DFT-64QAM	L	Outer_Full	21.05	PC3	PASS
n41	30	80	DFT-64QAM	L	Inner_Full	21.35	PC3	PASS
n41	30	80	DFT-256QAM	L	Edge_1RB_Left	22.27	PC3	PASS
n41	30	80	DFT-256QAM	L	Edge_1RB_Right	22.42	PC3	PASS
n41	30	80	DFT-256QAM	L	Outer_Full	22.32	PC3	PASS
n41	30	80	DFT-256QAM	L	Inner_Full	22.14	PC3	PASS
n41	30	80	CP-QPSK	L	Edge_1RB_Left	22.28	PC3	PASS
n41	30	80	CP-QPSK	L	Edge_1RB_Right	22.98	PC3	PASS
n41	30	80	CP-QPSK	L	Outer_Full	22.43	PC3	PASS
n41	30	80	CP-QPSK	L	Inner_Full	22.02	PC3	PASS
n41	30	80	CP-16QAM	L	Edge_1RB_Left	21.87	PC3	PASS
n41	30	80	CP-16QAM	L	Edge_1RB_Right	21.46	PC3	PASS
n41	30	80	CP-16QAM	L	Outer_Full	21.98	PC3	PASS
n41	30	80	CP-16QAM	L	Inner_Full	21.75	PC3	PASS
n41	30	80	CP-64QAM	L	Edge_1RB_Left	21.83	PC3	PASS
n41	30	80	CP-64QAM	L	Edge_1RB_Right	21.02	PC3	PASS
n41	30	80	CP-64QAM	L	Outer_Full	21.35	PC3	PASS
n41	30	80	CP-64QAM	L	Inner_Full	21.54	PC3	PASS
n41	30	80	CP-256QAM	L	Edge_1RB_Left	21.43	PC3	PASS
n41	30	80	CP-256QAM	L	Edge_1RB_Right	21.02	PC3	PASS
n41	30	80	CP-256QAM	L	Outer_Full	21.47	PC3	PASS
n41	30	80	CP-256QAM	L	Inner_Full	21.35	PC3	PASS

n41	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	21.36	PC3	PASS
n41	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	21.47	PC3	PASS
n41	30	80	DFT-PI2BPSK	M	Outer_Full	21.05	PC3	PASS
n41	30	80	DFT-PI2BPSK	M	Inner_Full	21.39	PC3	PASS
n41	30	80	DFT-QPSK	M	Edge_1RB_Left	21.79	PC3	PASS
n41	30	80	DFT-QPSK	M	Edge_1RB_Right	22.04	PC3	PASS
n41	30	80	DFT-QPSK	M	Outer_Full	22.15	PC3	PASS
n41	30	80	DFT-QPSK	M	Inner_Full	22.32	PC3	PASS
n41	30	80	DFT-16QAM	M	Edge_1RB_Left	21.14	PC3	PASS
n41	30	80	DFT-16QAM	M	Edge_1RB_Right	21.25	PC3	PASS
n41	30	80	DFT-16QAM	M	Outer_Full	21.28	PC3	PASS
n41	30	80	DFT-16QAM	M	Inner_Full	21.36	PC3	PASS
n41	30	80	DFT-64QAM	M	Edge_1RB_Left	21.27	PC3	PASS
n41	30	80	DFT-64QAM	M	Edge_1RB_Right	21.74	PC3	PASS
n41	30	80	DFT-64QAM	M	Outer_Full	21.25	PC3	PASS
n41	30	80	DFT-64QAM	M	Inner_Full	21.35	PC3	PASS
n41	30	80	DFT-256QAM	M	Edge_1RB_Left	21.25	PC3	PASS
n41	30	80	DFT-256QAM	M	Edge_1RB_Right	21.47	PC3	PASS
n41	30	80	DFT-256QAM	M	Outer_Full	21.47	PC3	PASS
n41	30	80	DFT-256QAM	M	Inner_Full	21.36	PC3	PASS
n41	30	80	CP-QPSK	M	Edge_1RB_Left	21.47	PC3	PASS
n41	30	80	CP-QPSK	M	Edge_1RB_Right	21.20	PC3	PASS
n41	30	80	CP-QPSK	M	Outer_Full	21.78	PC3	PASS
n41	30	80	CP-QPSK	M	Inner_Full	22.35	PC3	PASS
n41	30	80	CP-16QAM	M	Edge_1RB_Left	22.14	PC3	PASS
n41	30	80	CP-16QAM	M	Edge_1RB_Right	22.28	PC3	PASS
n41	30	80	CP-16QAM	M	Outer_Full	22.13	PC3	PASS
n41	30	80	CP-16QAM	M	Inner_Full	22.24	PC3	PASS
n41	30	80	CP-64QAM	M	Edge_1RB_Left	22.13	PC3	PASS
n41	30	80	CP-64QAM	M	Edge_1RB_Right	22.28	PC3	PASS
n41	30	80	CP-64QAM	M	Outer_Full	22.46	PC3	PASS
n41	30	80	CP-64QAM	M	Inner_Full	22.02	PC3	PASS
n41	30	80	CP-256QAM	M	Edge_1RB_Left	22.17	PC3	PASS
n41	30	80	CP-256QAM	M	Edge_1RB_Right	22.14	PC3	PASS
n41	30	80	CP-256QAM	M	Outer_Full	22.32	PC3	PASS
n41	30	80	CP-256QAM	M	Inner_Full	22.52	PC3	PASS
n41	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	21.72	PC3	PASS
n41	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	21.35	PC3	PASS
n41	30	80	DFT-PI2BPSK	H	Outer_Full	21.45	PC3	PASS
n41	30	80	DFT-PI2BPSK	H	Inner_Full	21.38	PC3	PASS
n41	30	80	DFT-QPSK	H	Edge_1RB_Left	21.28	PC3	PASS
n41	30	80	DFT-QPSK	H	Edge_1RB_Right	21.46	PC3	PASS

n41	30	80	DFT-QPSK	H	Outer_Full	21.28	PC3	PASS
n41	30	80	DFT-QPSK	H	Inner_Full	22.39	PC3	PASS
n41	30	80	DFT-16QAM	H	Edge_1RB_Left	22.47	PC3	PASS
n41	30	80	DFT-16QAM	H	Edge_1RB_Right	22.45	PC3	PASS
n41	30	80	DFT-16QAM	H	Outer_Full	22.39	PC3	PASS
n41	30	80	DFT-16QAM	H	Inner_Full	22.47	PC3	PASS
n41	30	80	DFT-64QAM	H	Edge_1RB_Left	22.28	PC3	PASS
n41	30	80	DFT-64QAM	H	Edge_1RB_Right	22.24	PC3	PASS
n41	30	80	DFT-64QAM	H	Outer_Full	22.16	PC3	PASS
n41	30	80	DFT-64QAM	H	Inner_Full	22.32	PC3	PASS
n41	30	80	DFT-256QAM	H	Edge_1RB_Left	22.16	PC3	PASS
n41	30	80	DFT-256QAM	H	Edge_1RB_Right	22.35	PC3	PASS
n41	30	80	DFT-256QAM	H	Outer_Full	22.27	PC3	PASS
n41	30	80	DFT-256QAM	H	Inner_Full	22.32	PC3	PASS
n41	30	80	CP-QPSK	H	Edge_1RB_Left	22.19	PC3	PASS
n41	30	80	CP-QPSK	H	Edge_1RB_Right	22.39	PC3	PASS
n41	30	80	CP-QPSK	H	Outer_Full	22.13	PC3	PASS
n41	30	80	CP-QPSK	H	Inner_Full	22.21	PC3	PASS
n41	30	80	CP-16QAM	H	Edge_1RB_Left	22.32	PC3	PASS
n41	30	80	CP-16QAM	H	Edge_1RB_Right	22.27	PC3	PASS
n41	30	80	CP-16QAM	H	Outer_Full	22.93	PC3	PASS
n41	30	80	CP-16QAM	H	Inner_Full	22.14	PC3	PASS
n41	30	80	CP-64QAM	H	Edge_1RB_Left	22.25	PC3	PASS
n41	30	80	CP-64QAM	H	Edge_1RB_Right	22.46	PC3	PASS
n41	30	80	CP-64QAM	H	Outer_Full	22.21	PC3	PASS
n41	30	80	CP-64QAM	H	Inner_Full	22.71	PC3	PASS
n41	30	80	CP-256QAM	H	Edge_1RB_Left	22.32	PC3	PASS
n41	30	80	CP-256QAM	H	Edge_1RB_Right	22.15	PC3	PASS
n41	30	80	CP-256QAM	H	Outer_Full	22.25	PC3	PASS
n41	30	80	CP-256QAM	H	Inner_Full	22.28	PC3	PASS
n41	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	22.13	PC3	PASS
n41	30	90	DFT-PI2BPSK	L	Edge_1RB_Right	21.87	PC3	PASS
n41	30	90	DFT-PI2BPSK	L	Outer_Full	21.72	PC3	PASS
n41	30	90	DFT-PI2BPSK	L	Inner_Full	21.65	PC3	PASS
n41	30	90	DFT-QPSK	L	Edge_1RB_Left	21.25	PC3	PASS
n41	30	90	DFT-QPSK	L	Edge_1RB_Right	21.79	PC3	PASS
n41	30	90	DFT-QPSK	L	Outer_Full	21.35	PC3	PASS
n41	30	90	DFT-QPSK	L	Inner_Full	21.25	PC3	PASS
n41	30	90	DFT-16QAM	L	Edge_1RB_Left	21.79	PC3	PASS
n41	30	90	DFT-16QAM	L	Edge_1RB_Right	21.35	PC3	PASS
n41	30	90	DFT-16QAM	L	Outer_Full	21.52	PC3	PASS
n41	30	90	DFT-16QAM	L	Inner_Full	21.35	PC3	PASS

n41	30	90	DFT-64QAM	L	Edge_1RB_Left	22.39	PC3	PASS
n41	30	90	DFT-64QAM	L	Edge_1RB_Right	22.71	PC3	PASS
n41	30	90	DFT-64QAM	L	Outer_Full	22.69	PC3	PASS
n41	30	90	DFT-64QAM	L	Inner_Full	22.72	PC3	PASS
n41	30	90	DFT-256QAM	L	Edge_1RB_Left	22.65	PC3	PASS
n41	30	90	DFT-256QAM	L	Edge_1RB_Right	22.32	PC3	PASS
n41	30	90	DFT-256QAM	L	Outer_Full	22.17	PC3	PASS
n41	30	90	DFT-256QAM	L	Inner_Full	22.28	PC3	PASS
n41	30	90	CP-QPSK	L	Edge_1RB_Left	22.93	PC3	PASS
n41	30	90	CP-QPSK	L	Edge_1RB_Right	22.71	PC3	PASS
n41	30	90	CP-QPSK	L	Outer_Full	22.62	PC3	PASS
n41	30	90	CP-QPSK	L	Inner_Full	22.35	PC3	PASS
n41	30	90	CP-16QAM	L	Edge_1RB_Left	22.46	PC3	PASS
n41	30	90	CP-16QAM	L	Edge_1RB_Right	22.89	PC3	PASS
n41	30	90	CP-16QAM	L	Outer_Full	22.35	PC3	PASS
n41	30	90	CP-16QAM	L	Inner_Full	22.79	PC3	PASS
n41	30	90	CP-64QAM	L	Edge_1RB_Left	23.02	PC3	PASS
n41	30	90	CP-64QAM	L	Edge_1RB_Right	23.12	PC3	PASS
n41	30	90	CP-64QAM	L	Outer_Full	23.25	PC3	PASS
n41	30	90	CP-64QAM	L	Inner_Full	23.02	PC3	PASS
n41	30	90	CP-256QAM	L	Edge_1RB_Left	22.62	PC3	PASS
n41	30	90	CP-256QAM	L	Edge_1RB_Right	22.16	PC3	PASS
n41	30	90	CP-256QAM	L	Outer_Full	22.39	PC3	PASS
n41	30	90	CP-256QAM	L	Inner_Full	21.47	PC3	PASS
n41	30	90	DFT-PI2BPSK	M	Edge_1RB_Left	21.32	PC3	PASS
n41	30	90	DFT-PI2BPSK	M	Edge_1RB_Right	21.52	PC3	PASS
n41	30	90	DFT-PI2BPSK	M	Outer_Full	22.39	PC3	PASS
n41	30	90	DFT-PI2BPSK	M	Inner_Full	22.46	PC3	PASS
n41	30	90	DFT-QPSK	M	Edge_1RB_Left	22.13	PC3	PASS
n41	30	90	DFT-QPSK	M	Edge_1RB_Right	22.24	PC3	PASS
n41	30	90	DFT-QPSK	M	Outer_Full	22.13	PC3	PASS
n41	30	90	DFT-QPSK	M	Inner_Full	21.87	PC3	PASS
n41	30	90	DFT-16QAM	M	Edge_1RB_Left	21.75	PC3	PASS
n41	30	90	DFT-16QAM	M	Edge_1RB_Right	21.73	PC3	PASS
n41	30	90	DFT-16QAM	M	Outer_Full	21.58	PC3	PASS
n41	30	90	DFT-16QAM	M	Inner_Full	21.62	PC3	PASS
n41	30	90	DFT-64QAM	M	Edge_1RB_Left	21.55	PC3	PASS
n41	30	90	DFT-64QAM	M	Edge_1RB_Right	21.39	PC3	PASS
n41	30	90	DFT-64QAM	M	Outer_Full	21.02	PC3	PASS
n41	30	90	DFT-64QAM	M	Inner_Full	21.46	PC3	PASS
n41	30	90	DFT-256QAM	M	Edge_1RB_Left	21.87	PC3	PASS
n41	30	90	DFT-256QAM	M	Edge_1RB_Right	22.39	PC3	PASS

n41	30	90	DFT-256QAM	M	Outer_Full	22.45	PC3	PASS
n41	30	90	DFT-256QAM	M	Inner_Full	22.18	PC3	PASS
n41	30	90	CP-QPSK	M	Edge_1RB_Left	21.02	PC3	PASS
n41	30	90	CP-QPSK	M	Edge_1RB_Right	21.05	PC3	PASS
n41	30	90	CP-QPSK	M	Outer_Full	20.62	PC3	PASS
n41	30	90	CP-QPSK	M	Inner_Full	20.78	PC3	PASS
n41	30	90	CP-16QAM	M	Edge_1RB_Left	20.62	PC3	PASS
n41	30	90	CP-16QAM	M	Edge_1RB_Right	20.77	PC3	PASS
n41	30	90	CP-16QAM	M	Outer_Full	20.36	PC3	PASS
n41	30	90	CP-16QAM	M	Inner_Full	20.78	PC3	PASS
n41	30	90	CP-64QAM	M	Edge_1RB_Left	20.32	PC3	PASS
n41	30	90	CP-64QAM	M	Edge_1RB_Right	20.44	PC3	PASS
n41	30	90	CP-64QAM	M	Outer_Full	20.35	PC3	PASS
n41	30	90	CP-64QAM	M	Inner_Full	20.25	PC3	PASS
n41	30	90	CP-256QAM	M	Edge_1RB_Left	20.28	PC3	PASS
n41	30	90	CP-256QAM	M	Edge_1RB_Right	20.39	PC3	PASS
n41	30	90	CP-256QAM	M	Outer_Full	20.79	PC3	PASS
n41	30	90	CP-256QAM	M	Inner_Full	20.65	PC3	PASS
n41	30	90	DFT-PI2BPSK	H	Edge_1RB_Left	20.35	PC3	PASS
n41	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	20.39	PC3	PASS
n41	30	90	DFT-PI2BPSK	H	Outer_Full	20.38	PC3	PASS
n41	30	90	DFT-PI2BPSK	H	Inner_Full	21.46	PC3	PASS
n41	30	90	DFT-QPSK	H	Edge_1RB_Left	21.05	PC3	PASS
n41	30	90	DFT-QPSK	H	Edge_1RB_Right	21.28	PC3	PASS
n41	30	90	DFT-QPSK	H	Outer_Full	21.62	PC3	PASS
n41	30	90	DFT-QPSK	H	Inner_Full	21.42	PC3	PASS
n41	30	90	DFT-16QAM	H	Edge_1RB_Left	21.25	PC3	PASS
n41	30	90	DFT-16QAM	H	Edge_1RB_Right	21.35	PC3	PASS
n41	30	90	DFT-16QAM	H	Outer_Full	21.74	PC3	PASS
n41	30	90	DFT-16QAM	H	Inner_Full	21.35	PC3	PASS
n41	30	90	DFT-64QAM	H	Edge_1RB_Left	21.28	PC3	PASS
n41	30	90	DFT-64QAM	H	Edge_1RB_Right	21.69	PC3	PASS
n41	30	90	DFT-64QAM	H	Outer_Full	21.45	PC3	PASS
n41	30	90	DFT-64QAM	H	Inner_Full	21.35	PC3	PASS
n41	30	90	DFT-256QAM	H	Edge_1RB_Left	22.08	PC3	PASS
n41	30	90	DFT-256QAM	H	Edge_1RB_Right	22.42	PC3	PASS
n41	30	90	DFT-256QAM	H	Outer_Full	22.69	PC3	PASS
n41	30	90	DFT-256QAM	H	Inner_Full	22.71	PC3	PASS
n41	30	90	CP-QPSK	H	Edge_1RB_Left	22.35	PC3	PASS
n41	30	90	CP-QPSK	H	Edge_1RB_Right	22.42	PC3	PASS
n41	30	90	CP-QPSK	H	Outer_Full	22.28	PC3	PASS
n41	30	90	CP-QPSK	H	Inner_Full	22.62	PC3	PASS



n41	30	90	CP-16QAM	H	Edge_1RB_Left	22.43	PC3	PASS
n41	30	90	CP-16QAM	H	Edge_1RB_Right	22.28	PC3	PASS
n41	30	90	CP-16QAM	H	Outer_Full	22.36	PC3	PASS
n41	30	90	CP-16QAM	H	Inner_Full	22.71	PC3	PASS
n41	30	90	CP-64QAM	H	Edge_1RB_Left	21.39	PC3	PASS
n41	30	90	CP-64QAM	H	Edge_1RB_Right	21.79	PC3	PASS
n41	30	90	CP-64QAM	H	Outer_Full	21.62	PC3	PASS
n41	30	90	CP-64QAM	H	Inner_Full	21.65	PC3	PASS
n41	30	90	CP-256QAM	H	Edge_1RB_Left	21.53	PC3	PASS
n41	30	90	CP-256QAM	H	Edge_1RB_Right	21.51	PC3	PASS
n41	30	90	CP-256QAM	H	Outer_Full	21.38	PC3	PASS
n41	30	90	CP-256QAM	H	Inner_Full	21.46	PC3	PASS
n41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	21.53	PC3	PASS
n41	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	21.35	PC3	PASS
n41	30	100	DFT-PI2BPSK	L	Outer_Full	21.49	PC3	PASS
n41	30	100	DFT-PI2BPSK	L	Inner_Full	21.79	PC3	PASS
n41	30	100	DFT-QPSK	L	Edge_1RB_Left	22.05	PC3	PASS
n41	30	100	DFT-QPSK	L	Edge_1RB_Right	22.36	PC3	PASS
n41	30	100	DFT-QPSK	L	Outer_Full	22.46	PC3	PASS
n41	30	100	DFT-QPSK	L	Inner_Full	22.32	PC3	PASS
n41	30	100	DFT-16QAM	L	Edge_1RB_Left	22.19	PC3	PASS
n41	30	100	DFT-16QAM	L	Edge_1RB_Right	22.19	PC3	PASS
n41	30	100	DFT-16QAM	L	Outer_Full	23.25	PC3	PASS
n41	30	100	DFT-16QAM	L	Inner_Full	23.14	PC3	PASS
n41	30	100	DFT-64QAM	L	Edge_1RB_Left	22.96	PC3	PASS
n41	30	100	DFT-64QAM	L	Edge_1RB_Right	22.75	PC3	PASS
n41	30	100	DFT-64QAM	L	Outer_Full	22.36	PC3	PASS
n41	30	100	DFT-64QAM	L	Inner_Full	22.58	PC3	PASS
n41	30	100	DFT-256QAM	L	Edge_1RB_Left	22.35	PC3	PASS
n41	30	100	DFT-256QAM	L	Edge_1RB_Right	22.79	PC3	PASS
n41	30	100	DFT-256QAM	L	Outer_Full	22.62	PC3	PASS
n41	30	100	DFT-256QAM	L	Inner_Full	22.46	PC3	PASS
n41	30	100	CP-QPSK	L	Edge_1RB_Left	22.25	PC3	PASS
n41	30	100	CP-QPSK	L	Edge_1RB_Right	22.15	PC3	PASS
n41	30	100	CP-QPSK	L	Outer_Full	22.39	PC3	PASS
n41	30	100	CP-QPSK	L	Inner_Full	22.57	PC3	PASS
n41	30	100	CP-16QAM	L	Edge_1RB_Left	22.35	PC3	PASS
n41	30	100	CP-16QAM	L	Edge_1RB_Right	23.15	PC3	PASS
n41	30	100	CP-16QAM	L	Outer_Full	23.34	PC3	PASS
n41	30	100	CP-16QAM	L	Inner_Full	23.25	PC3	PASS
n41	30	100	CP-64QAM	L	Edge_1RB_Left	23.47	PC3	PASS
n41	30	100	CP-64QAM	L	Edge_1RB_Right	23.09	PC3	PASS

n41	30	100	CP-64QAM	L	Outer_Full	23.37	PC3	PASS
n41	30	100	CP-64QAM	L	Inner_Full	23.52	PC3	PASS
n41	30	100	CP-256QAM	L	Edge_1RB_Left	23.14	PC3	PASS
n41	30	100	CP-256QAM	L	Edge_1RB_Right	22.96	PC3	PASS
n41	30	100	CP-256QAM	L	Outer_Full	22.39	PC3	PASS
n41	30	100	CP-256QAM	L	Inner_Full	22.17	PC3	PASS
n41	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	22.28	PC3	PASS
n41	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	22.32	PC3	PASS
n41	30	100	DFT-PI2BPSK	M	Outer_Full	21.27	PC3	PASS
n41	30	100	DFT-PI2BPSK	M	Inner_Full	22.91	PC3	PASS
n41	30	100	DFT-QPSK	M	Edge_1RB_Left	22.62	PC3	PASS
n41	30	100	DFT-QPSK	M	Edge_1RB_Right	22.75	PC3	PASS
n41	30	100	DFT-QPSK	M	Outer_Full	23.35	PC3	PASS
n41	30	100	DFT-QPSK	M	Inner_Full	23.97	PC3	PASS
n41	30	100	DFT-16QAM	M	Edge_1RB_Left	22.78	PC3	PASS
n41	30	100	DFT-16QAM	M	Edge_1RB_Right	21.65	PC3	PASS
n41	30	100	DFT-16QAM	M	Outer_Full	21.79	PC3	PASS
n41	30	100	DFT-16QAM	M	Inner_Full	21.02	PC3	PASS
n41	30	100	DFT-64QAM	M	Edge_1RB_Left	21.35	PC3	PASS
n41	30	100	DFT-64QAM	M	Edge_1RB_Right	21.02	PC3	PASS
n41	30	100	DFT-64QAM	M	Outer_Full	21.30	PC3	PASS
n41	30	100	DFT-64QAM	M	Inner_Full	21.87	PC3	PASS
n41	30	100	DFT-256QAM	M	Edge_1RB_Left	20.93	PC3	PASS
n41	30	100	DFT-256QAM	M	Edge_1RB_Right	21.74	PC3	PASS
n41	30	100	DFT-256QAM	M	Outer_Full	21.50	PC3	PASS
n41	30	100	DFT-256QAM	M	Inner_Full	21.05	PC3	PASS
n41	30	100	CP-QPSK	M	Edge_1RB_Left	21.41	PC3	PASS
n41	30	100	CP-QPSK	M	Edge_1RB_Right	21.28	PC3	PASS
n41	30	100	CP-QPSK	M	Outer_Full	21.39	PC3	PASS
n41	30	100	CP-QPSK	M	Inner_Full	21.71	PC3	PASS
n41	30	100	CP-16QAM	M	Edge_1RB_Left	21.29	PC3	PASS
n41	30	100	CP-16QAM	M	Edge_1RB_Right	21.65	PC3	PASS
n41	30	100	CP-16QAM	M	Outer_Full	21.51	PC3	PASS
n41	30	100	CP-16QAM	M	Inner_Full	21.03	PC3	PASS
n41	30	100	CP-64QAM	M	Edge_1RB_Left	22.32	PC3	PASS
n41	30	100	CP-64QAM	M	Edge_1RB_Right	22.12	PC3	PASS
n41	30	100	CP-64QAM	M	Outer_Full	22.52	PC3	PASS
n41	30	100	CP-64QAM	M	Inner_Full	22.31	PC3	PASS
n41	30	100	CP-256QAM	M	Edge_1RB_Left	22.14	PC3	PASS
n41	30	100	CP-256QAM	M	Edge_1RB_Right	21.93	PC3	PASS
n41	30	100	CP-256QAM	M	Outer_Full	21.87	PC3	PASS
n41	30	100	CP-256QAM	M	Inner_Full	21.83	PC3	PASS

n41	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	21.71	PC3	PASS
n41	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	21.63	PC3	PASS
n41	30	100	DFT-PI2BPSK	H	Outer_Full	21.35	PC3	PASS
n41	30	100	DFT-PI2BPSK	H	Inner_Full	21.28	PC3	PASS
n41	30	100	DFT-QPSK	H	Edge_1RB_Left	21.72	PC3	PASS
n41	30	100	DFT-QPSK	H	Edge_1RB_Right	21.35	PC3	PASS
n41	30	100	DFT-QPSK	H	Outer_Full	21.73	PC3	PASS
n41	30	100	DFT-QPSK	H	Inner_Full	21.32	PC3	PASS
n41	30	100	DFT-16QAM	H	Edge_1RB_Left	21.36	PC3	PASS
n41	30	100	DFT-16QAM	H	Edge_1RB_Right	22.71	PC3	PASS
n41	30	100	DFT-16QAM	H	Outer_Full	22.32	PC3	PASS
n41	30	100	DFT-16QAM	H	Inner_Full	22.54	PC3	PASS
n41	30	100	DFT-64QAM	H	Edge_1RB_Left	22.93	PC3	PASS
n41	30	100	DFT-64QAM	H	Edge_1RB_Right	21.74	PC3	PASS
n41	30	100	DFT-64QAM	H	Outer_Full	21.32	PC3	PASS
n41	30	100	DFT-64QAM	H	Inner_Full	22.12	PC3	PASS
n41	30	100	DFT-256QAM	H	Edge_1RB_Left	22.54	PC3	PASS
n41	30	100	DFT-256QAM	H	Edge_1RB_Right	21.87	PC3	PASS
n41	30	100	DFT-256QAM	H	Outer_Full	21.62	PC3	PASS
n41	30	100	DFT-256QAM	H	Inner_Full	21.43	PC3	PASS
n41	30	100	CP-QPSK	H	Edge_1RB_Left	22.02	PC3	PASS
n41	30	100	CP-QPSK	H	Edge_1RB_Right	22.35	PC3	PASS
n41	30	100	CP-QPSK	H	Outer_Full	22.41	PC3	PASS
n41	30	100	CP-QPSK	H	Inner_Full	22.08	PC3	PASS
n41	30	100	CP-16QAM	H	Edge_1RB_Left	22.35	PC3	PASS
n41	30	100	CP-16QAM	H	Edge_1RB_Right	22.17	PC3	PASS
n41	30	100	CP-16QAM	H	Outer_Full	22.12	PC3	PASS
n41	30	100	CP-16QAM	H	Inner_Full	22.39	PC3	PASS
n41	30	100	CP-64QAM	H	Edge_1RB_Left	22.42	PC3	PASS
n41	30	100	CP-64QAM	H	Edge_1RB_Right	22.02	PC3	PASS
n41	30	100	CP-64QAM	H	Outer_Full	22.32	PC3	PASS
n41	30	100	CP-64QAM	H	Inner_Full	22.41	PC3	PASS
n41	30	100	CP-256QAM	H	Edge_1RB_Left	22.32	PC3	PASS
n41	30	100	CP-256QAM	H	Edge_1RB_Right	22.38	PC3	PASS
n41	30	100	CP-256QAM	H	Outer_Full	22.14	PC3	PASS
n41	30	100	CP-256QAM	H	Inner_Full	22.32	PC3	PASS

## Appendix B: Peak-to-Average Ratio

### Peak-to-Average Ratio(CCDF)

#### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
n41	30	100	DFT-PI2BPSK	M	Outer_Full	5.21	≤13	PASS
n41	30	100	DFT-QPSK	M	Outer_Full	5.78	≤13	PASS
n41	30	100	DFT-16QAM	M	Outer_Full	6.59	≤13	PASS
n41	30	100	DFT-64QAM	M	Outer_Full	6.78	≤13	PASS
n41	30	100	DFT-256QAM	M	Outer_Full	6.81	≤13	PASS
n41	30	100	CP-QPSK	M	Outer_Full	8.49	≤13	PASS
n41	30	100	CP-16QAM	M	Outer_Full	8.53	≤13	PASS
n41	30	100	CP-64QAM	M	Outer_Full	8.58	≤13	PASS
n41	30	100	CP-256QAM	M	Outer_Full	8.57	≤13	PASS