

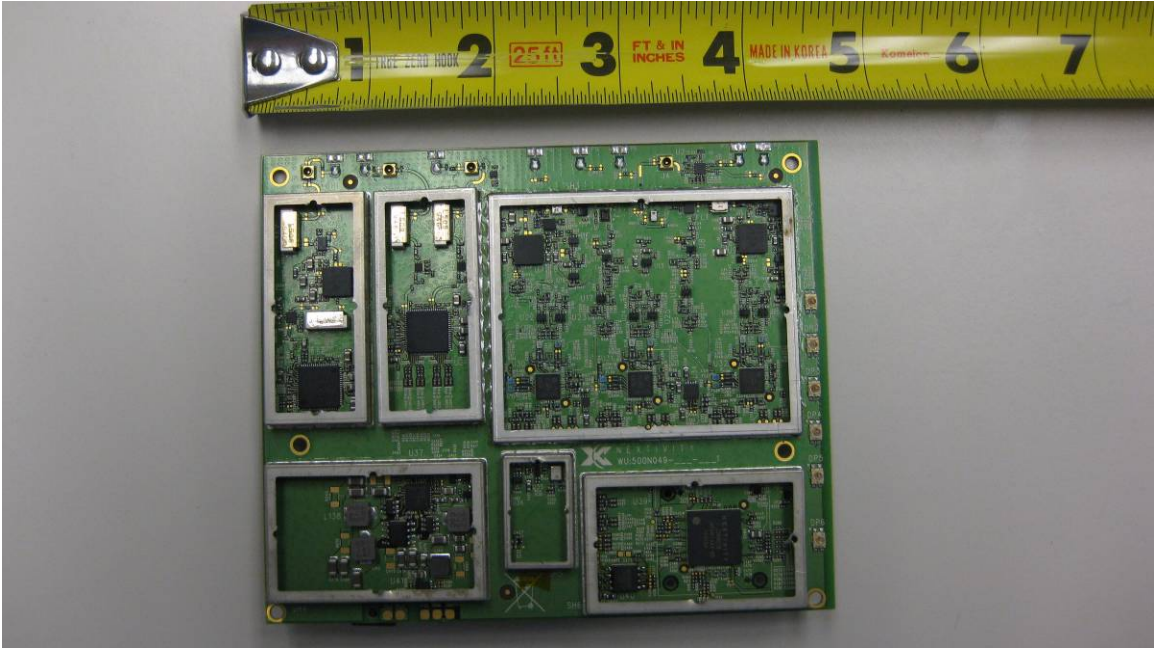
Back Cell Ant



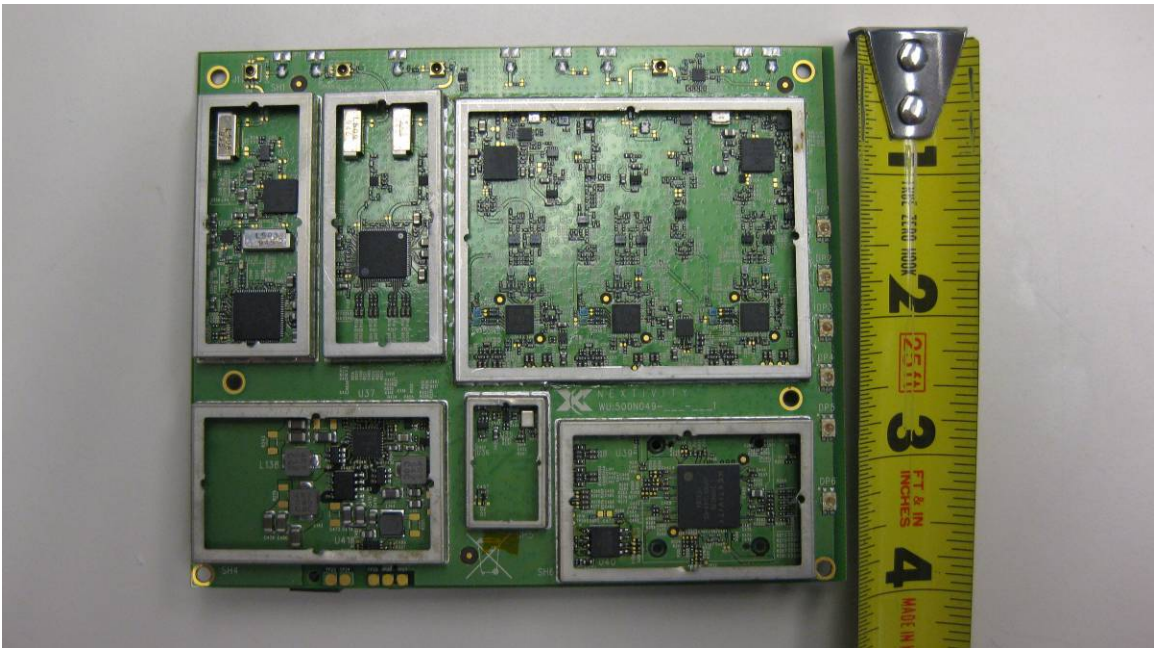
Bareboard Back1



Bareboard Back2

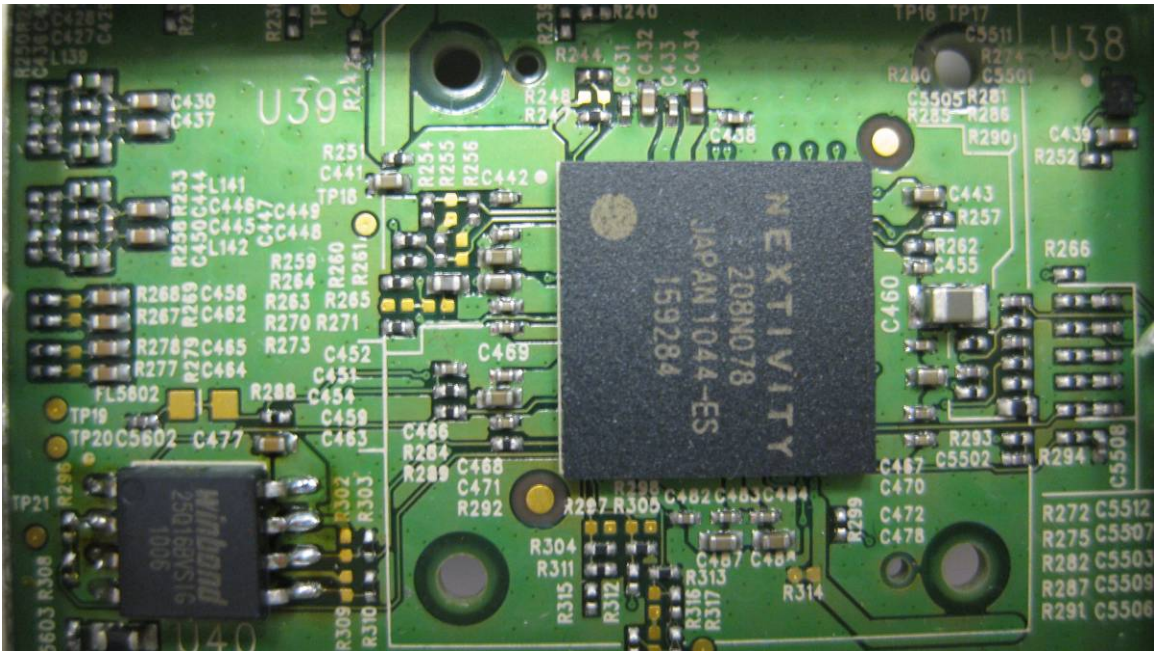


Bareboard Front1

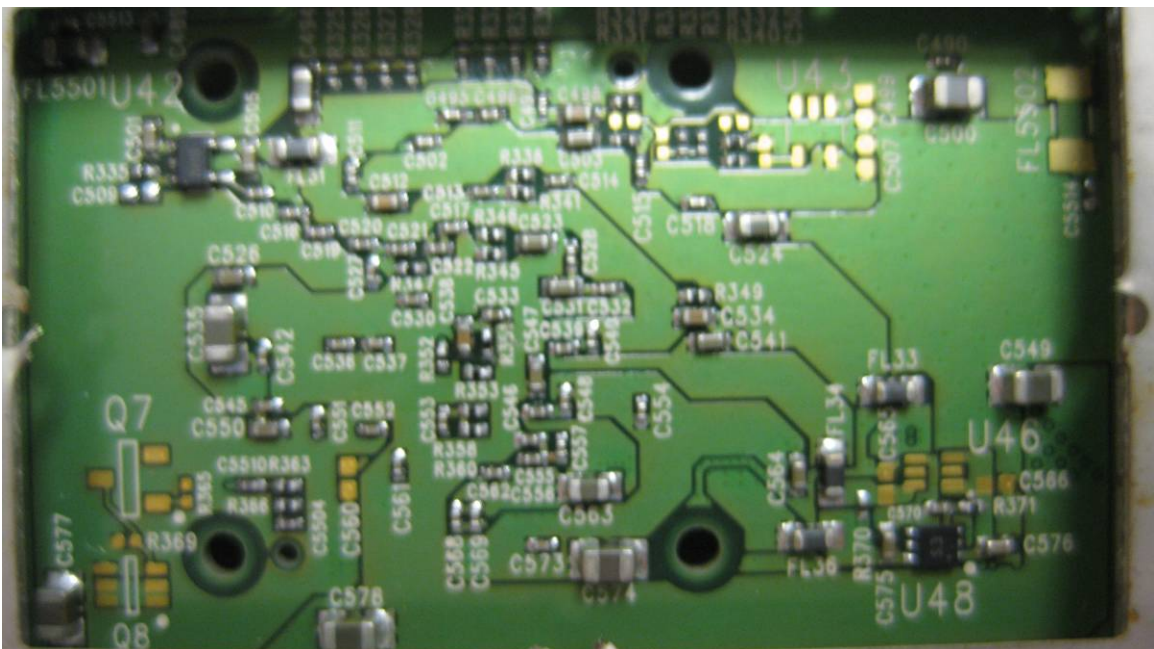


Bareboard Front2





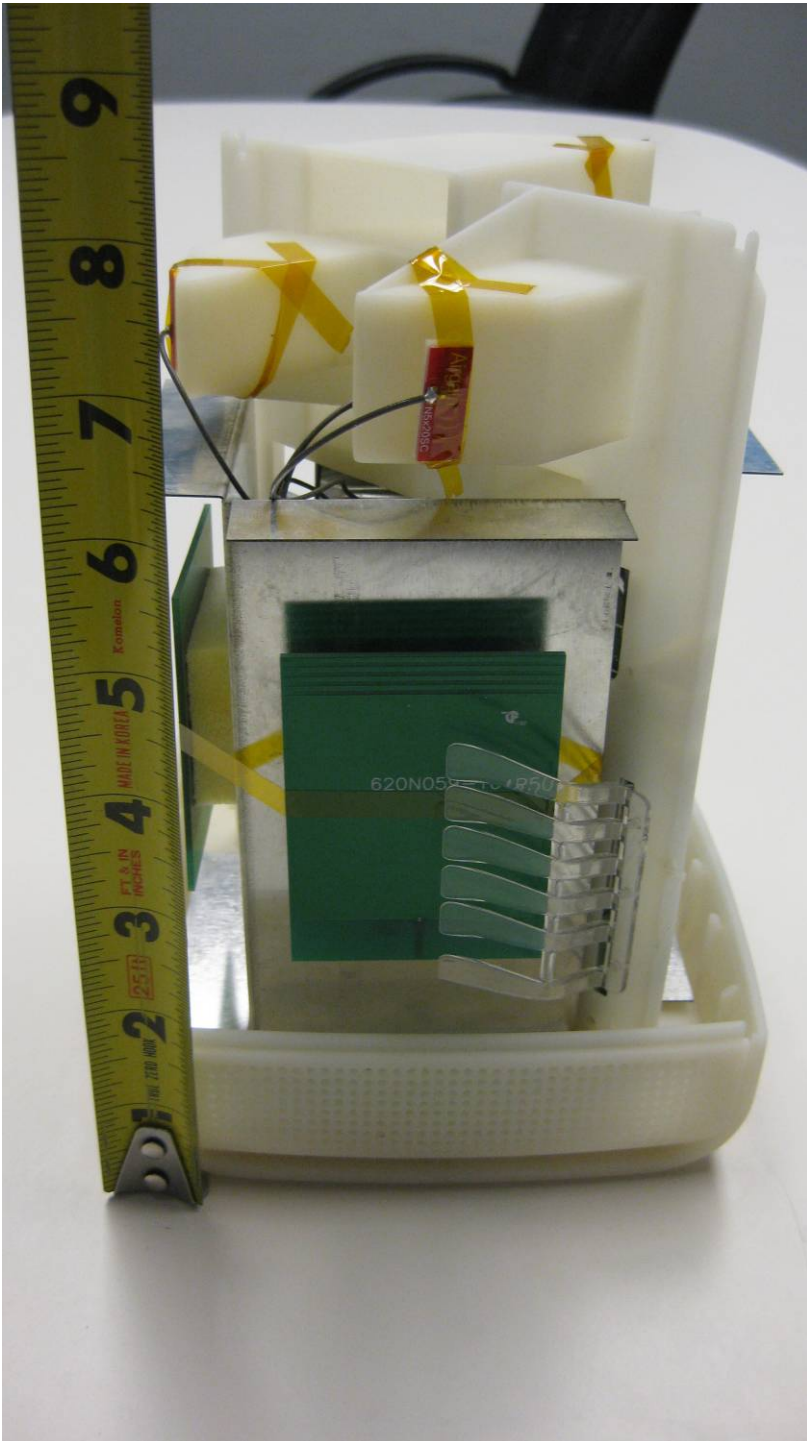
Baseband



Baseband Power

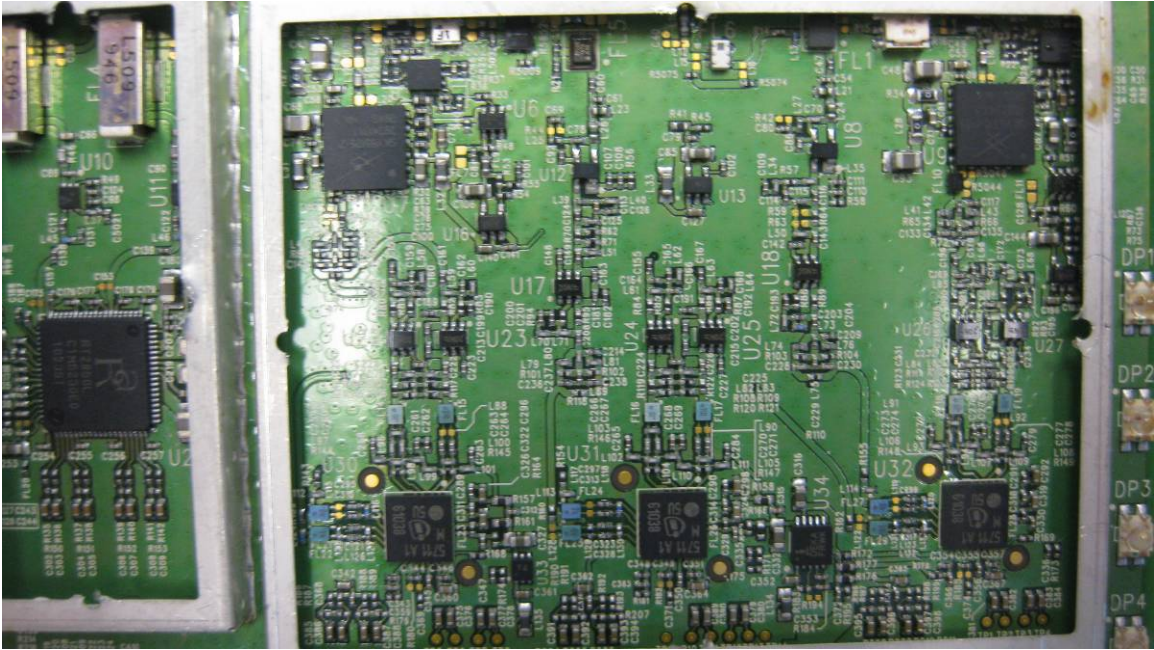


Chassis 1

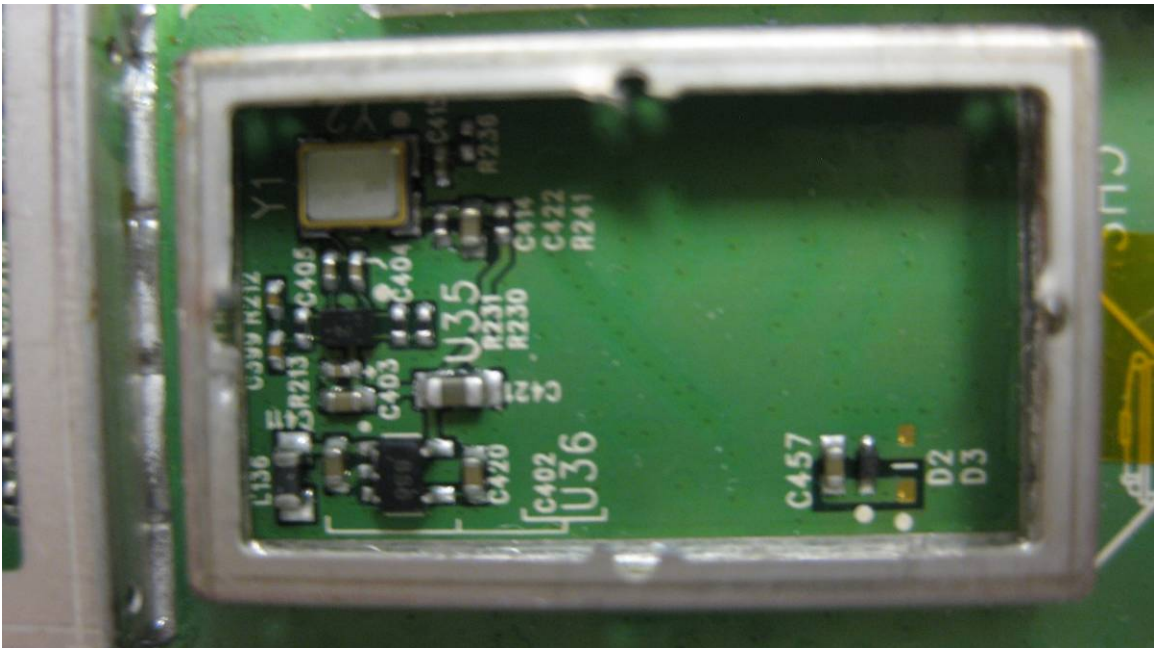


Chassis 2

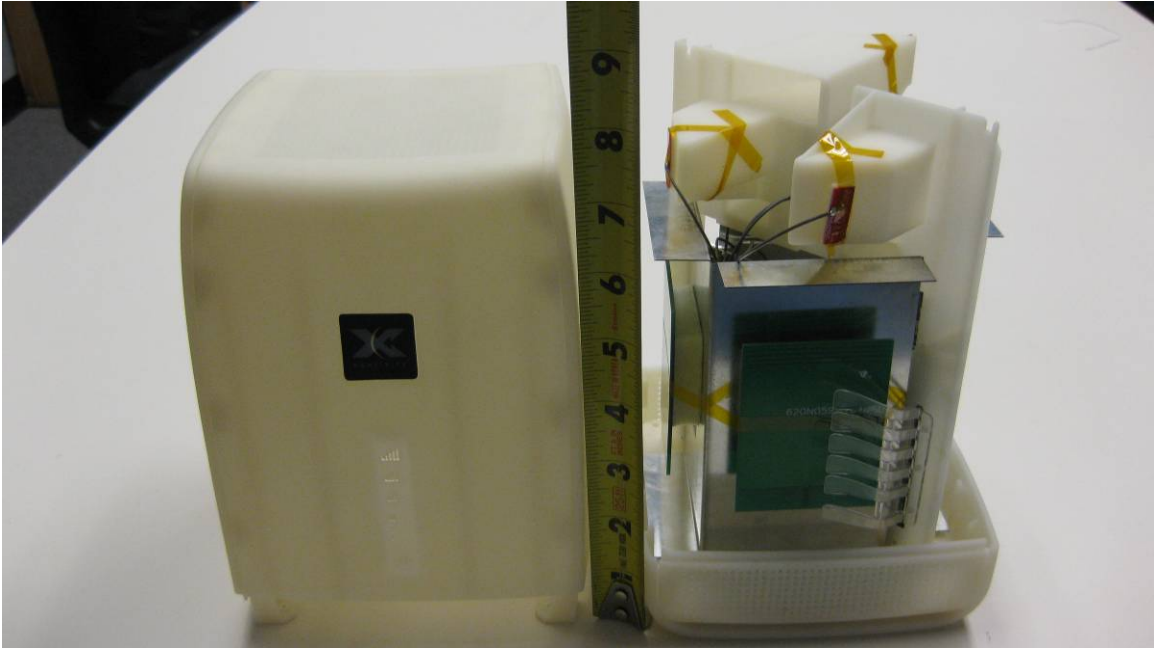




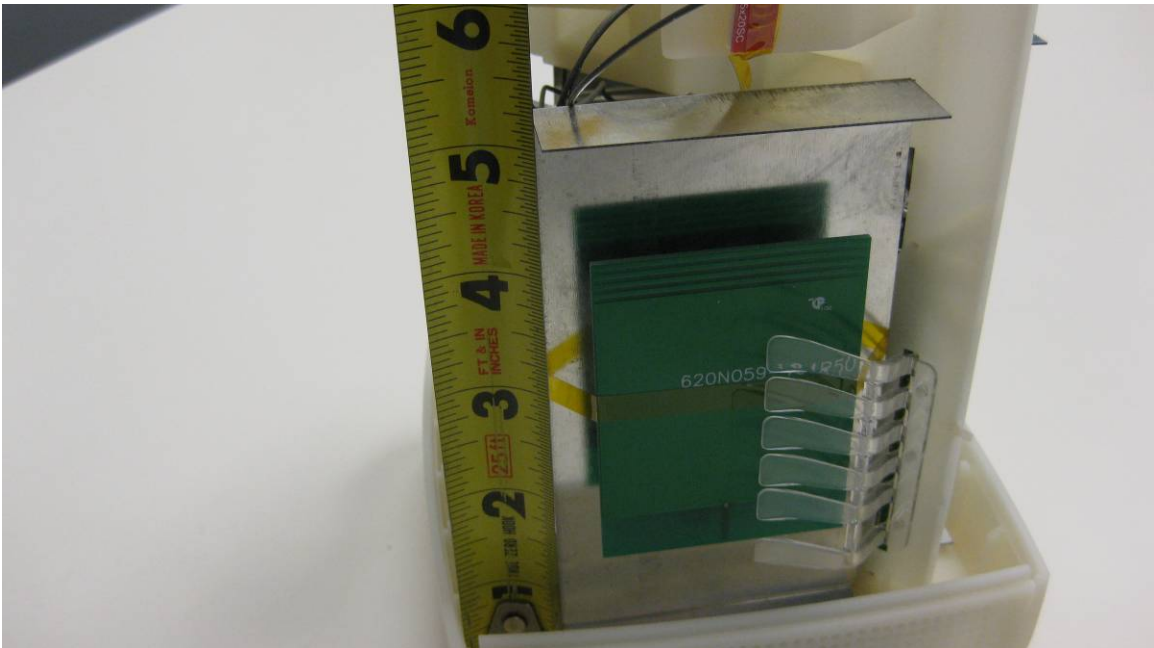
Cell



Clock

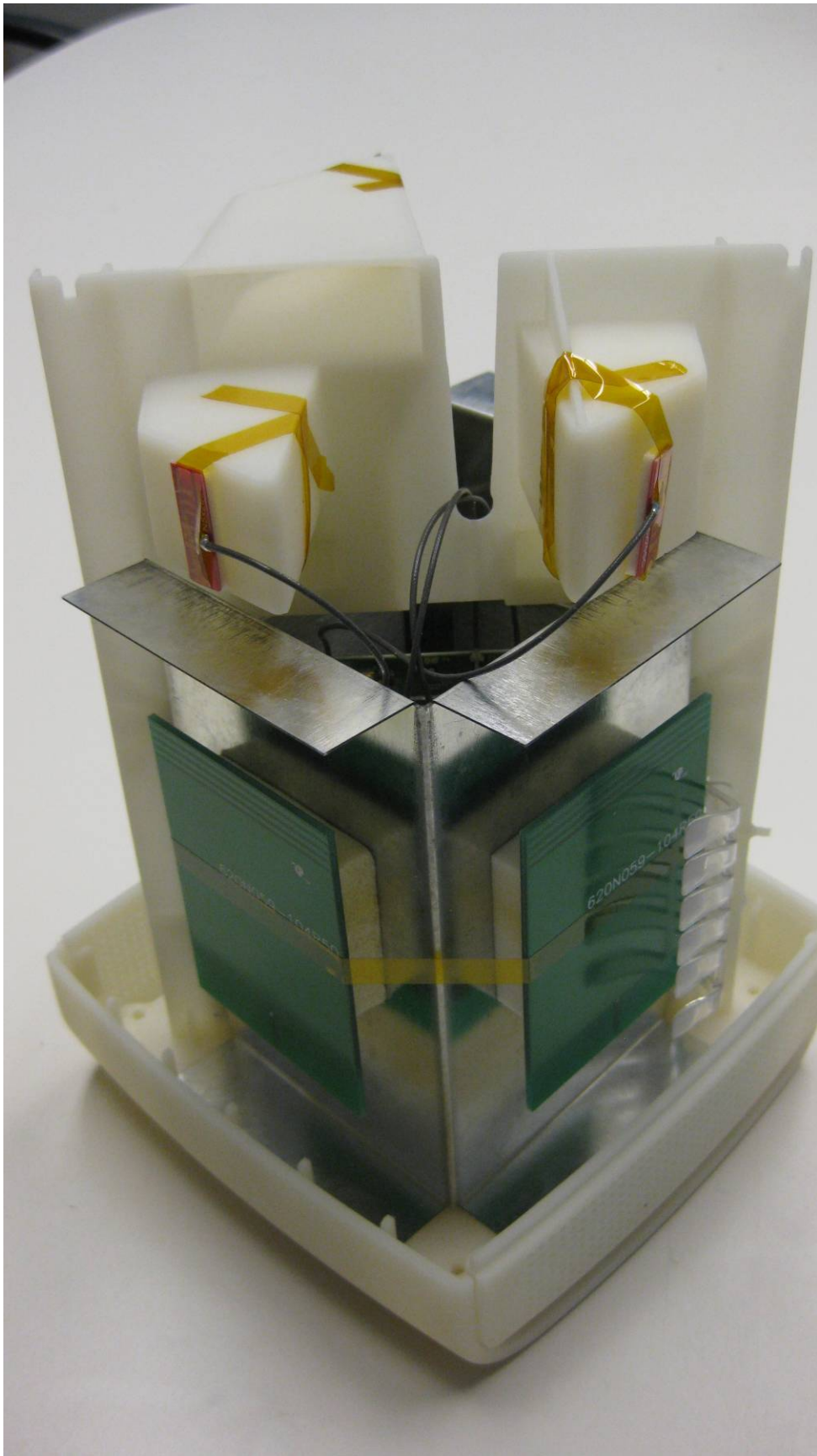


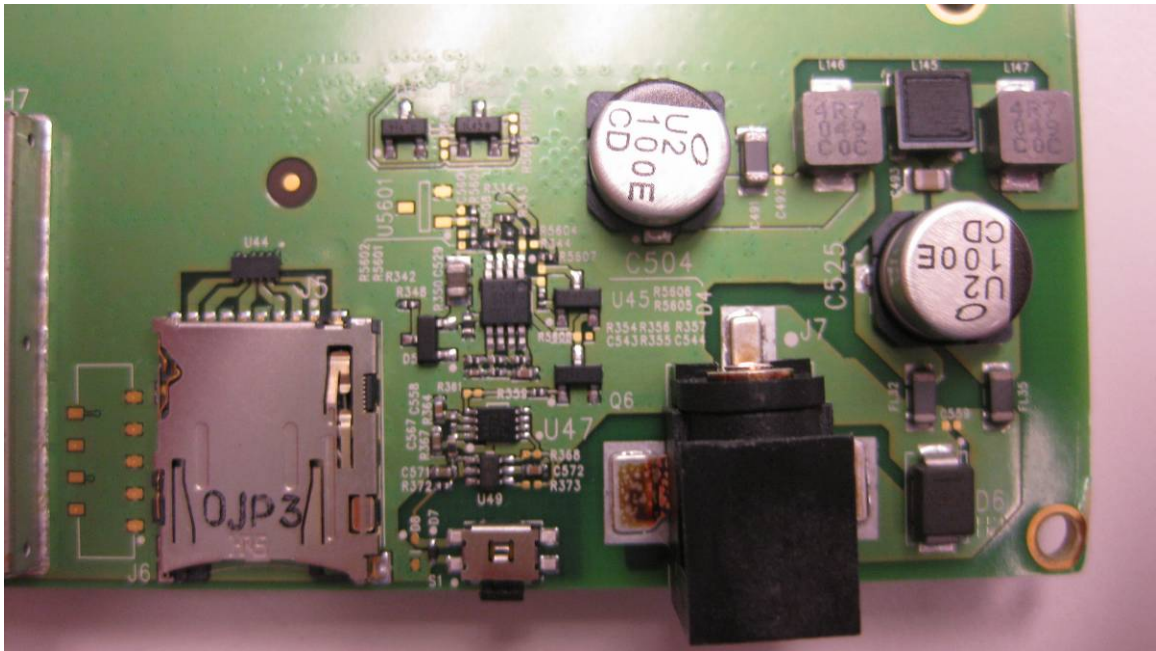
Cover and chassis



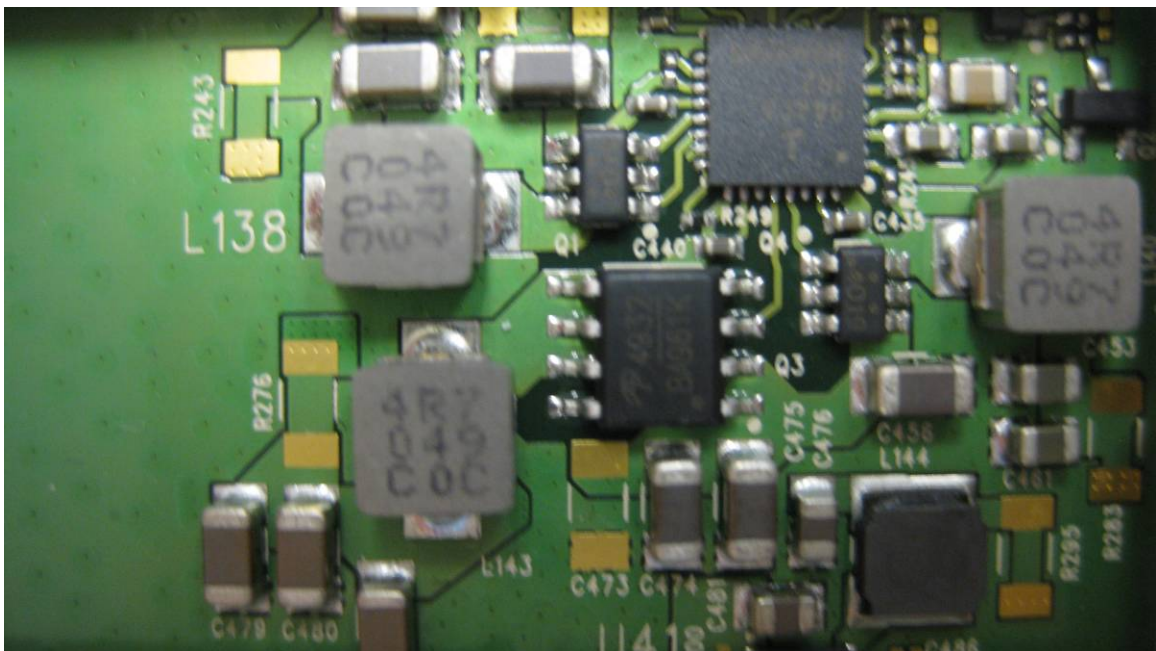
Front Cell ant







Interface



Power

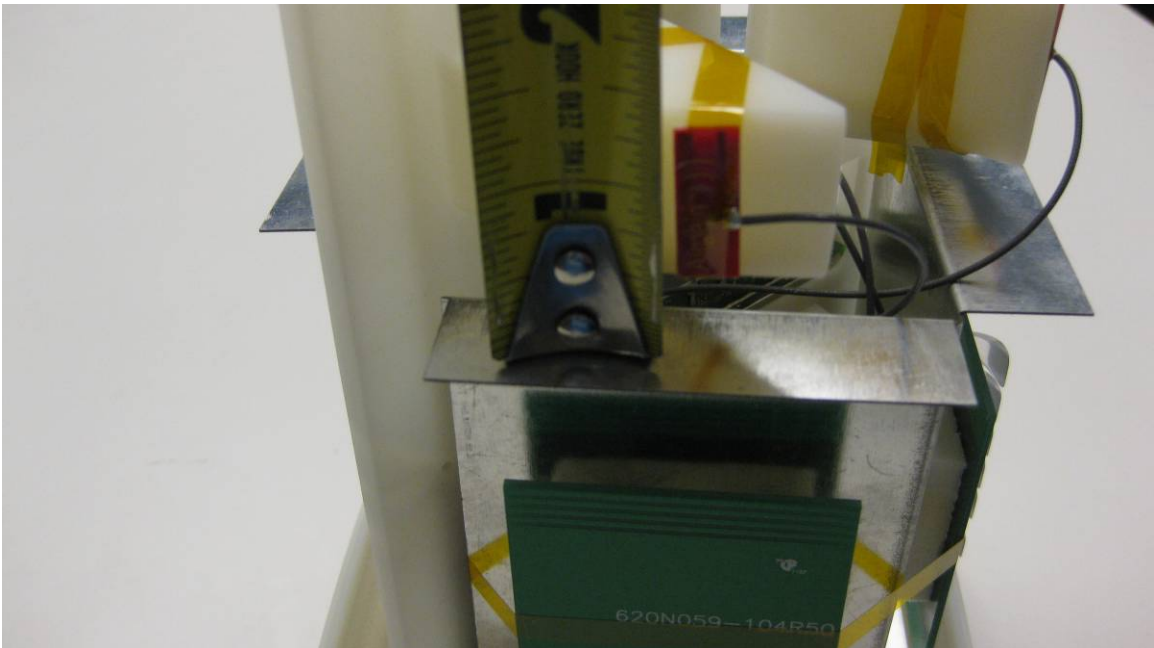








UNII RX1



UNII RX2

