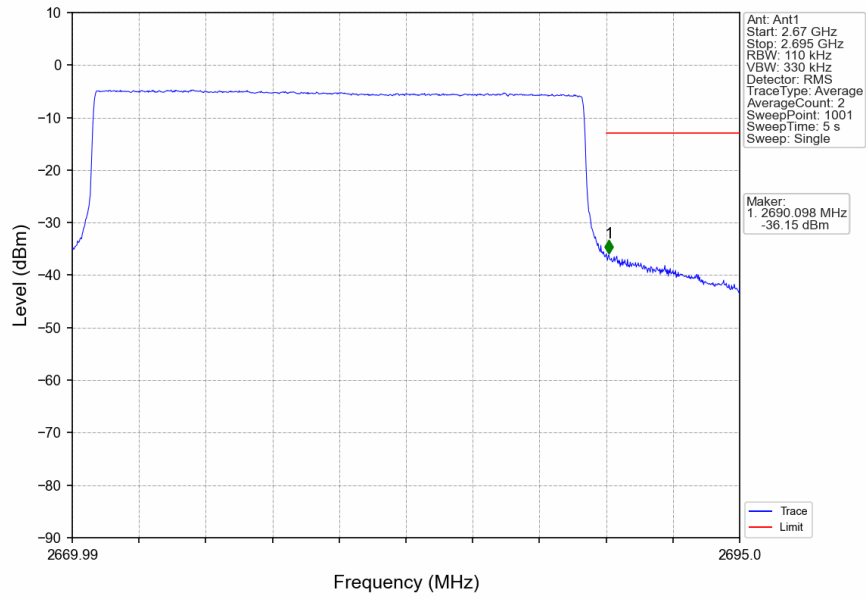
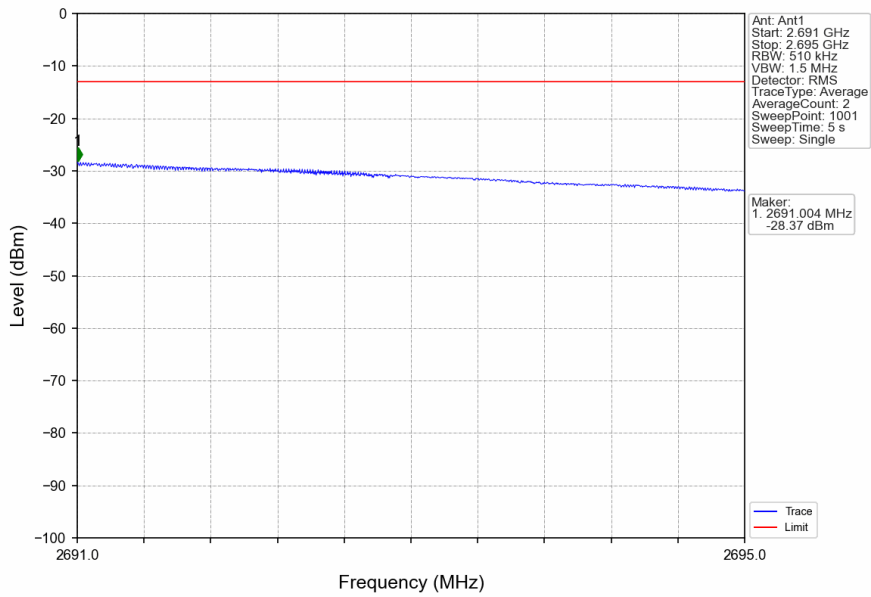


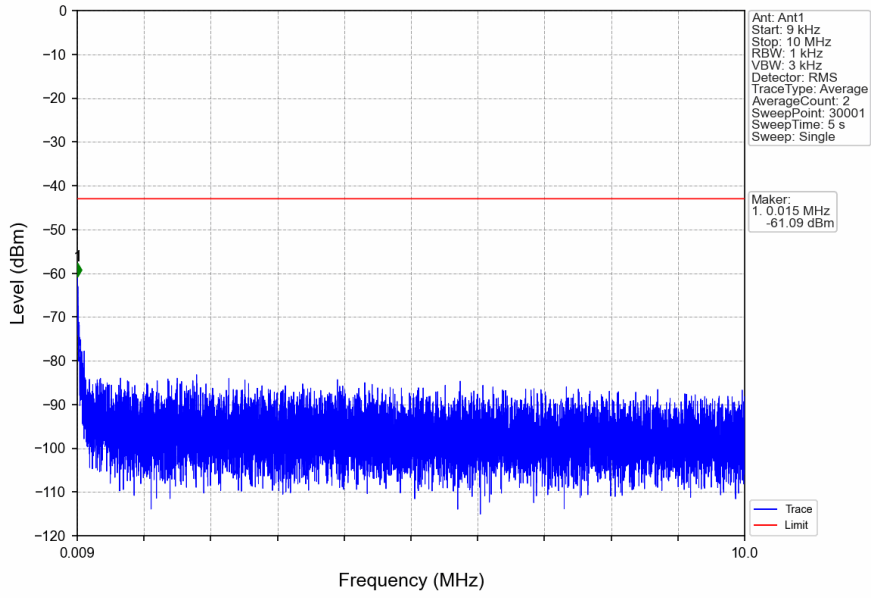
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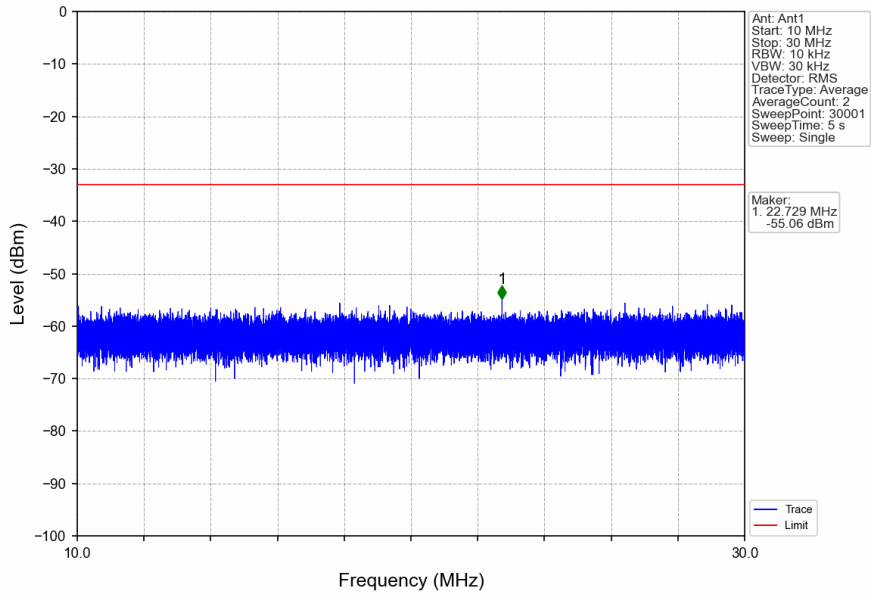
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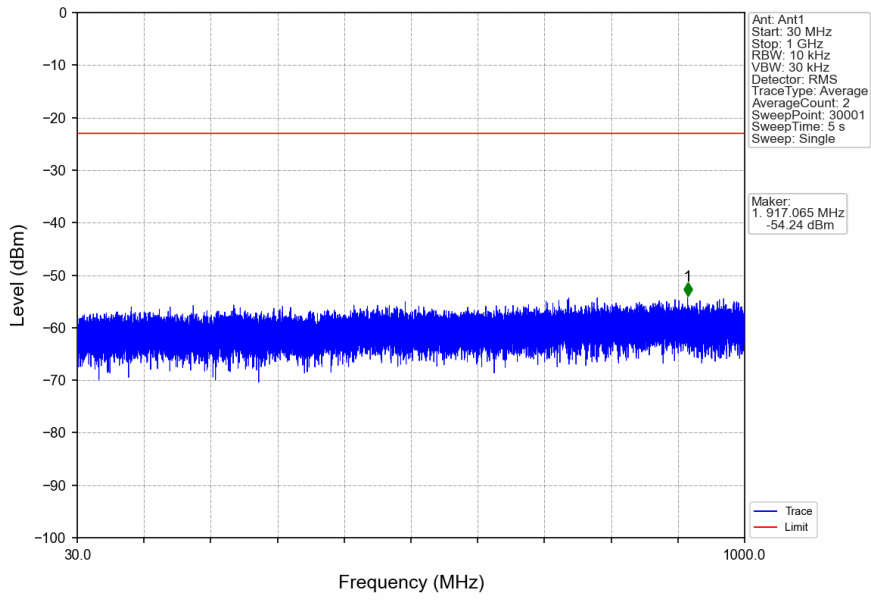
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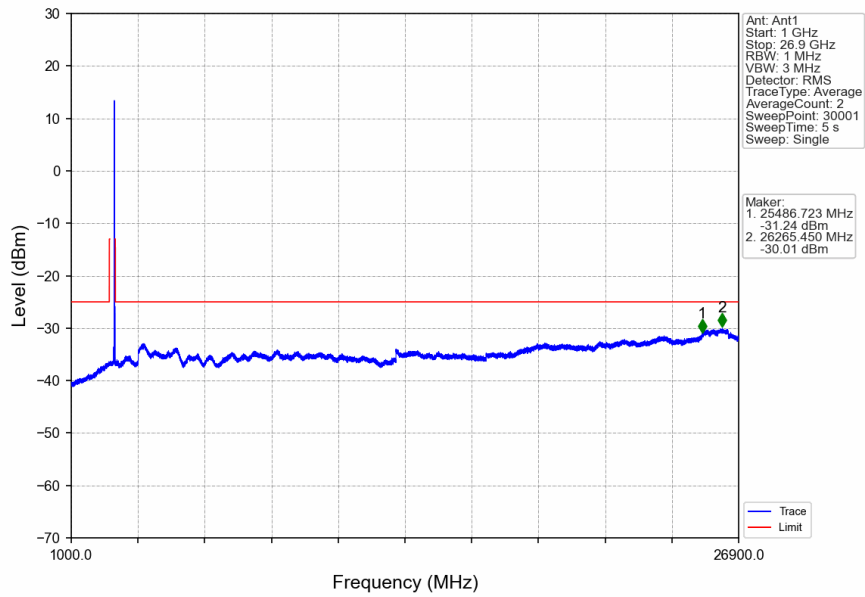
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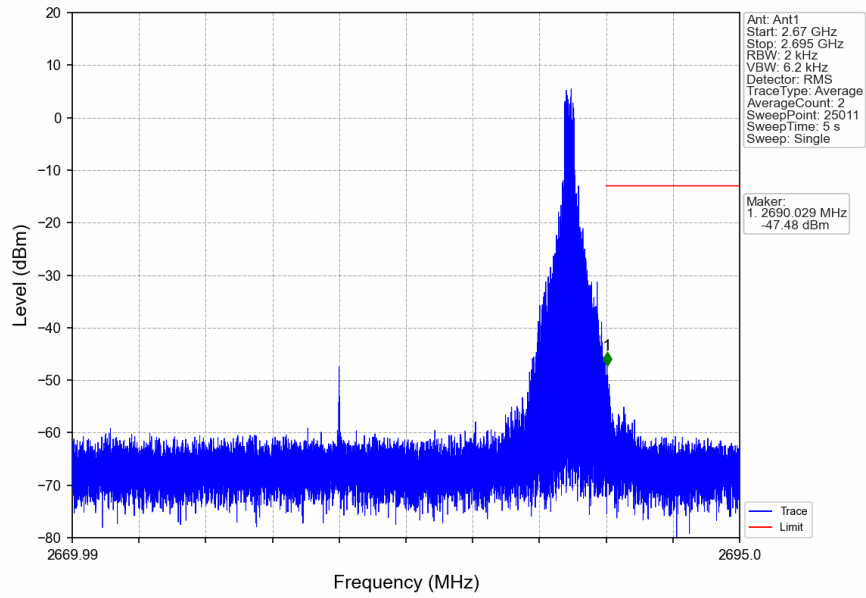
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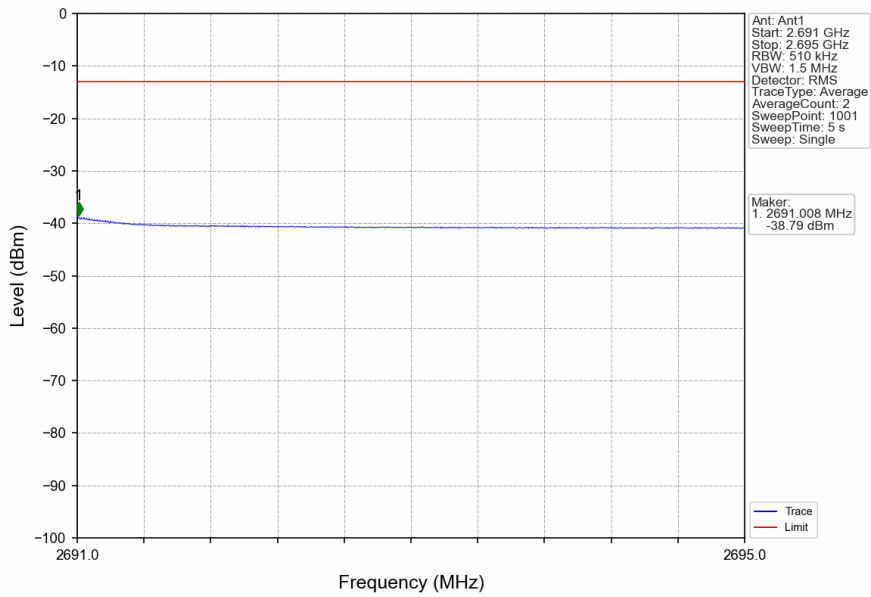
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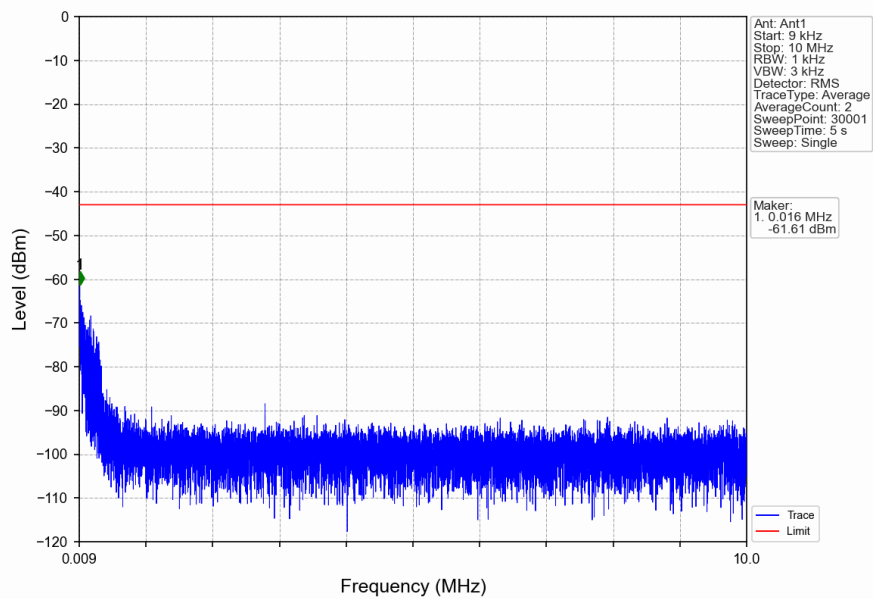
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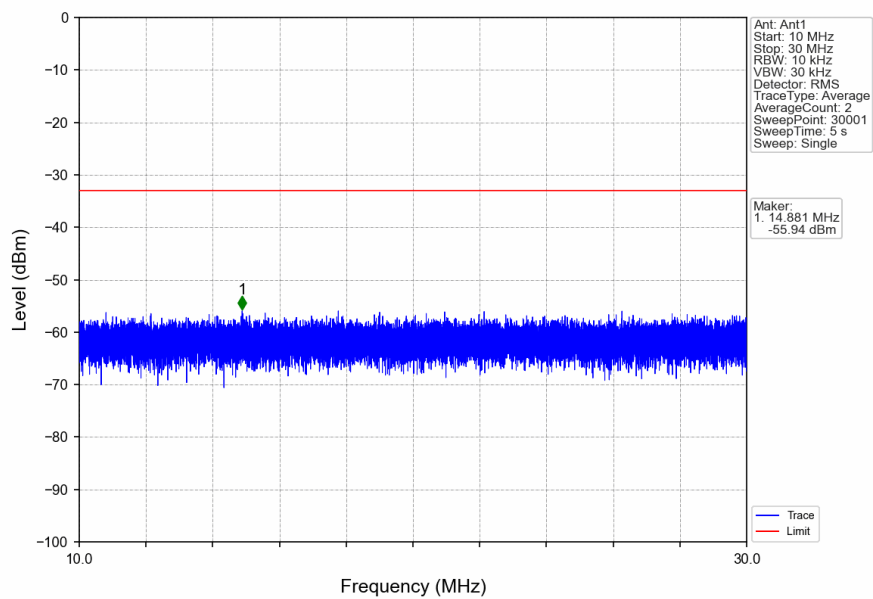
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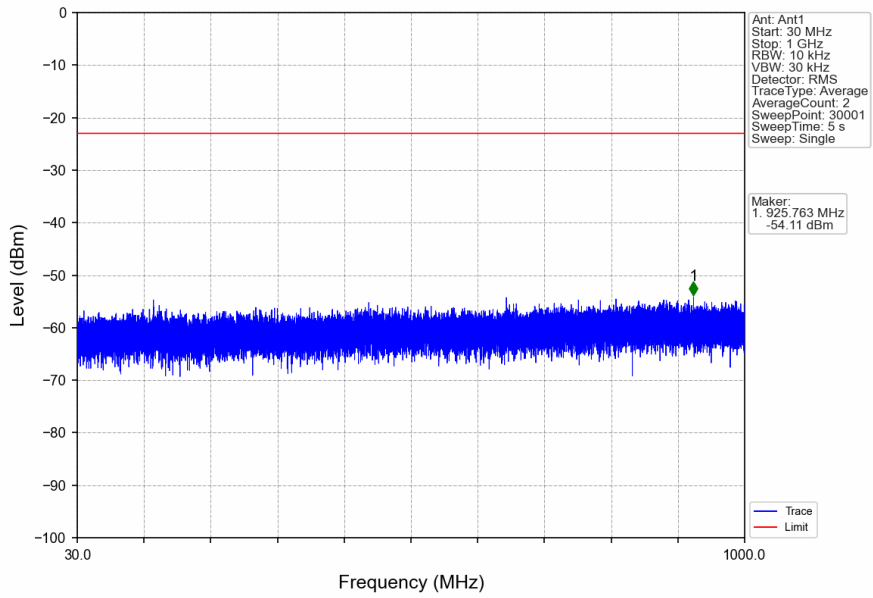
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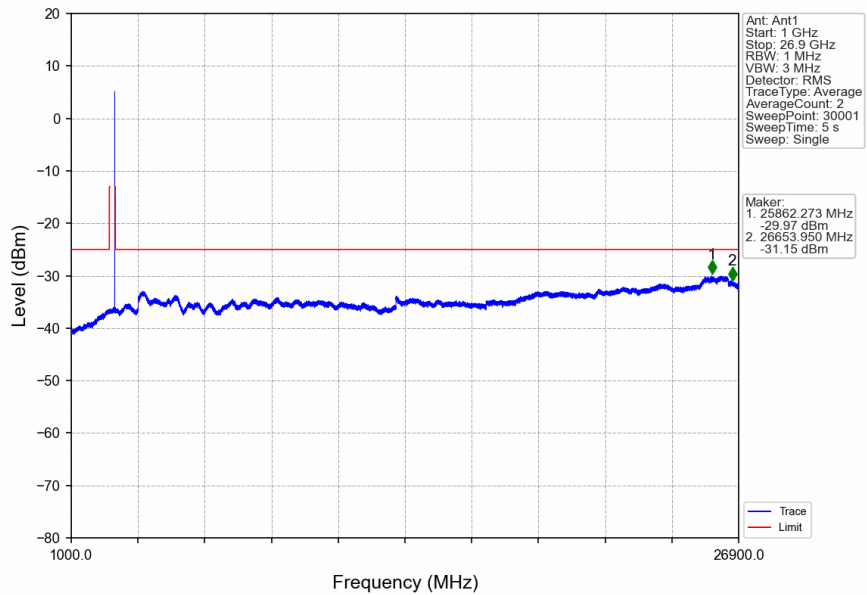
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n41_30kHz_SISO_NTNV_20MHz_CP-OFDM QPSK_2679.99MHz_Inner_1RB_Right



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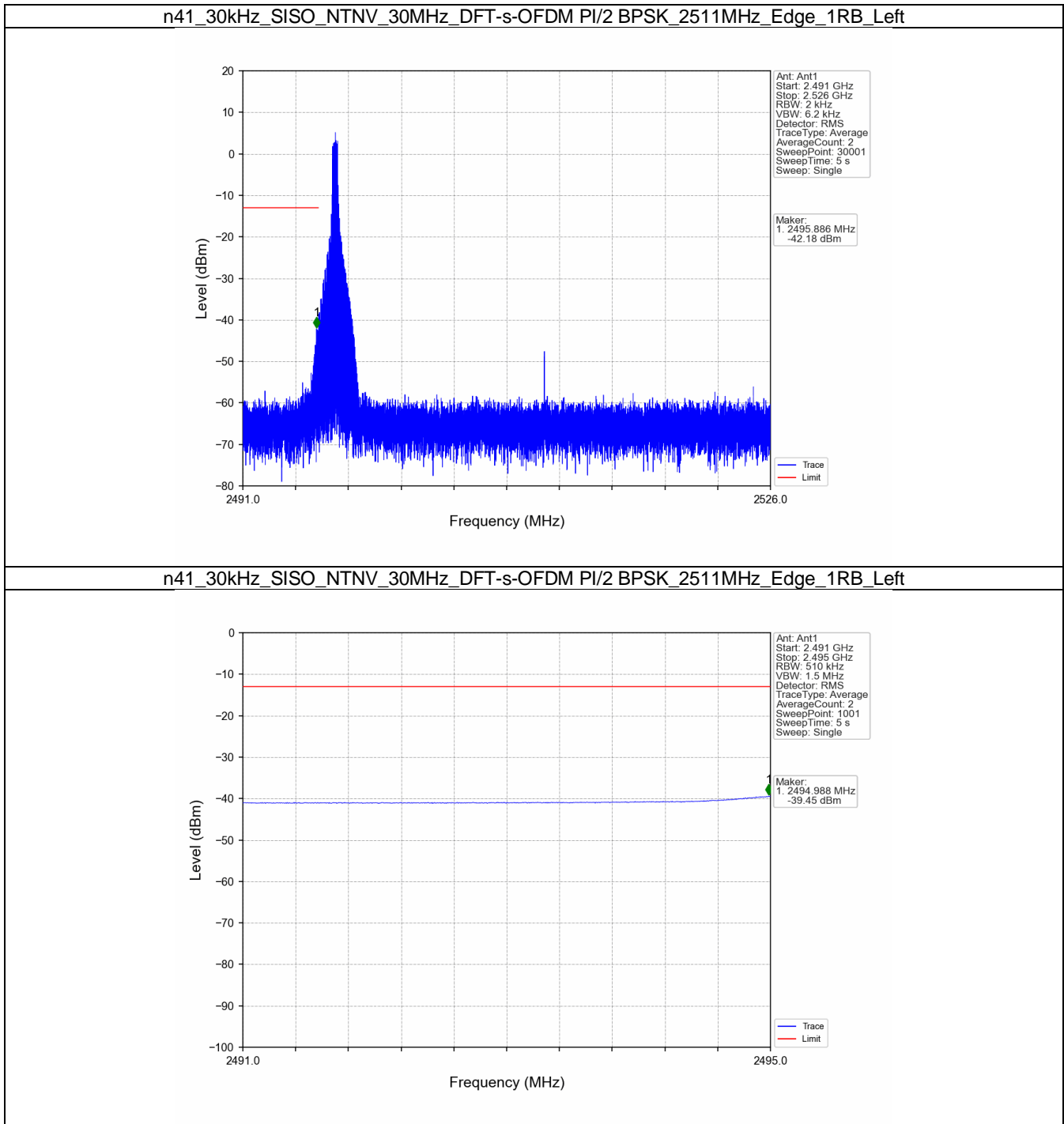


1.2 30k_SISO_30MHz_NTNV

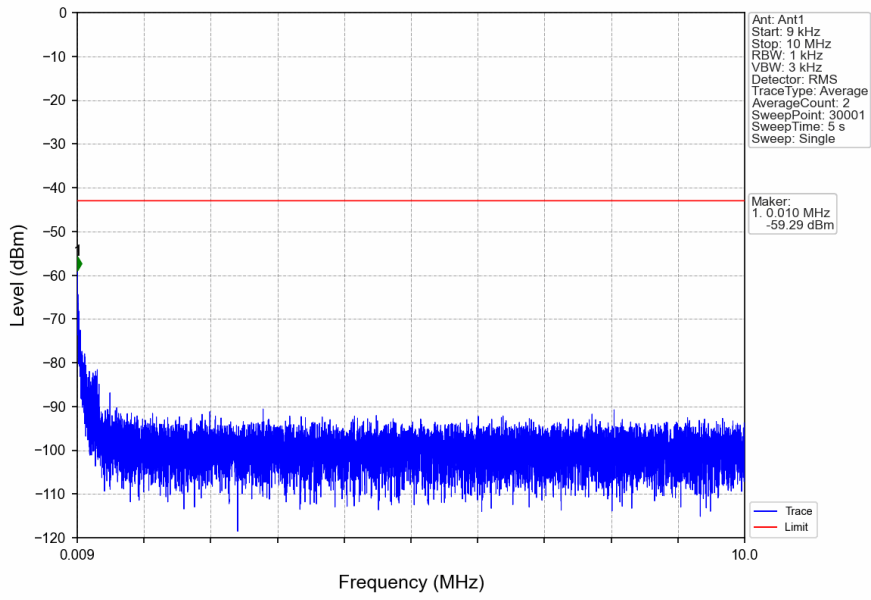
1.2.1 Test Result

5G NR n41 SCS=30kHz SISO 30MHz NTN							
Modulation	Frequency (MHz)	RB Allocation	Spurious Emission				Verdict
			Ant1	Ant2	Sum	Limit	
DFT-s-OFDM PI/2 BPSK	2511	Edge_1RB_Left	Refer To Test Graph			Pass	
		Outer_Full	Refer To Test Graph			Pass	
		Inner_1RB_Left	Refer To Test Graph			Pass	
	2592.99	Edge_1RB_Left	Refer To Test Graph			Pass	
		Edge_1RB_Right	Refer To Test Graph			Pass	
		Outer_Full	Refer To Test Graph			Pass	
	2674.98	Inner_1RB_Right	Refer To Test Graph			Pass	
		Edge_1RB_Left	Refer To Test Graph			Pass	
		Outer_Full	Refer To Test Graph			Pass	
DFT-s-OFDM QPSK	2511	Inner_1RB_Left	Refer To Test Graph			Pass	
		Edge_1RB_Left	Refer To Test Graph			Pass	
		Edge_1RB_Right	Refer To Test Graph			Pass	
	2592.99	Outer_Full	Refer To Test Graph			Pass	
		Inner_1RB_Right	Refer To Test Graph			Pass	
		Edge_1RB_Left	Refer To Test Graph			Pass	
	2674.98	Outer_Full	Refer To Test Graph			Pass	
		Inner_1RB_Right	Refer To Test Graph			Pass	
		Edge_1RB_Left	Refer To Test Graph			Pass	
CP-OFDM QPSK	2511	Outer_Full	Refer To Test Graph			Pass	
		Inner_1RB_Left	Refer To Test Graph			Pass	
		Edge_1RB_Left	Refer To Test Graph			Pass	
	2592.99	Edge_1RB_Right	Refer To Test Graph			Pass	
		Outer_Full	Refer To Test Graph			Pass	
		Edge_1RB_Left	Refer To Test Graph			Pass	
	2674.98	Edge_1RB_Right	Refer To Test Graph			Pass	
		Outer_Full	Refer To Test Graph			Pass	
		Inner_1RB_Right	Refer To Test Graph			Pass	

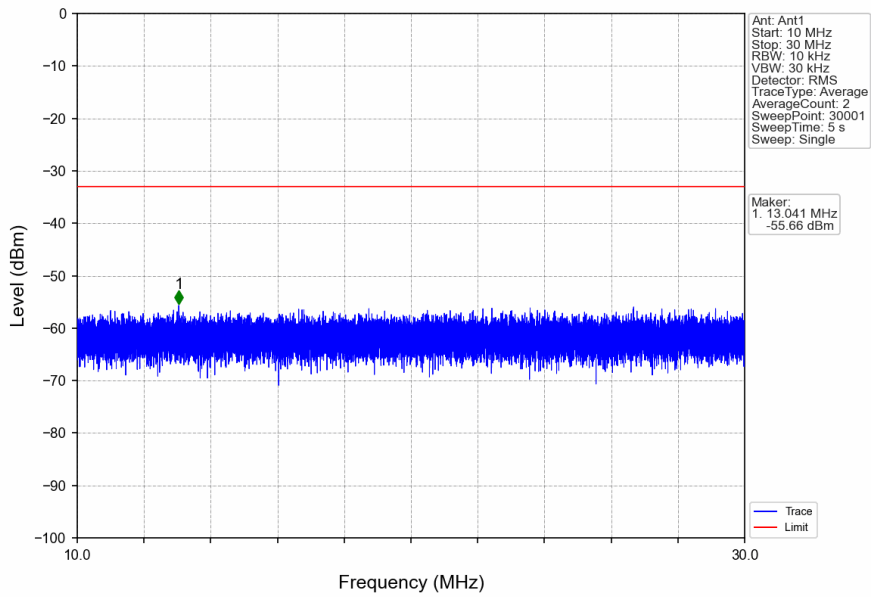
1.2.2 Test Graph



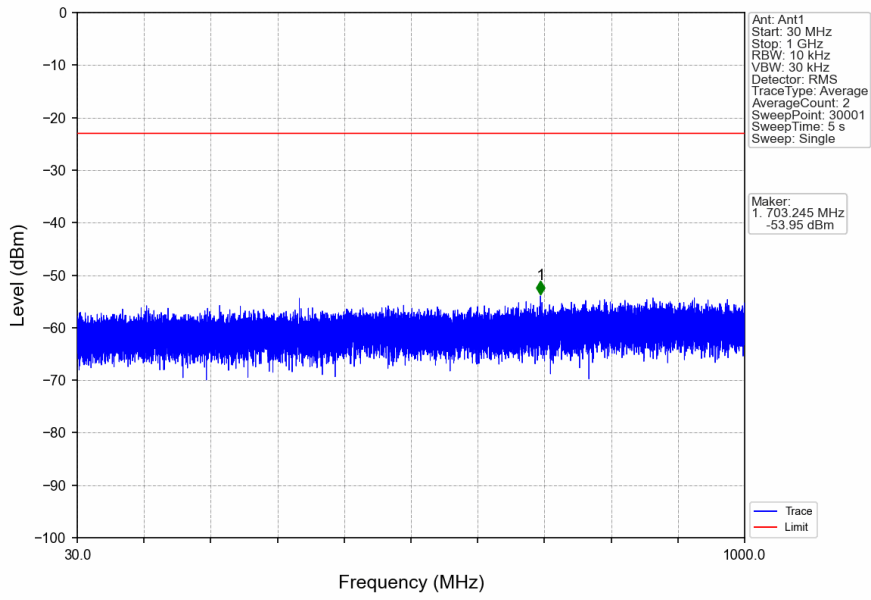
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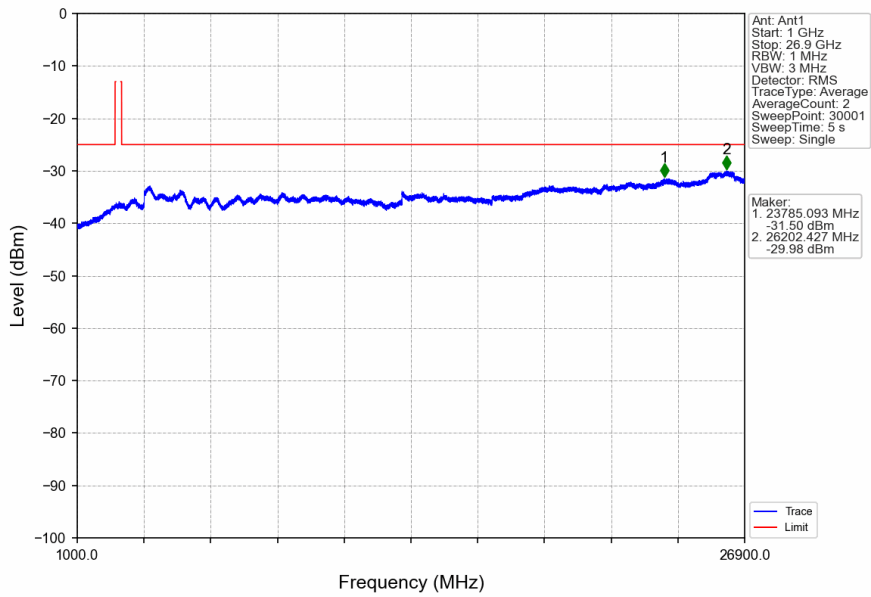
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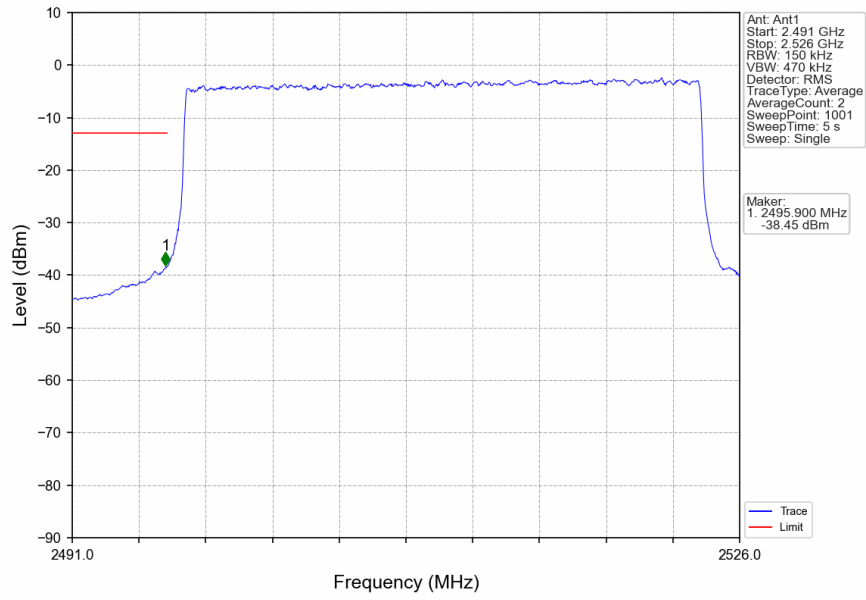
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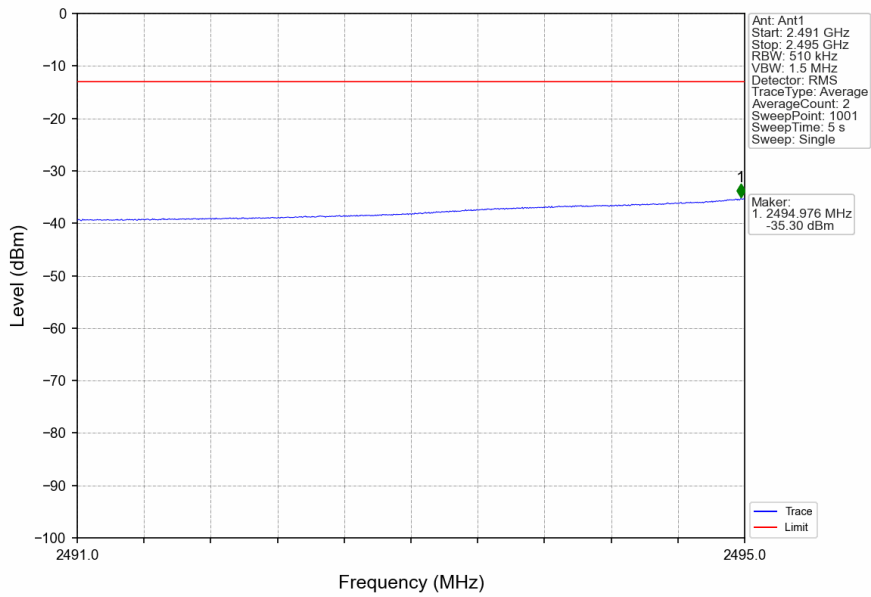
n41_30kHz_SISO_NTNV_30MHz_DFT-s-OFDM PI/2 BPSK_2511MHz_Edge_1RB_Left



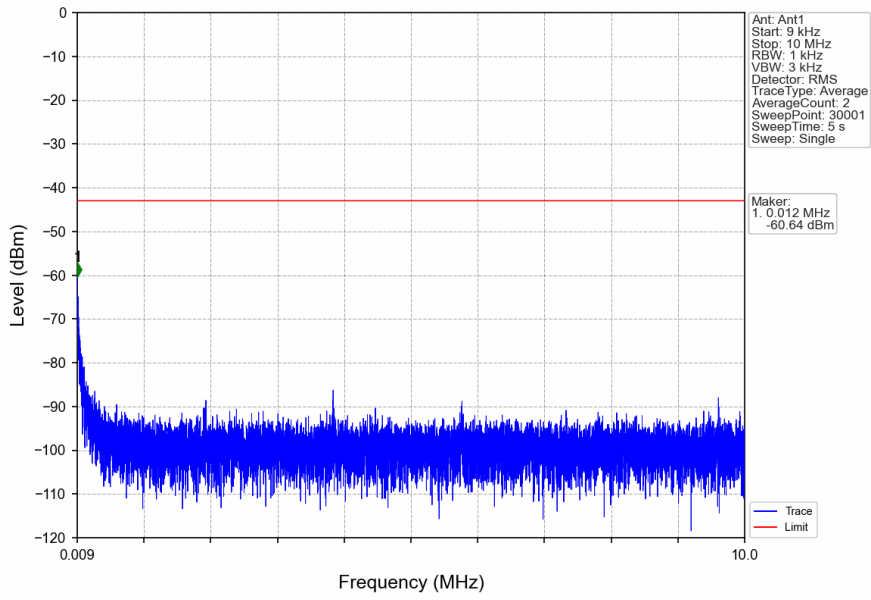
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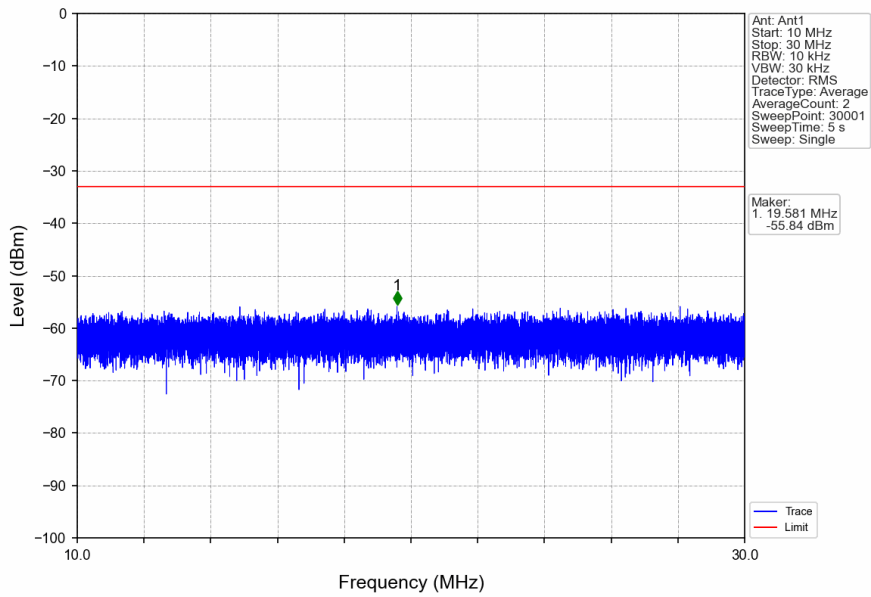
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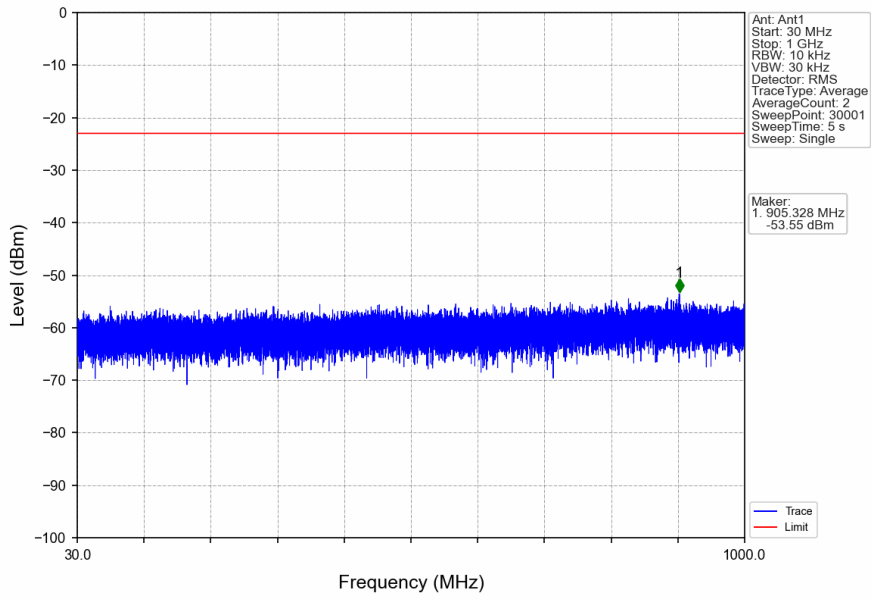
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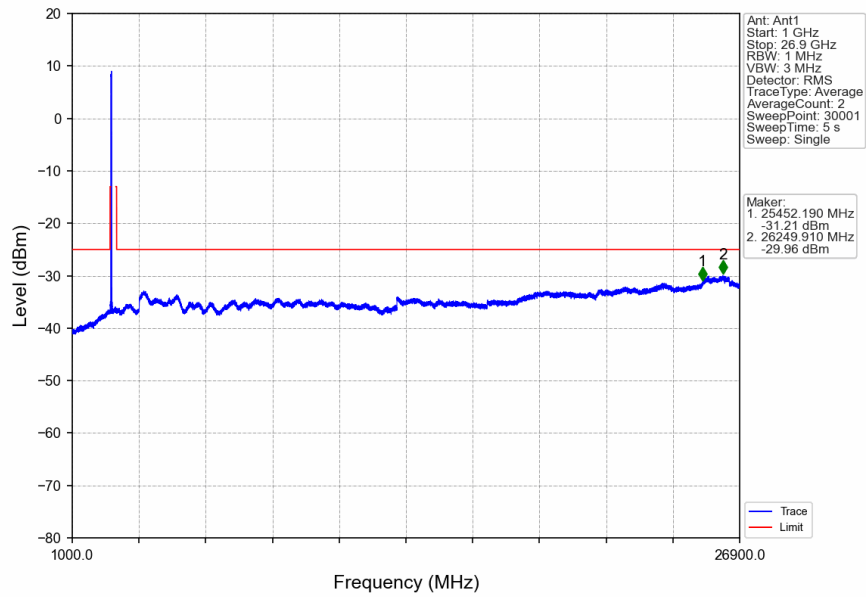
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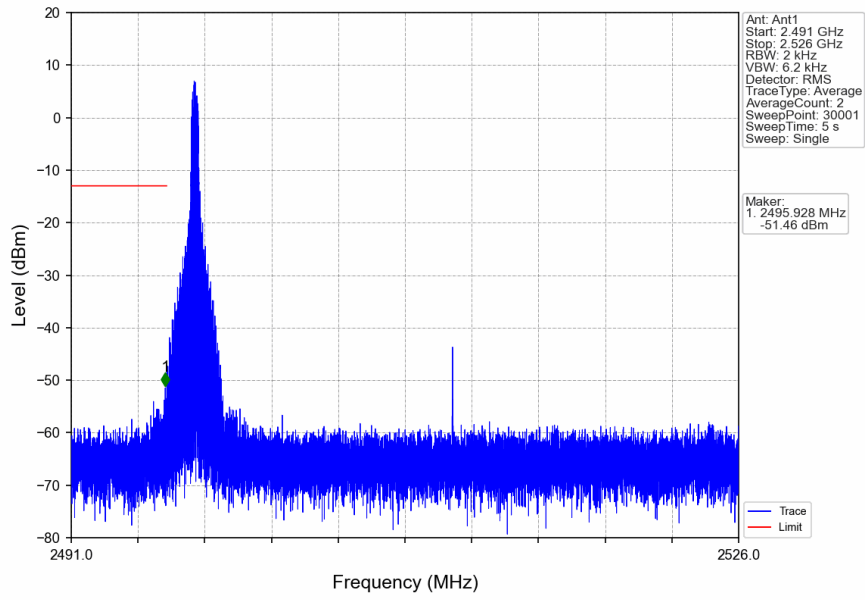
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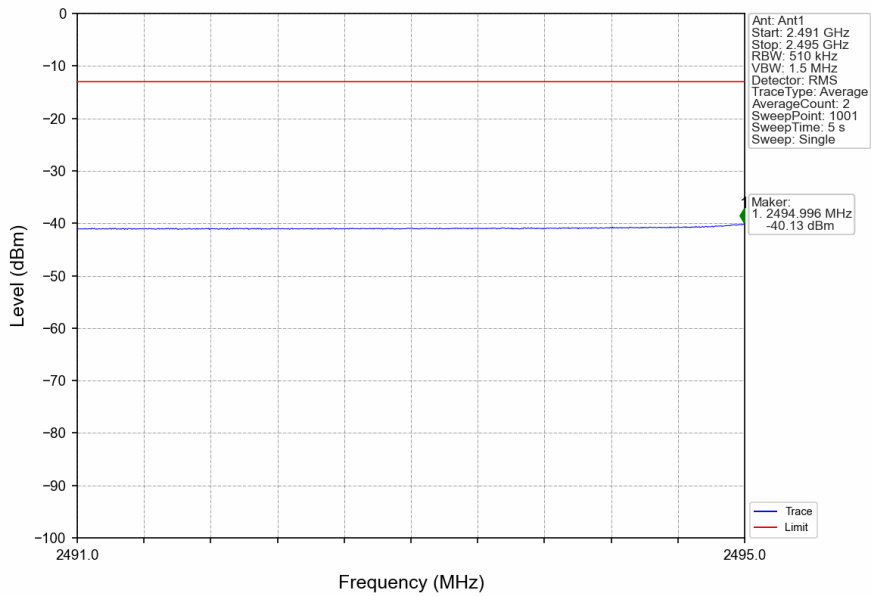
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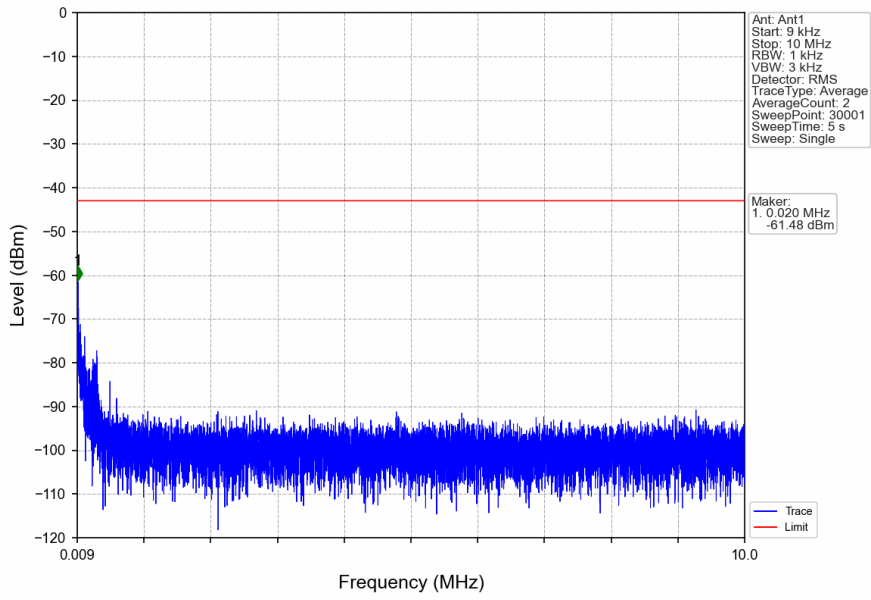
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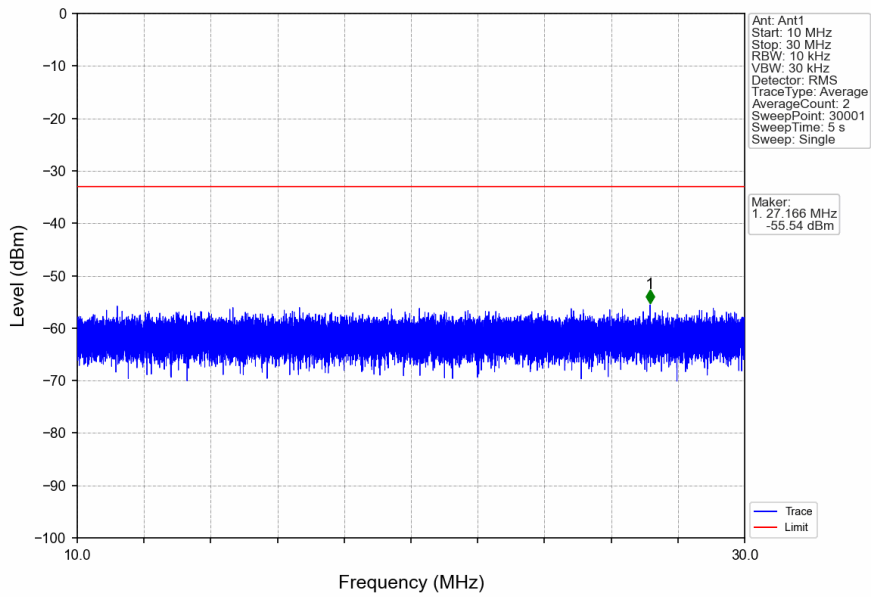
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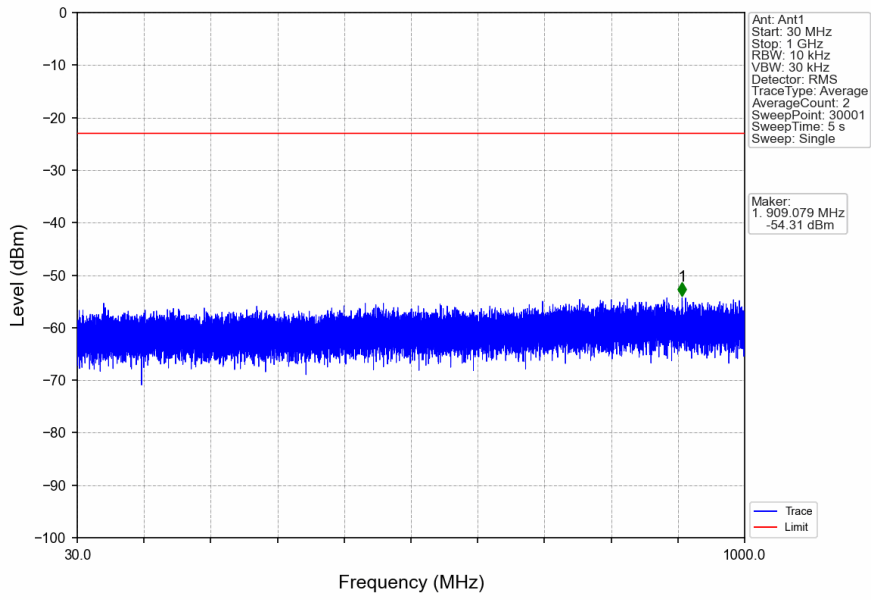
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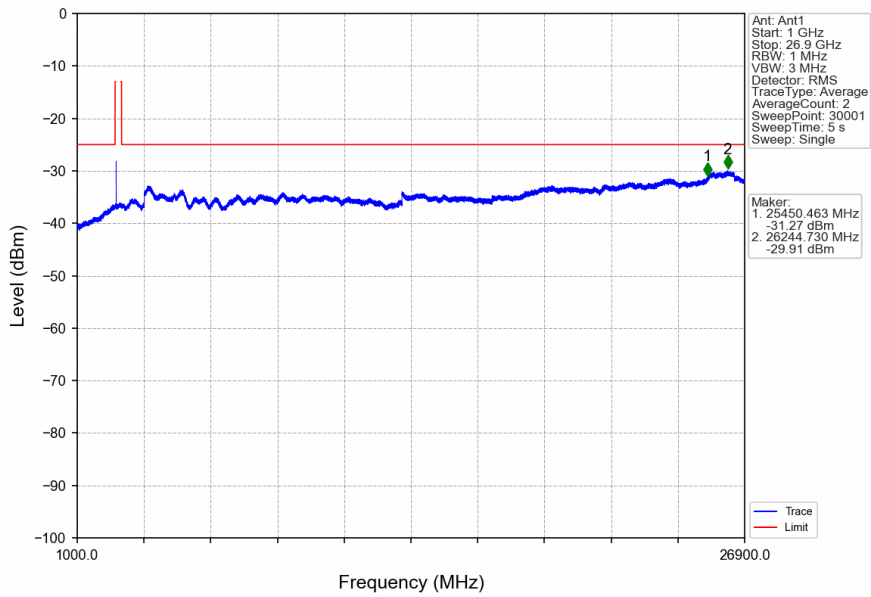
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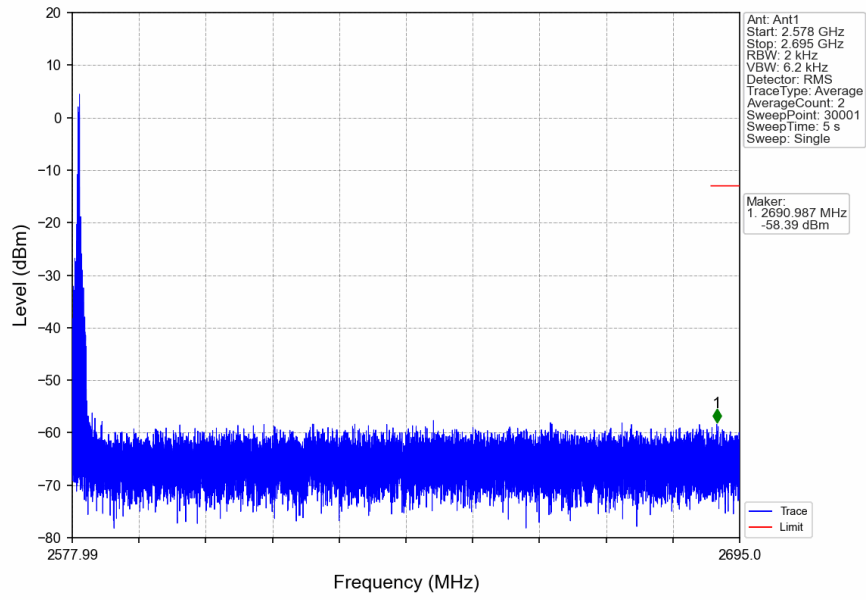
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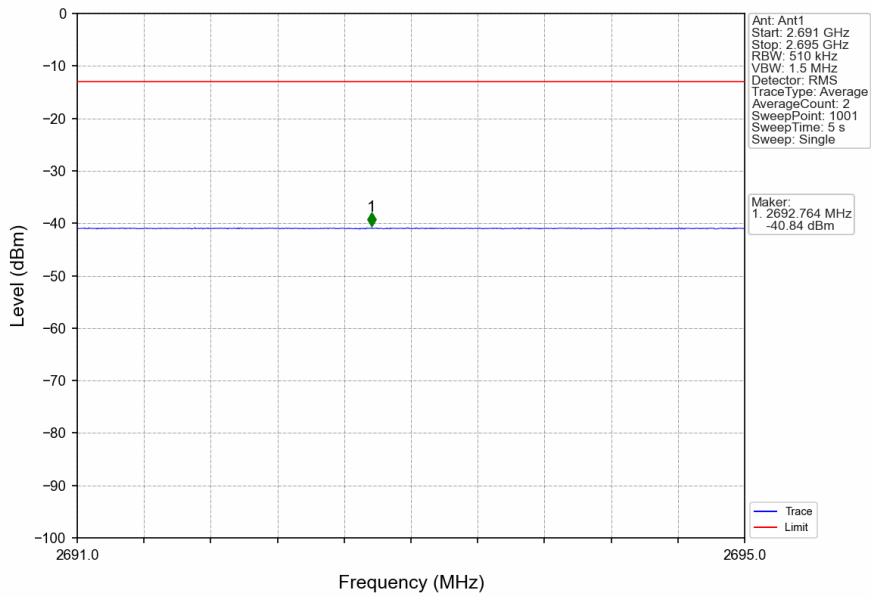
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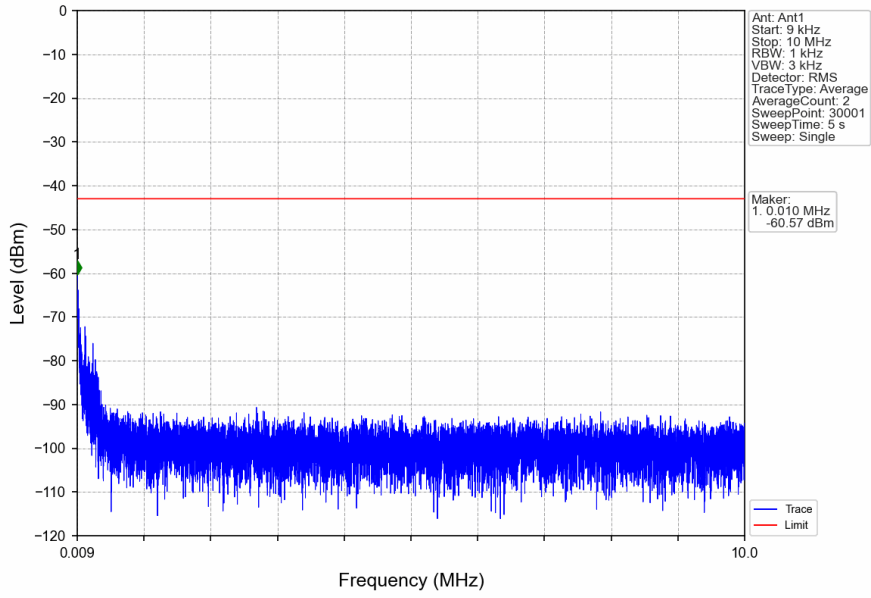
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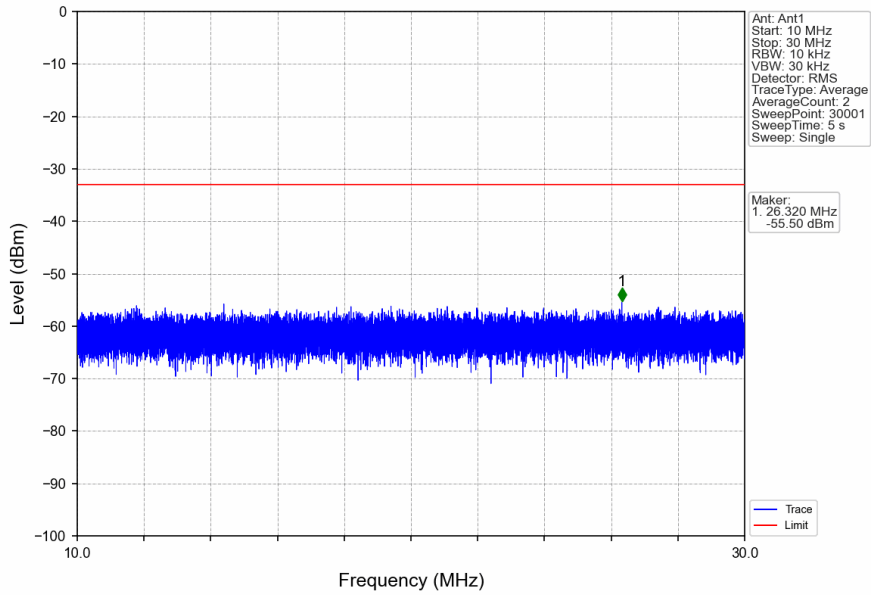
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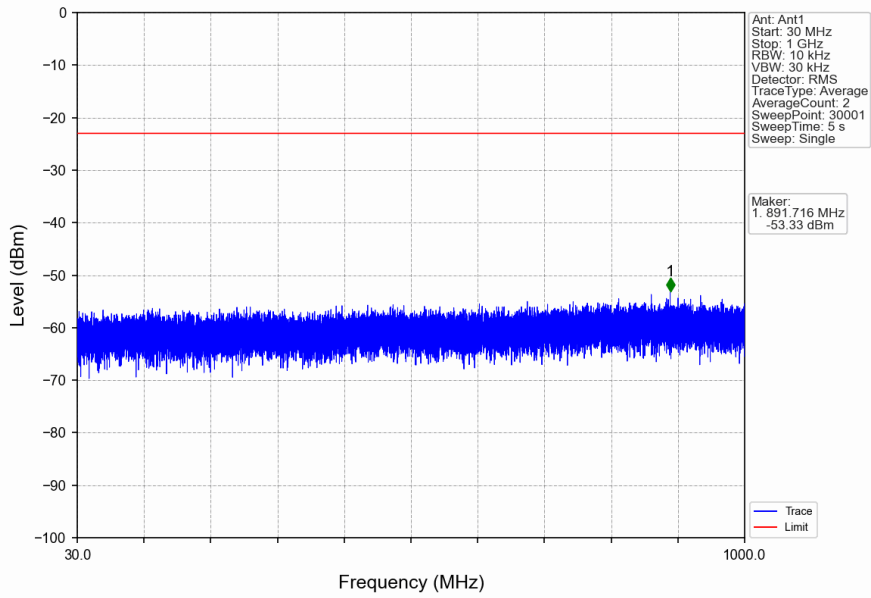
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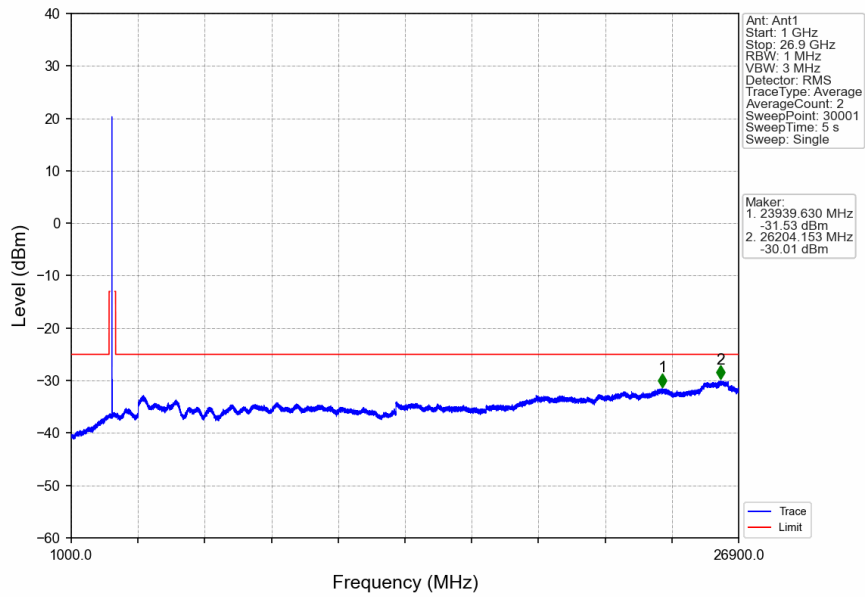
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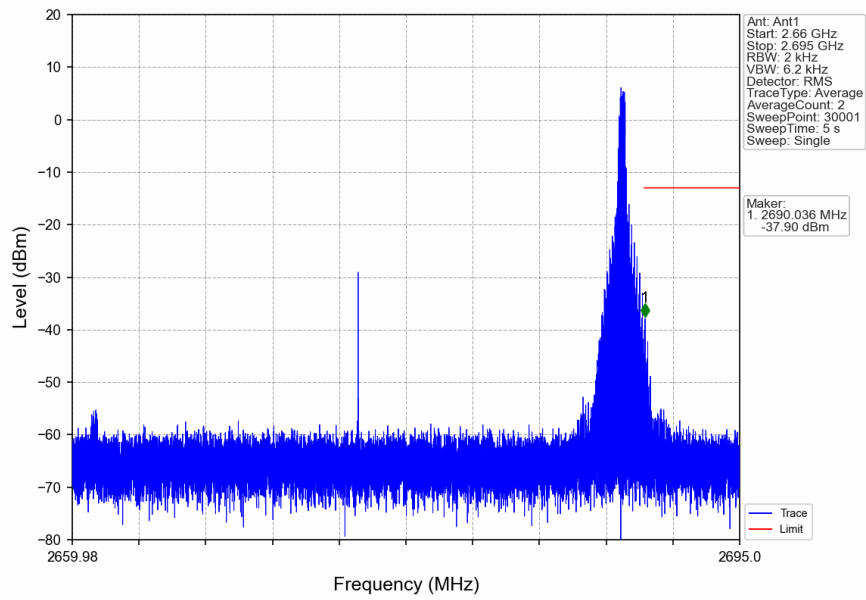
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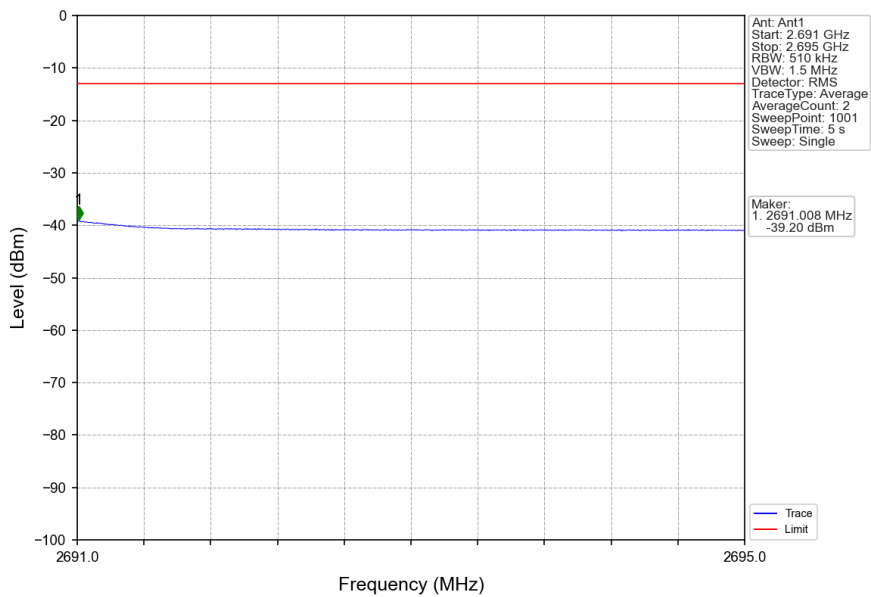
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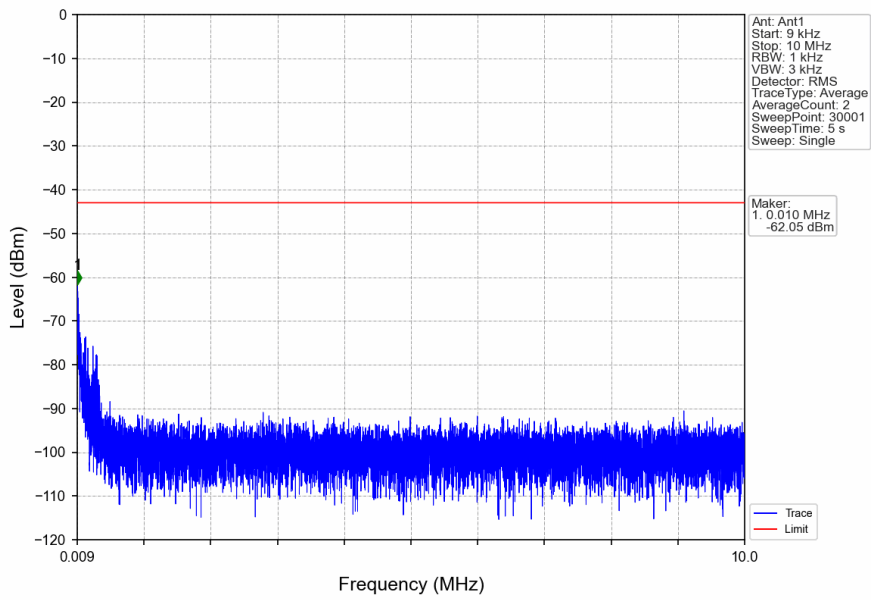
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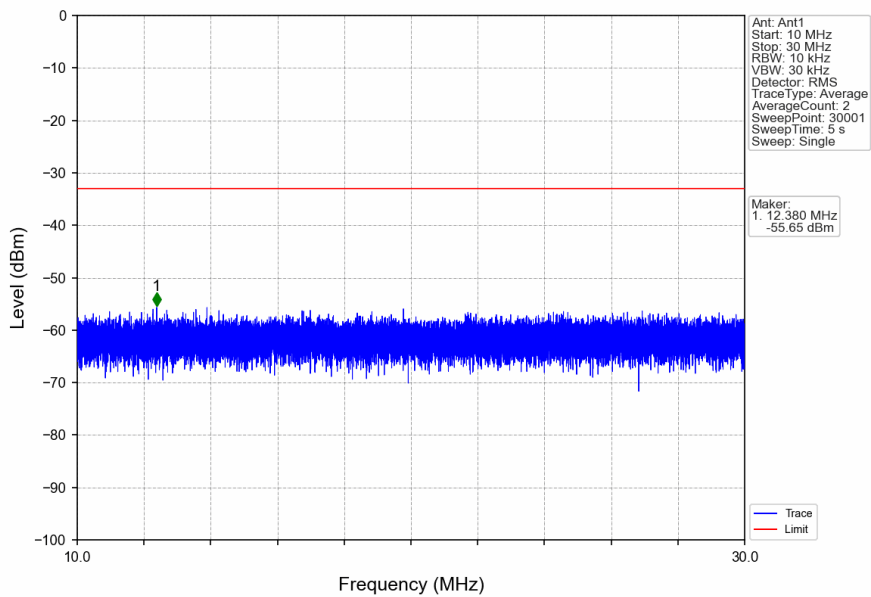
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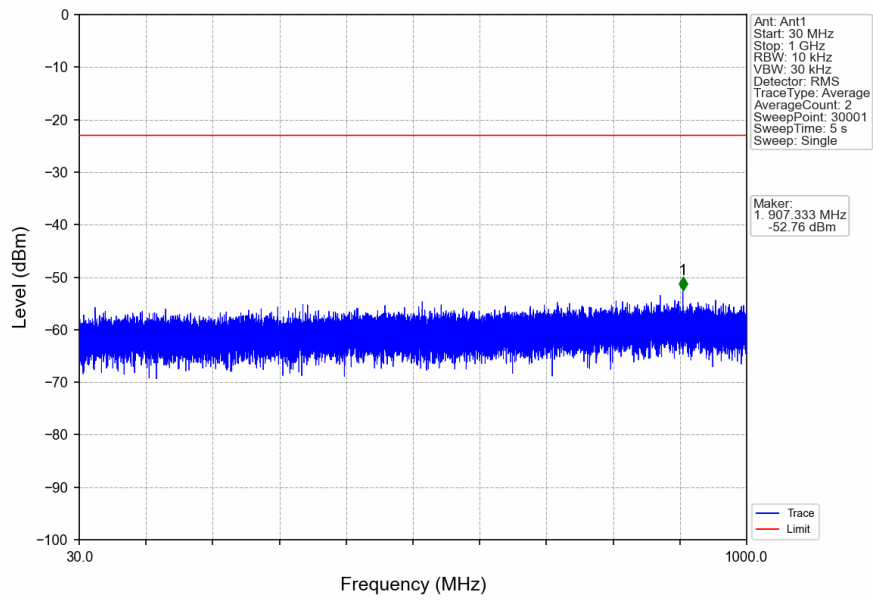
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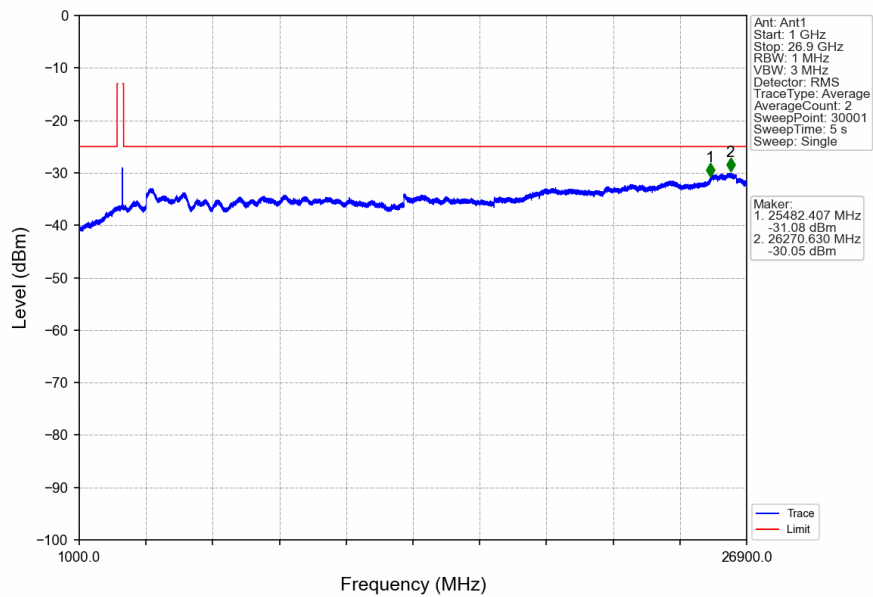
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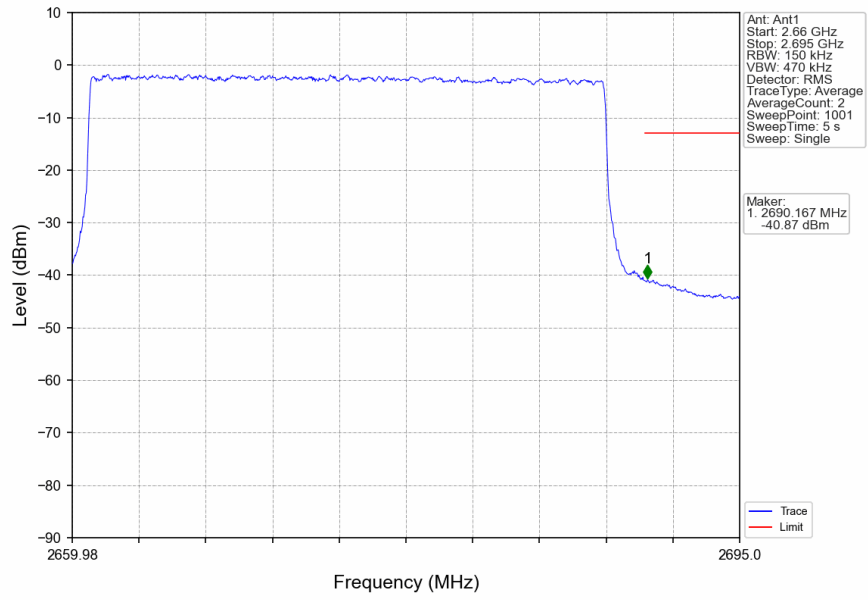
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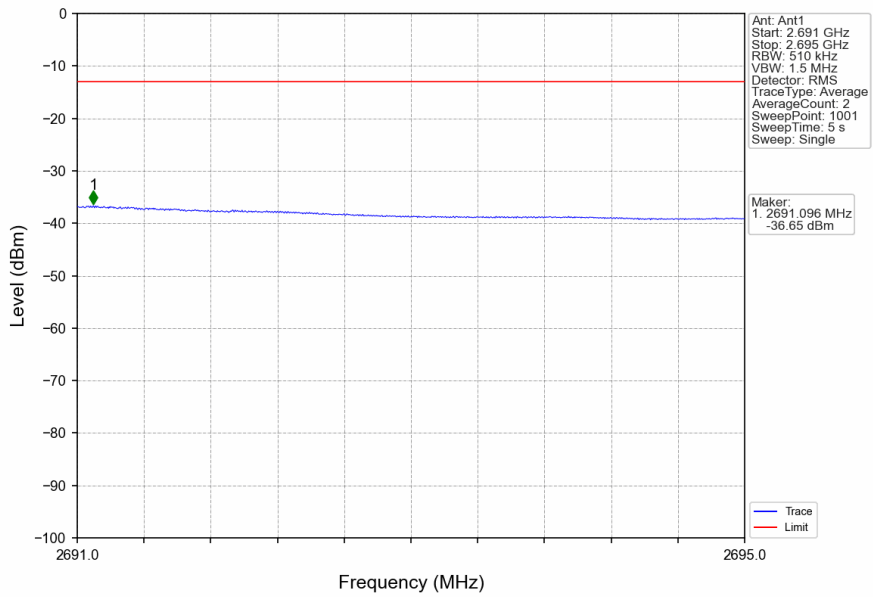
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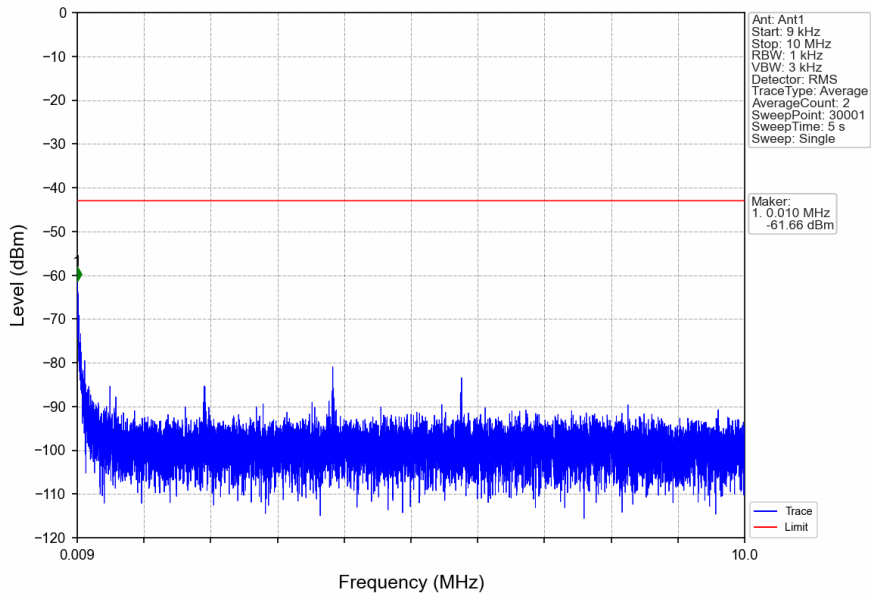
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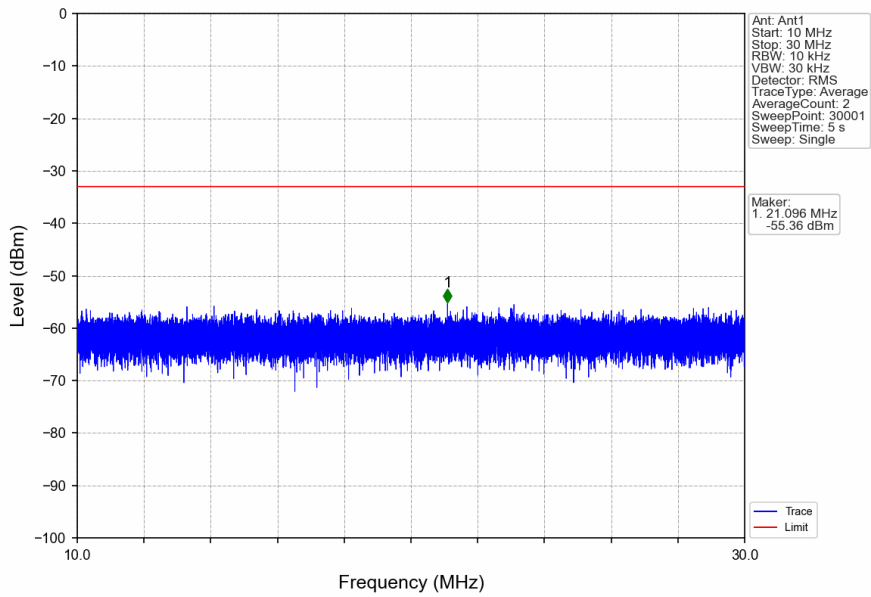
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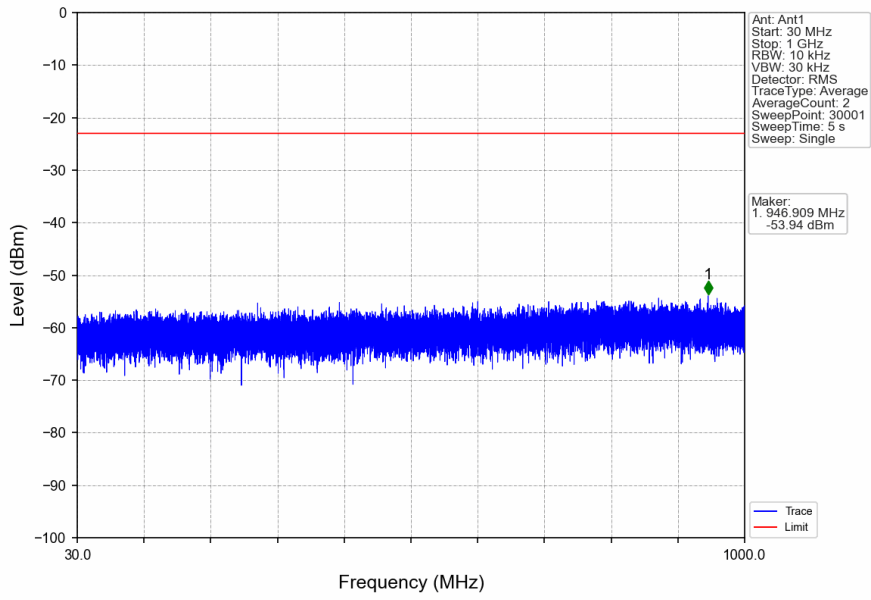
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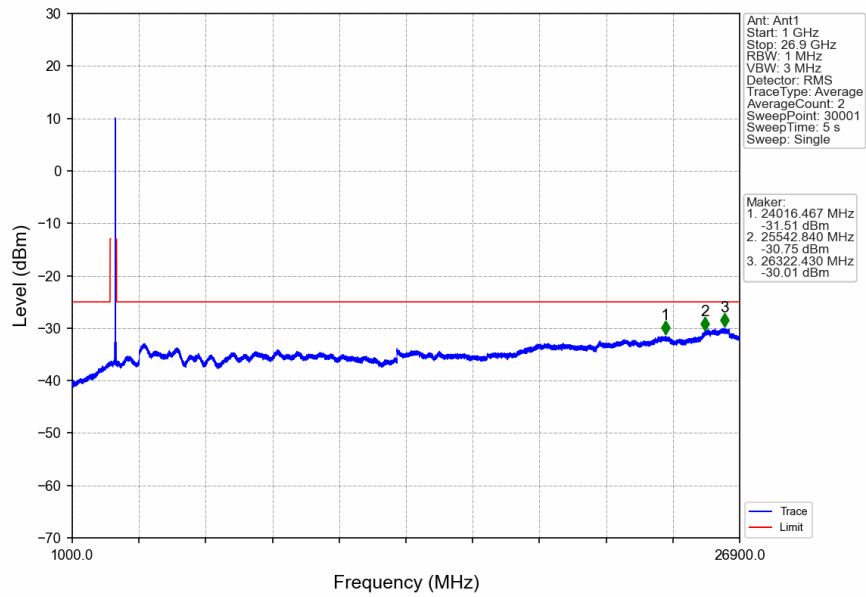
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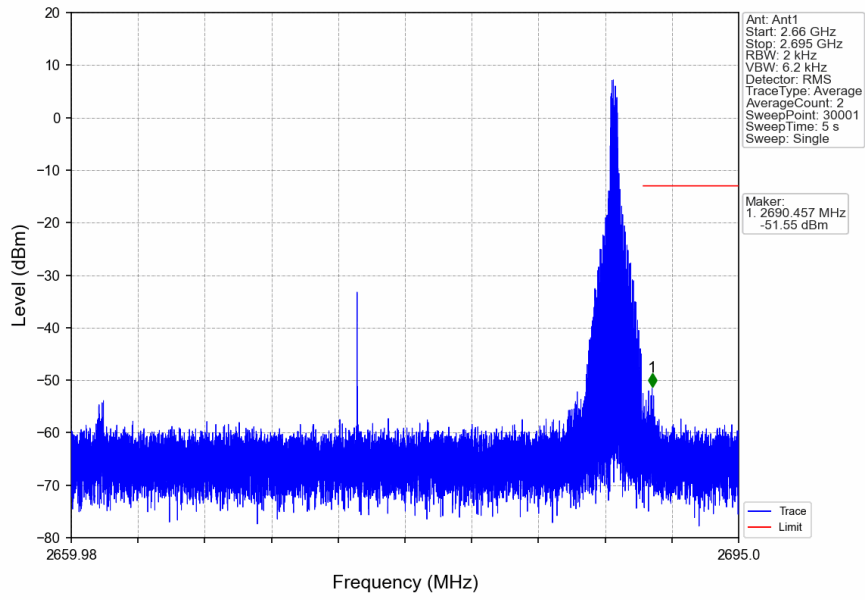
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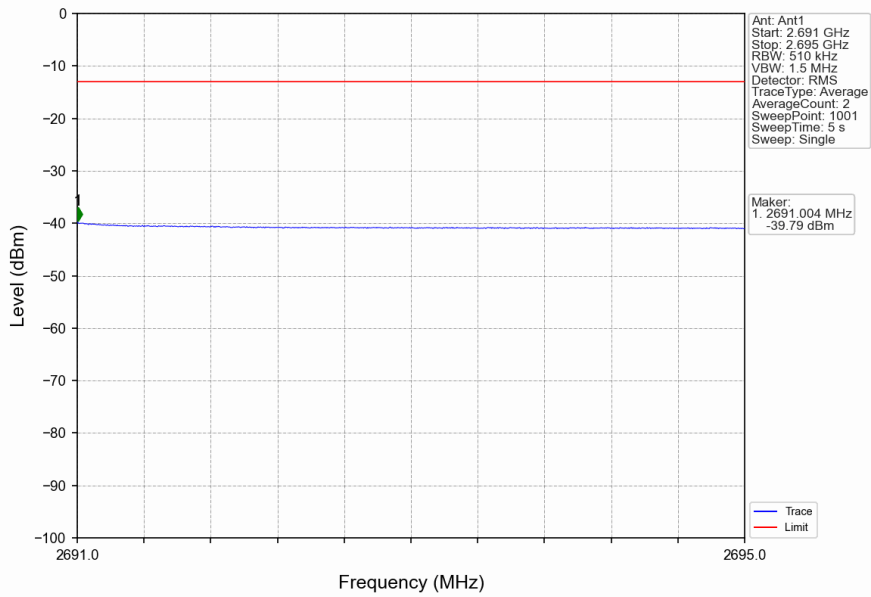
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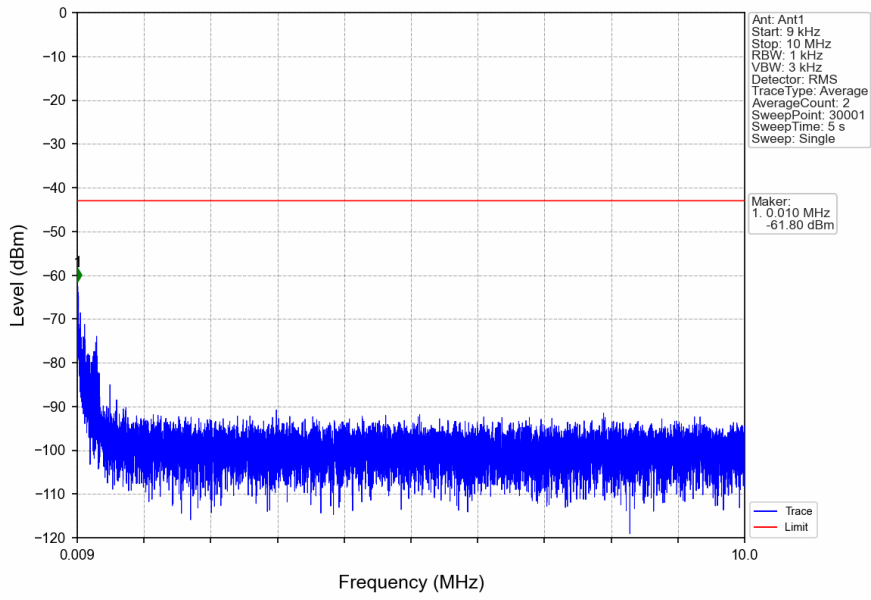
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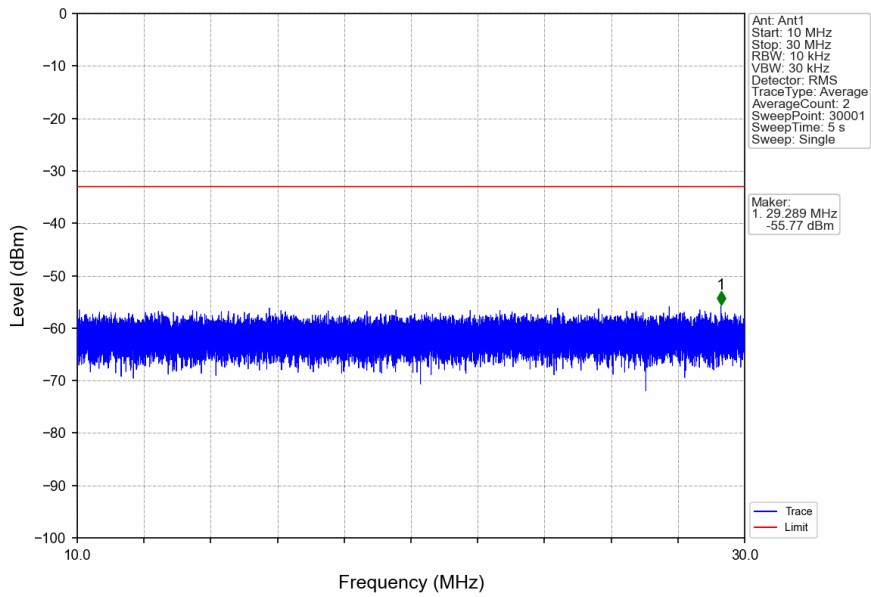
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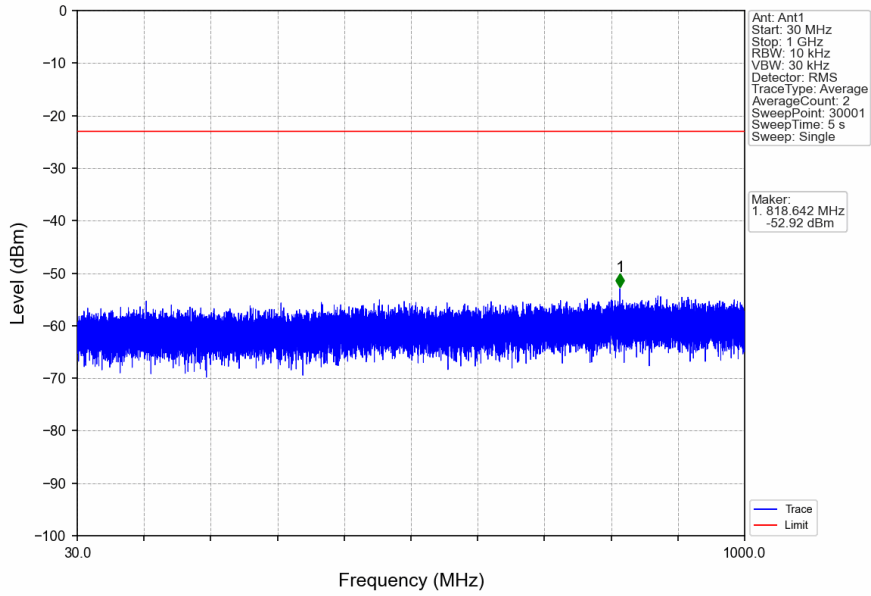
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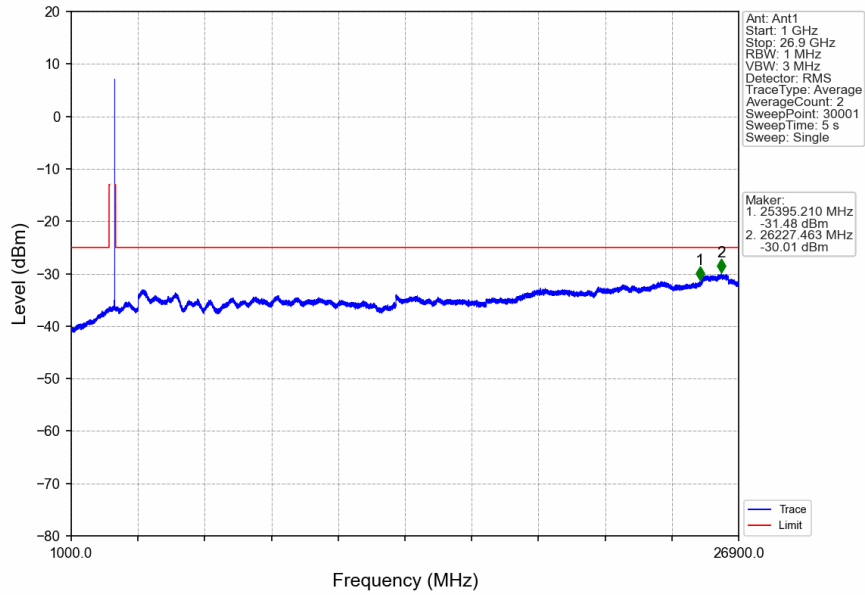
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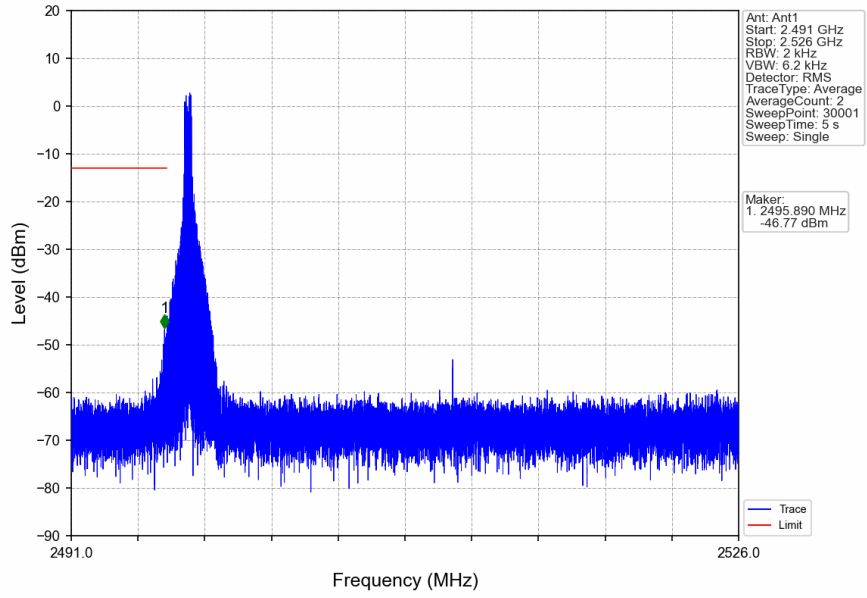
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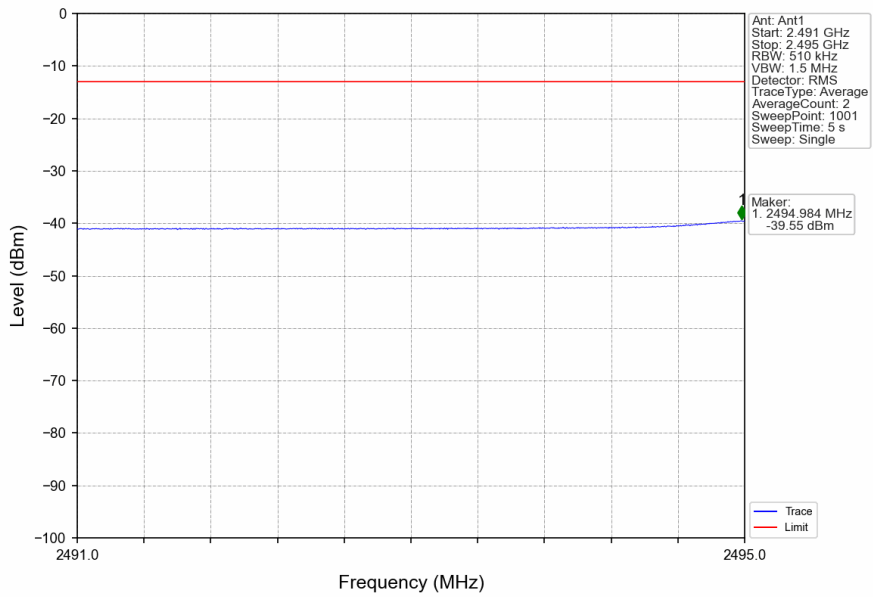
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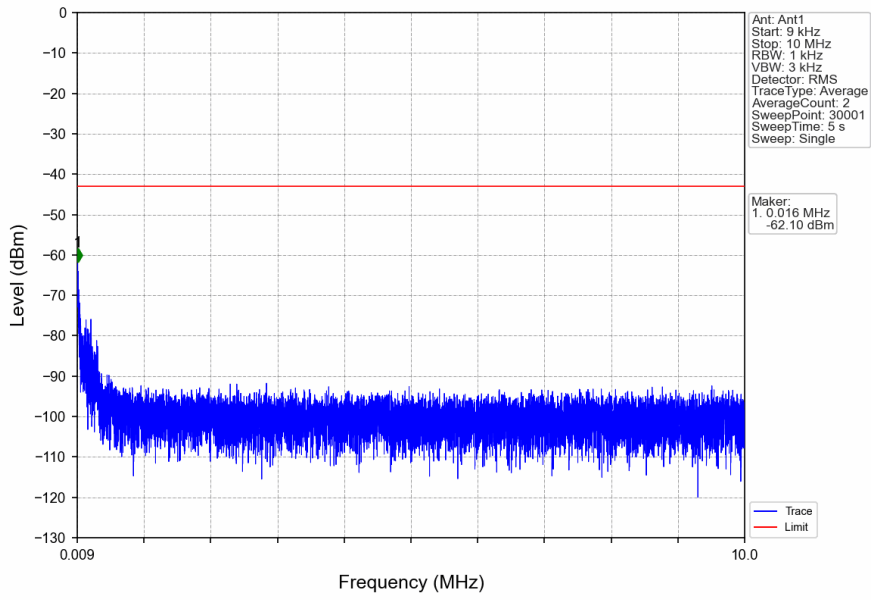
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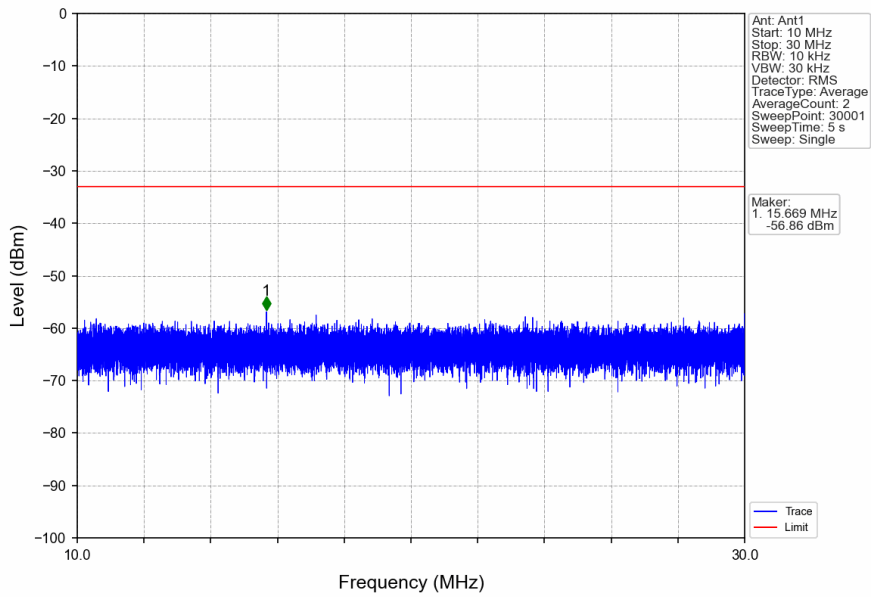
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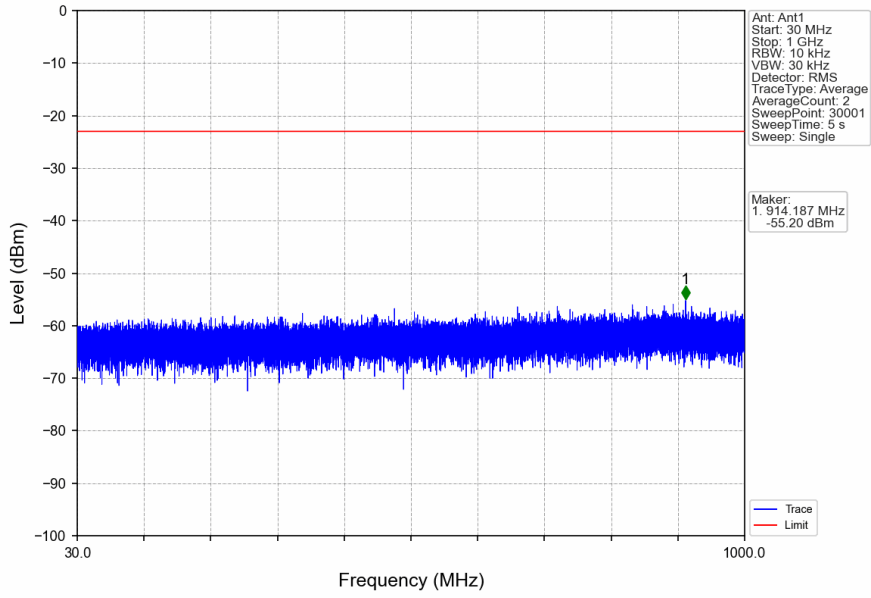
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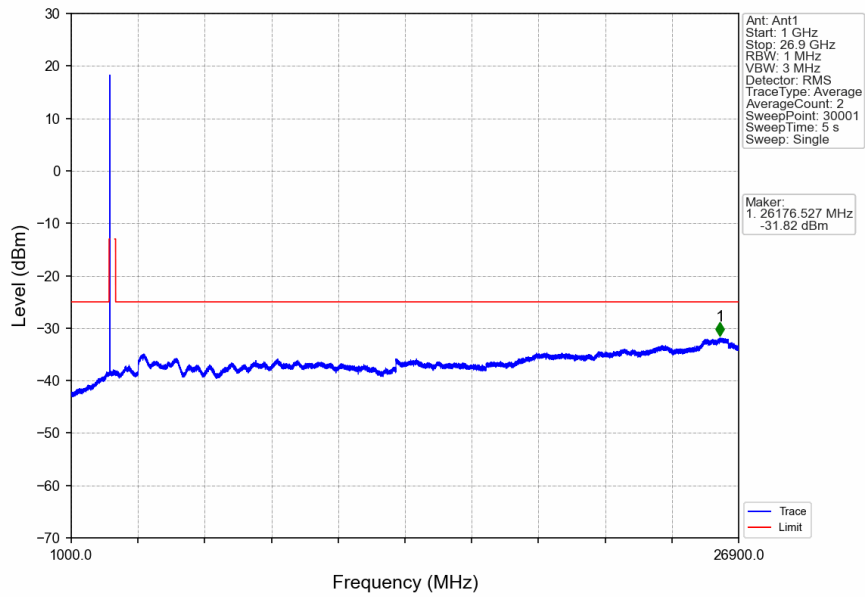
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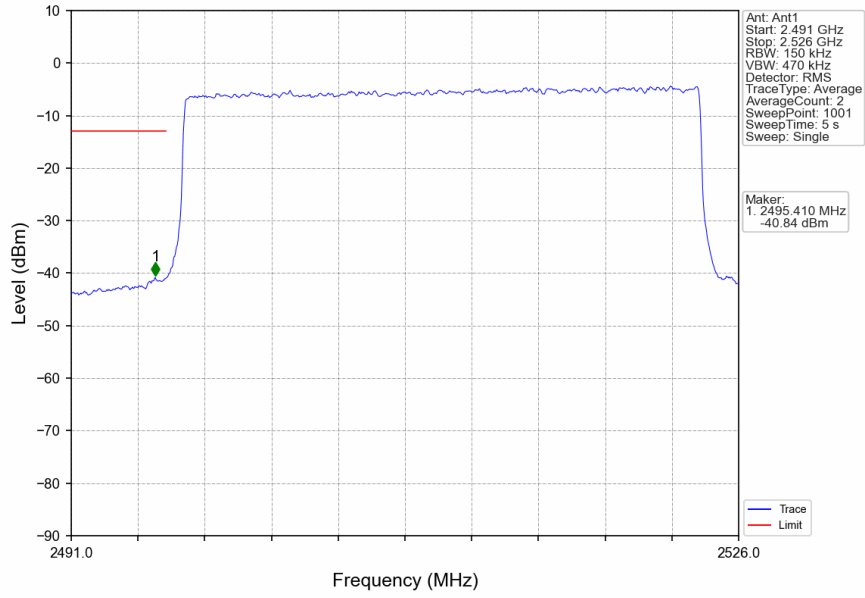
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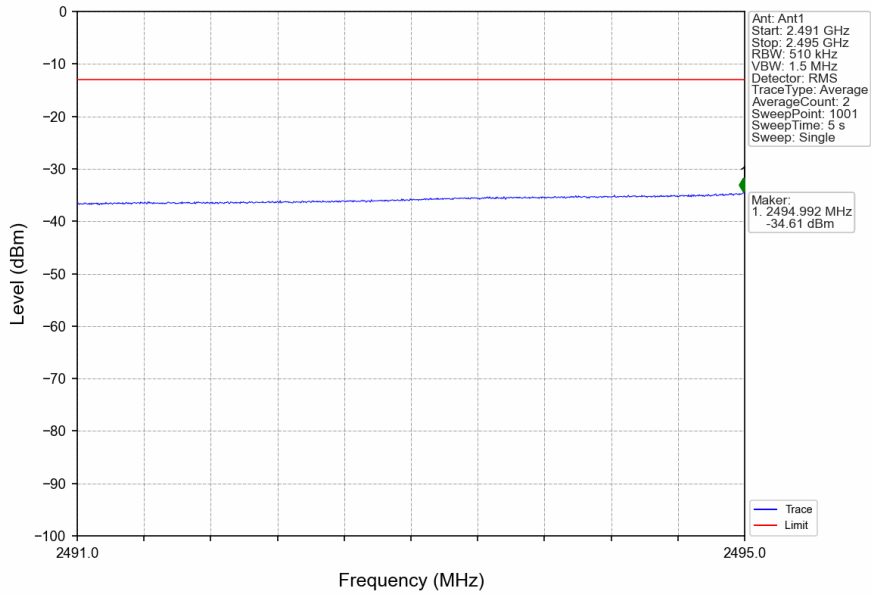
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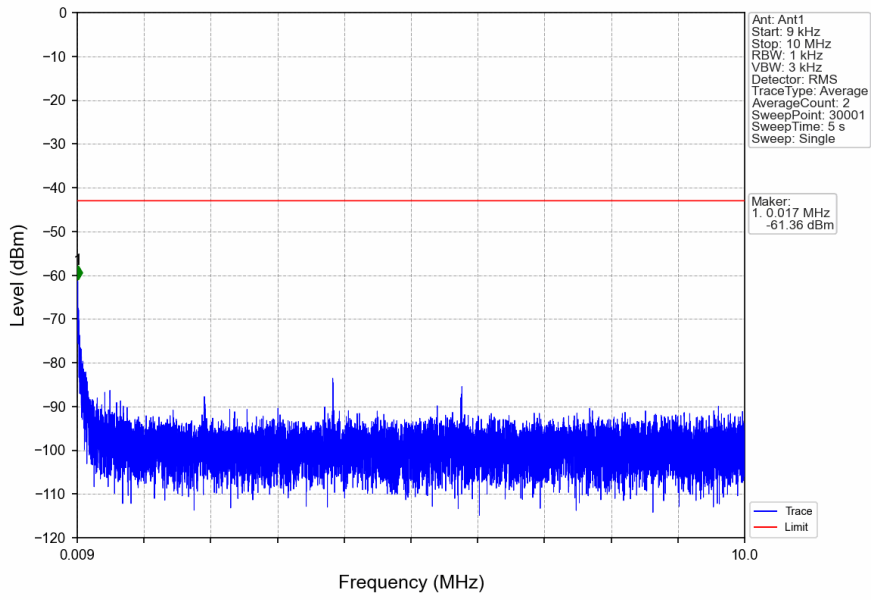
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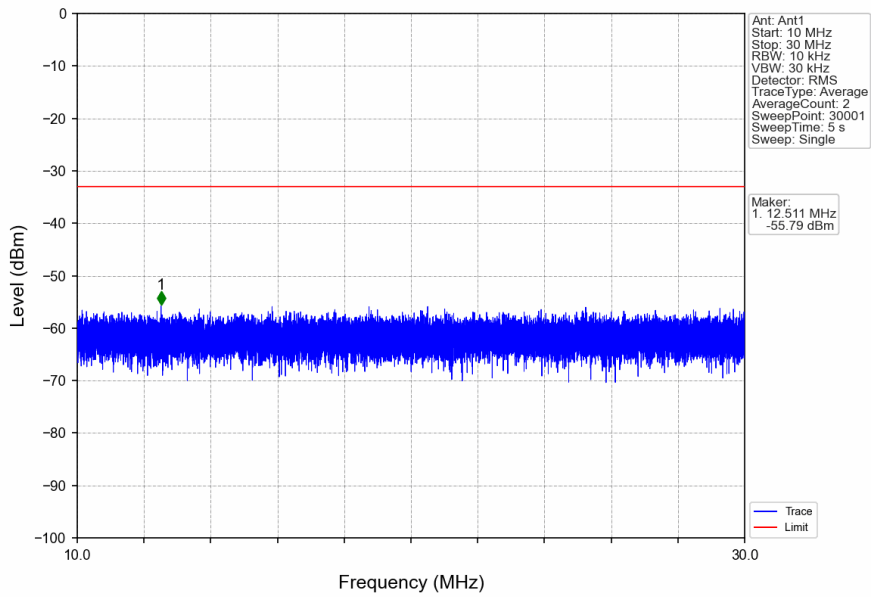
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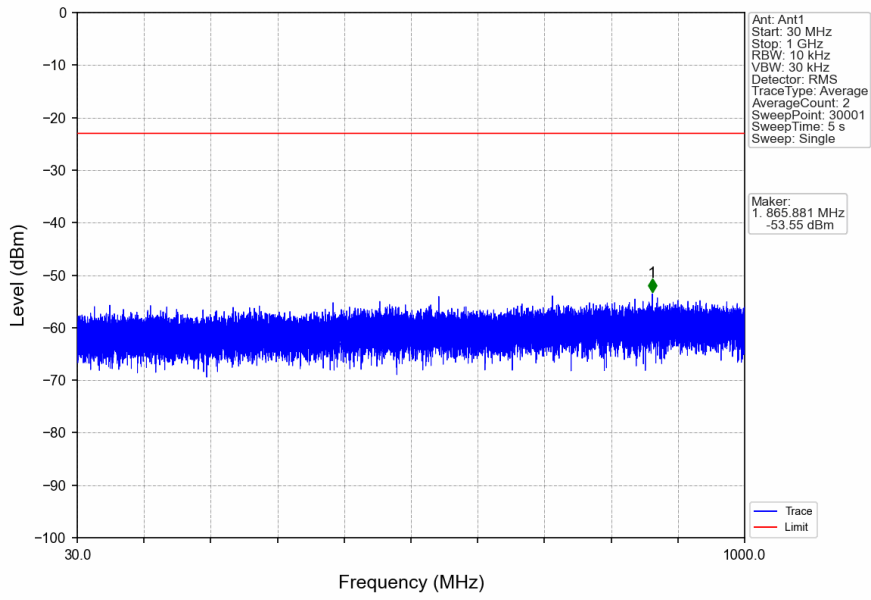
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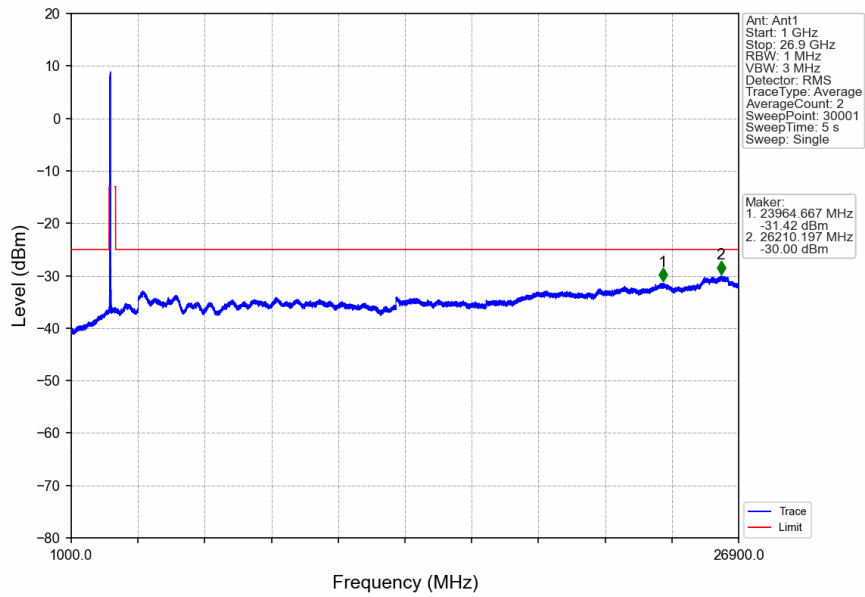
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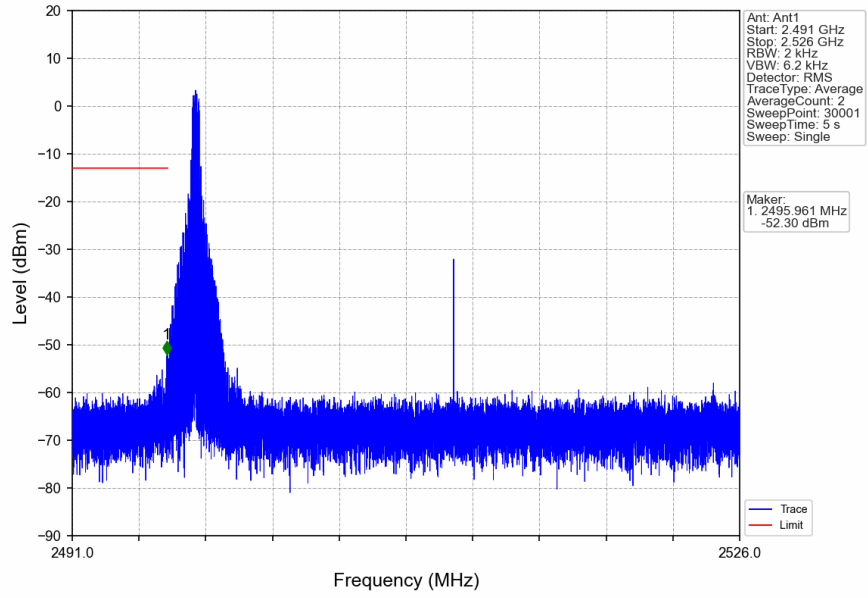
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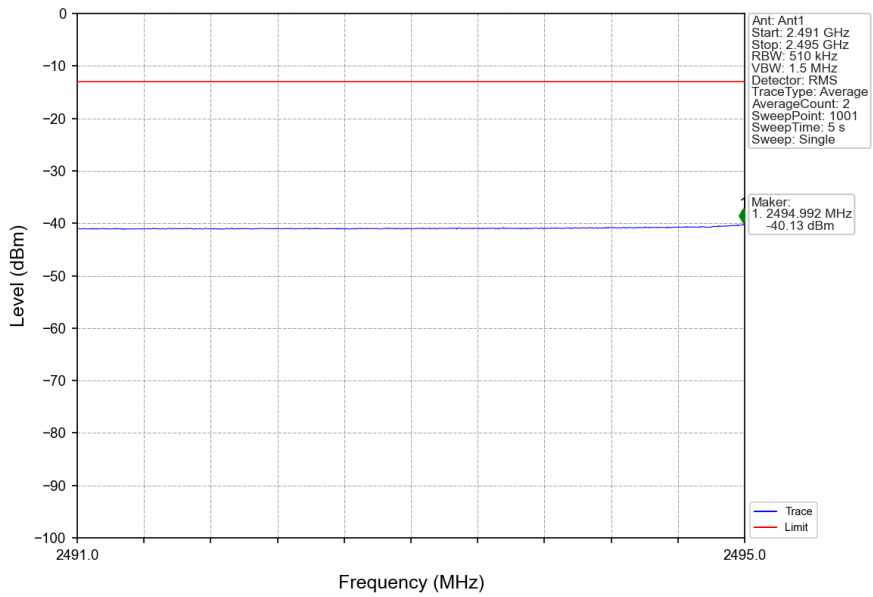
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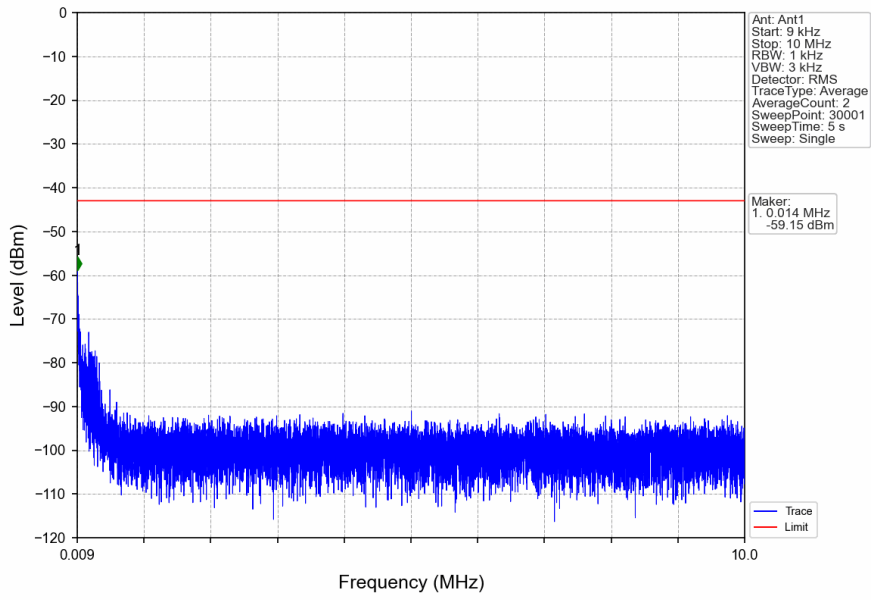
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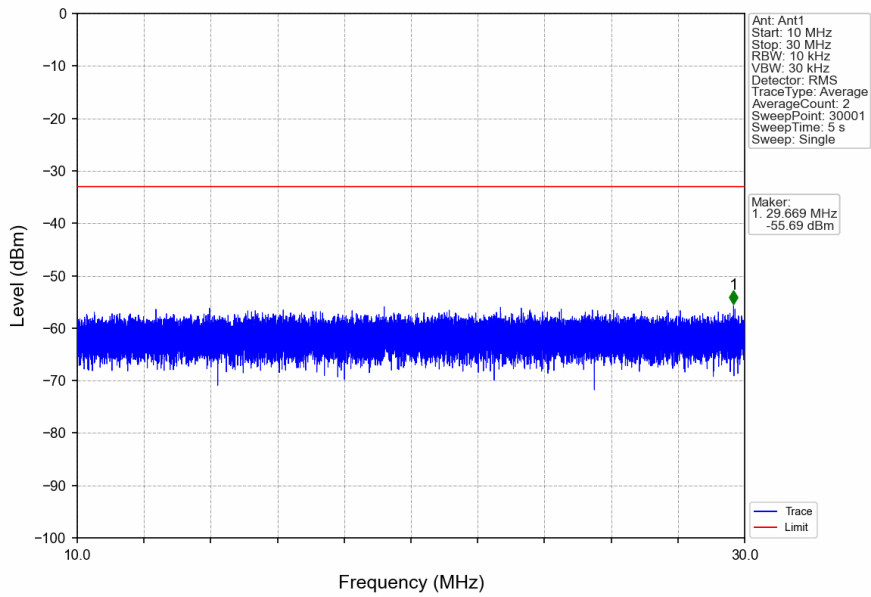
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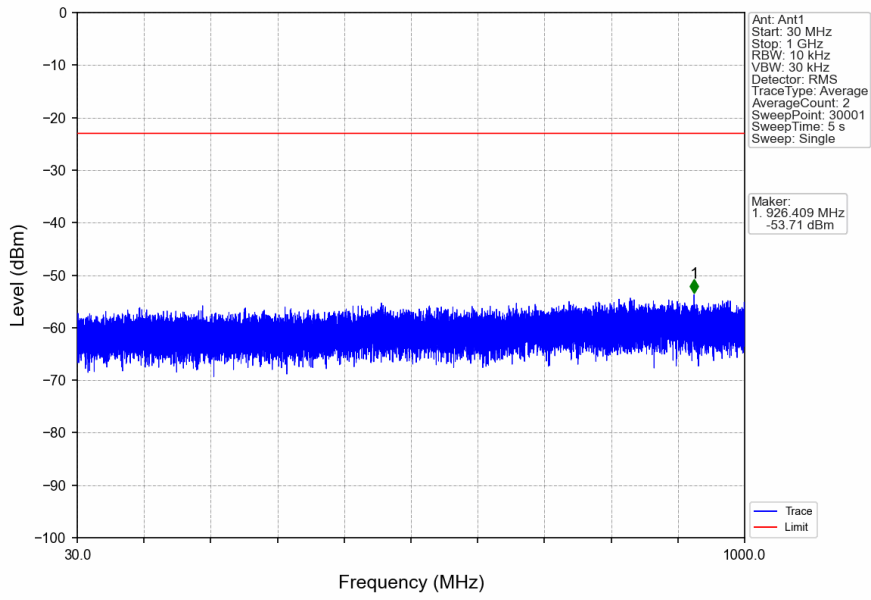
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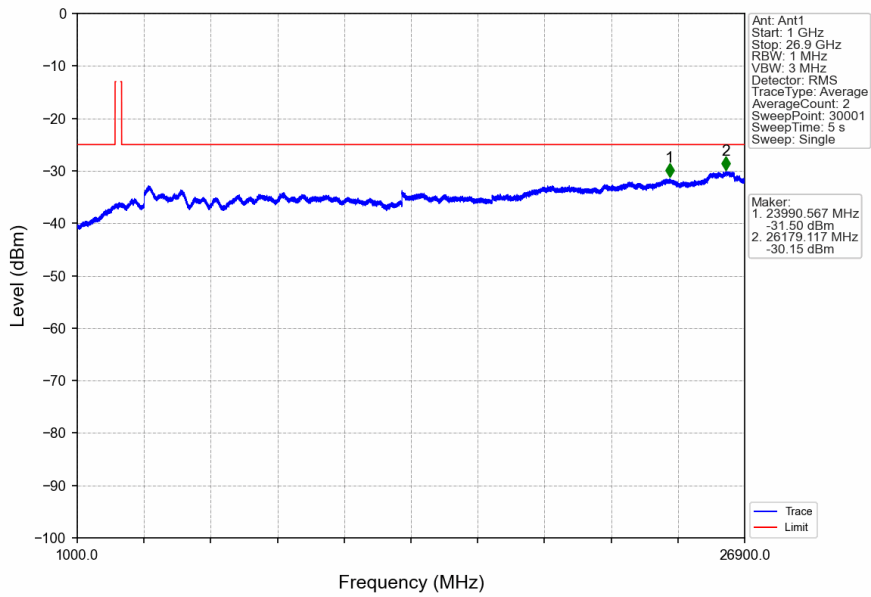
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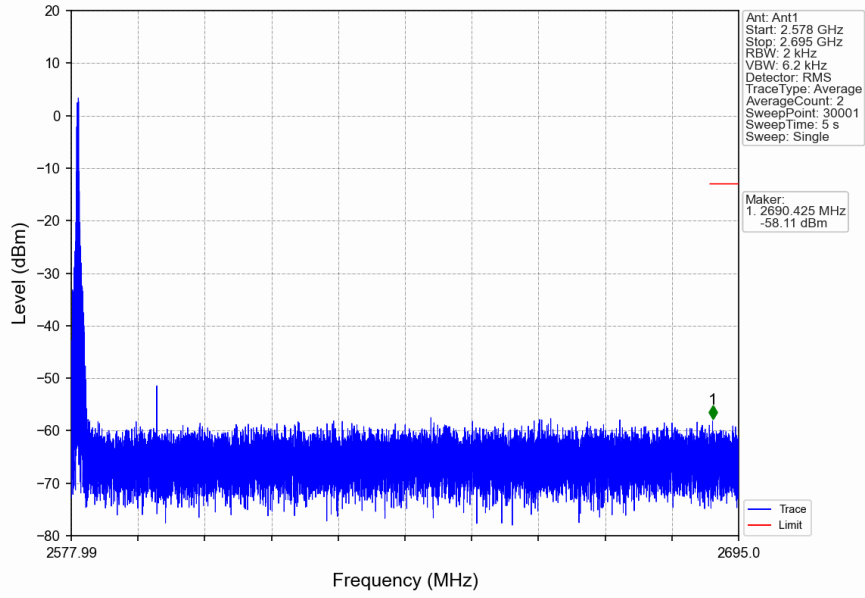
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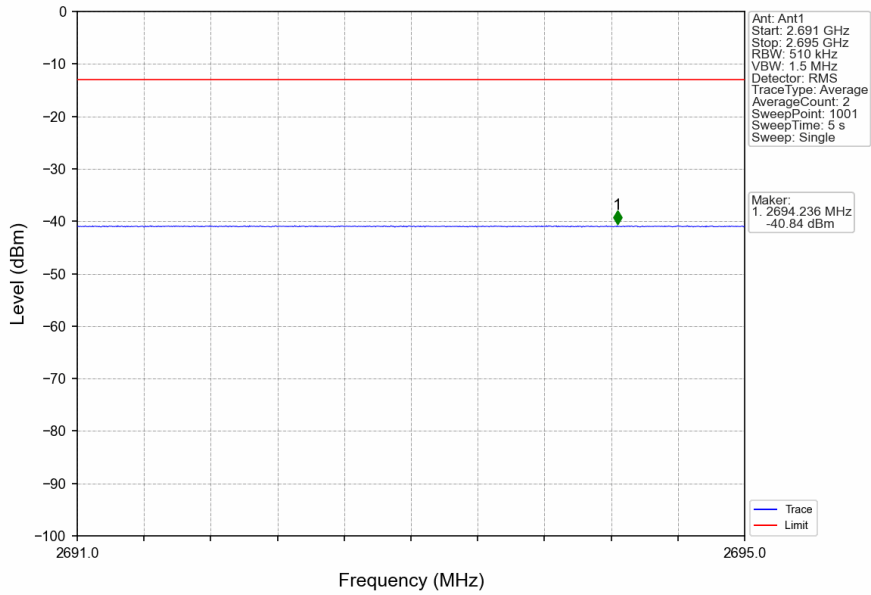
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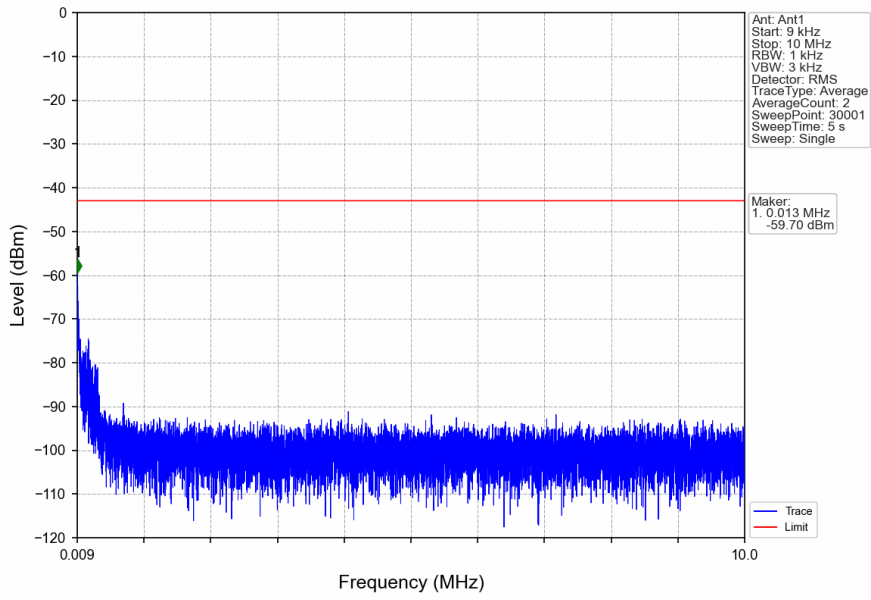
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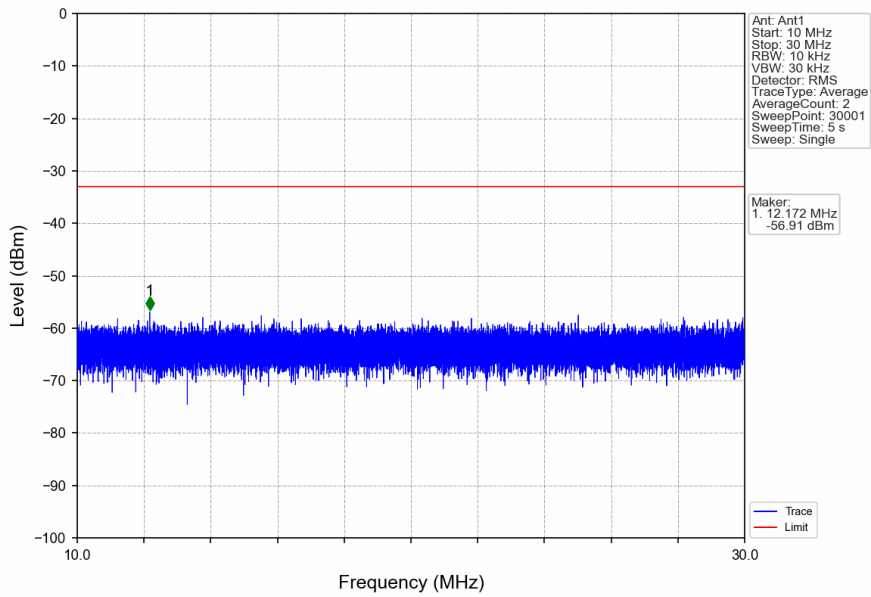
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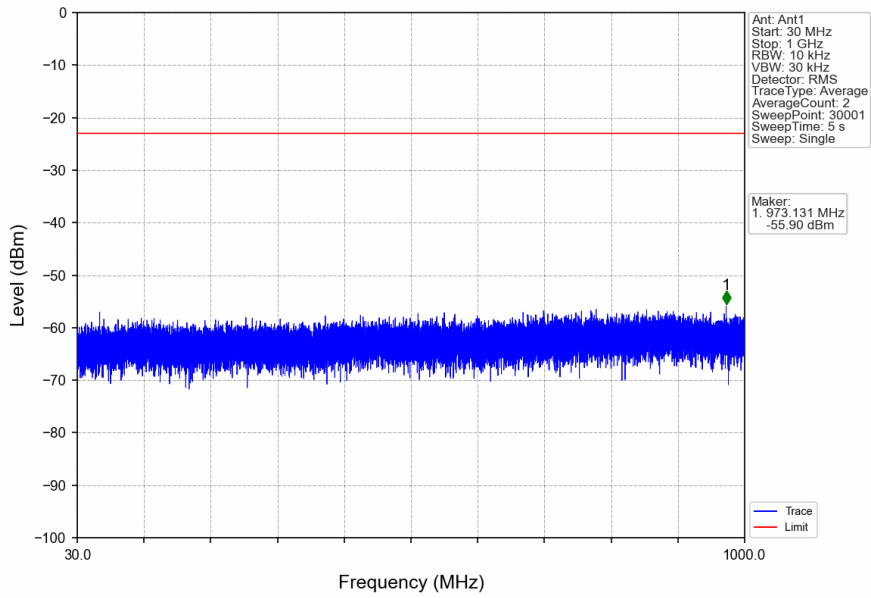
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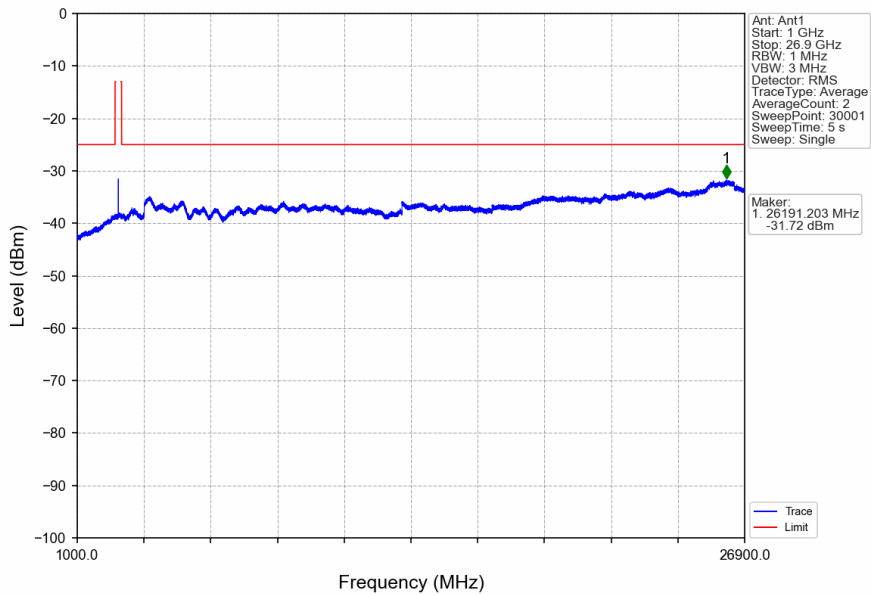
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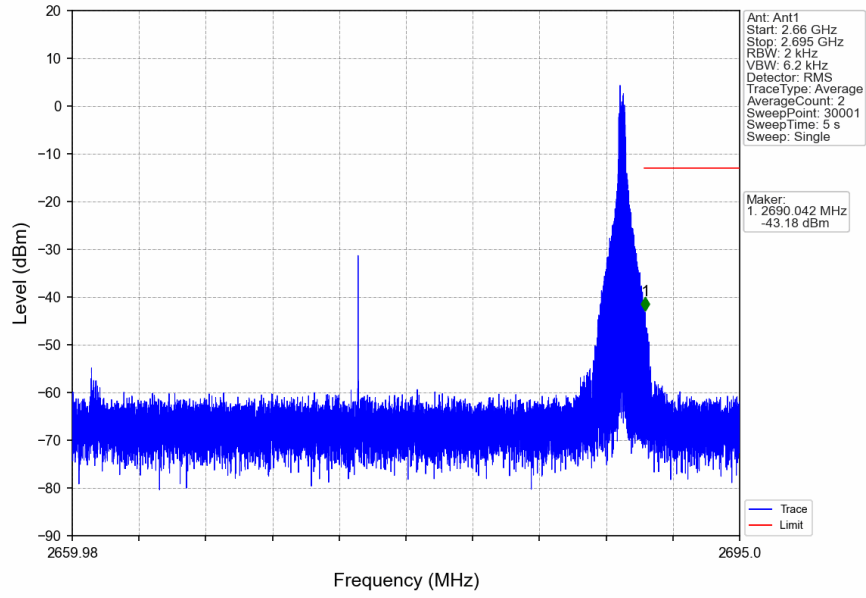
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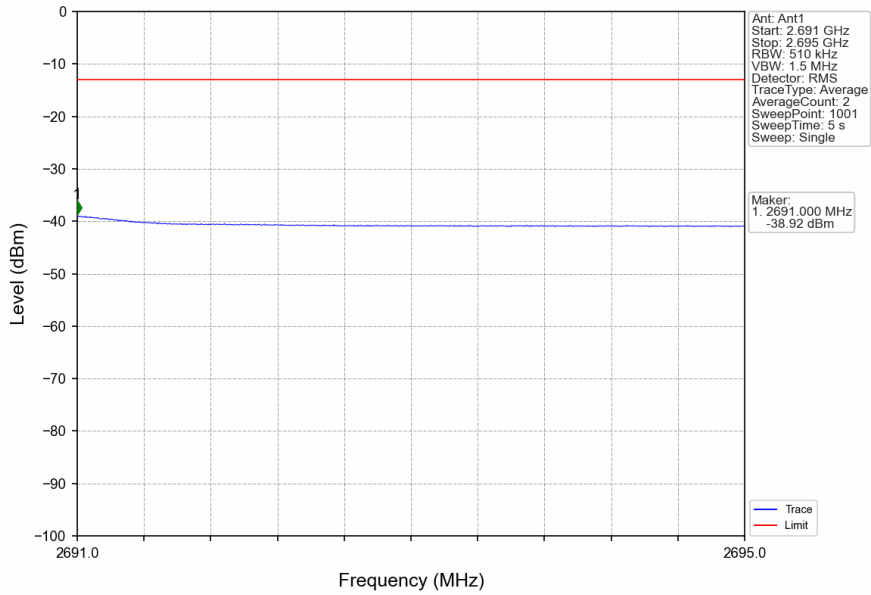
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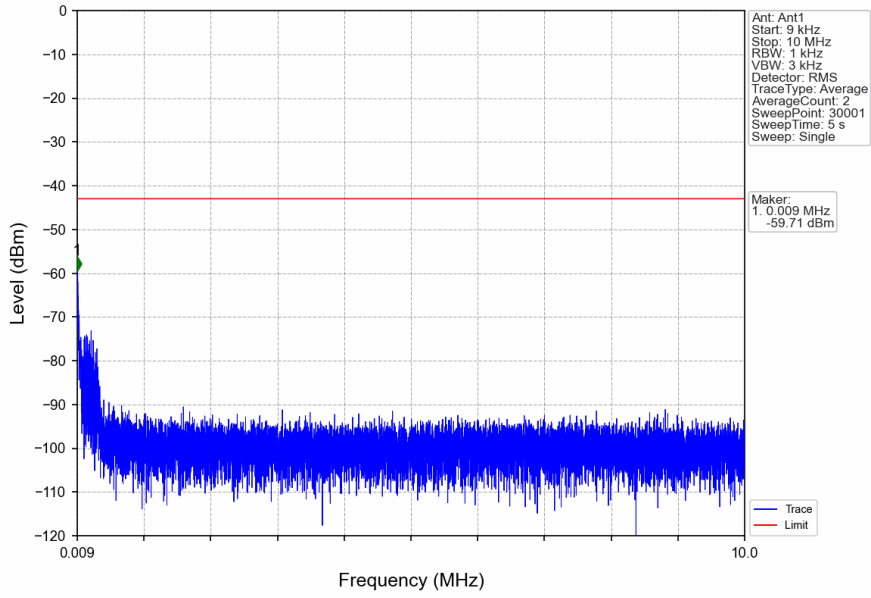
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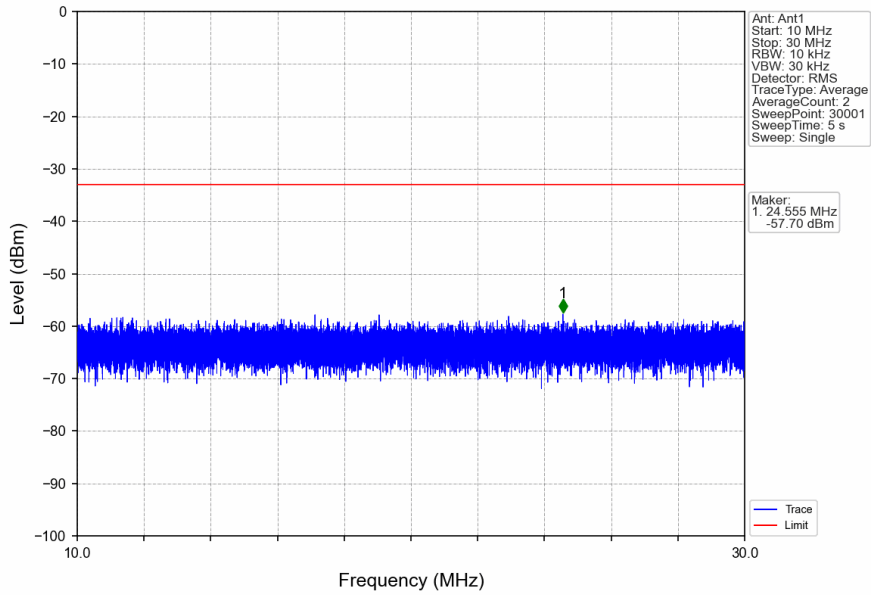
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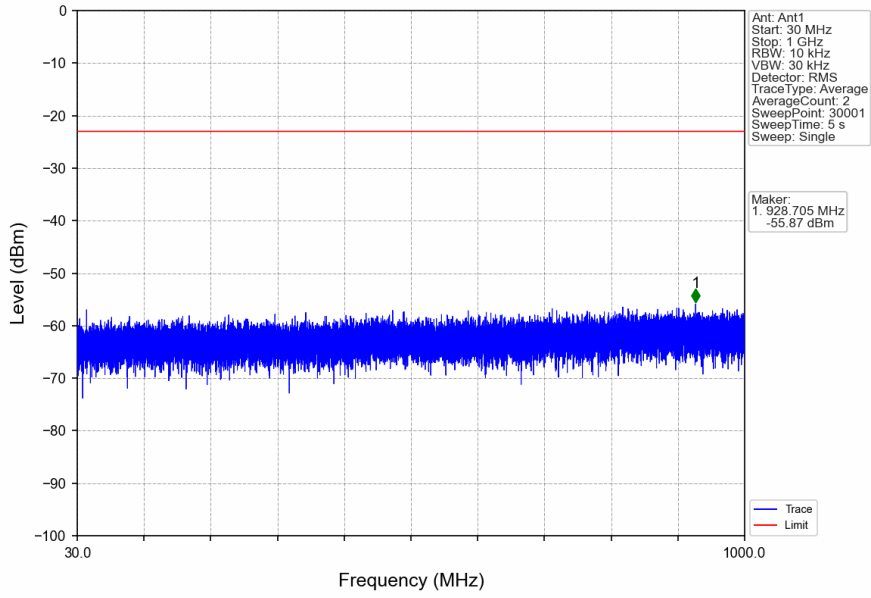
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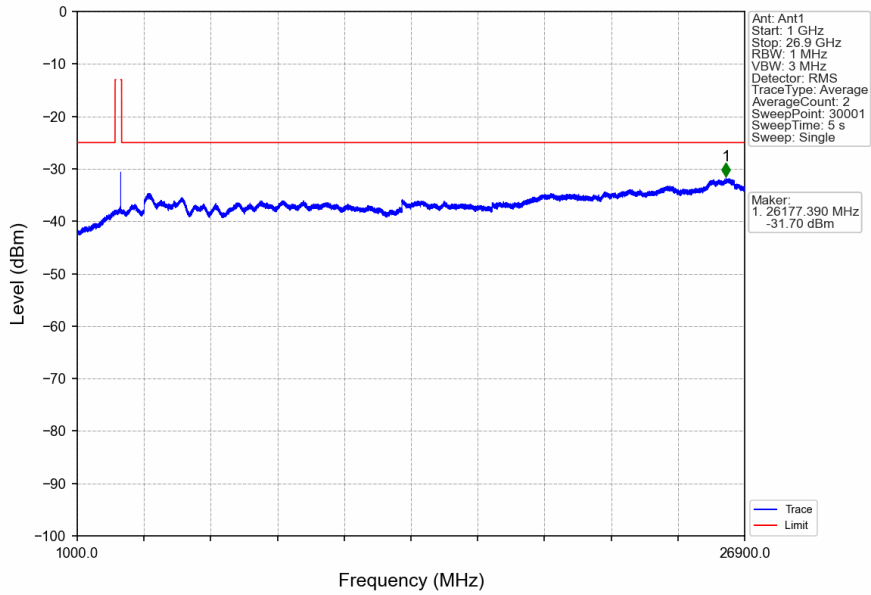
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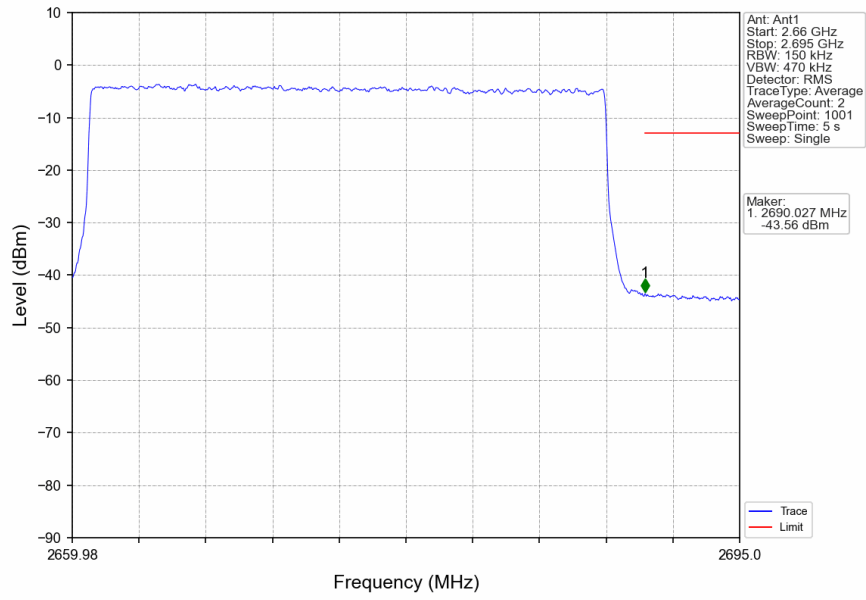
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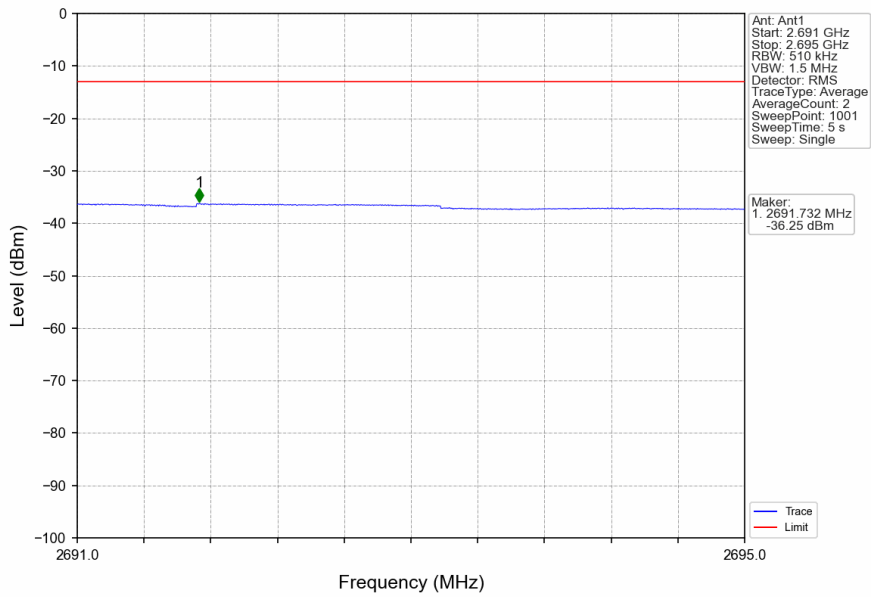
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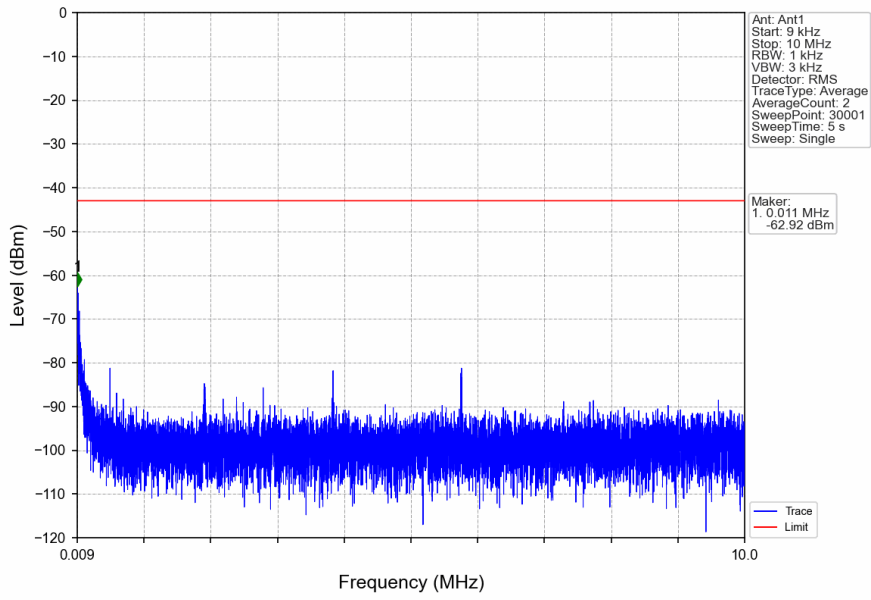
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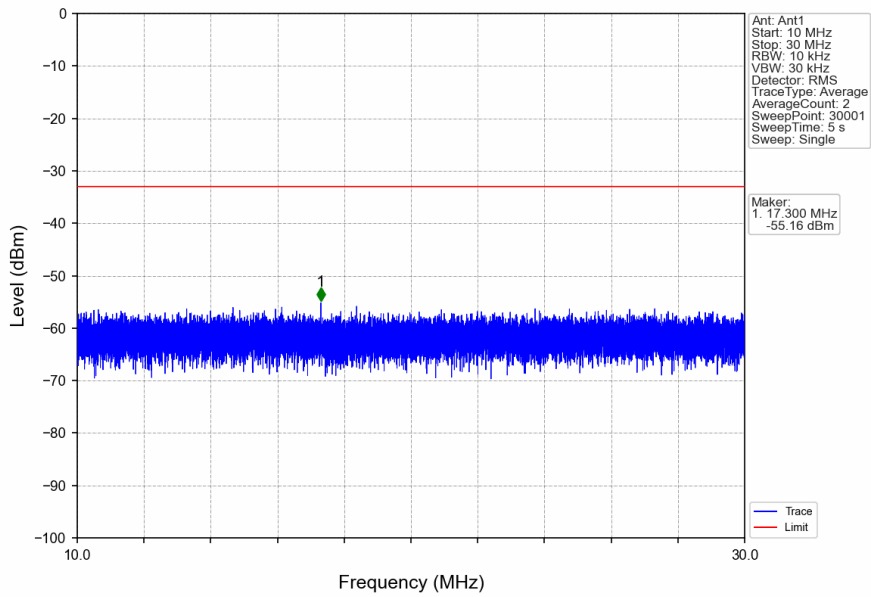
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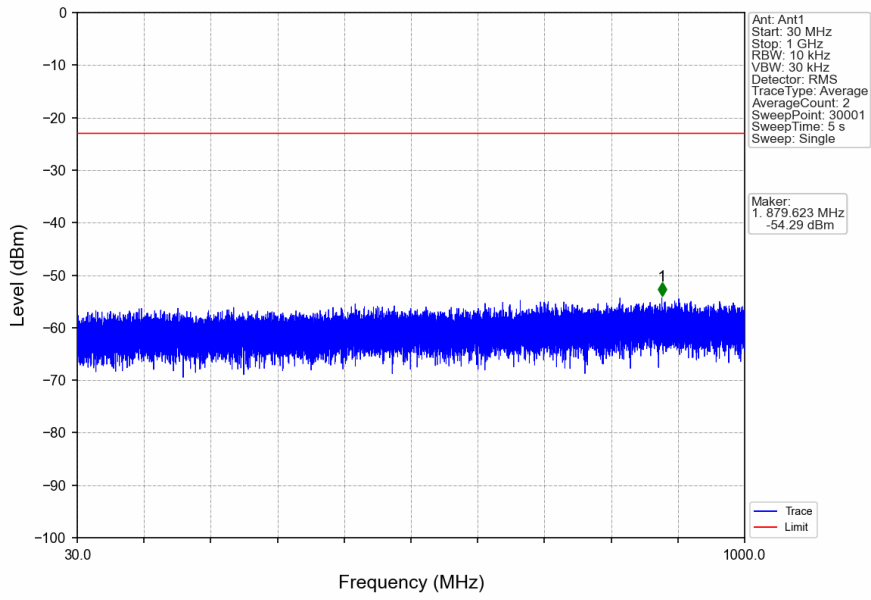
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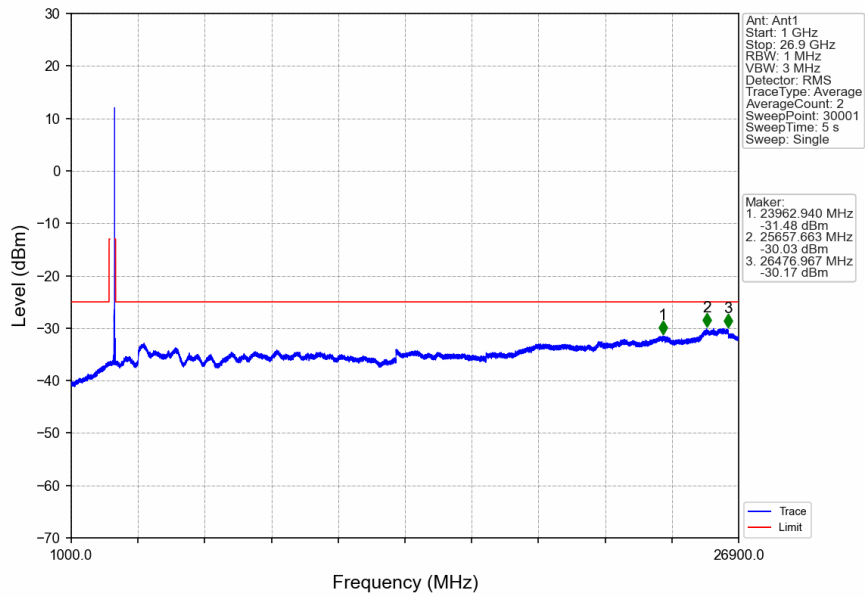
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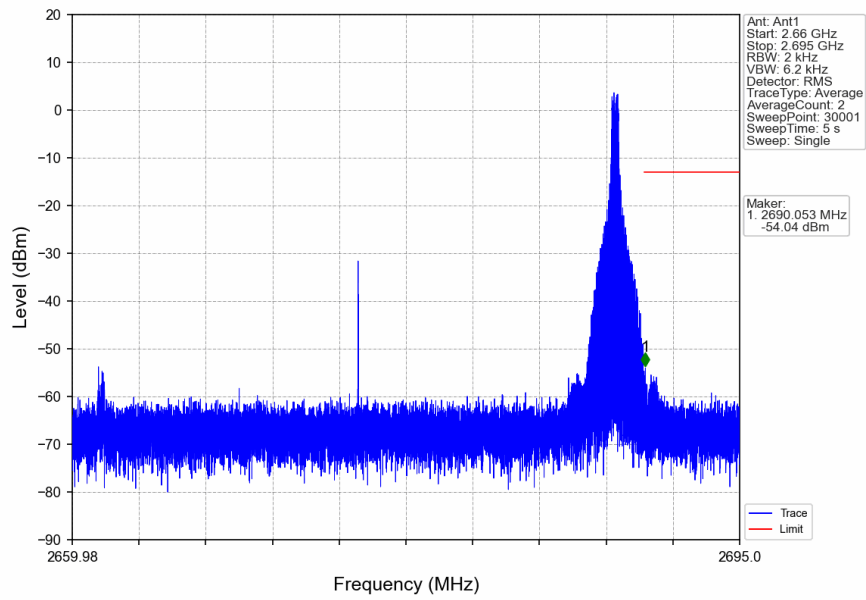
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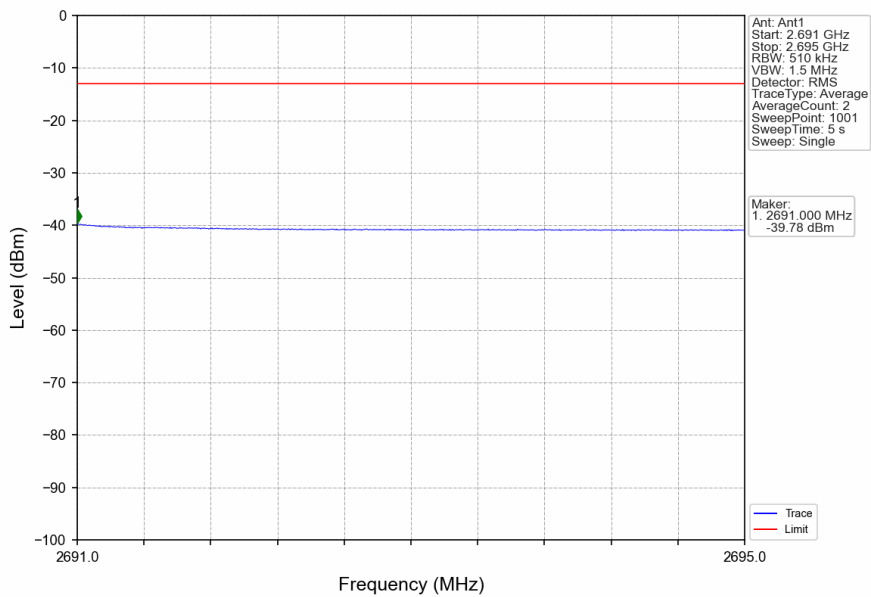
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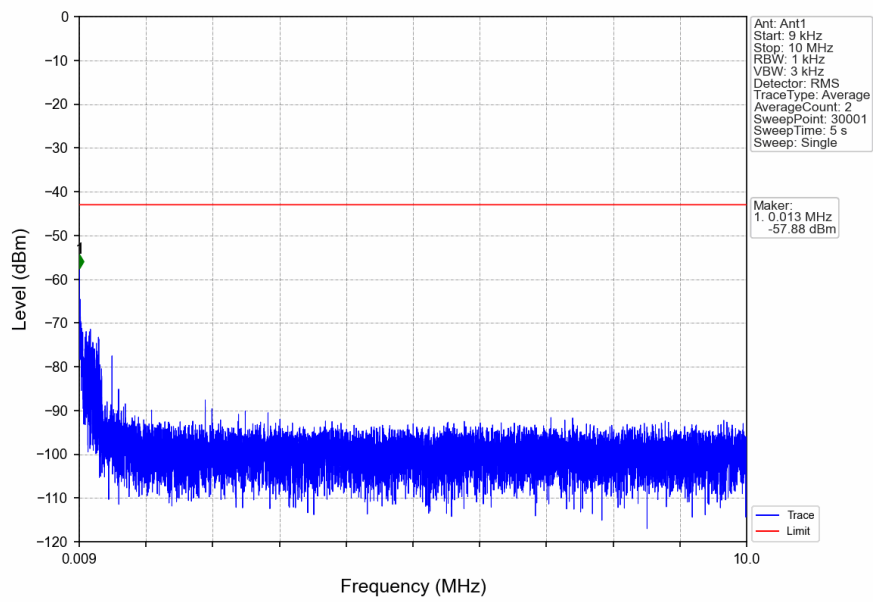
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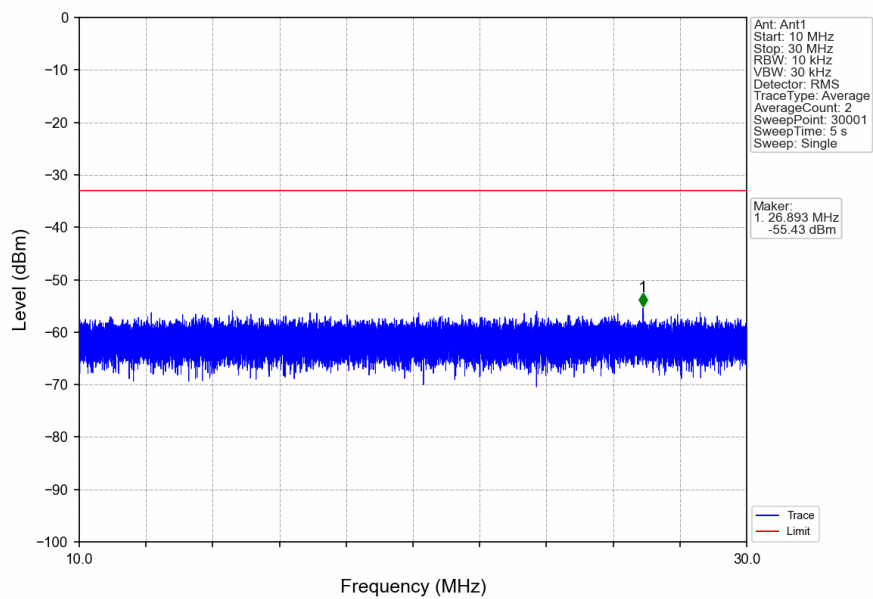
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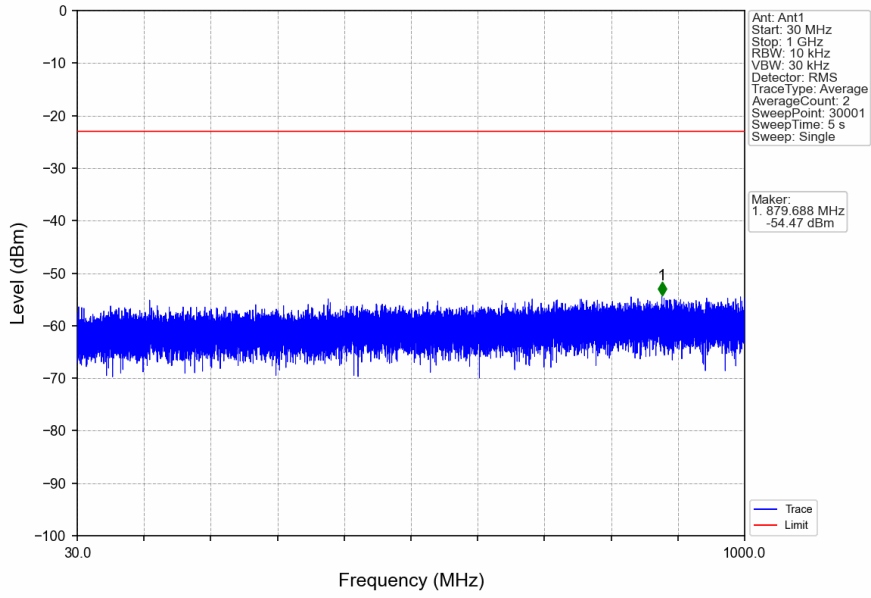
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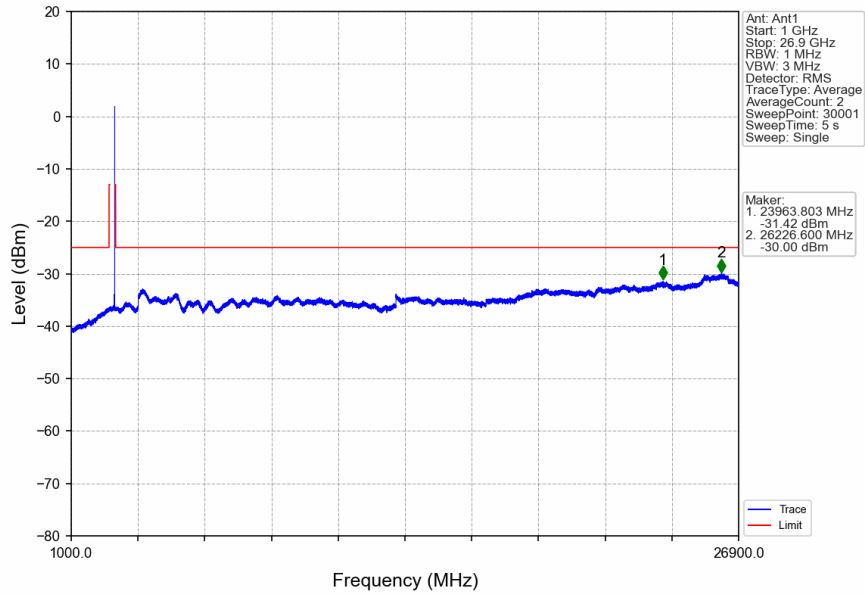
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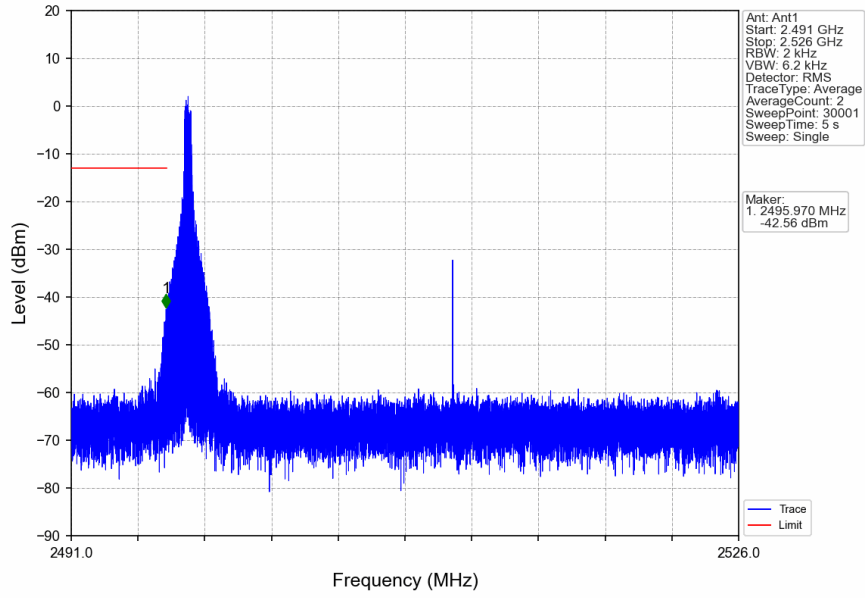
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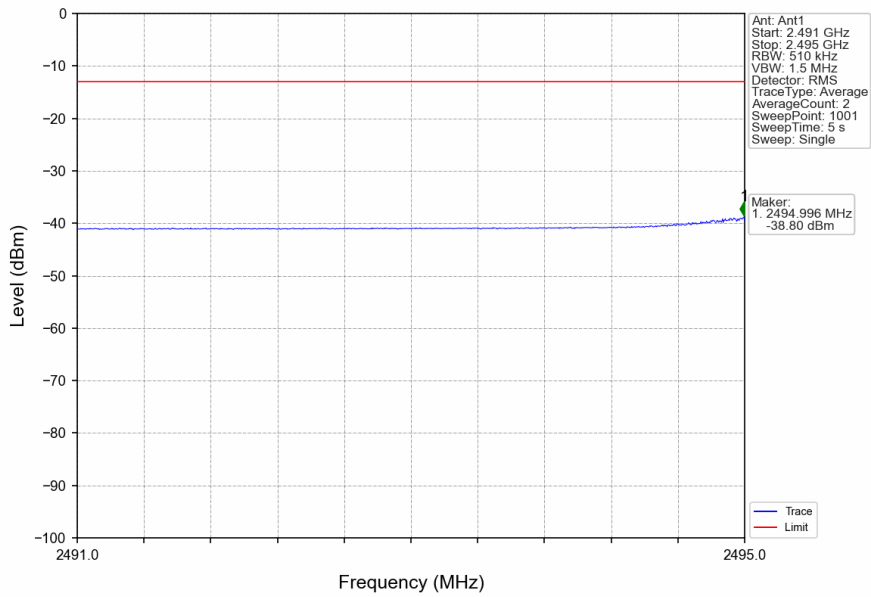
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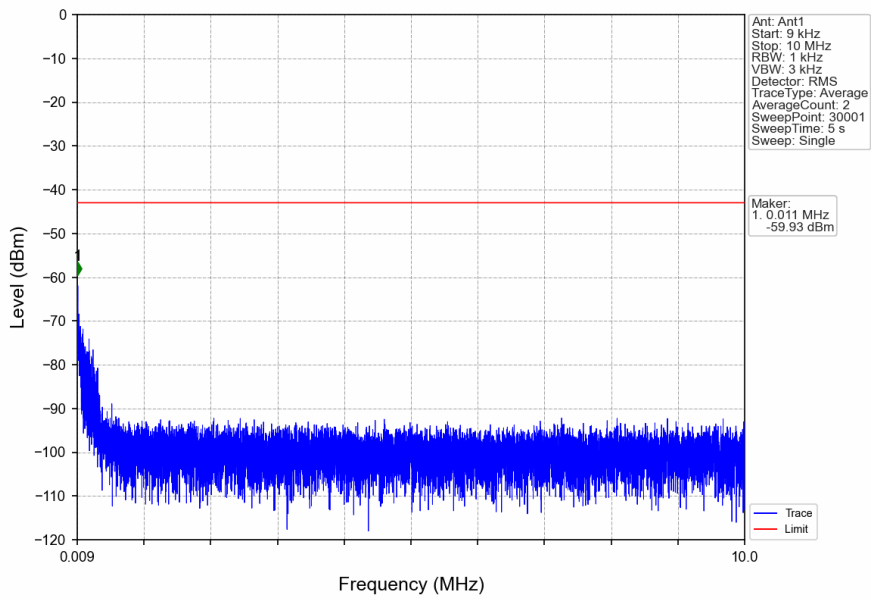
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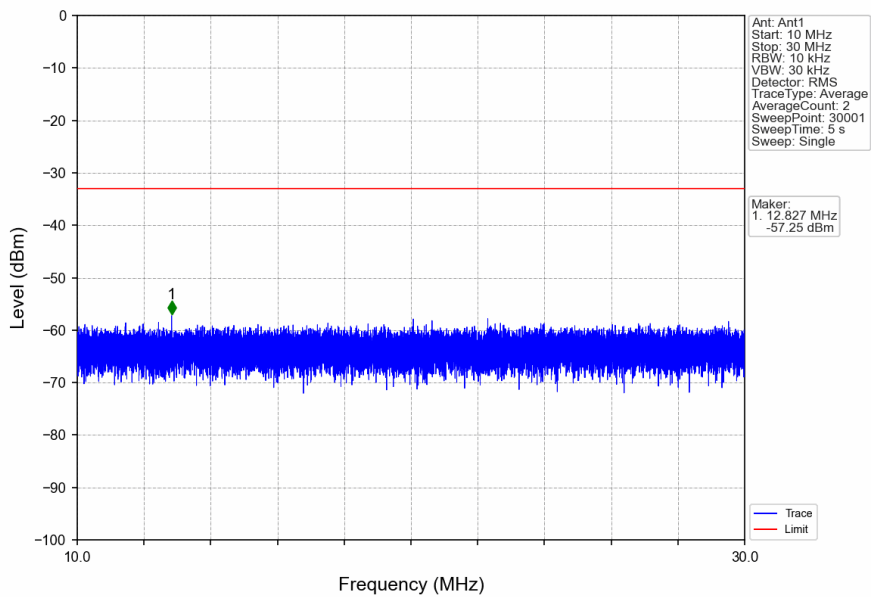
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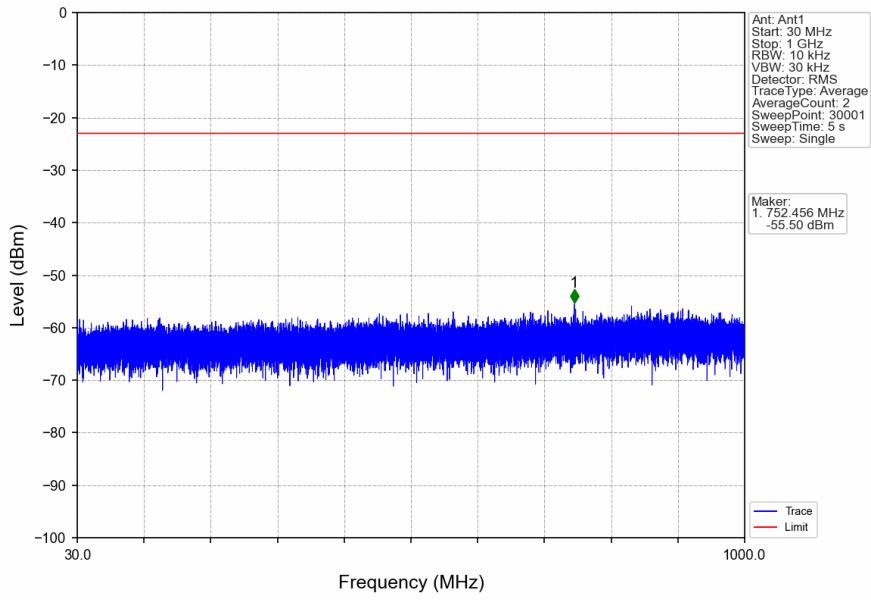
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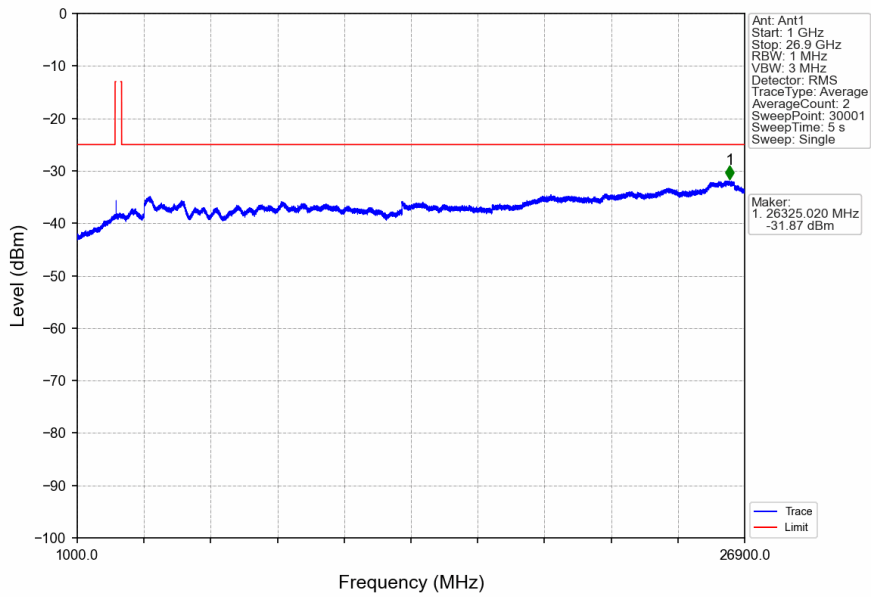
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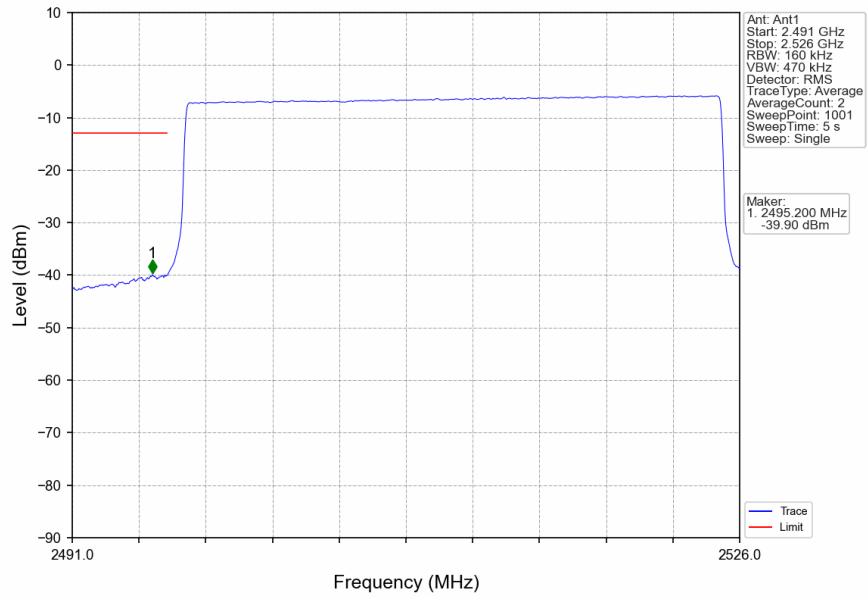
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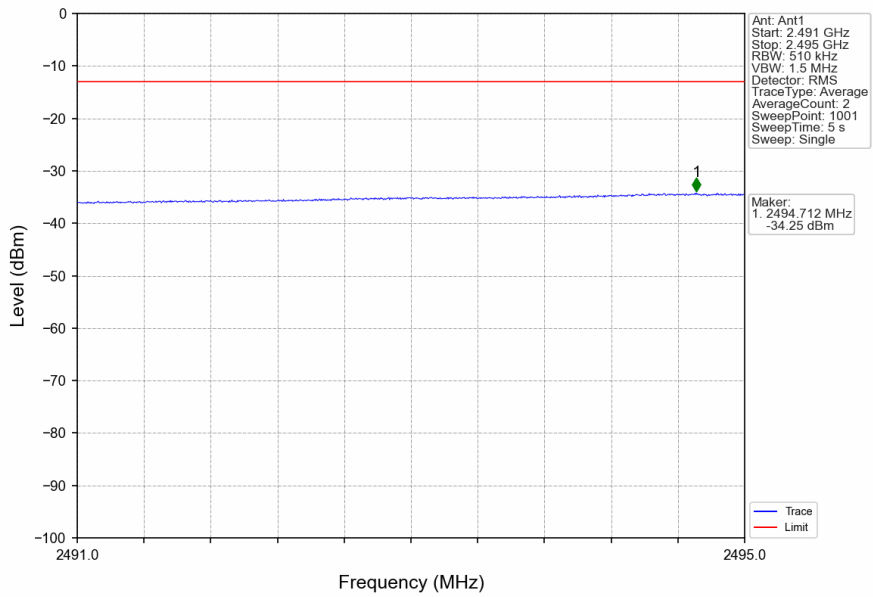
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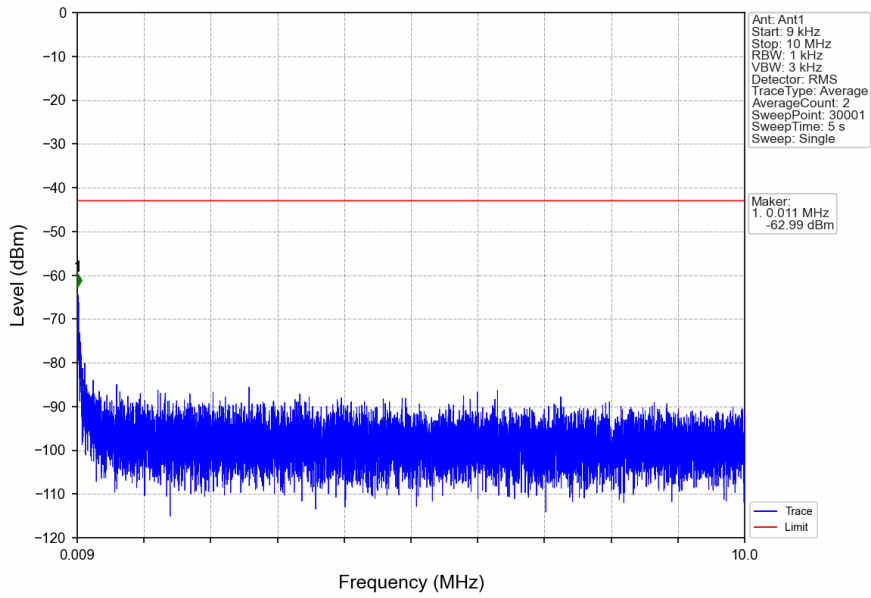
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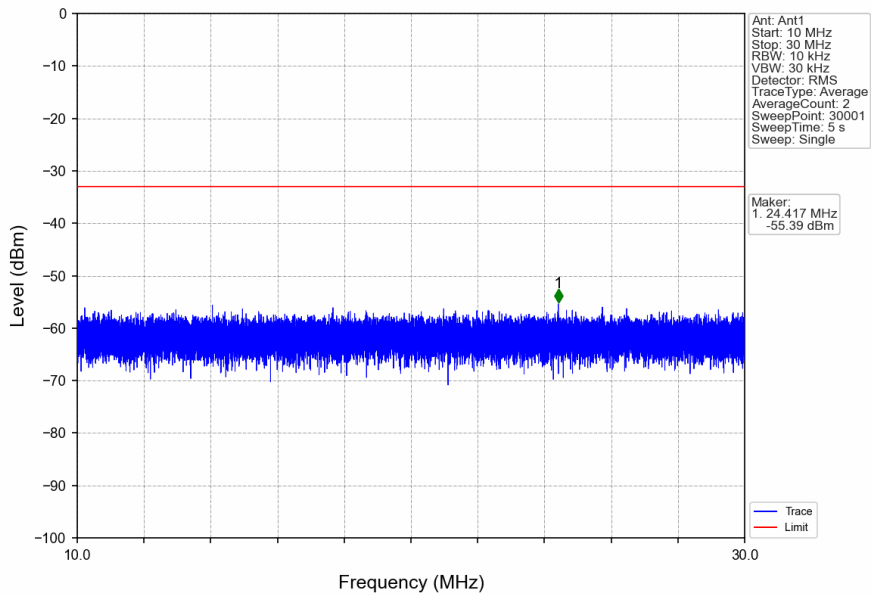
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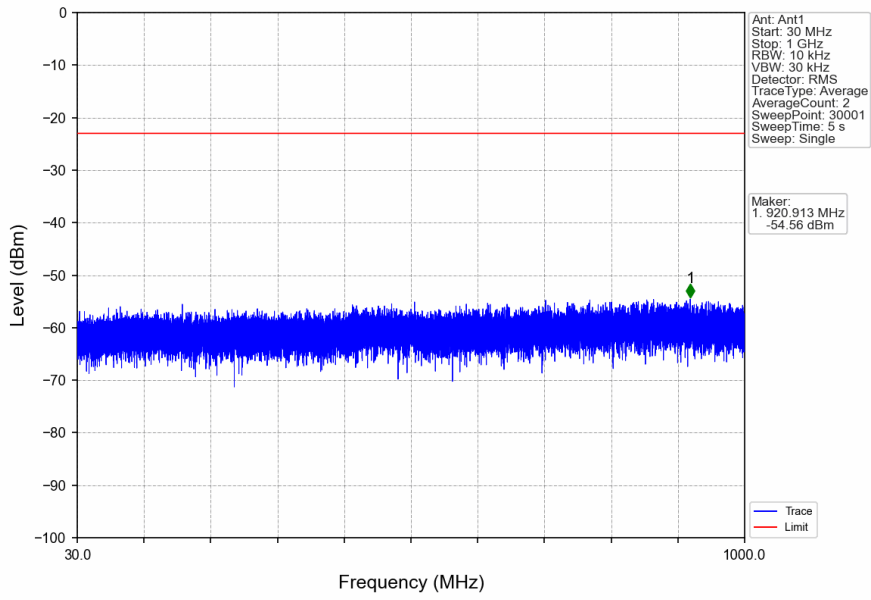
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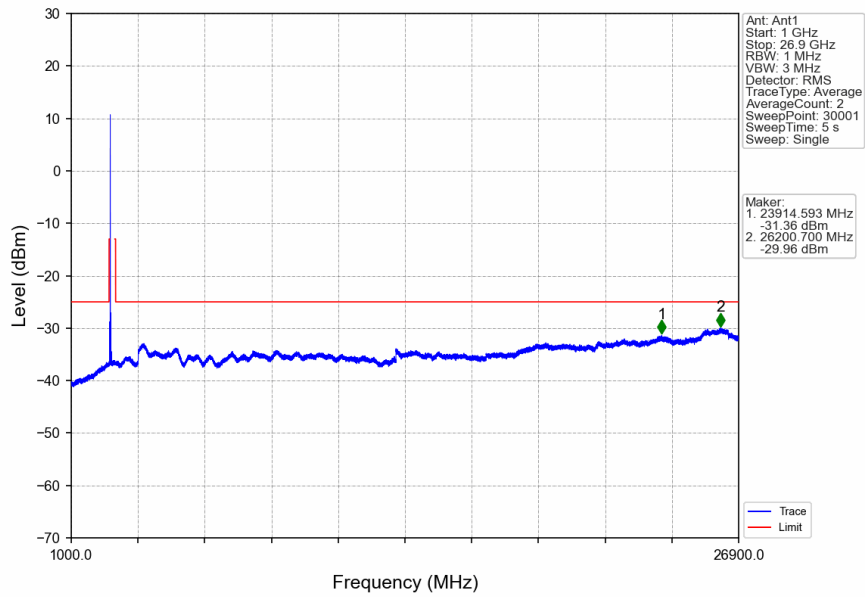
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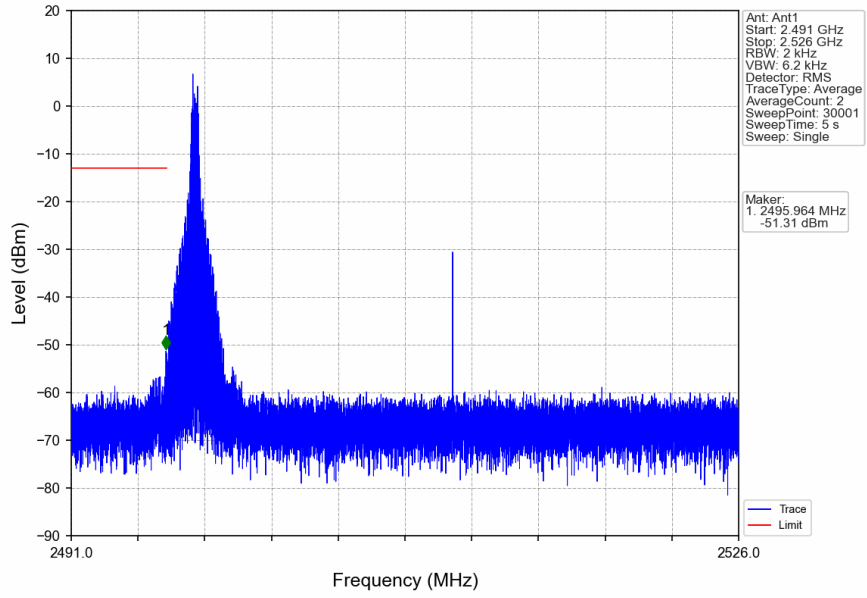
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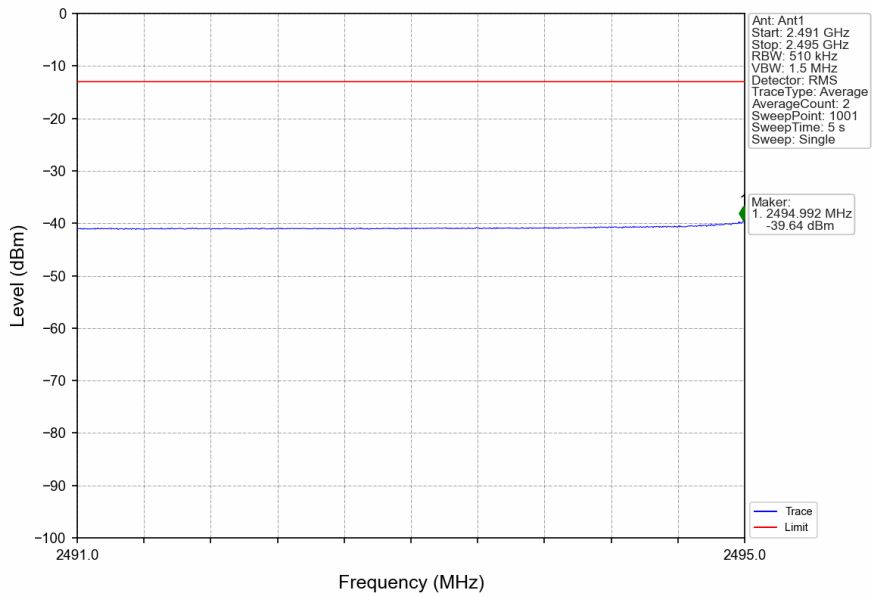
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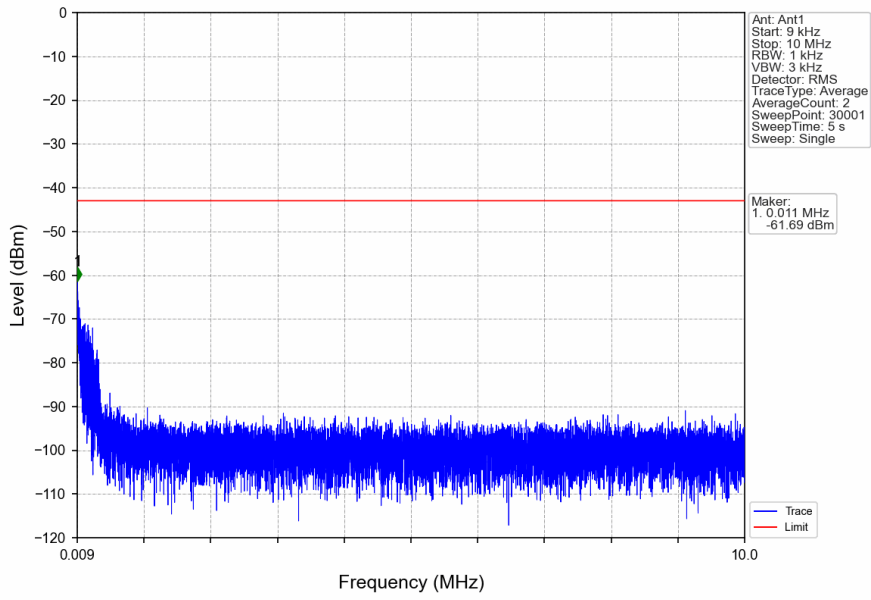
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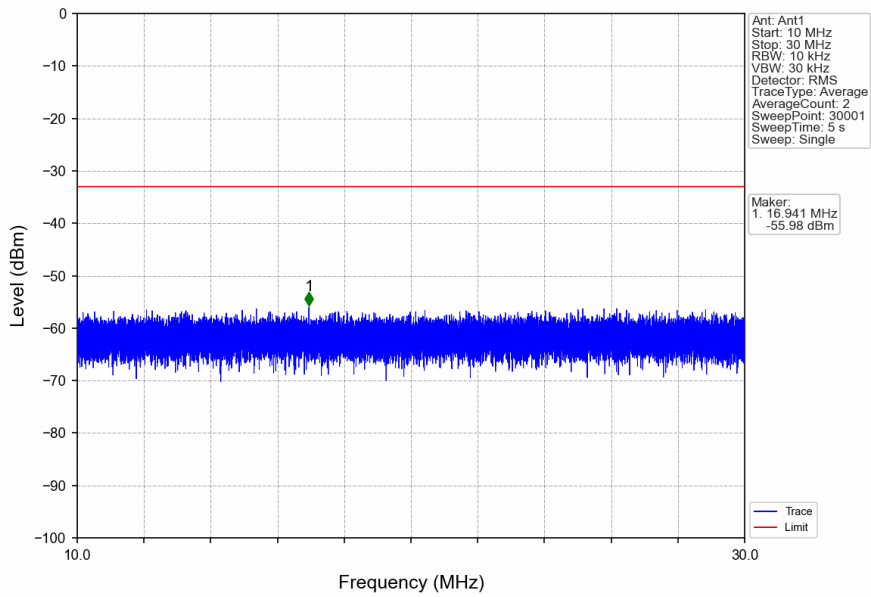
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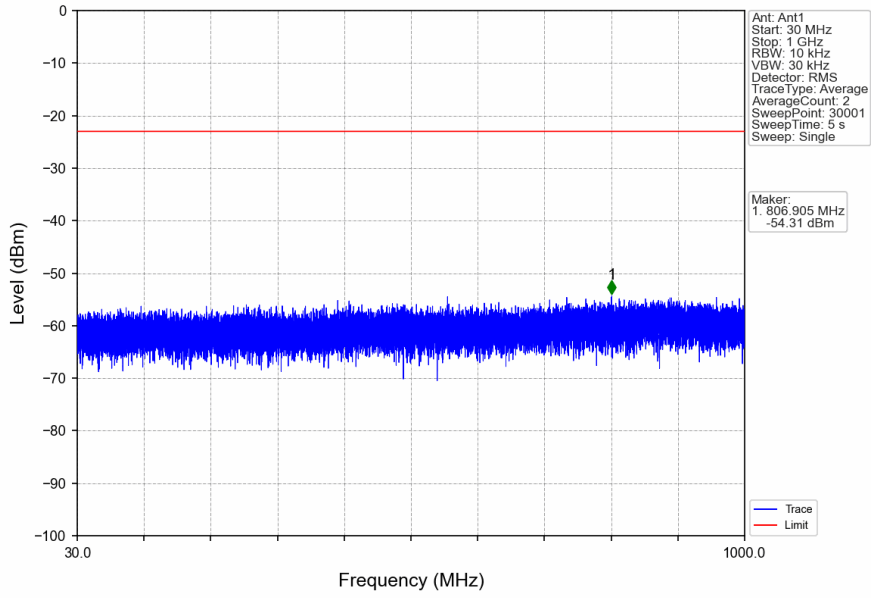
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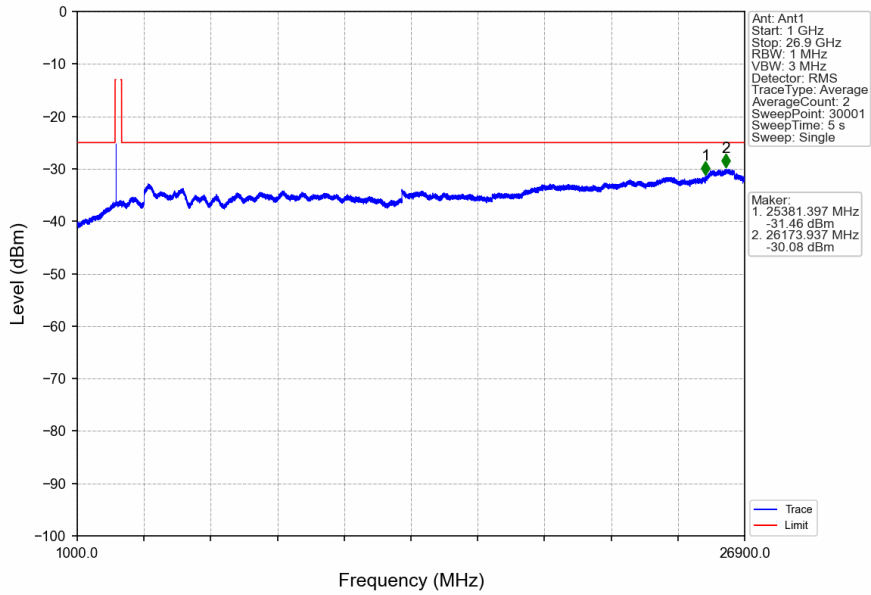
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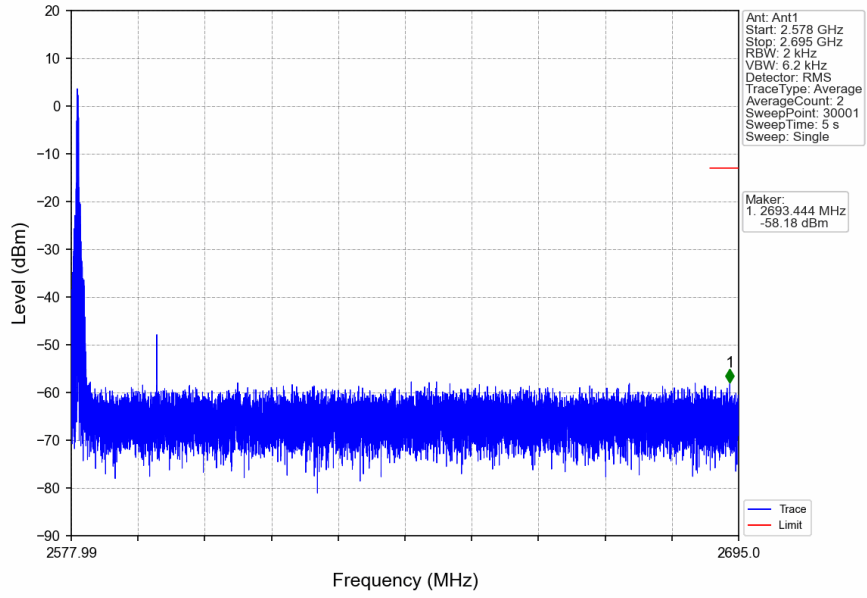
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n41_30kHz_SISO_NTNV_30MHz_CP-OFDM QPSK_2511MHz_Inner_1RB_Left



n41_30kHz_SISO_NTNV_30MHz_CP-OFDM QPSK_2592.99MHz_Edge_1RB_Left



n41_30kHz_SISO_NTNV_30MHz_CP-OFDM QPSK_2592.99MHz_Edge_1RB_Left

