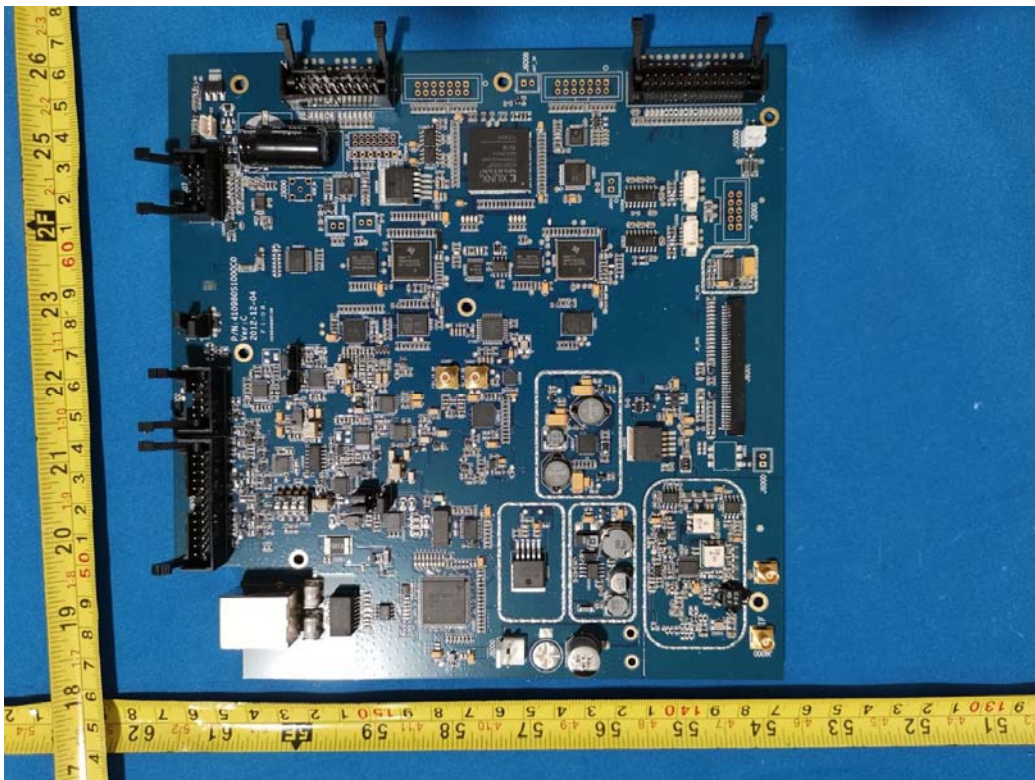
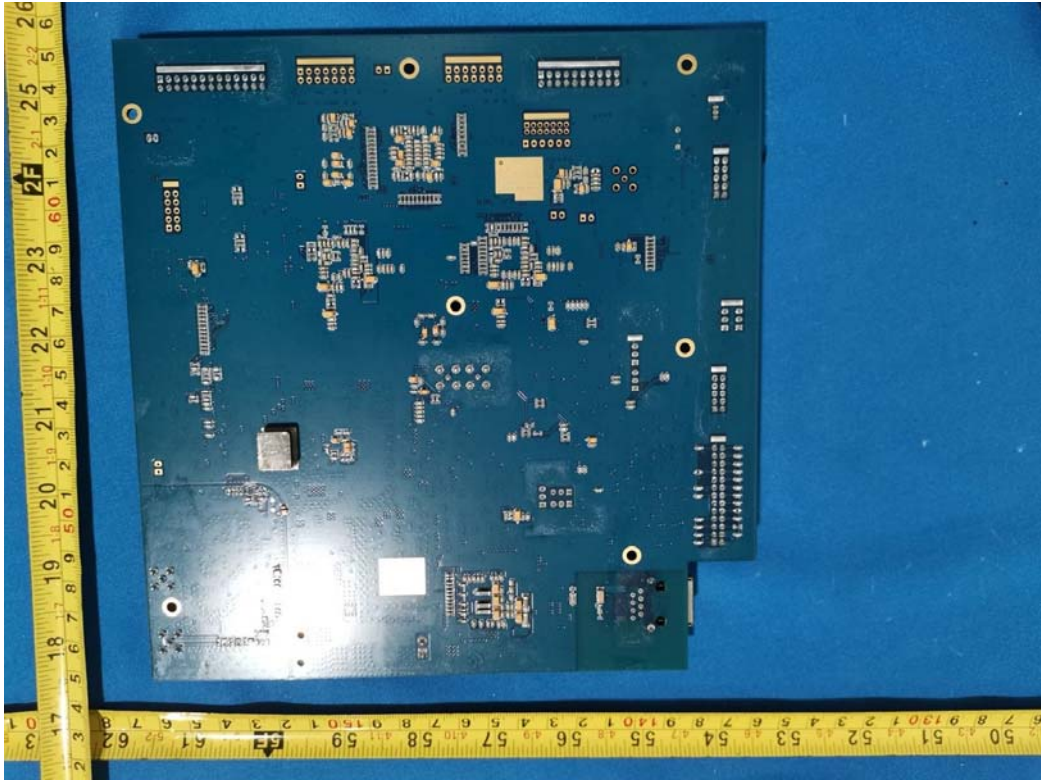
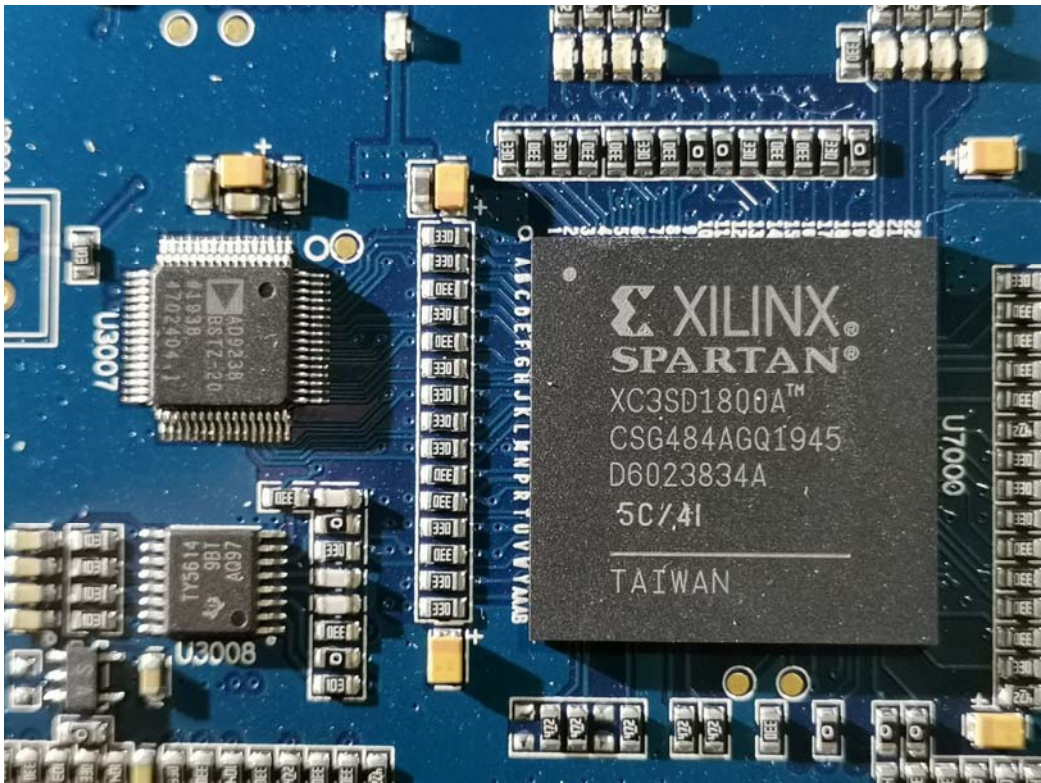


# EXHIBIT C - EUT INTERNAL PHOTOGRAPHS

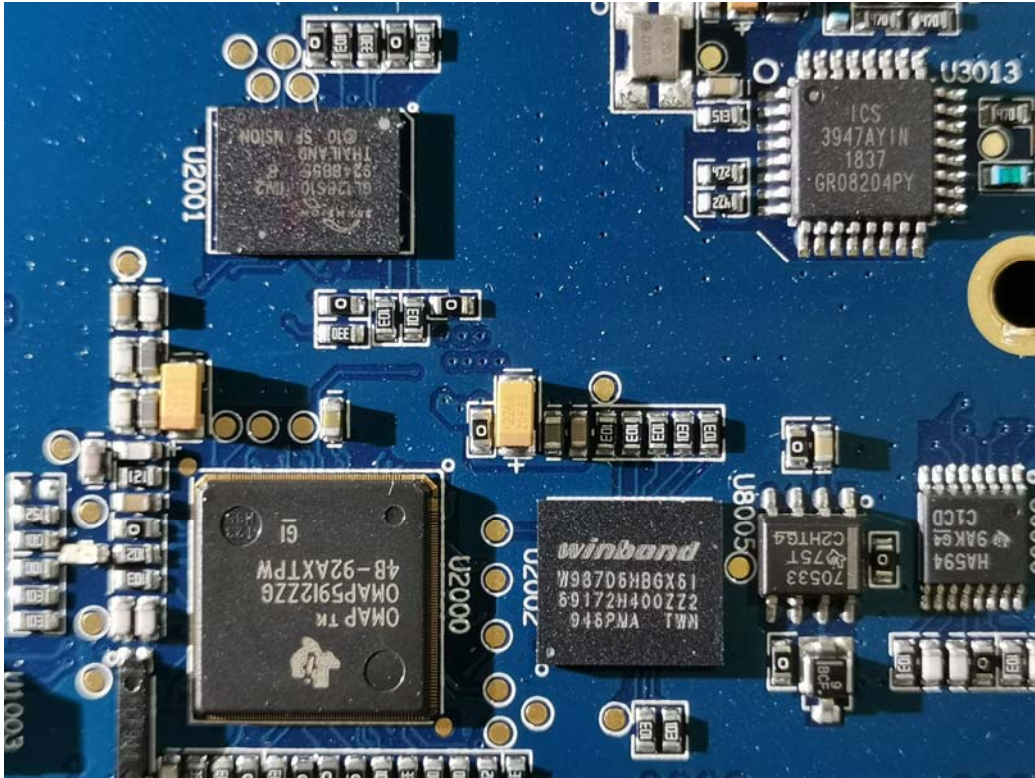




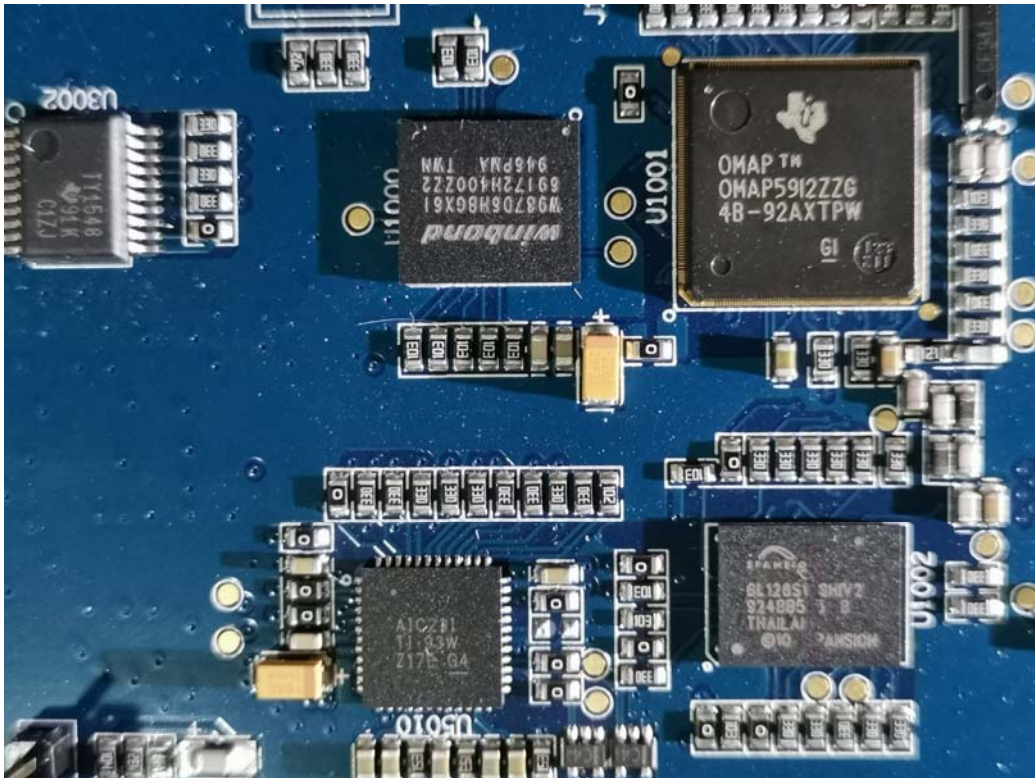
Chip 1



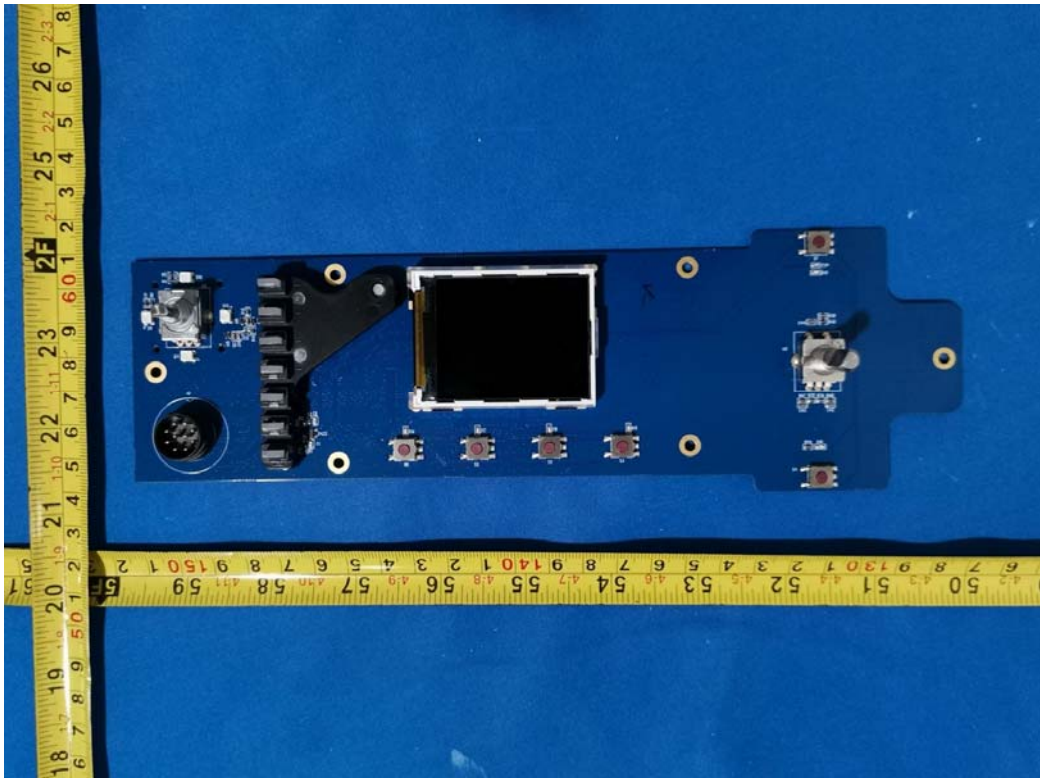
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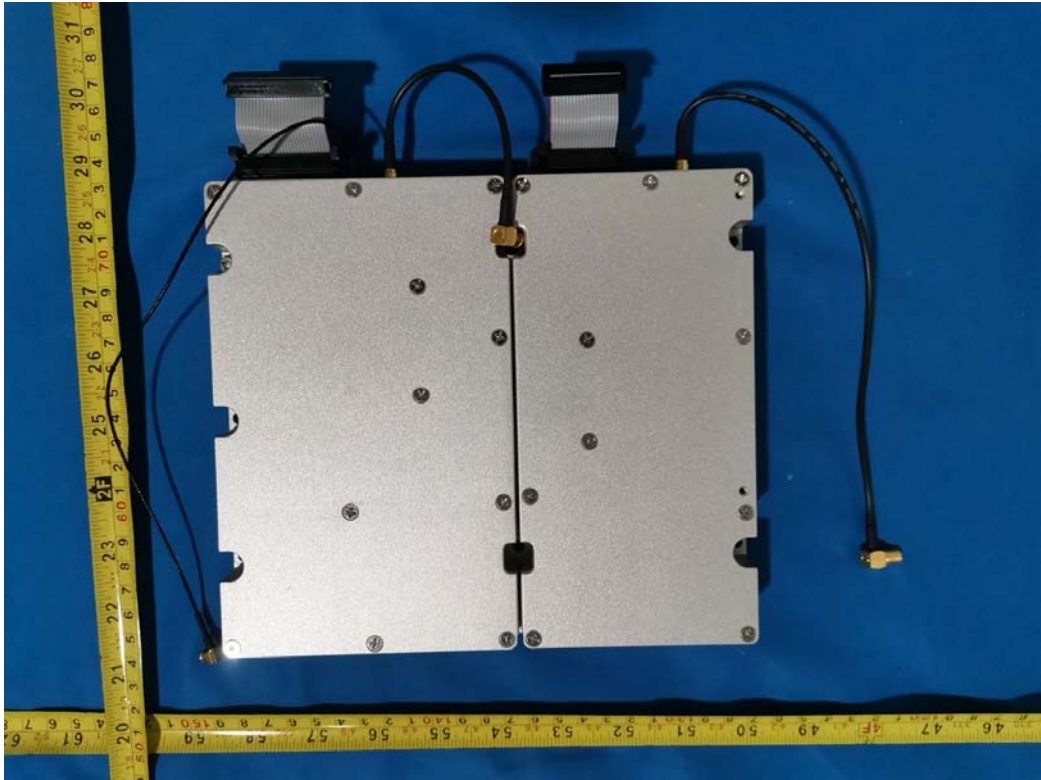
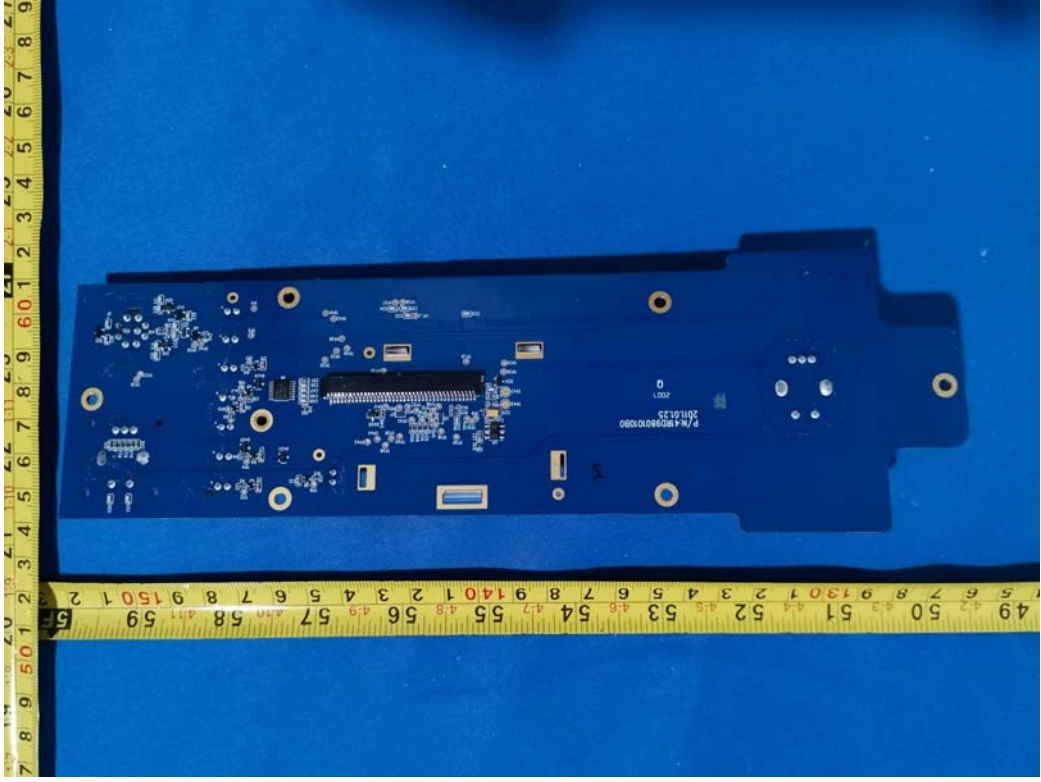


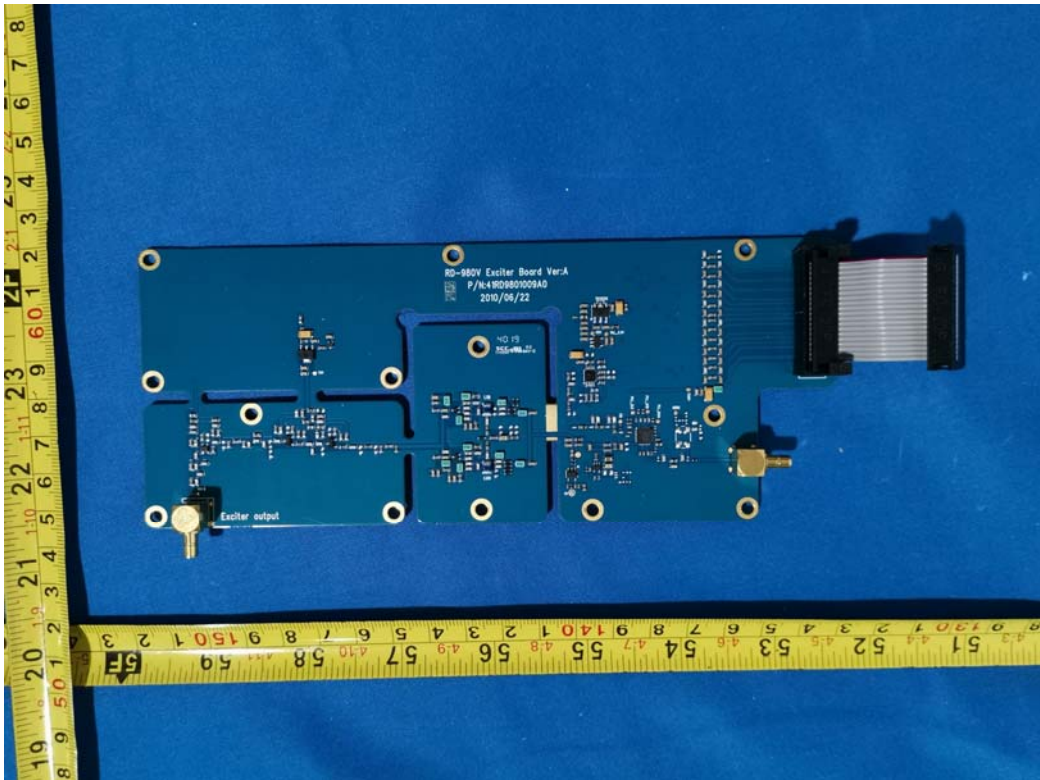
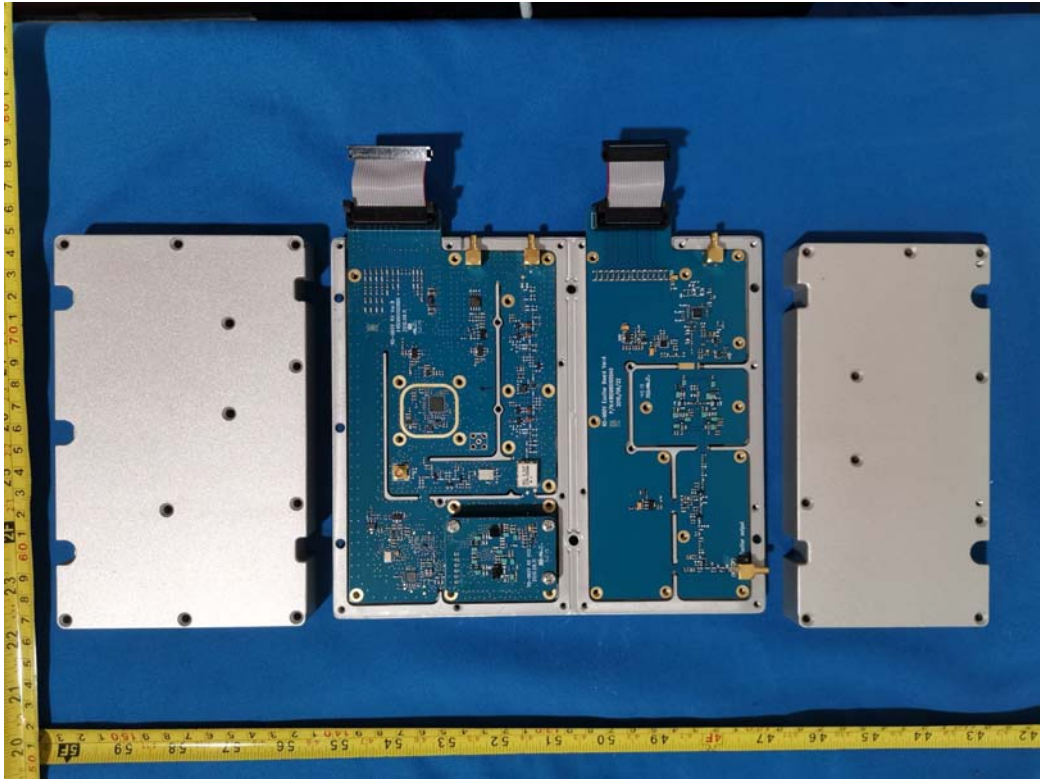
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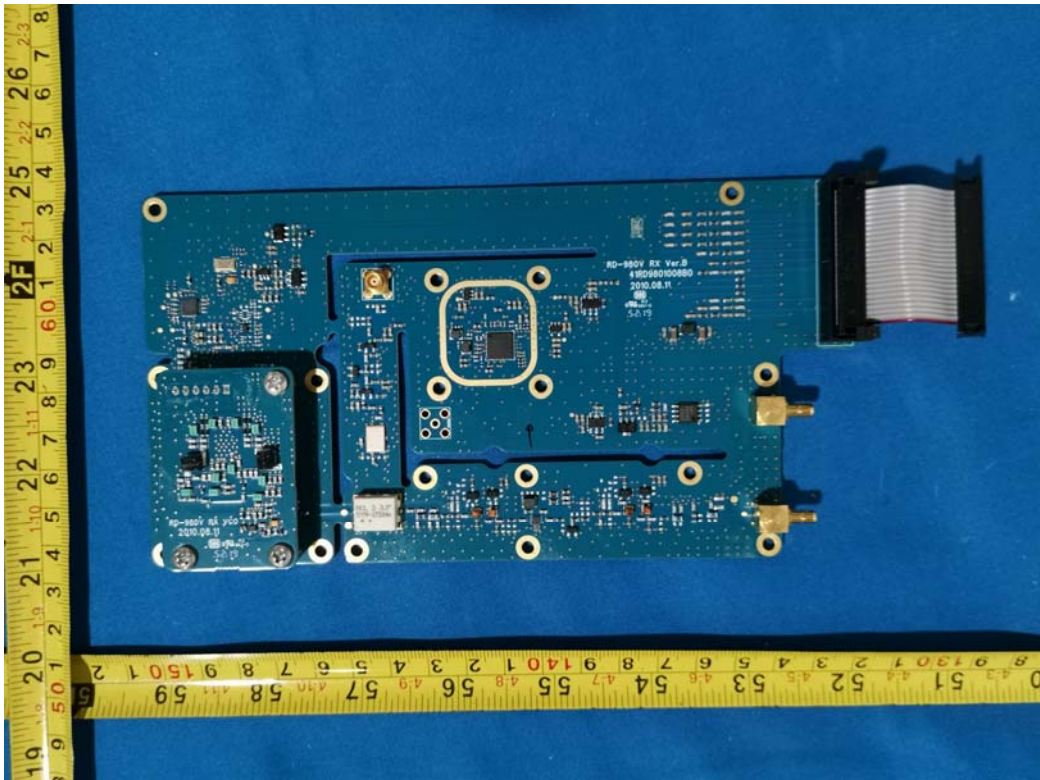
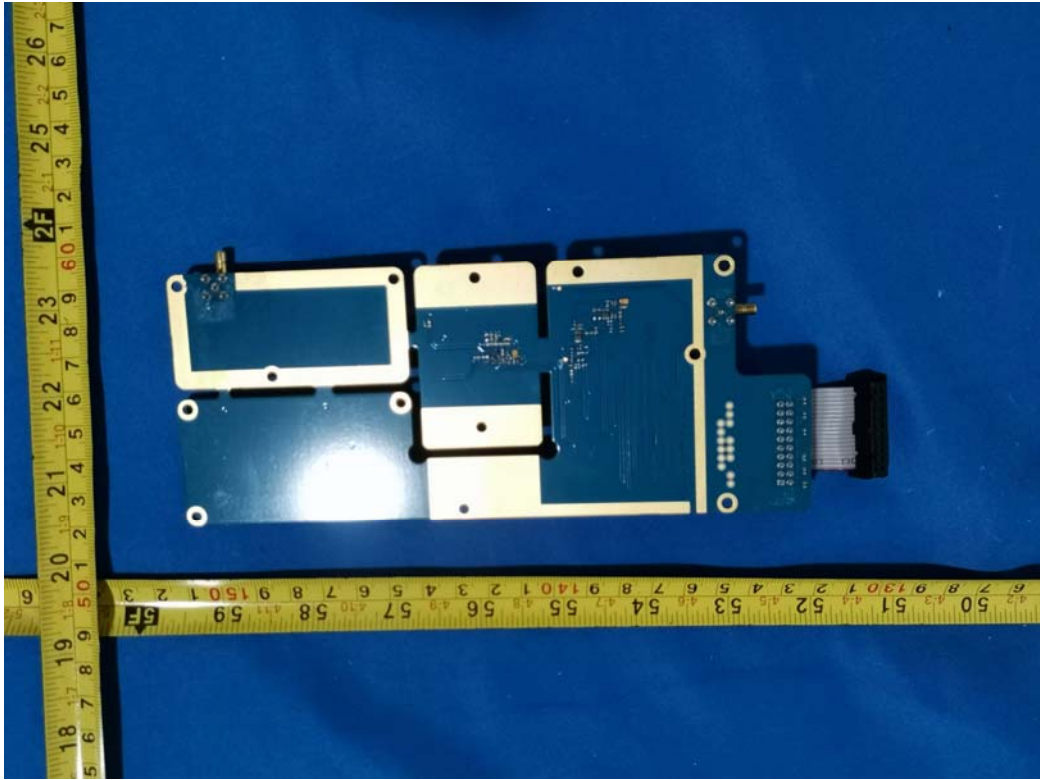


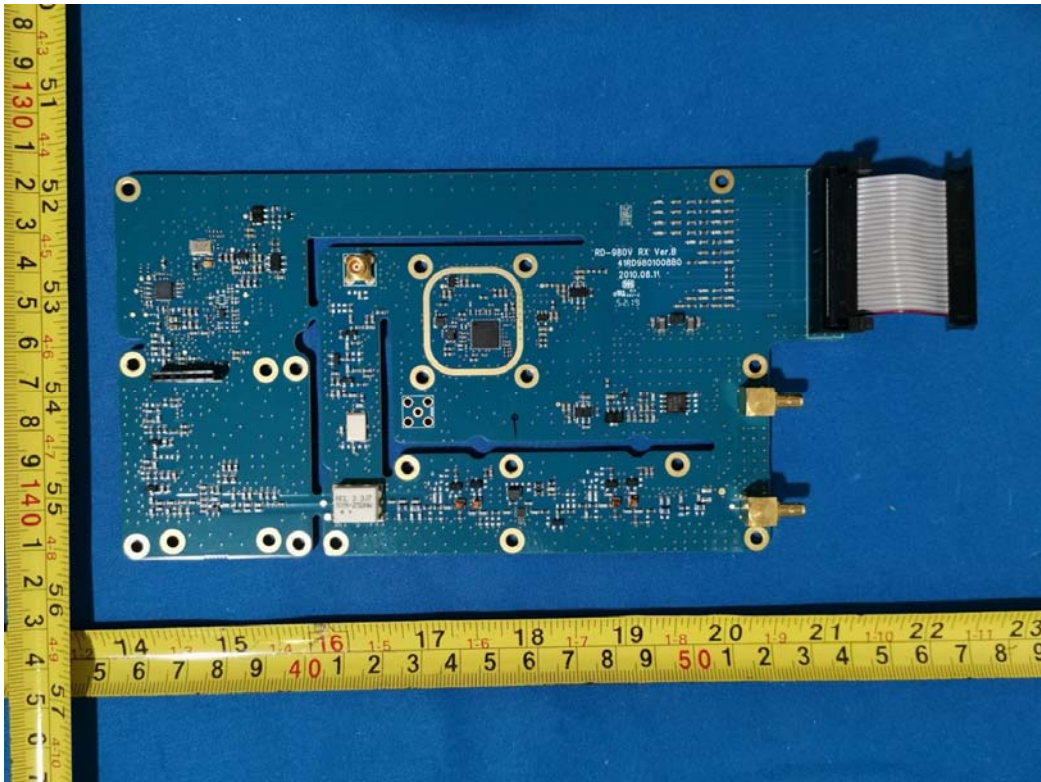
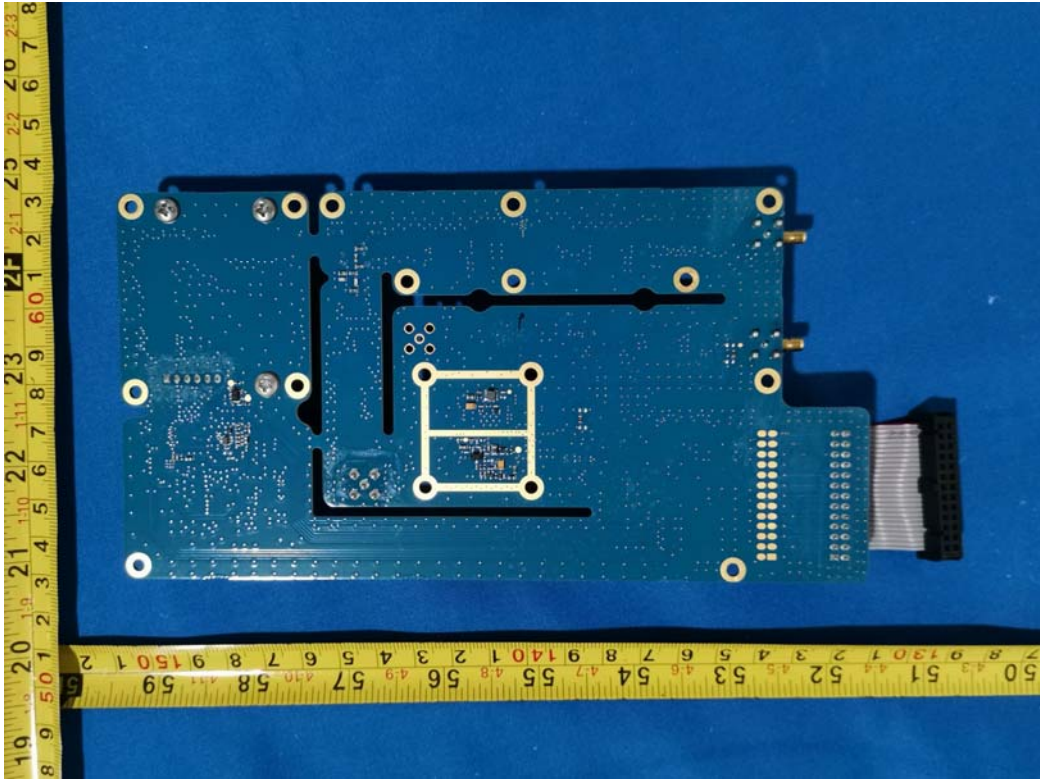
Chip 4



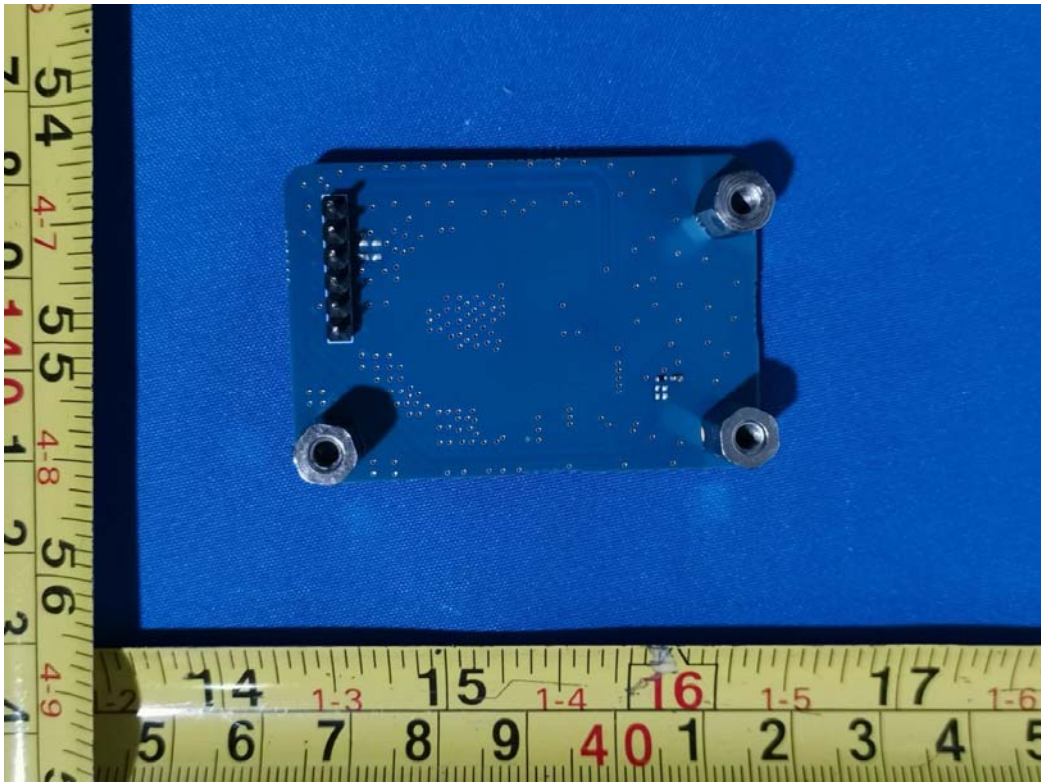
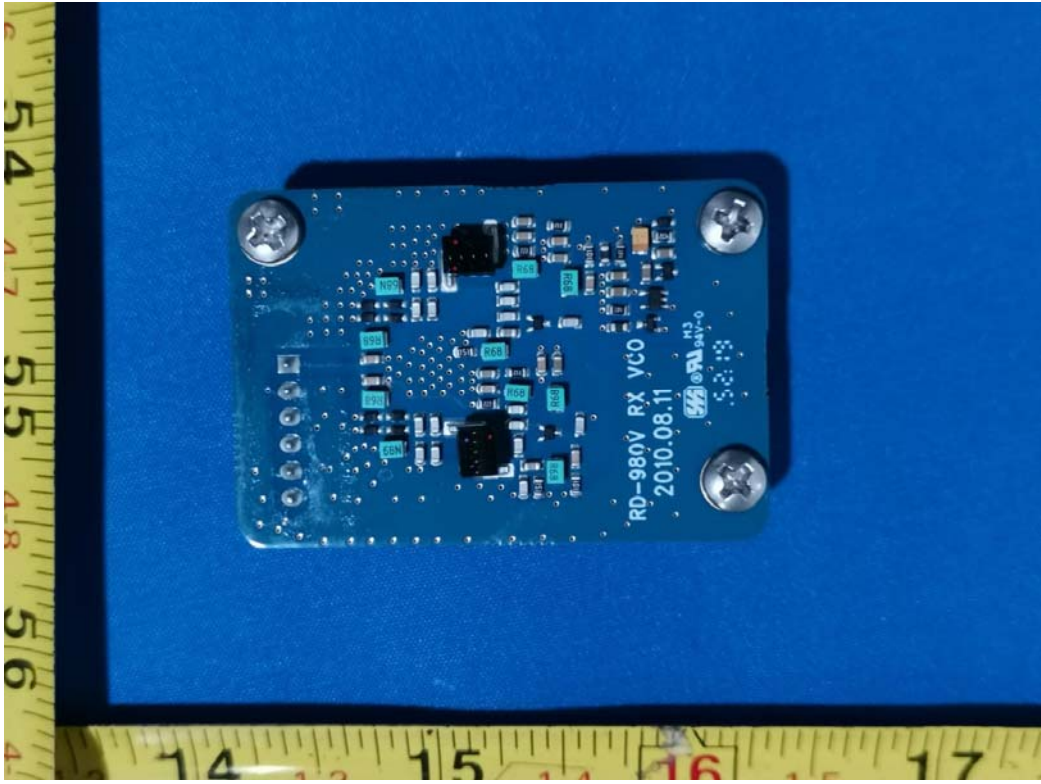


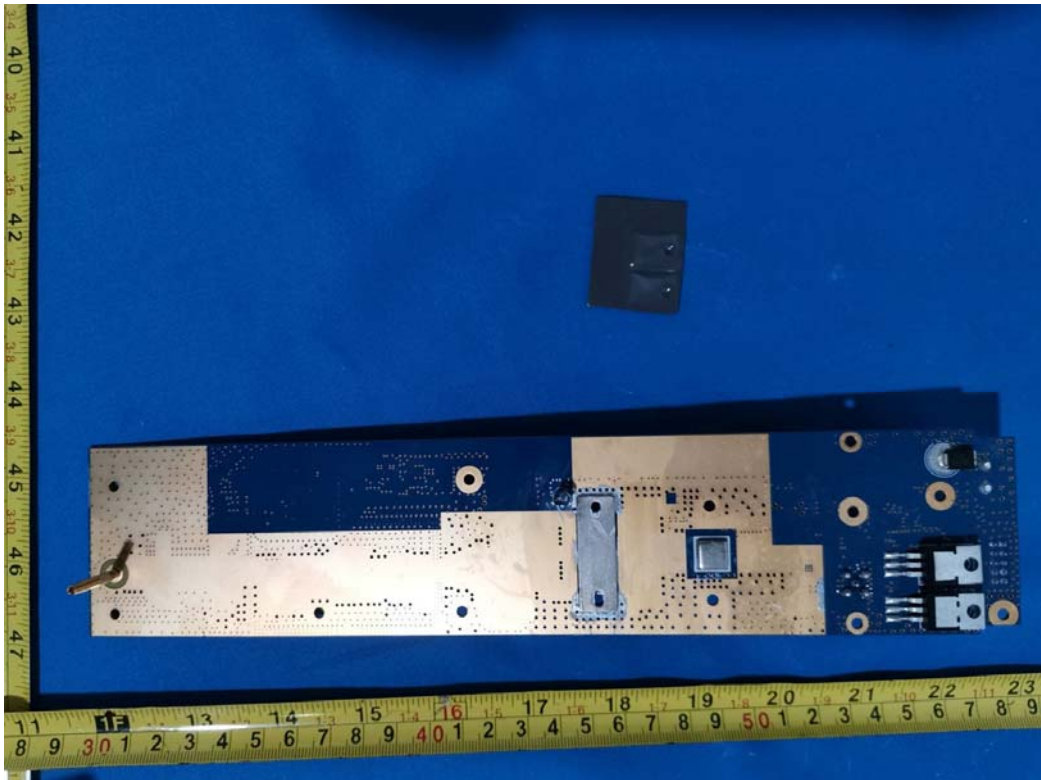
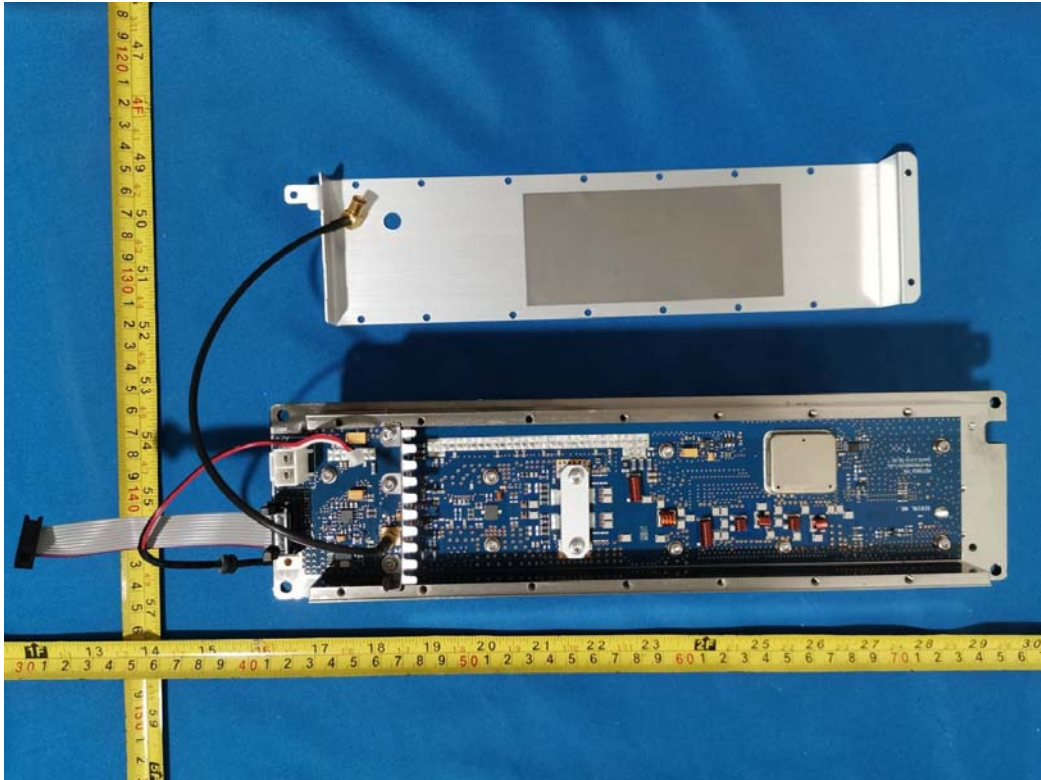


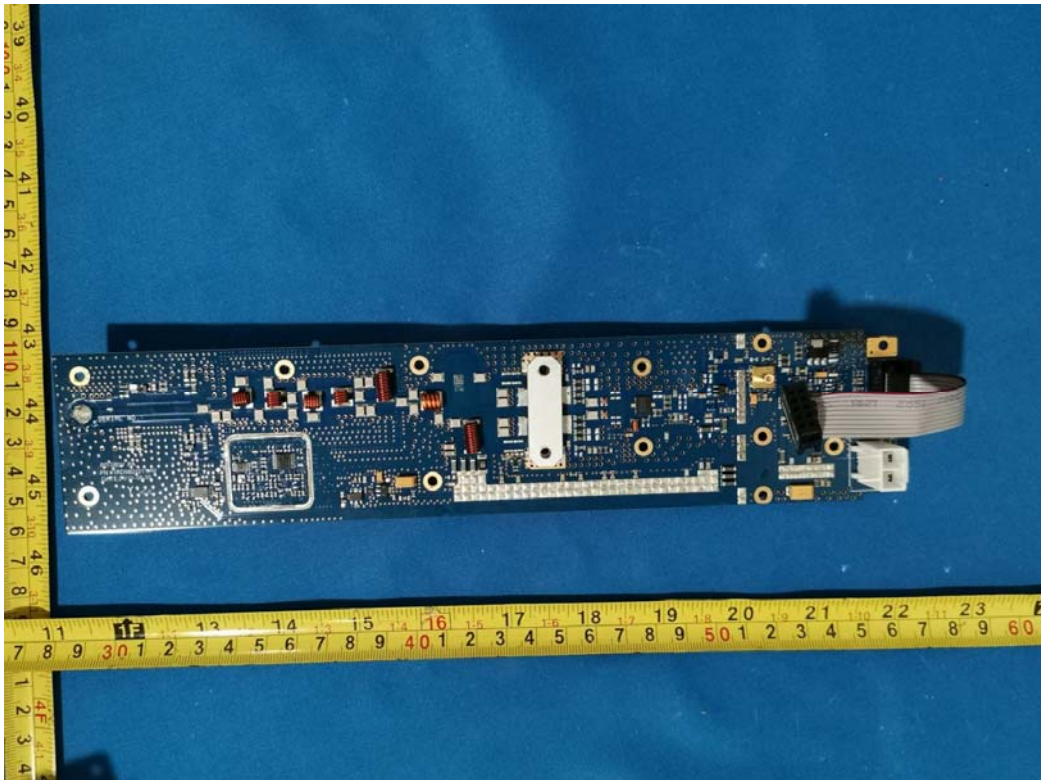
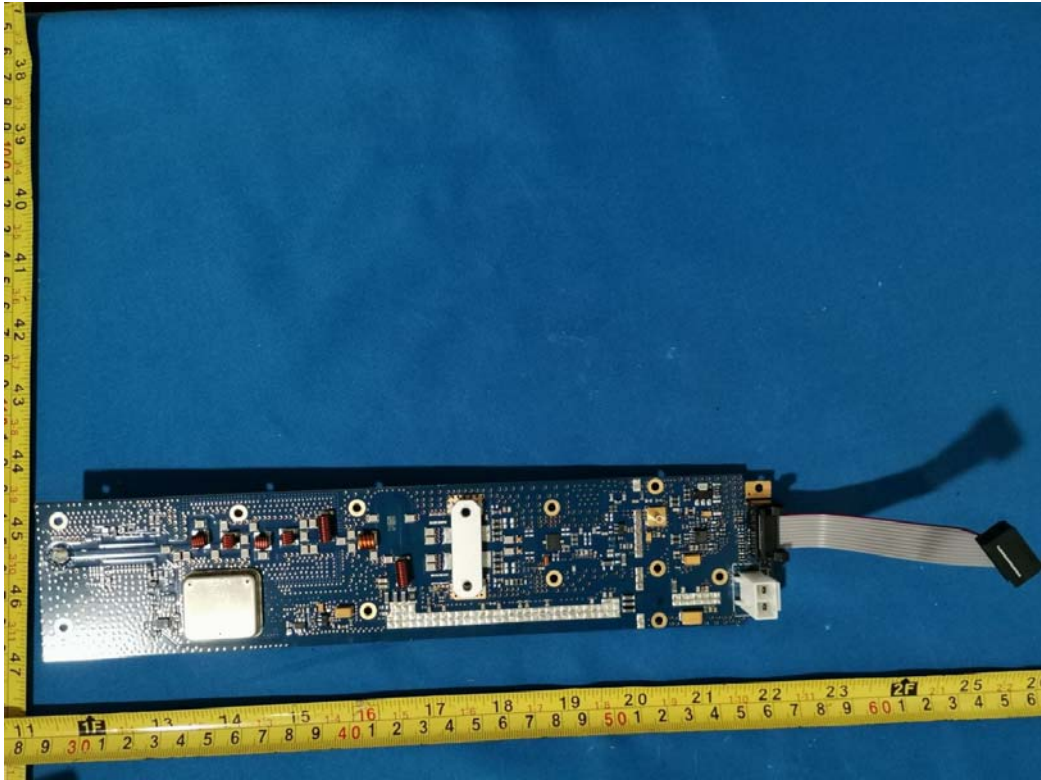














*Port*

