

Figure 348: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_Spectral Density_Port 2.

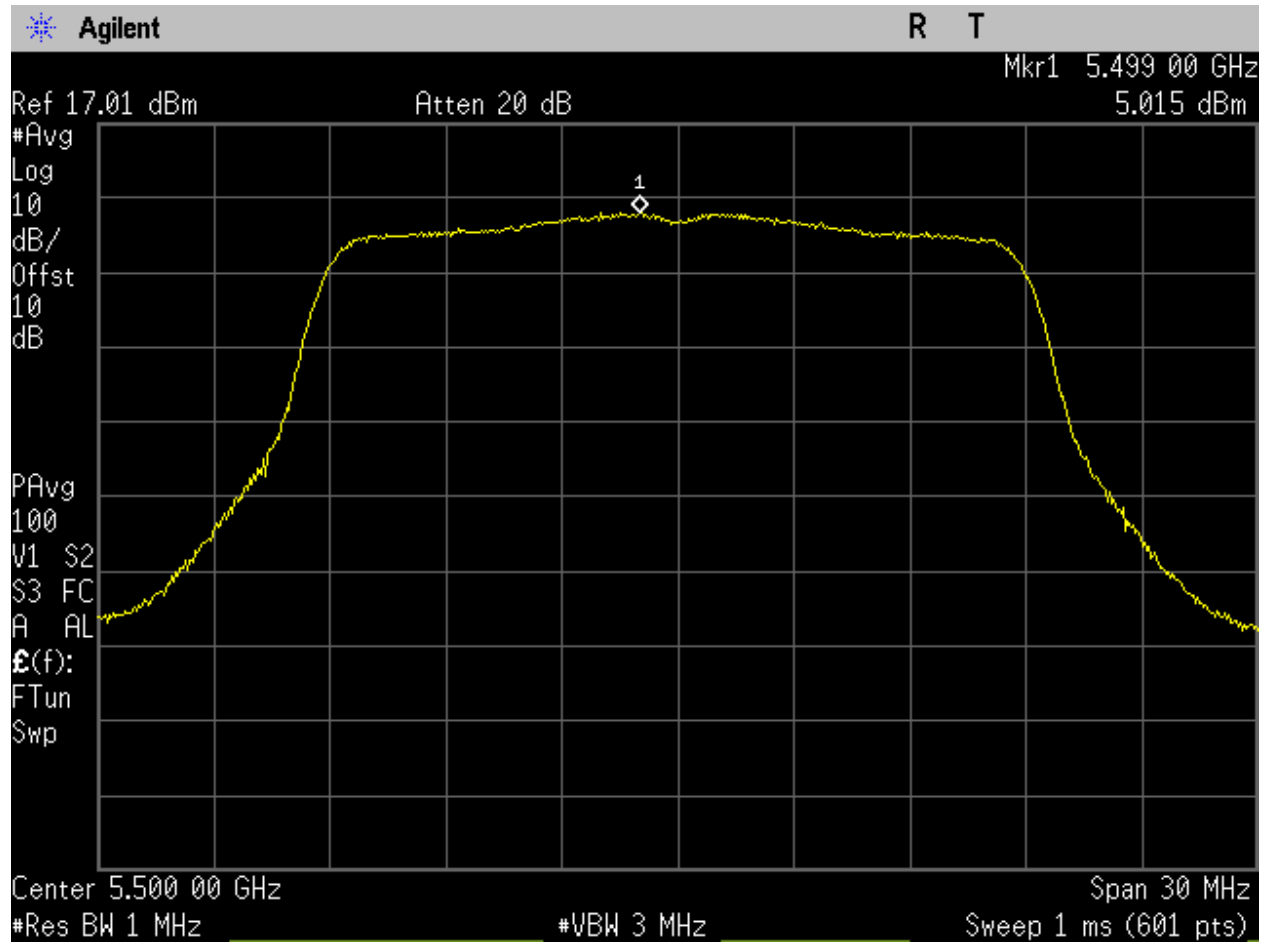


Figure 349: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_Spectral Density_Port 1.

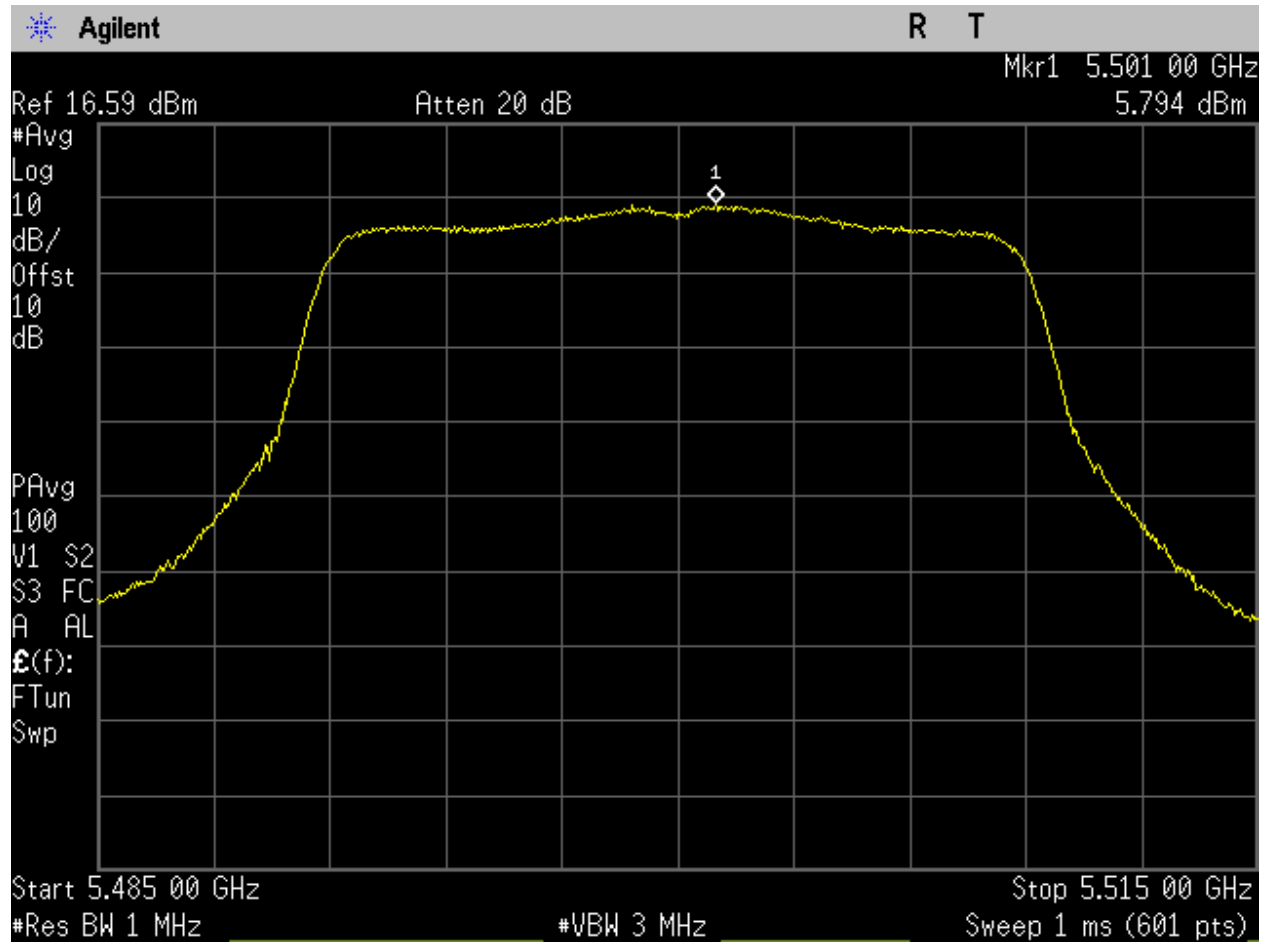


Figure 350: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_Spectral Density_Port 2.

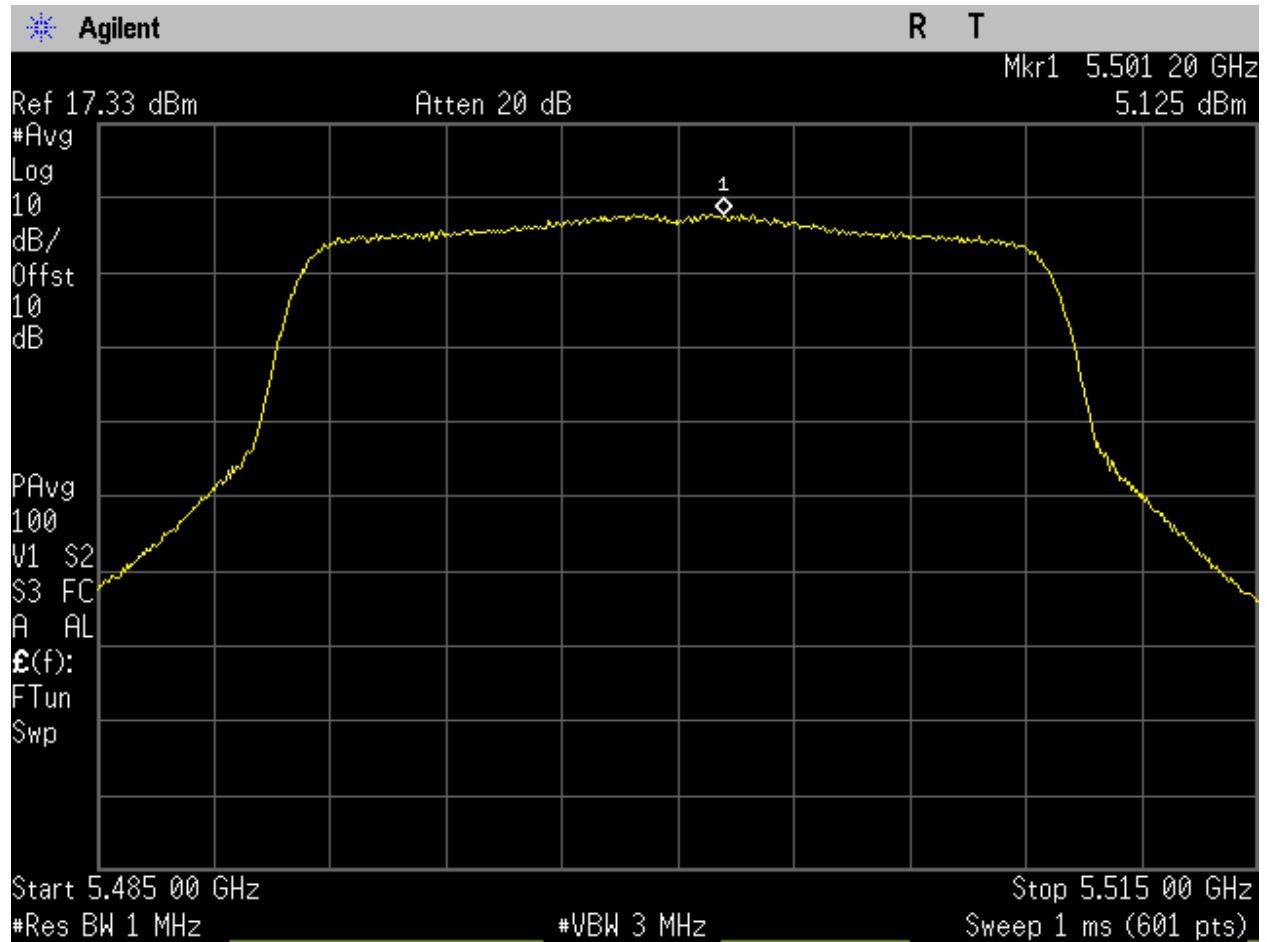


Figure 351: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_Spectral Density_Port 1.

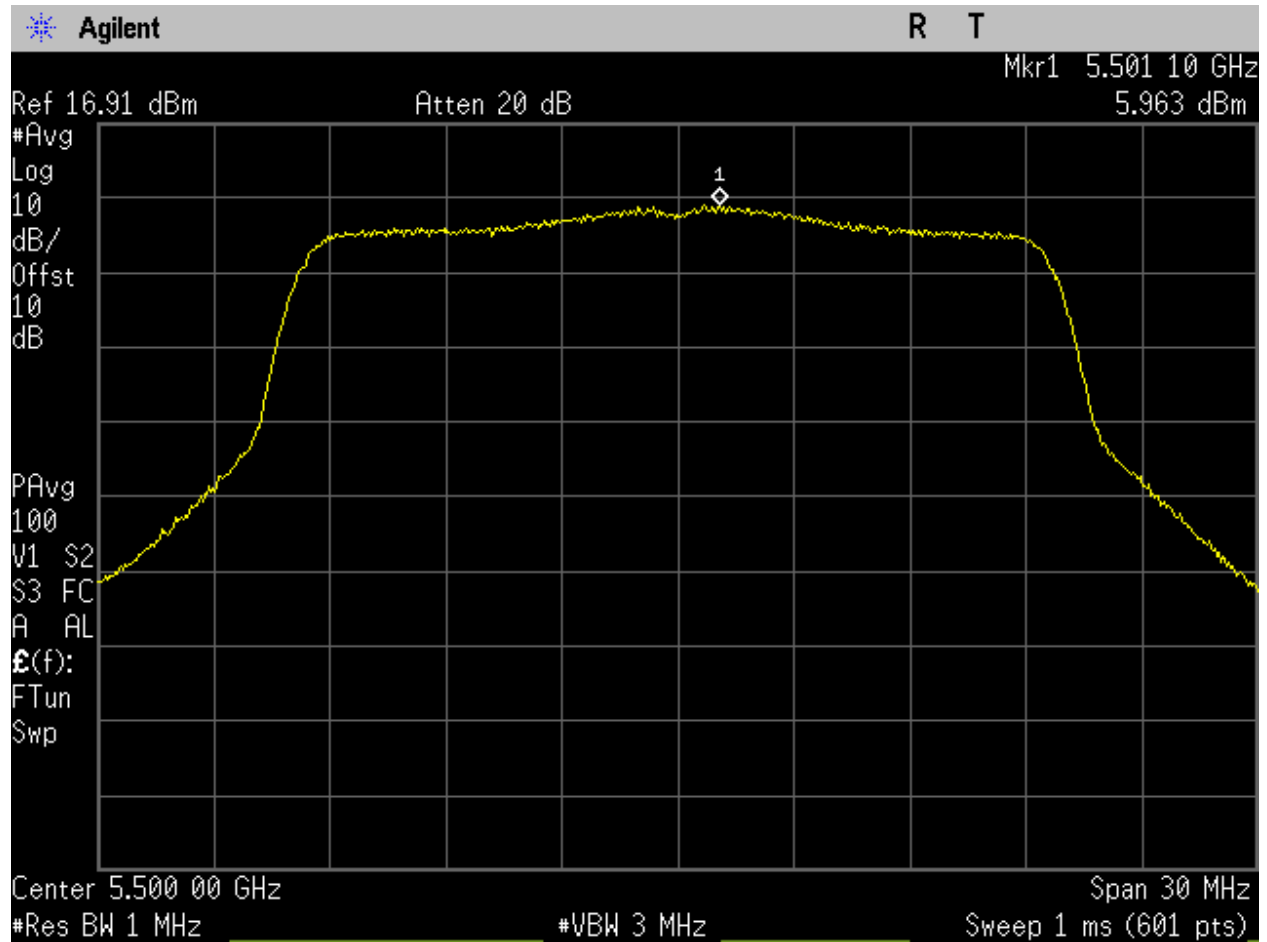


Figure 352: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_Spectral Density_Port 2.

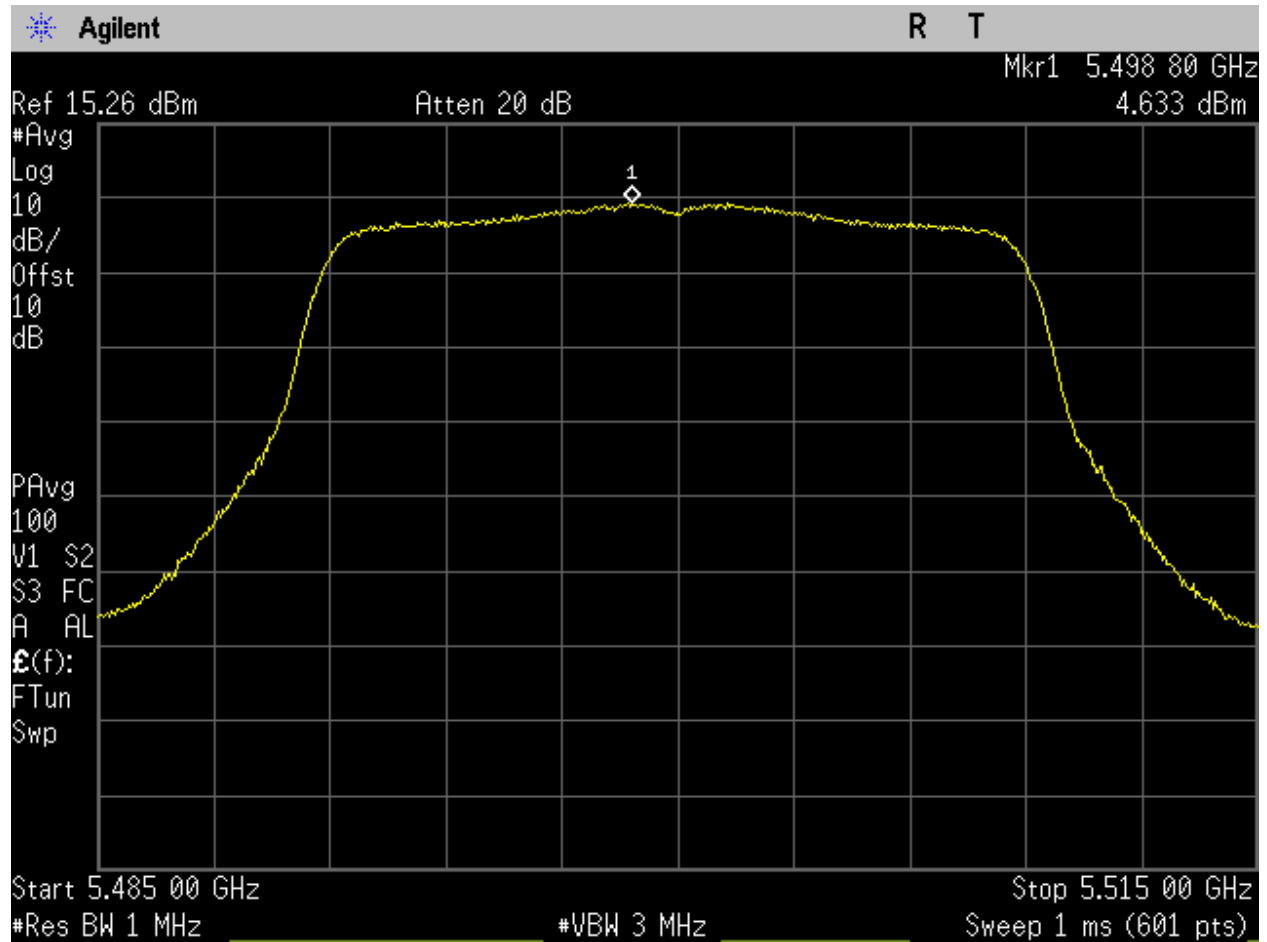


Figure 353: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_Spectral Density_Port 1.

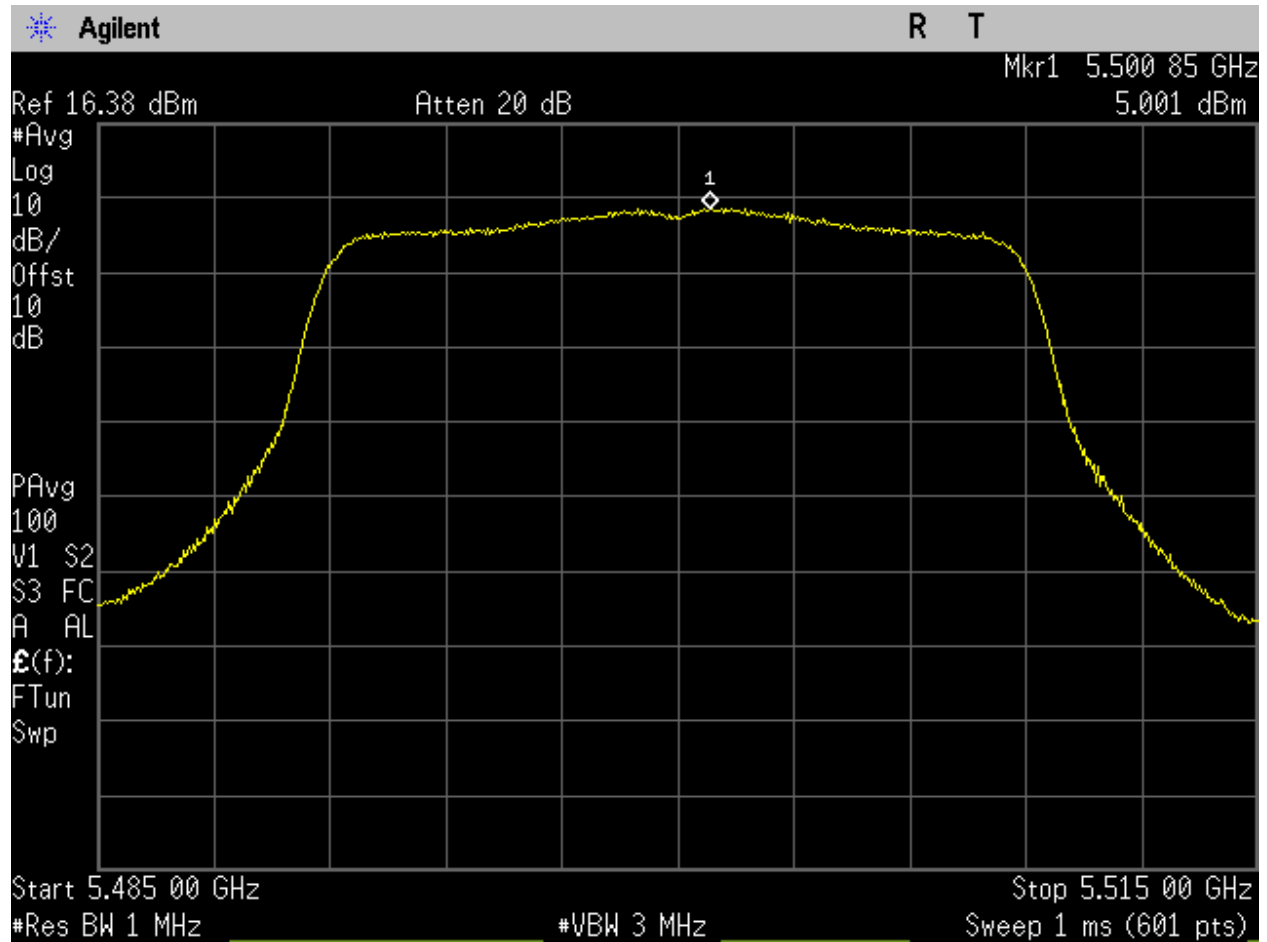


Figure 354: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_Spectral Density_Port 2.

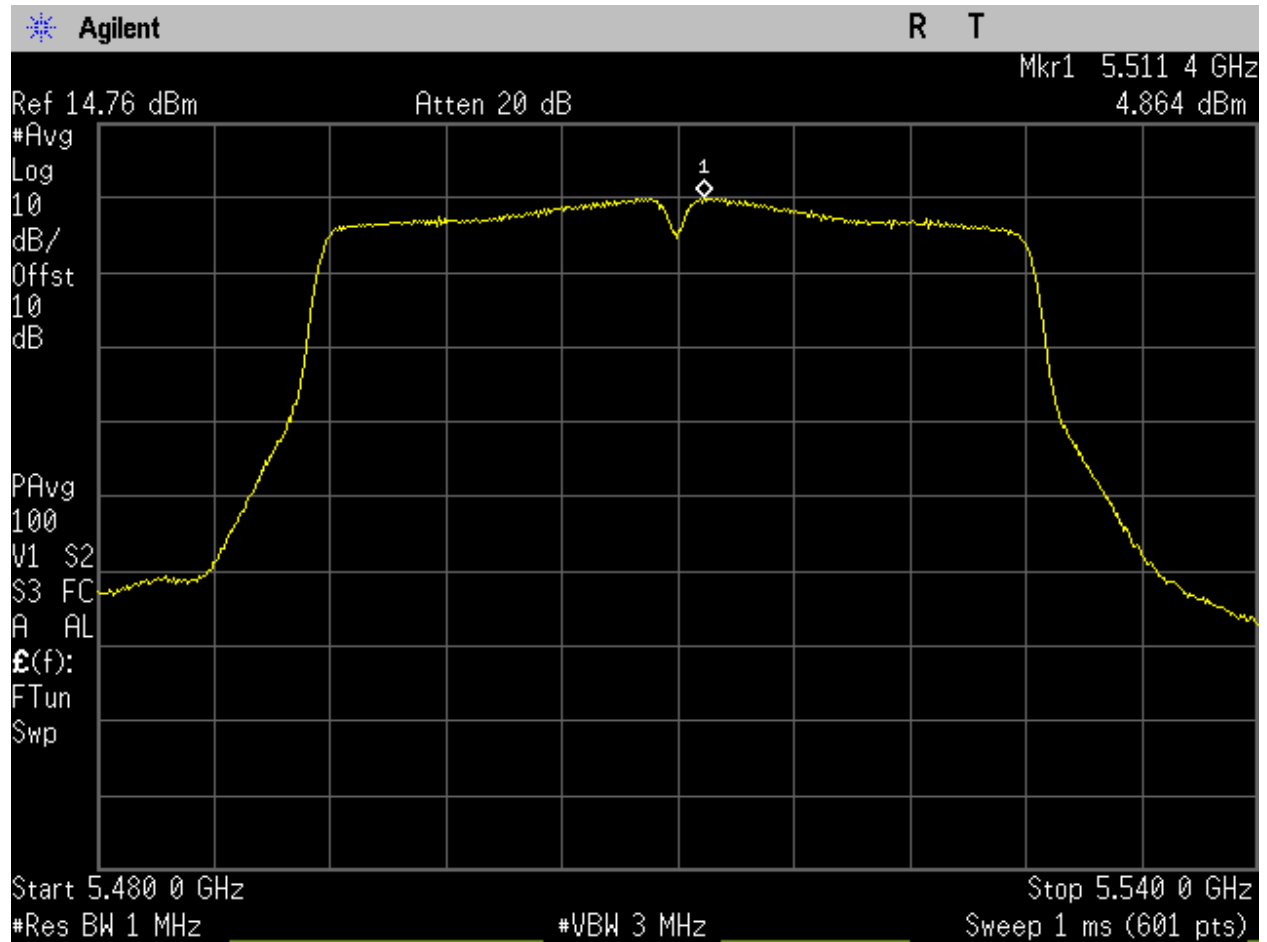


Figure 355: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_Spectral Density_Port 1.

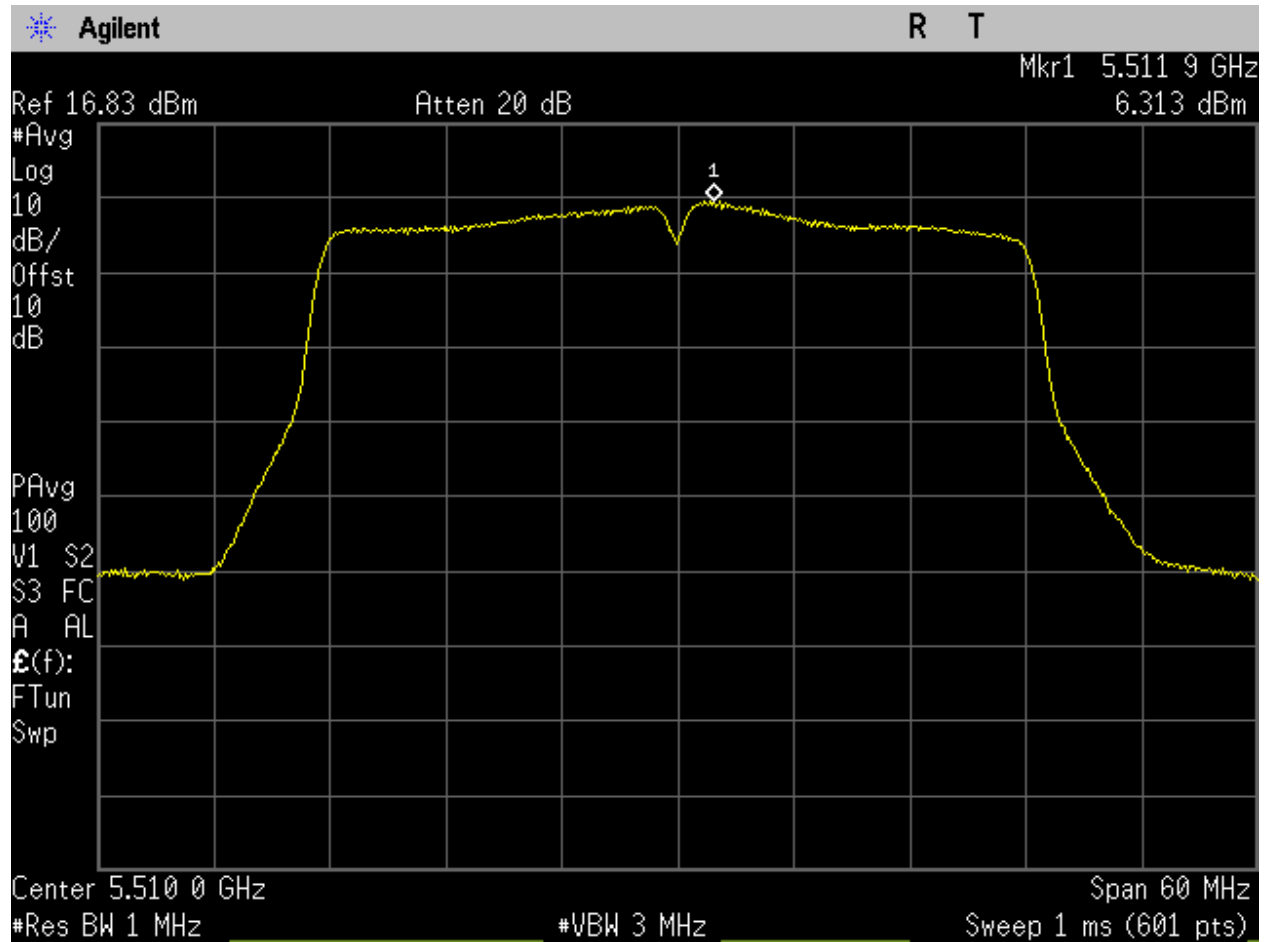


Figure 356: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_Spectral Density_Port 2.

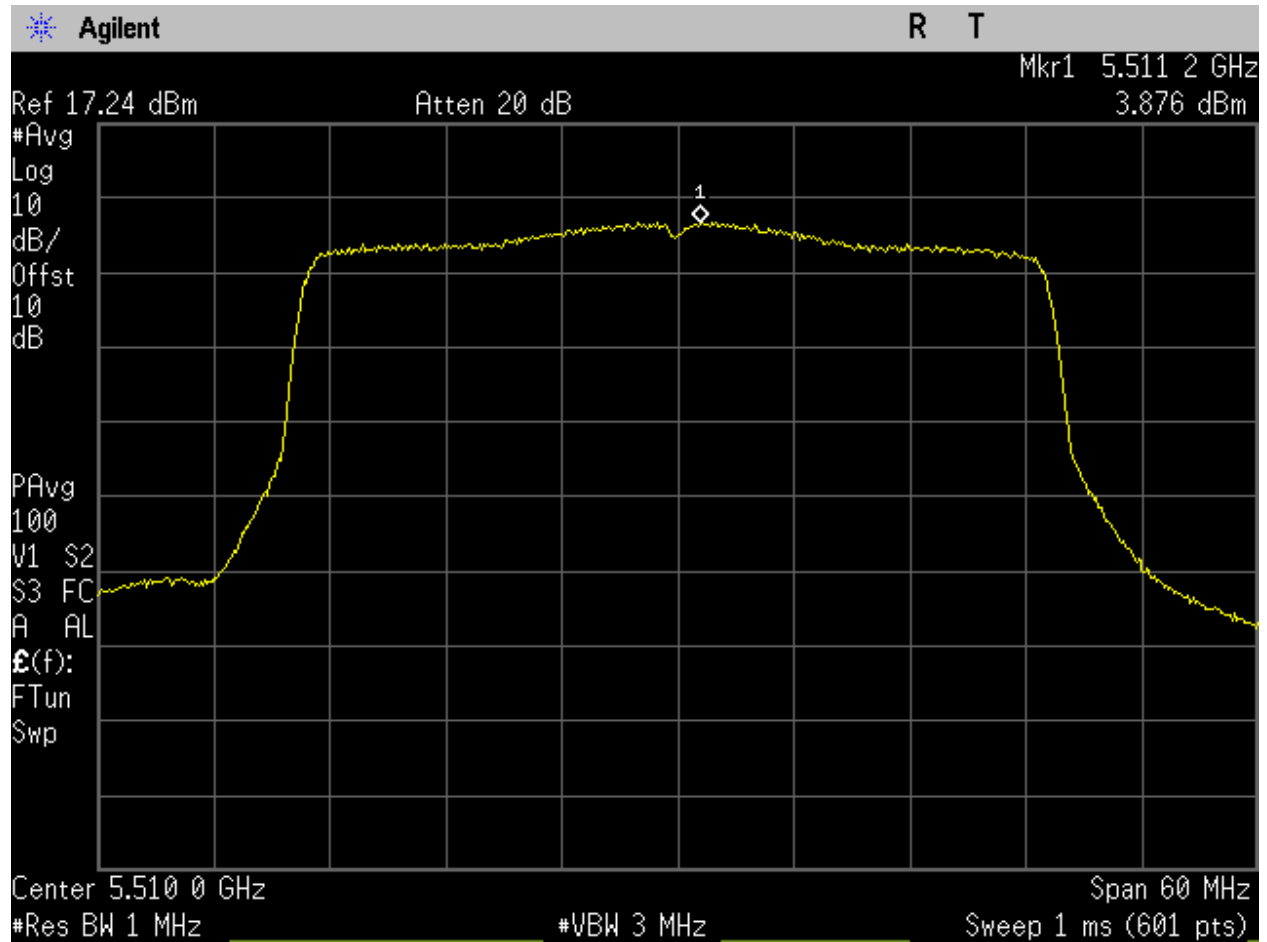


Figure 357: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_Spectral Density_Port 1.

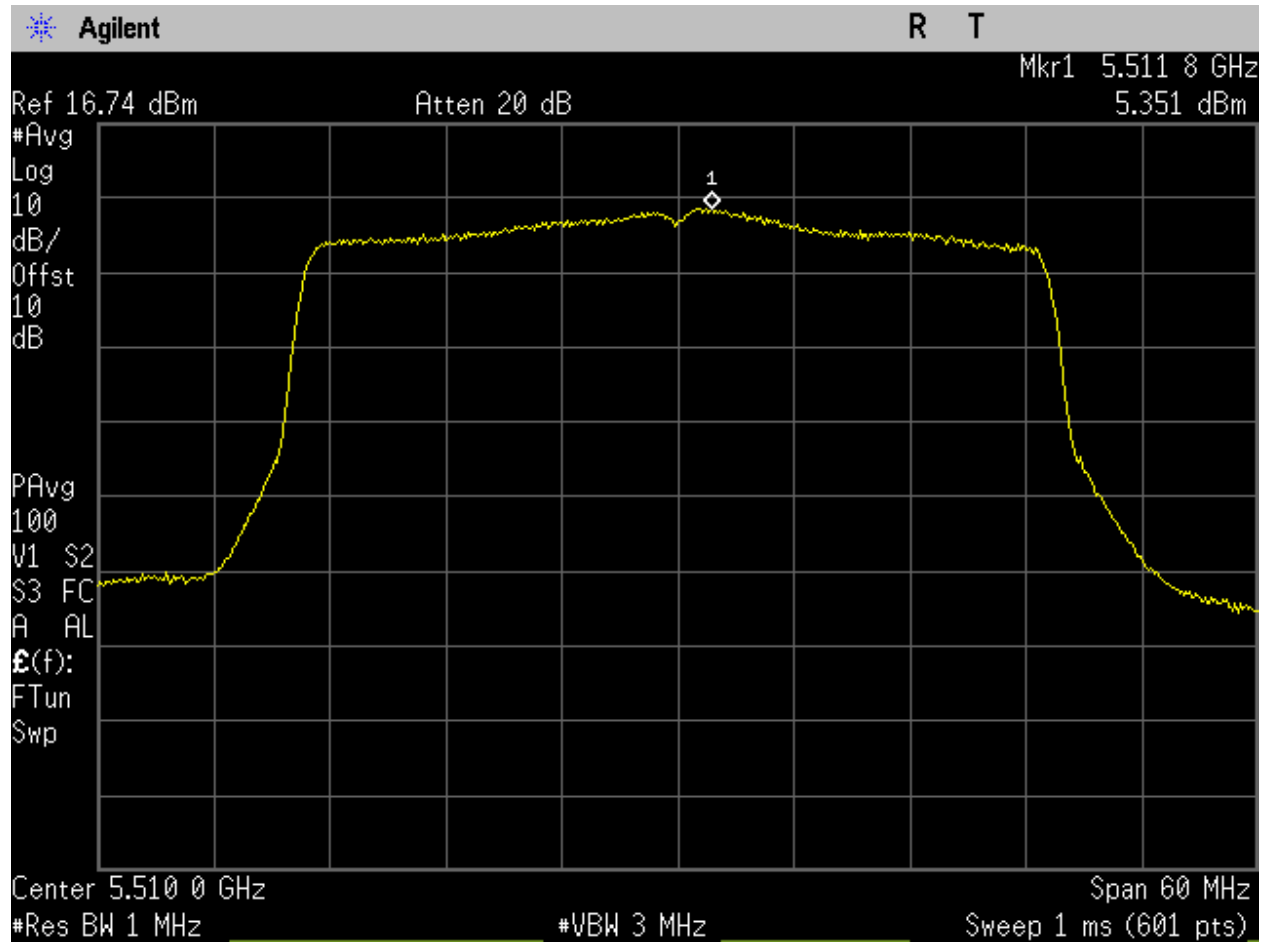


Figure 358: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_Spectral Density_Port 2.

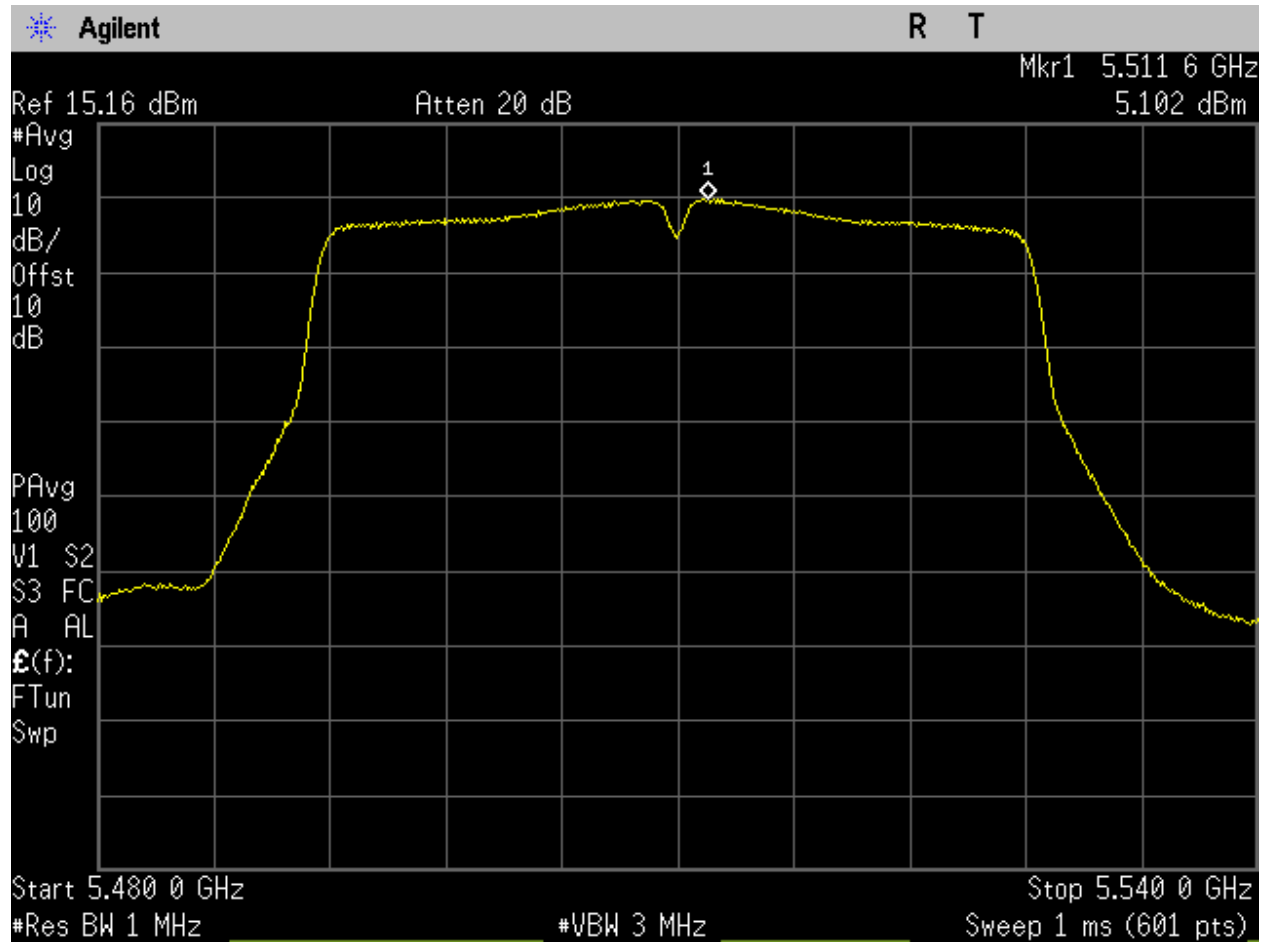


Figure 359: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_Spectral Density_Port 1.

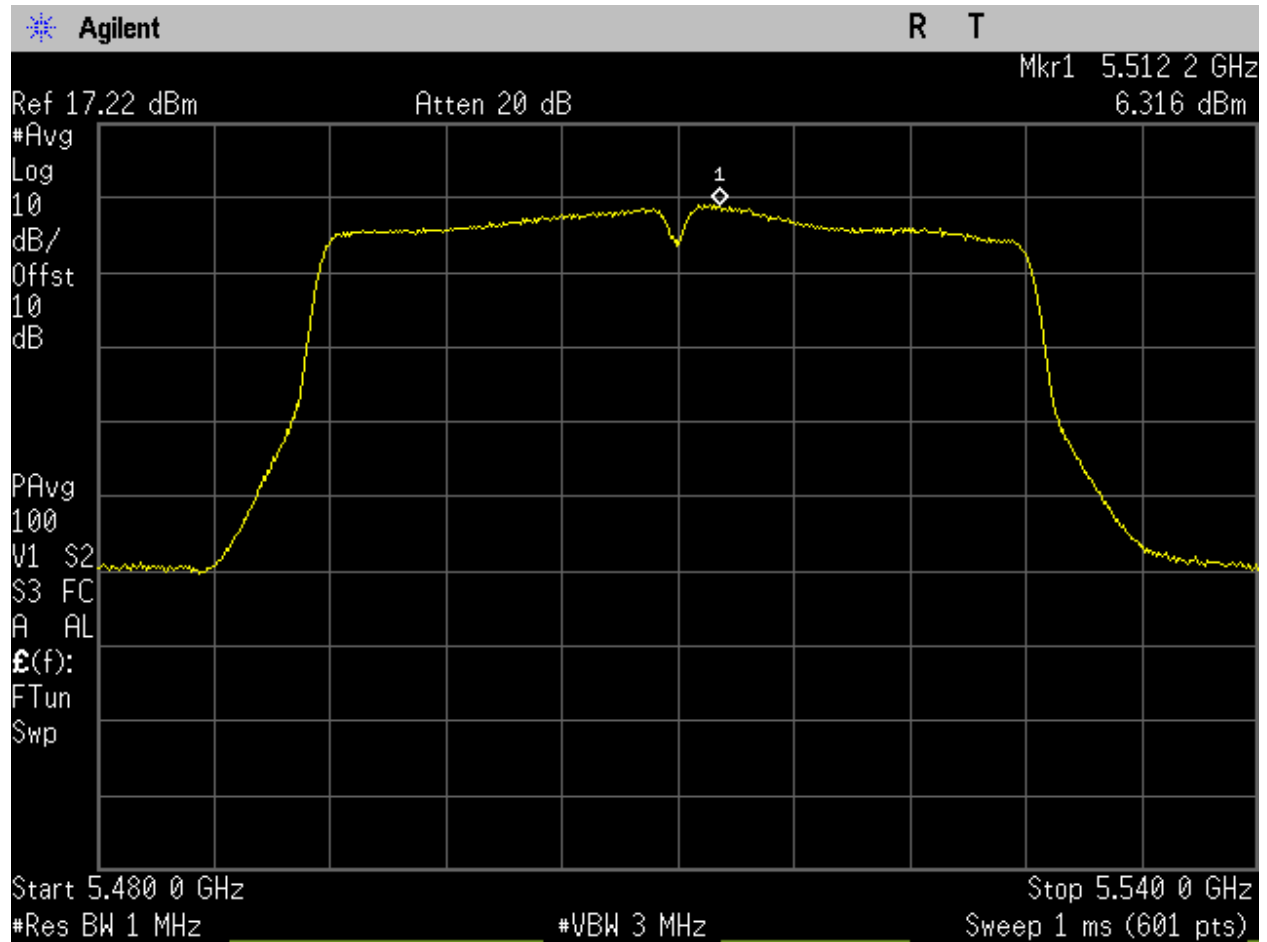


Figure 360: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_Spectral Density_Port 2.

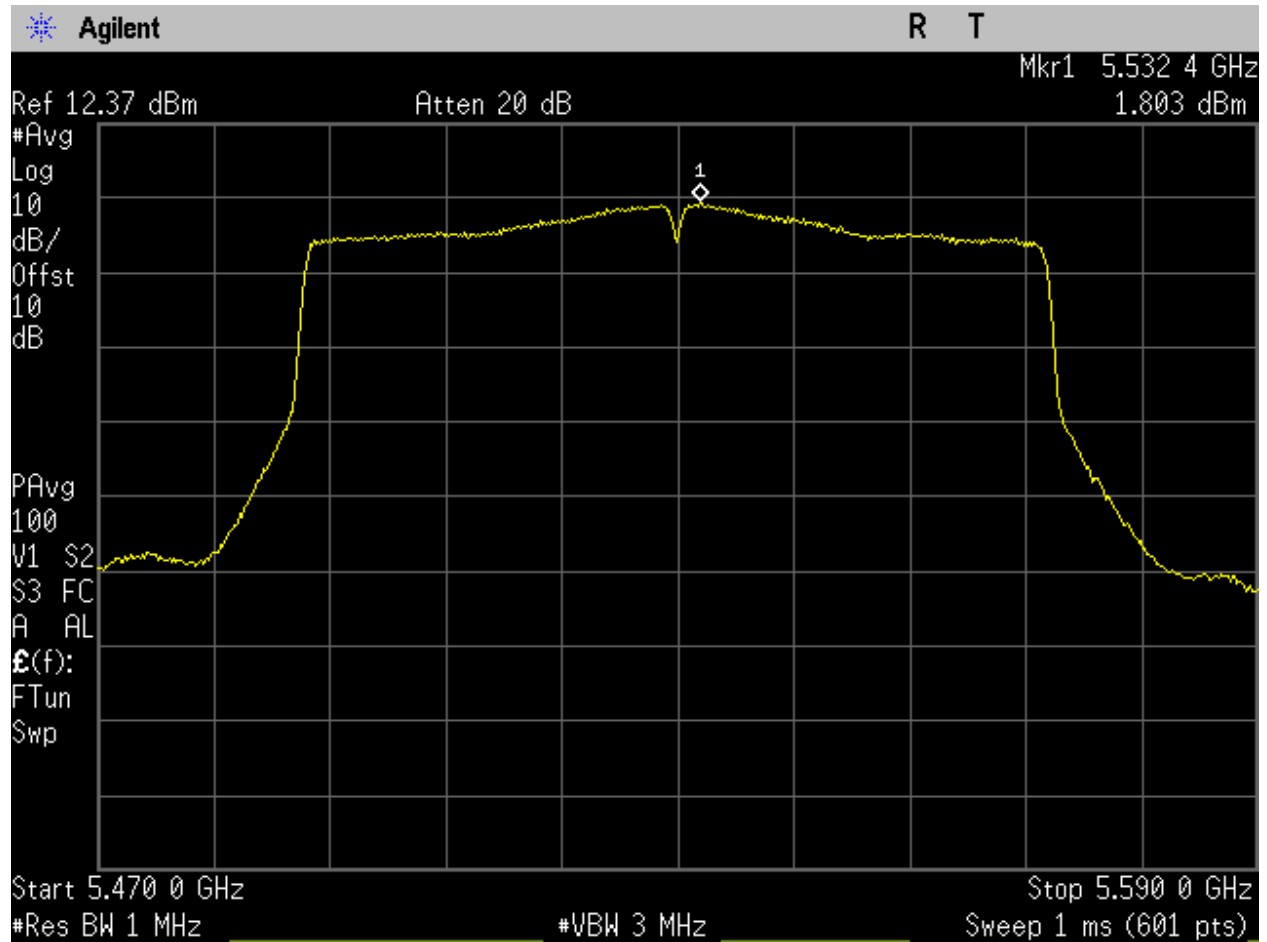


Figure 361: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_Spectral Density_Port 1.

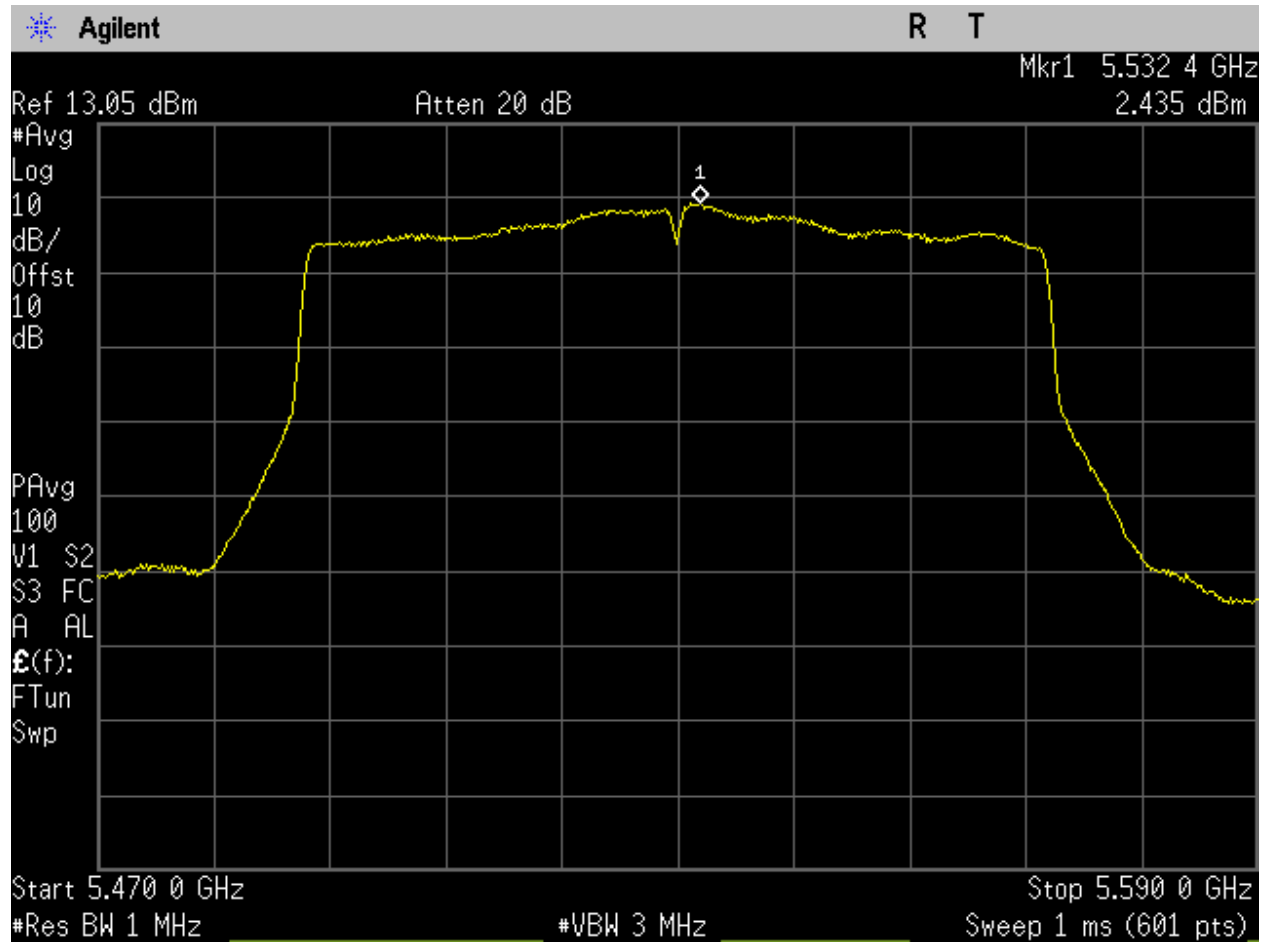


Figure 362: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_Spectral Density_Port 2.

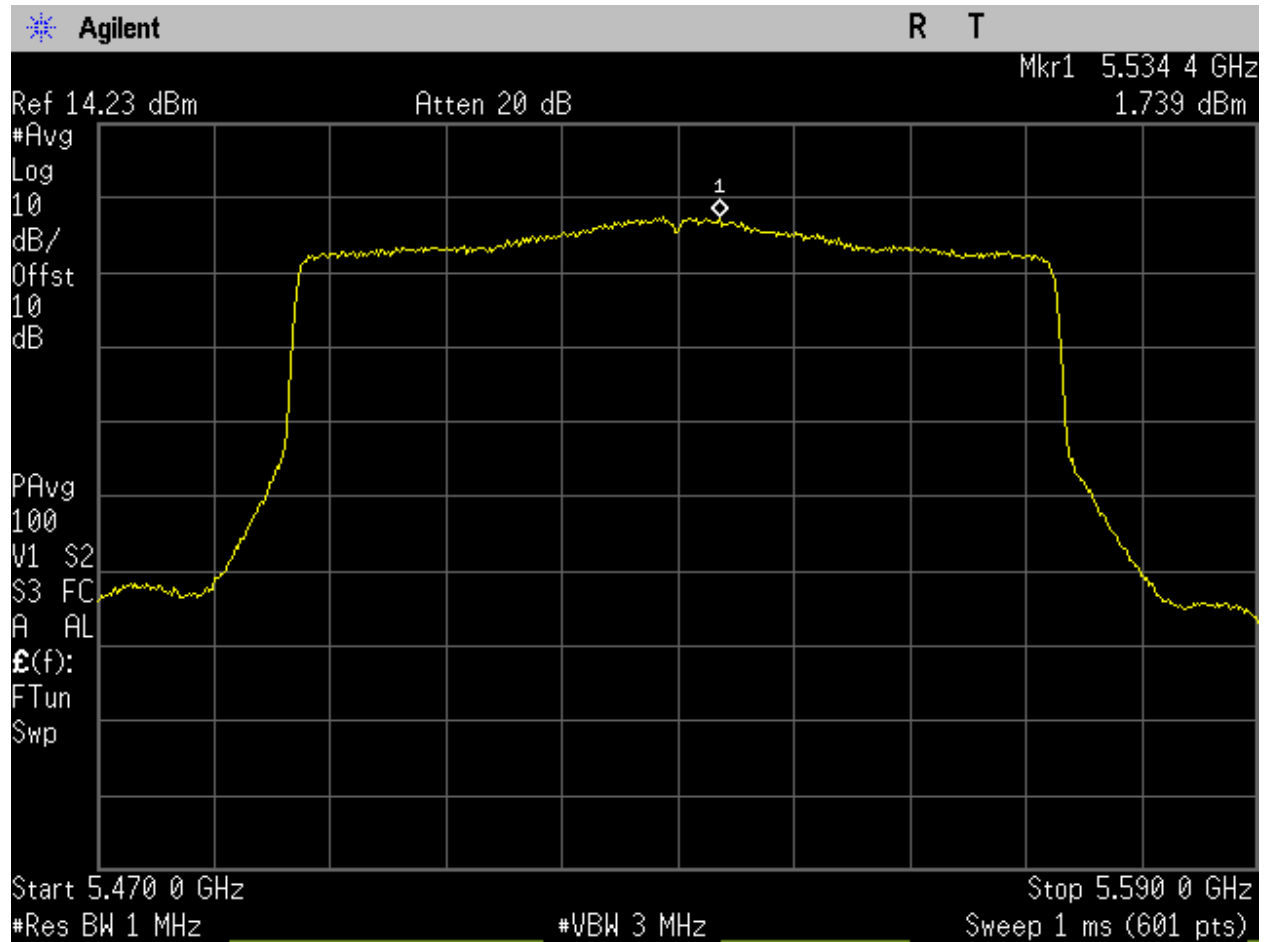


Figure 363: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_Spectral Density_Port 1.

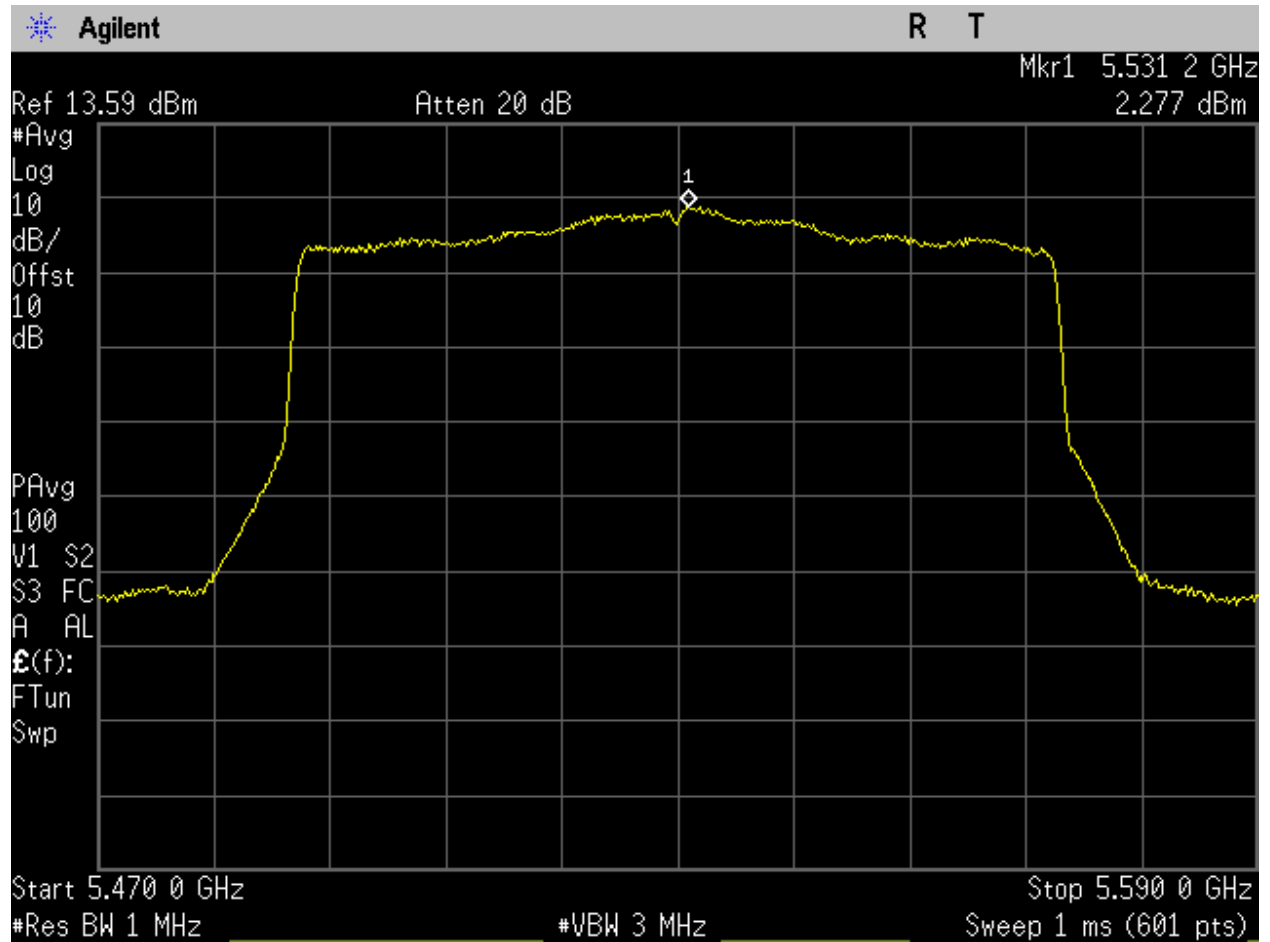


Figure 364: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_Spectral Density_Port 2.

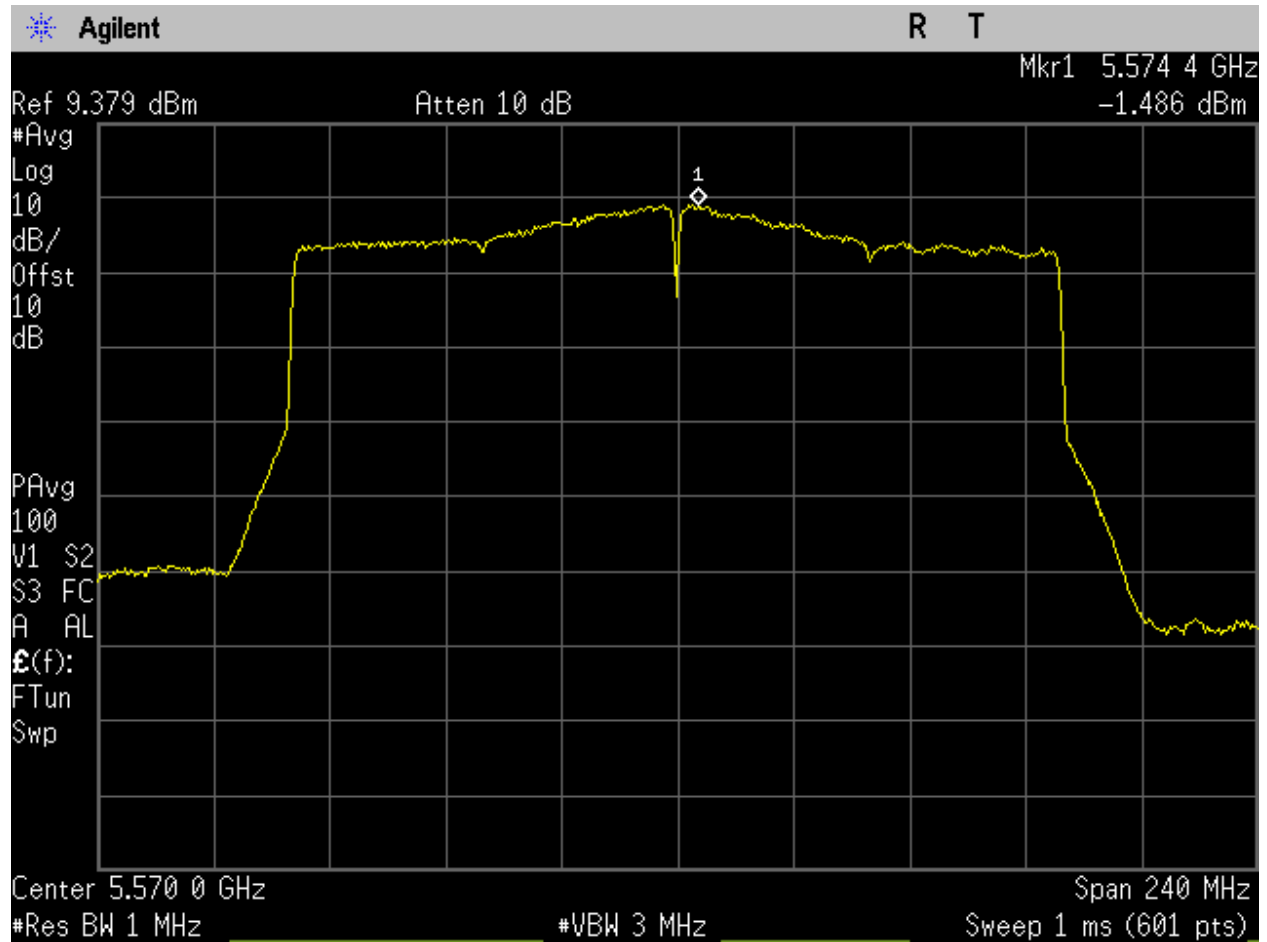


Figure 365: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_Spectral Density_Port 1.

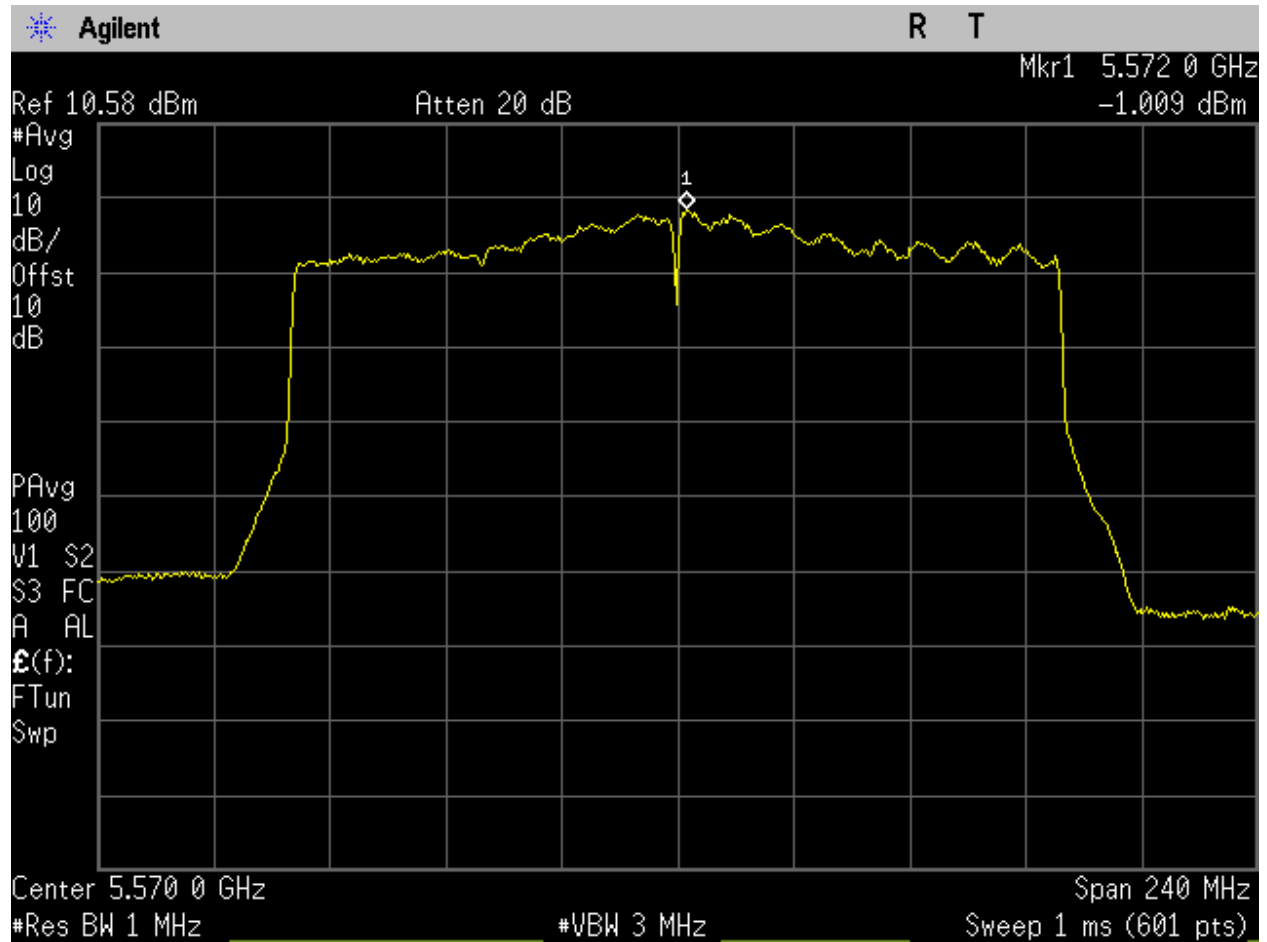


Figure 366: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_Spectral Density_Port 2.

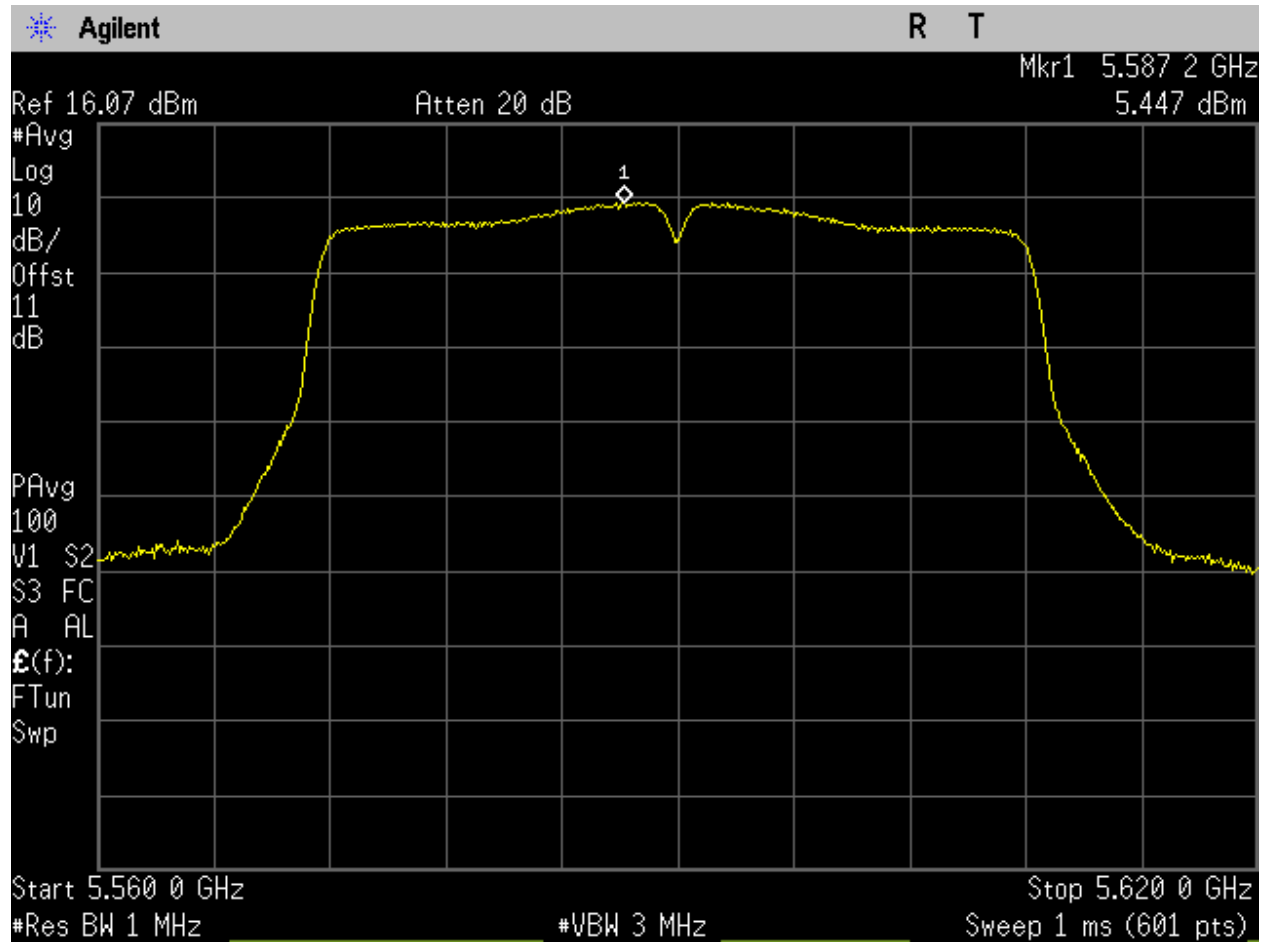


Figure 367: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ac-mode_Spectral Density_Port 1.

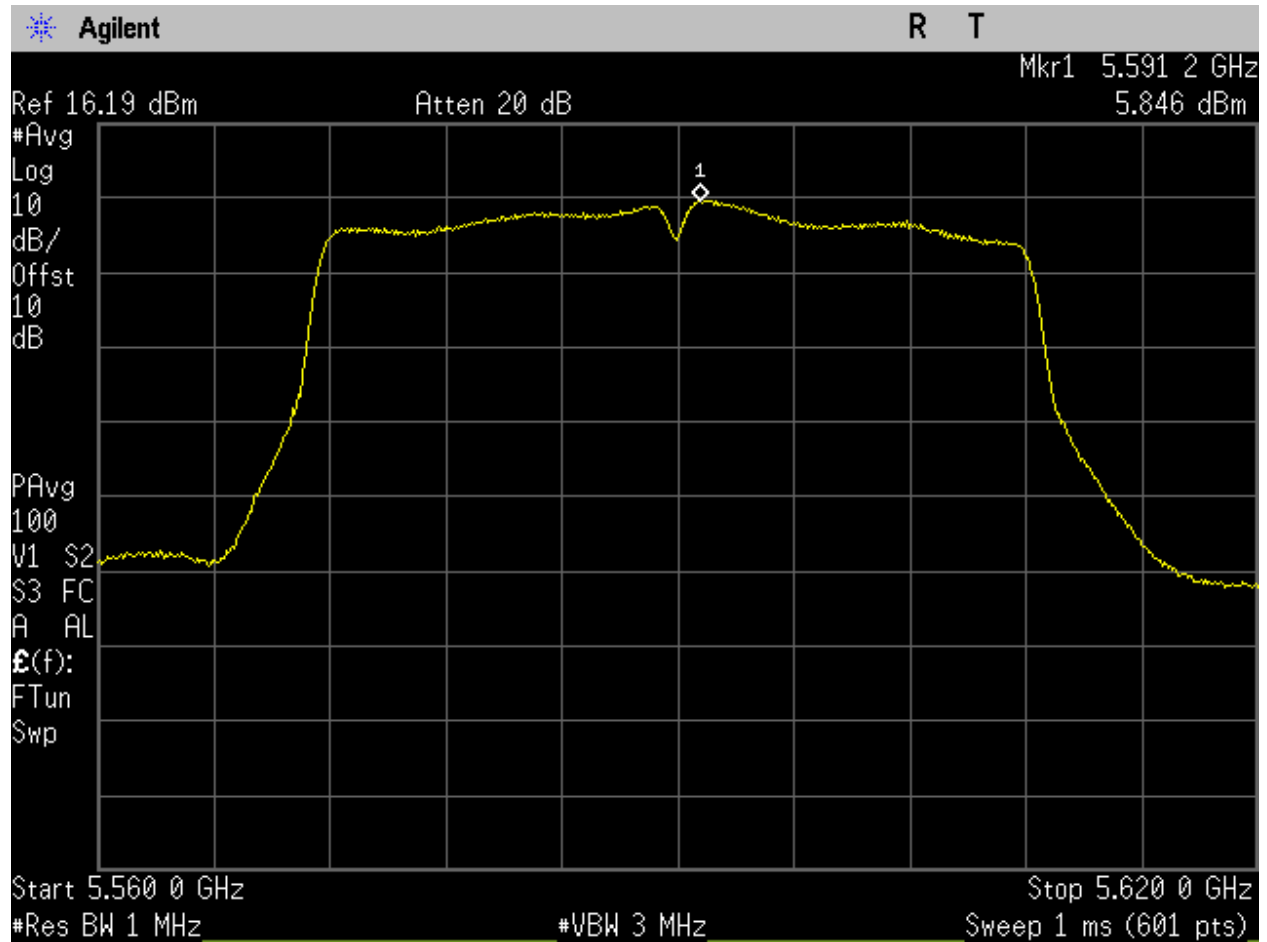


Figure 368: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ac-mode_Spectral Density_Port 2.

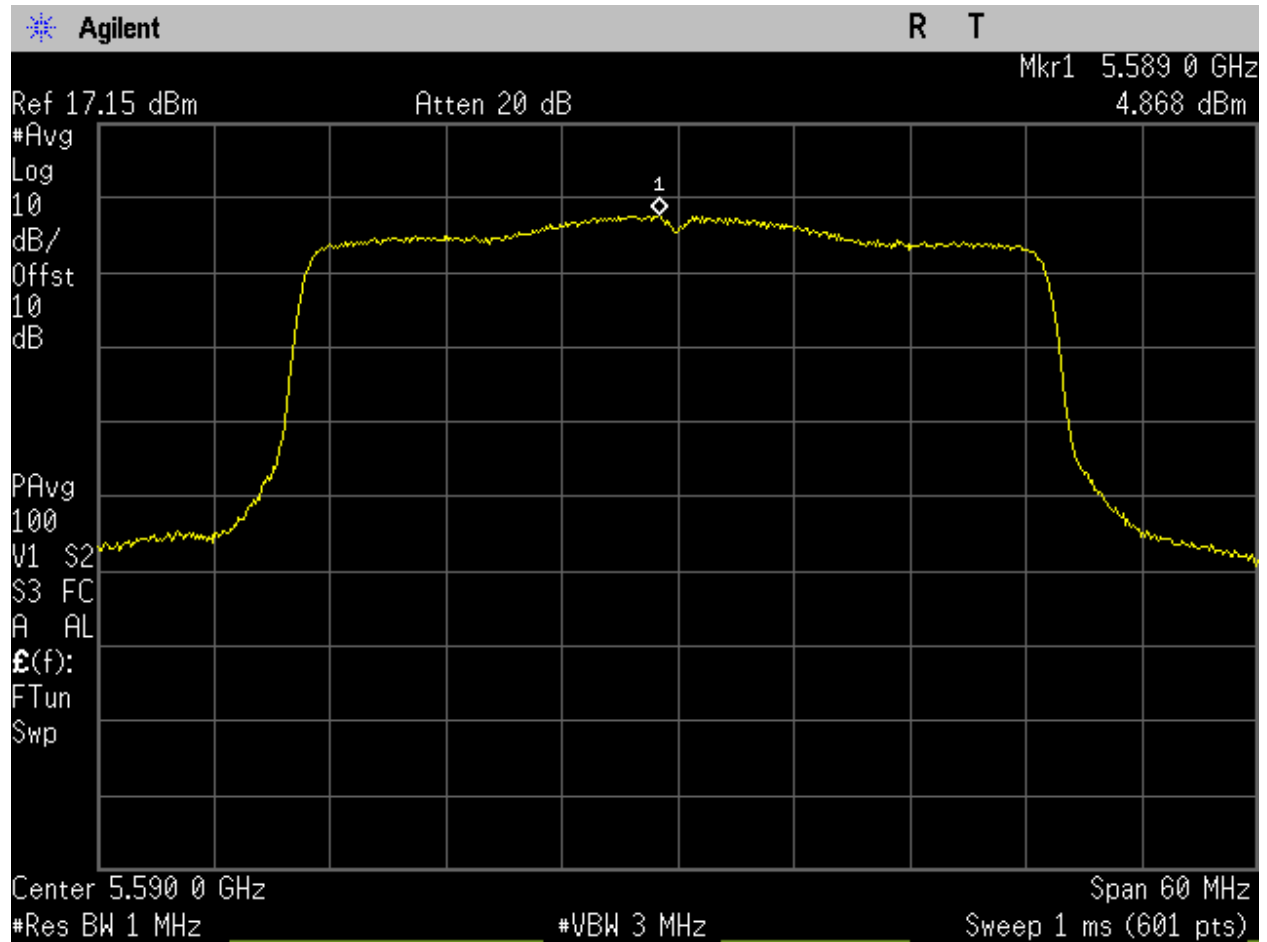


Figure 369: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ax-mode_Spectral Density_Port 1.

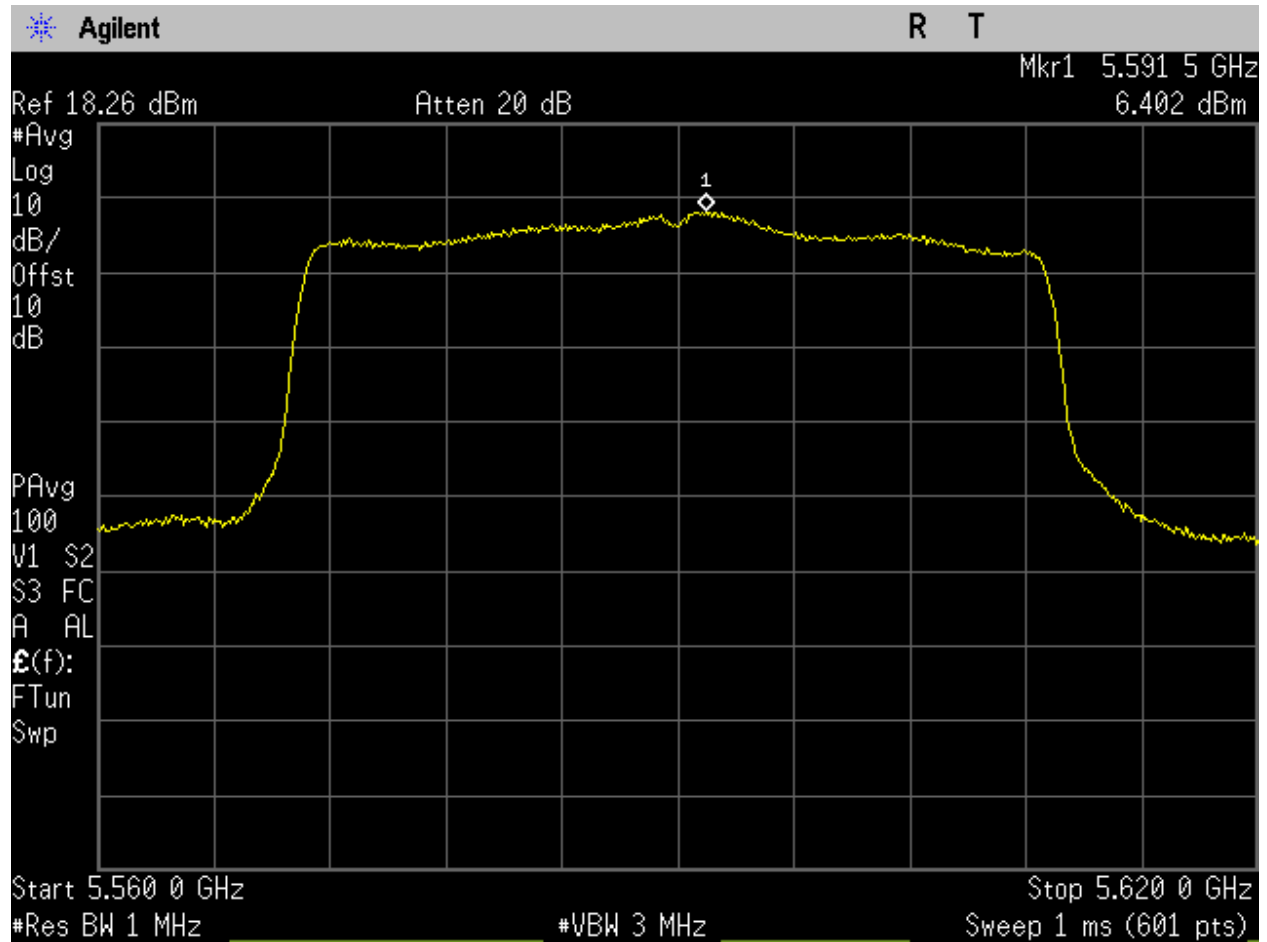


Figure 370: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ax-mode_Spectral Density_Port 2.

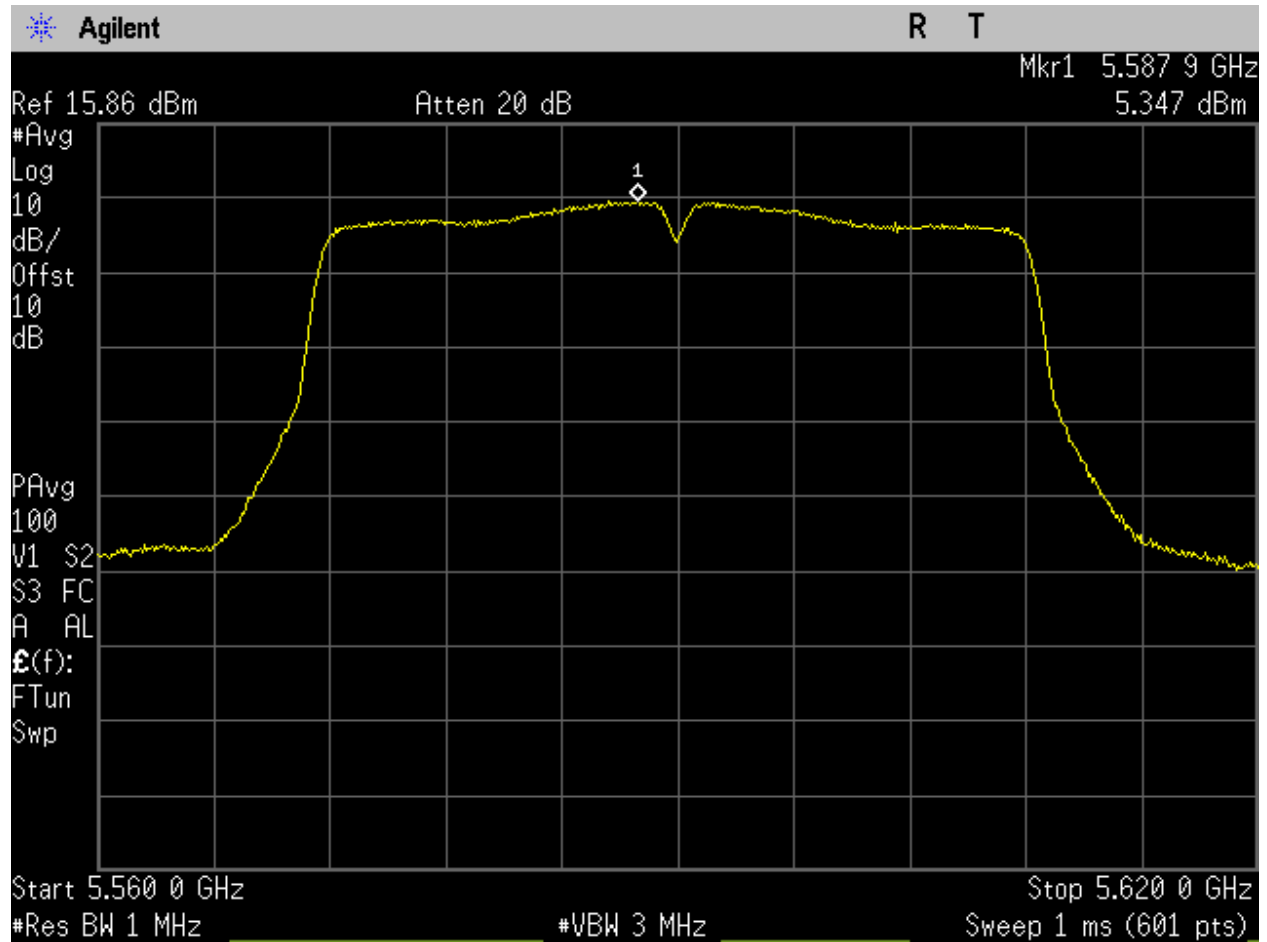


Figure 371: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-mode_Spectral Density_Port 1.

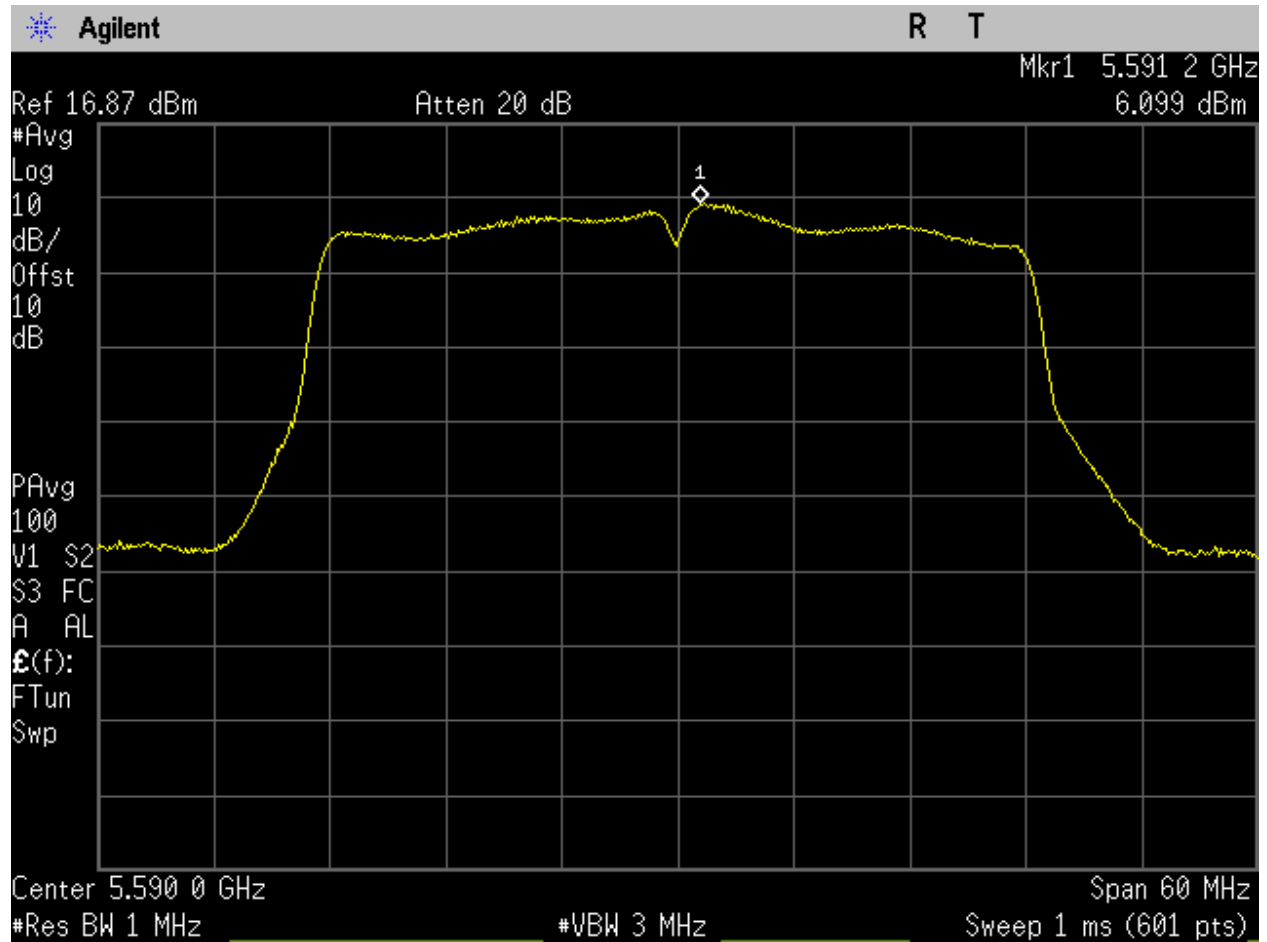


Figure 372: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-mode_Spectral Density_Port 2.

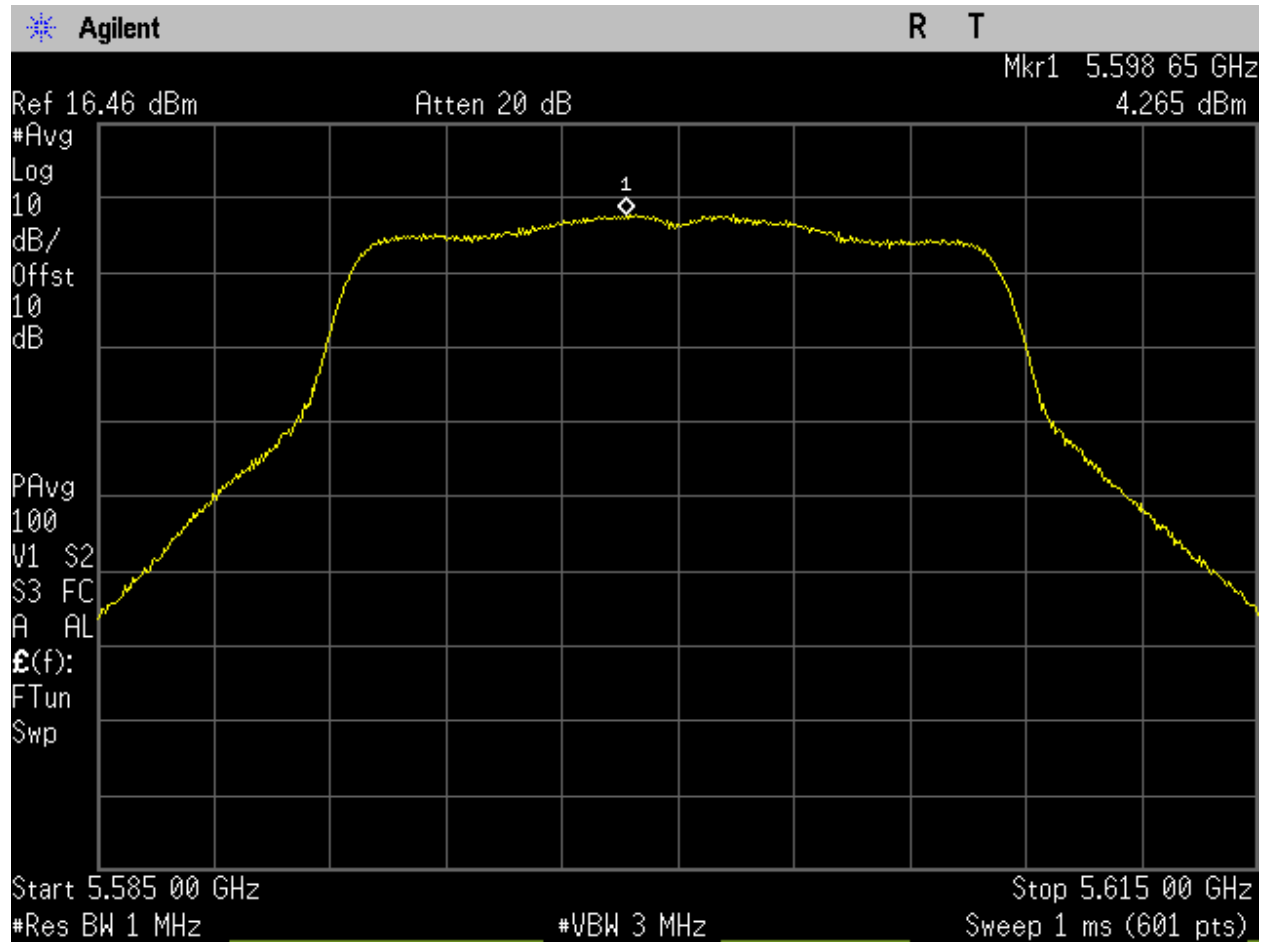


Figure 373: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Spectral Density_Port 1.

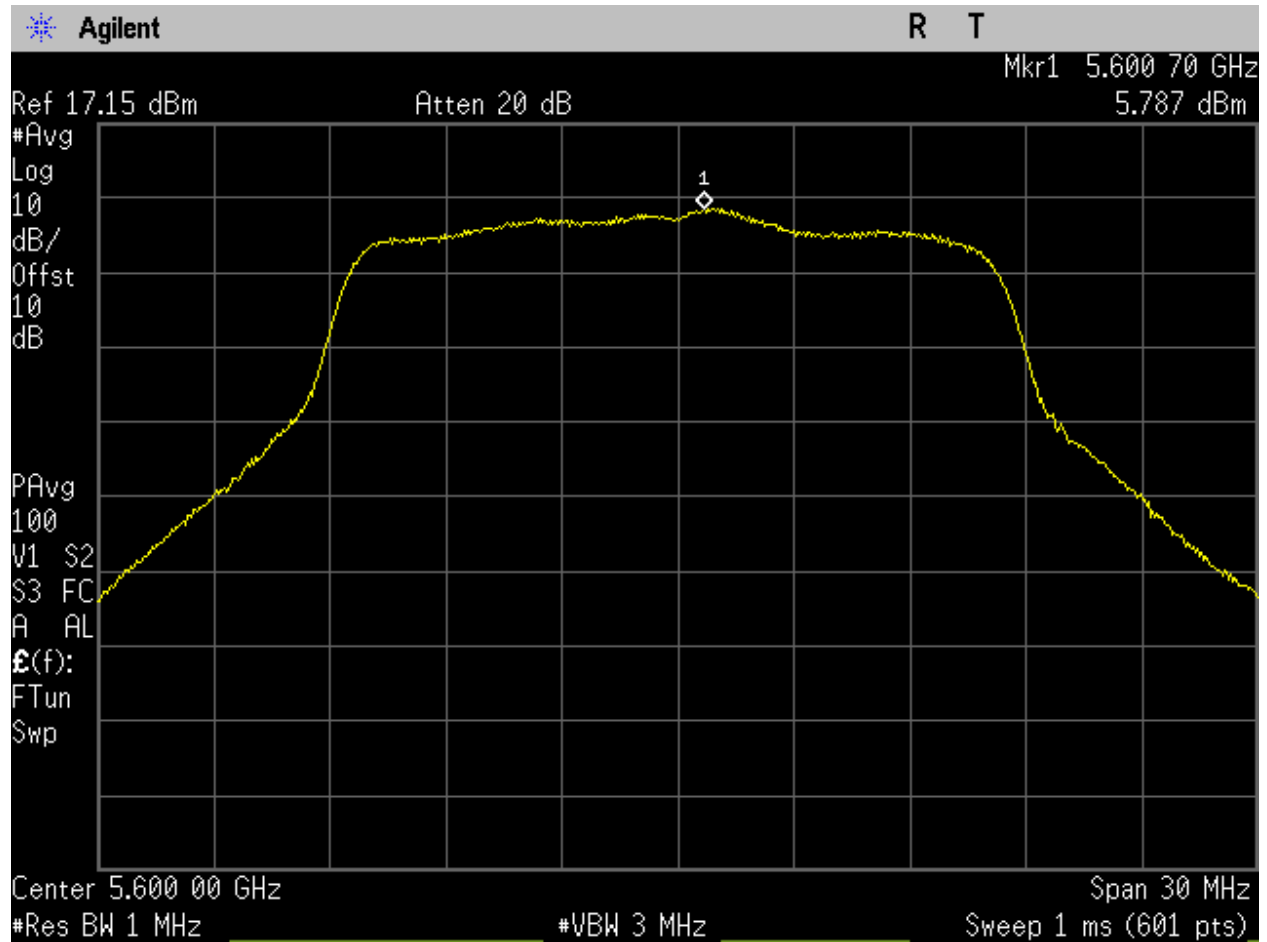


Figure 374: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Spectral Density_Port 2.

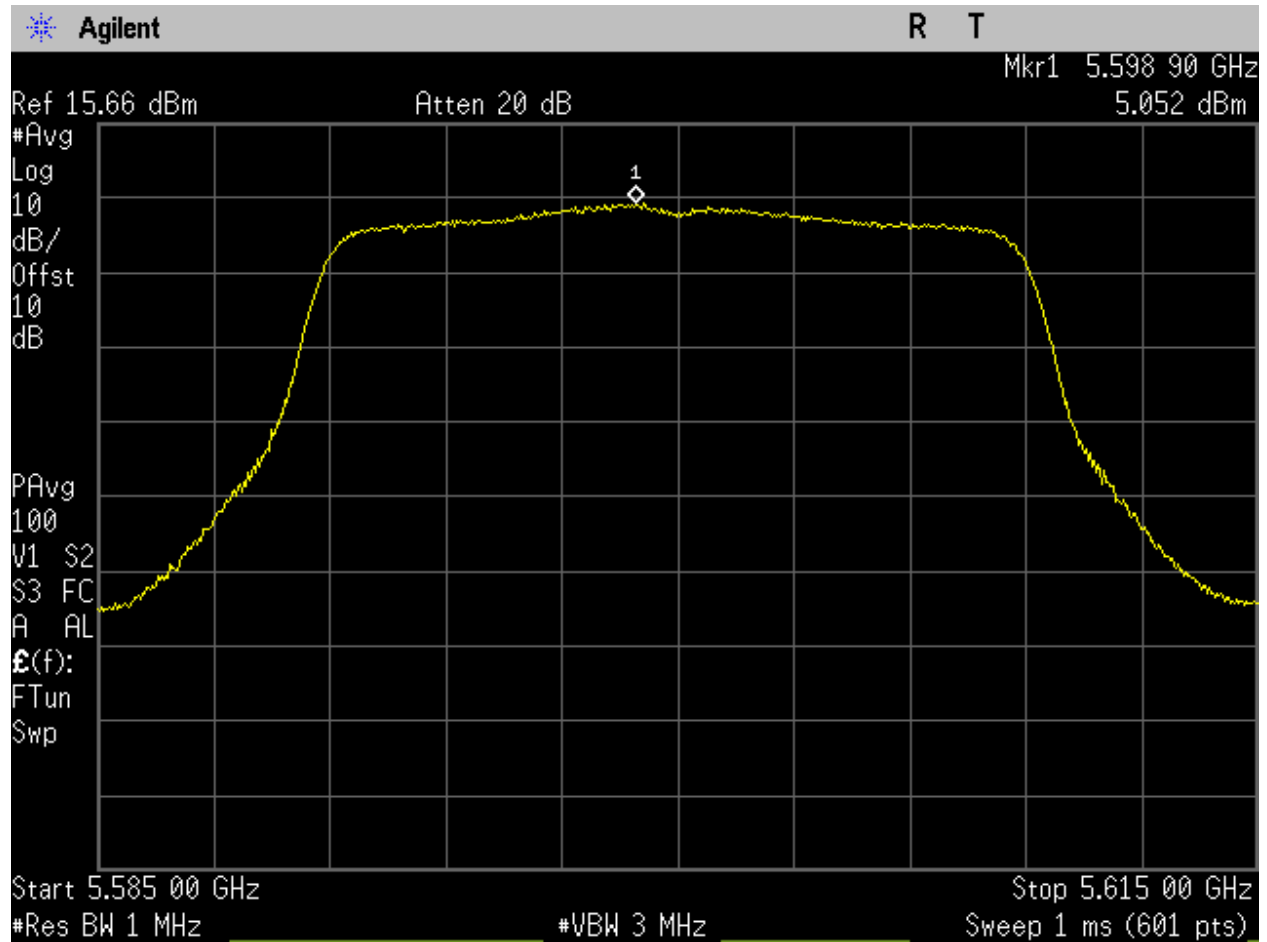


Figure 375: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Spectral Density_Port 1.

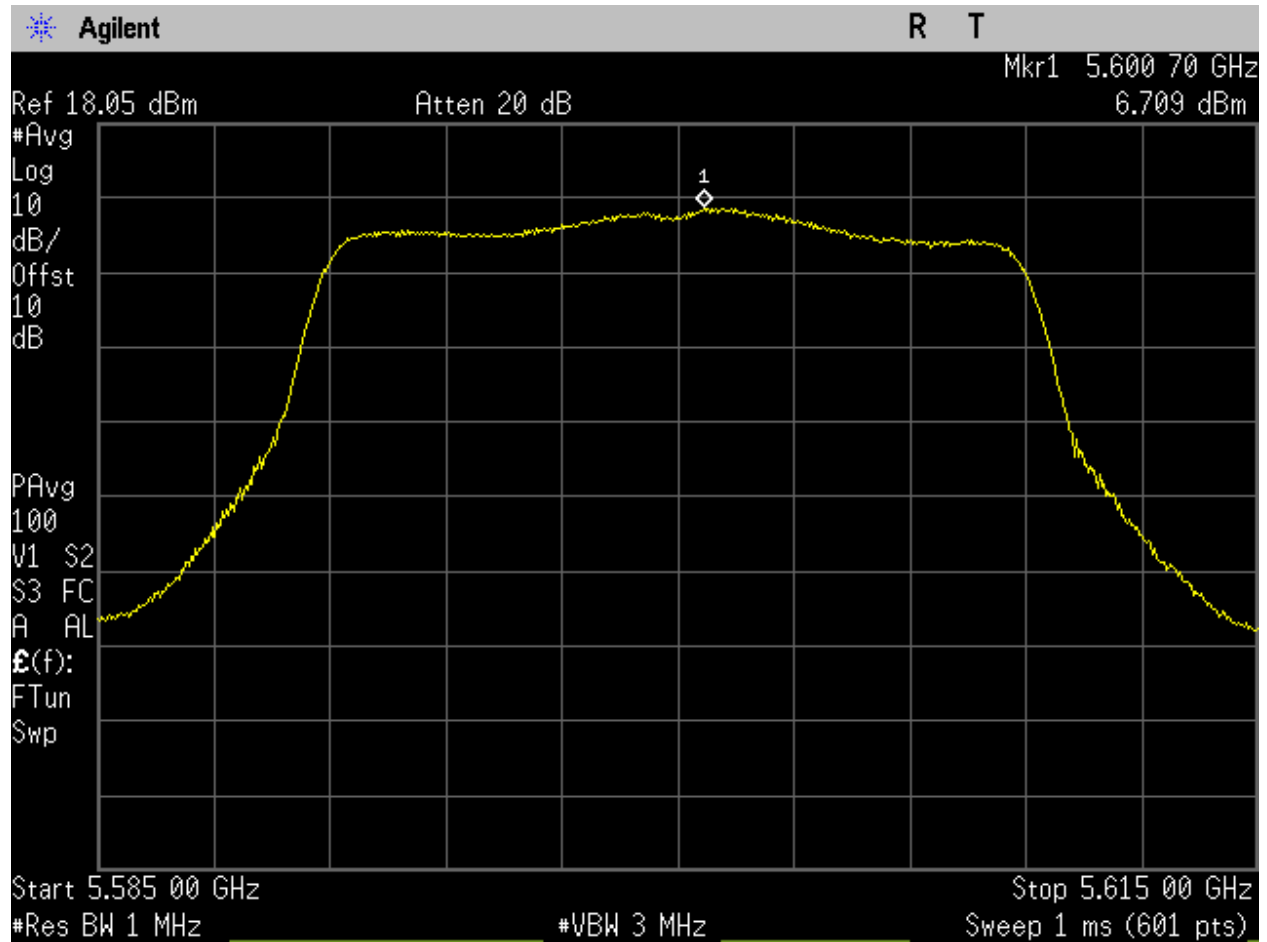


Figure 376: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Spectral Density_Port 2.

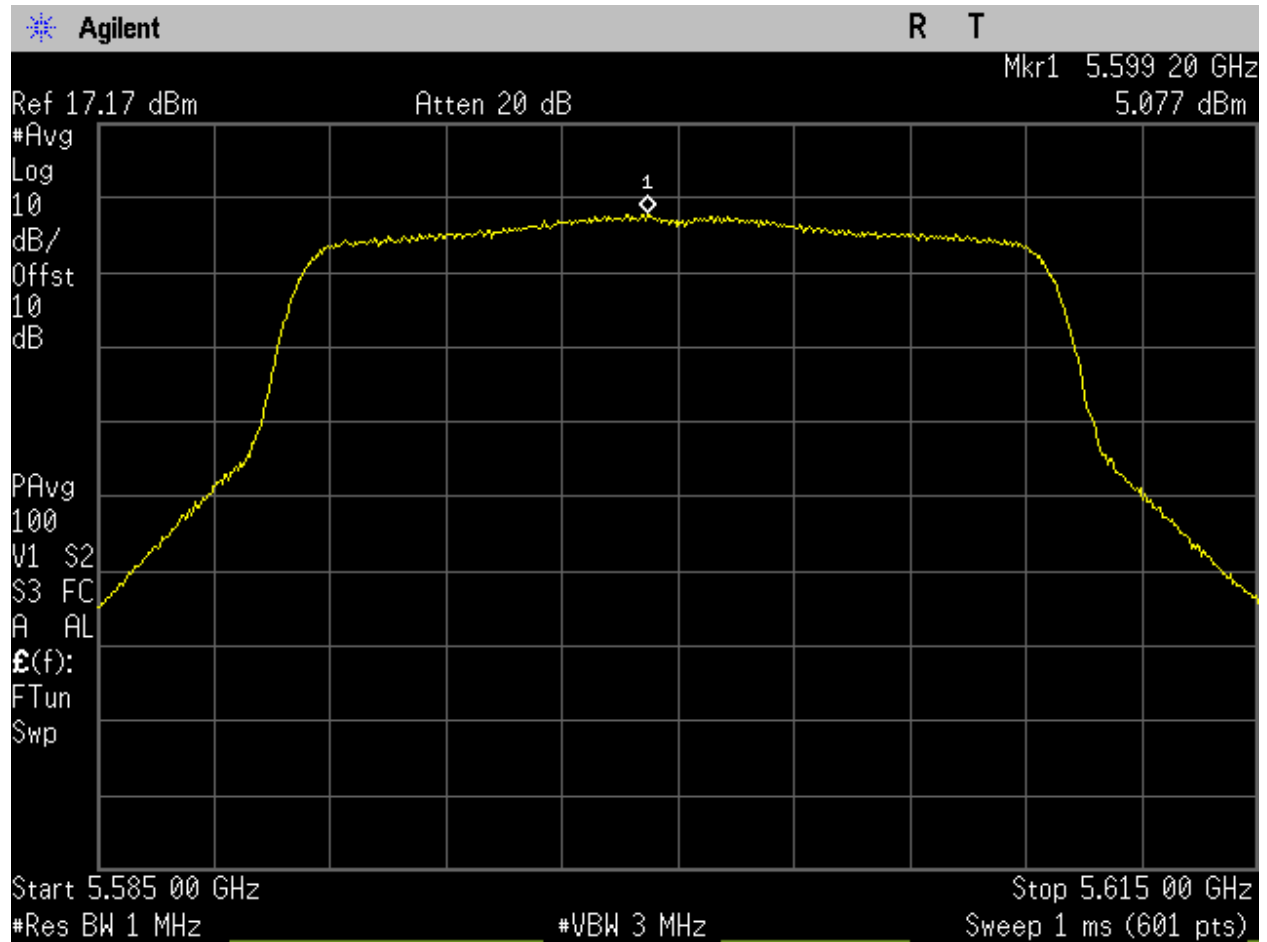


Figure 377: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ax-mode_Spectral Density_Port 1.

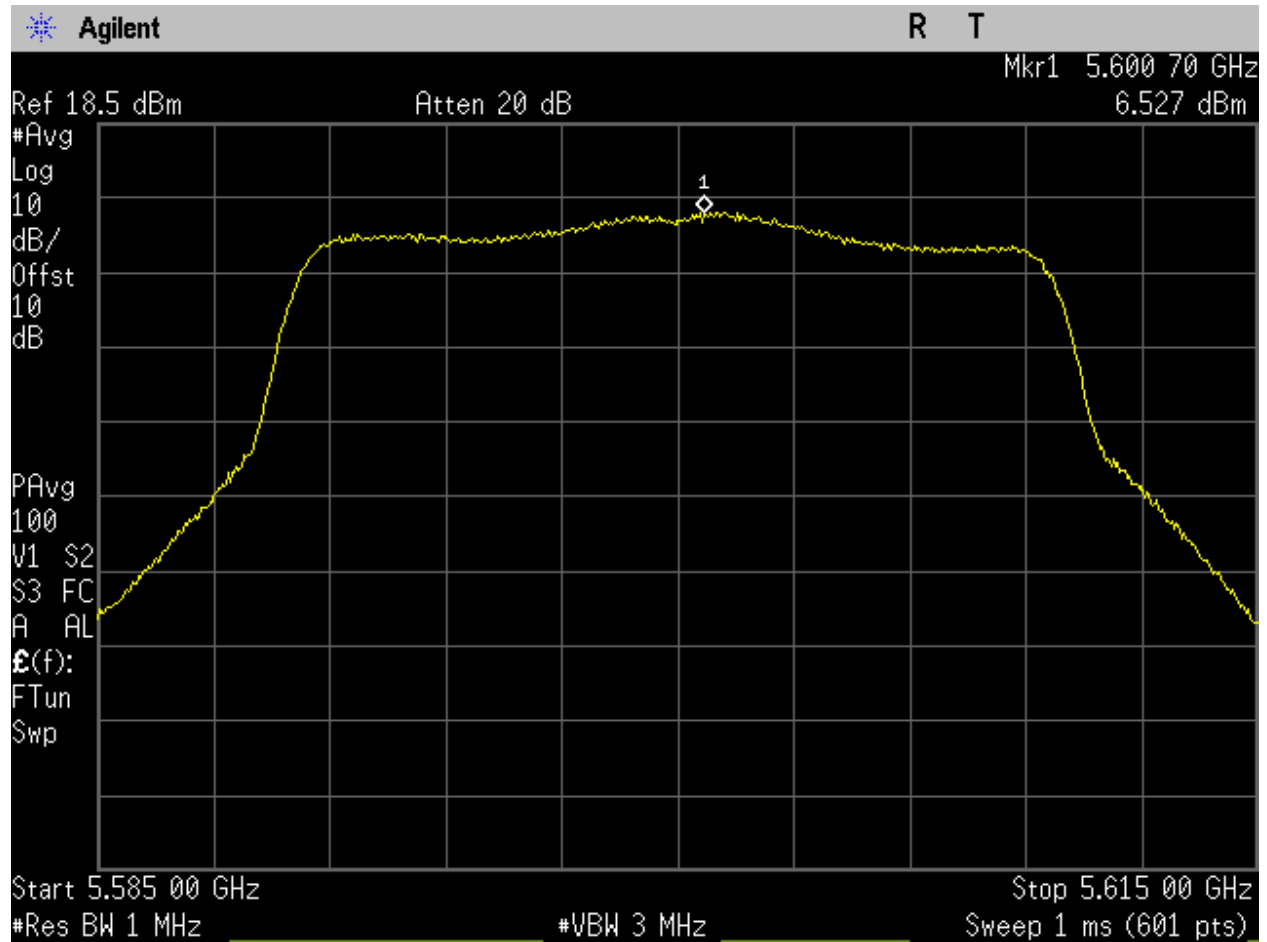


Figure 378: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ax-mode_Spectral Density_Port 2.

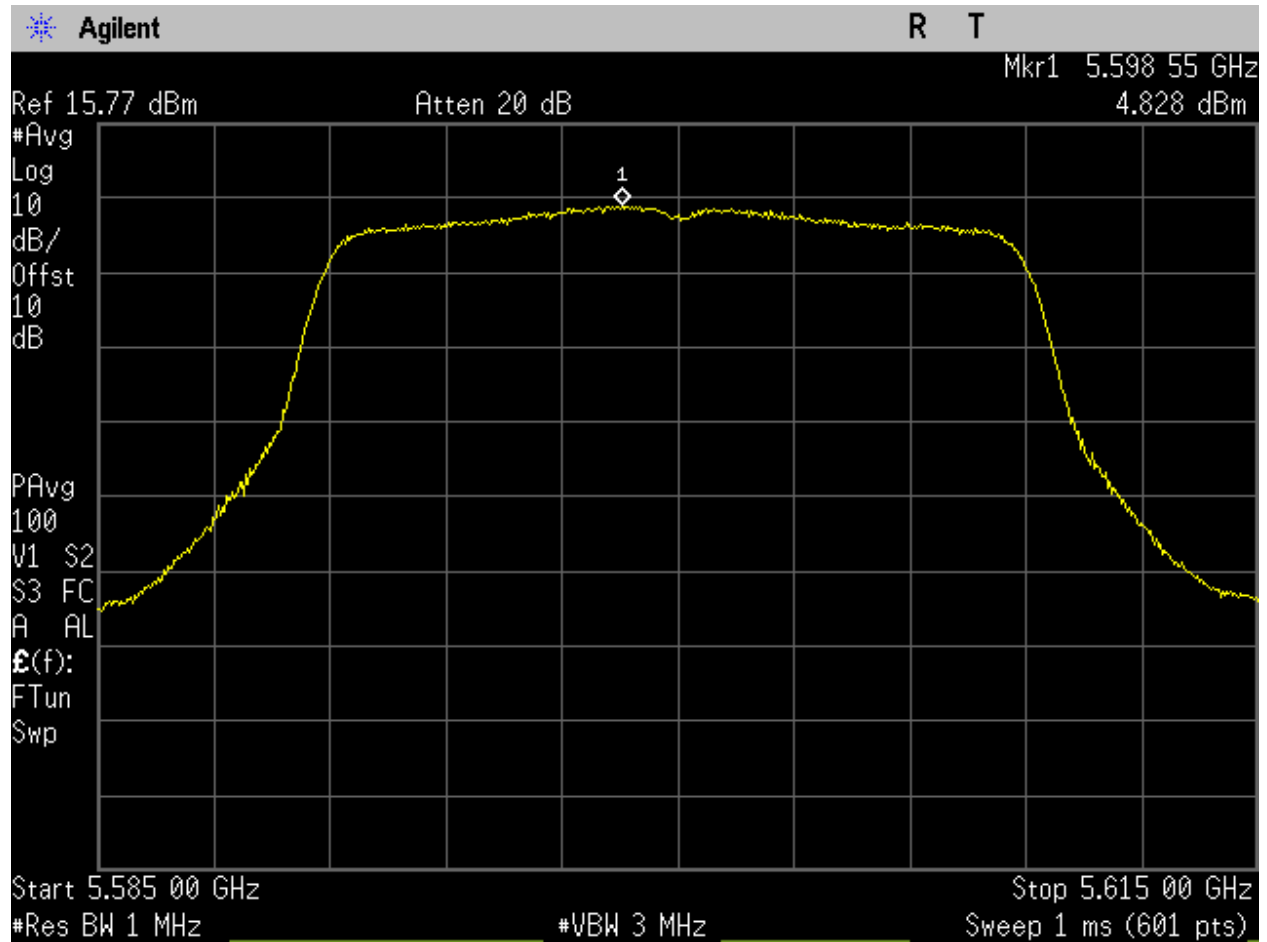


Figure 379: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_n-mode_Spectral Density_Port 1.

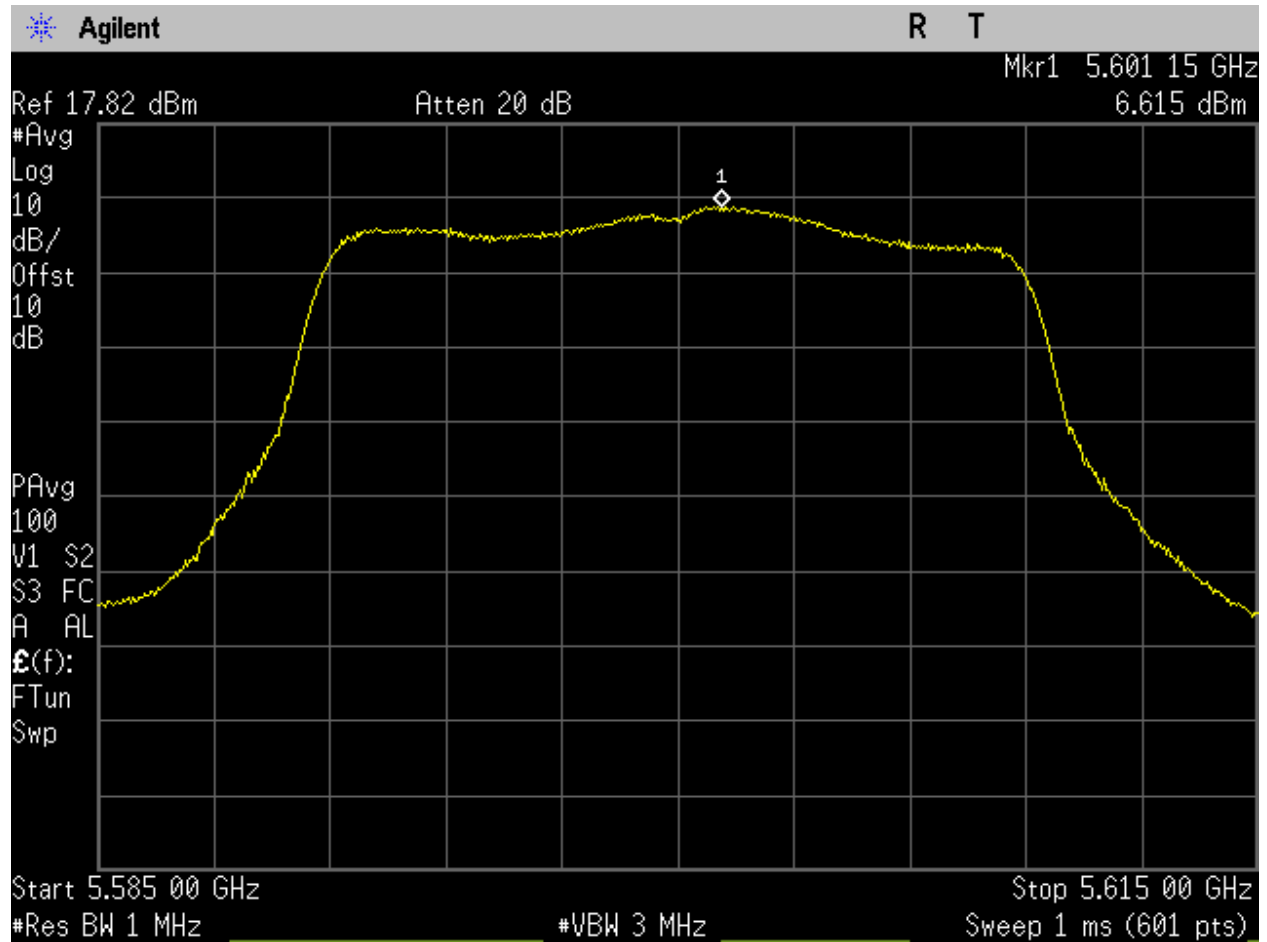


Figure 380: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_n-mode_Spectral Density_Port 2.

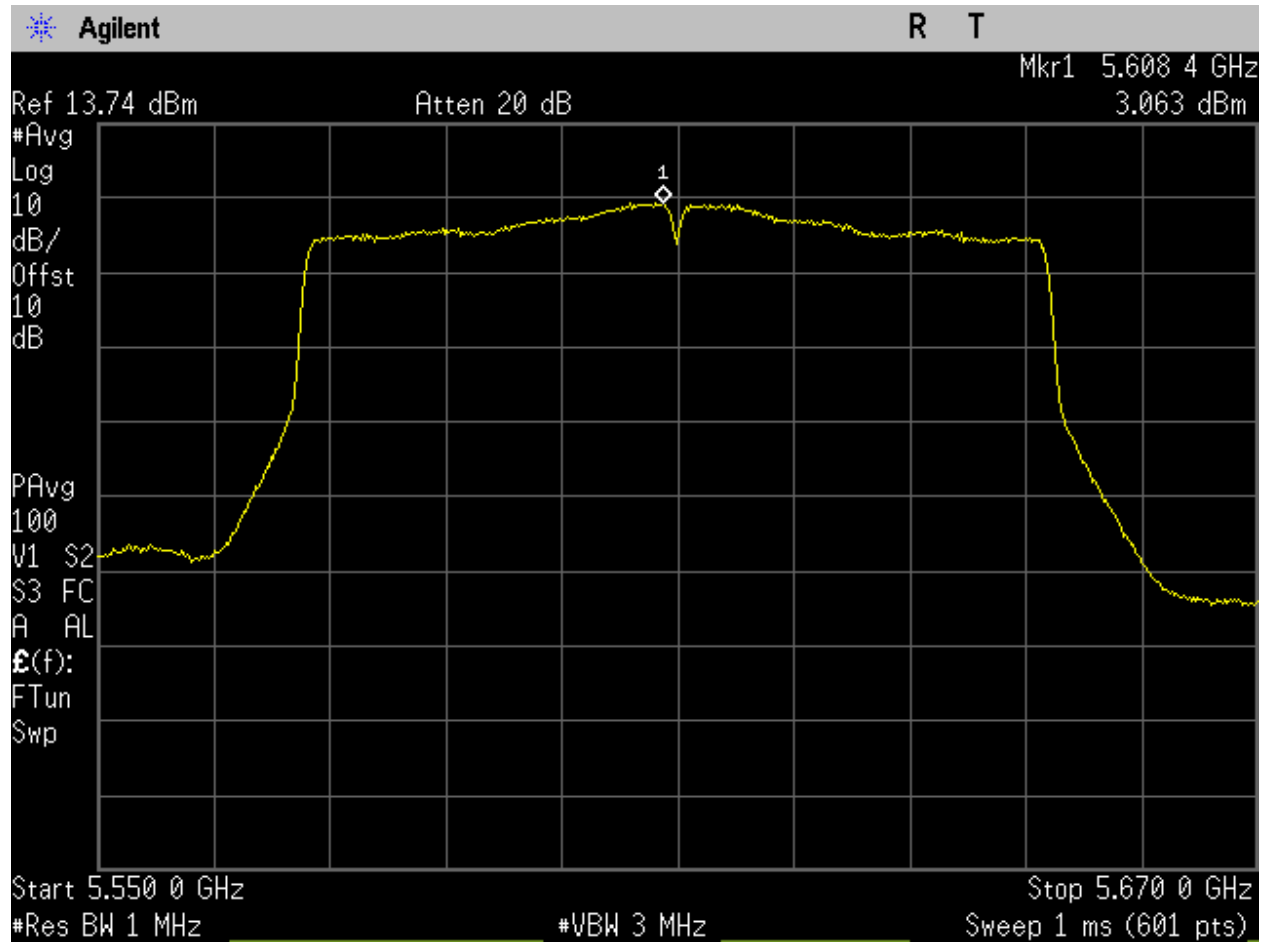


Figure 381: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Spectral Density_Port 1.

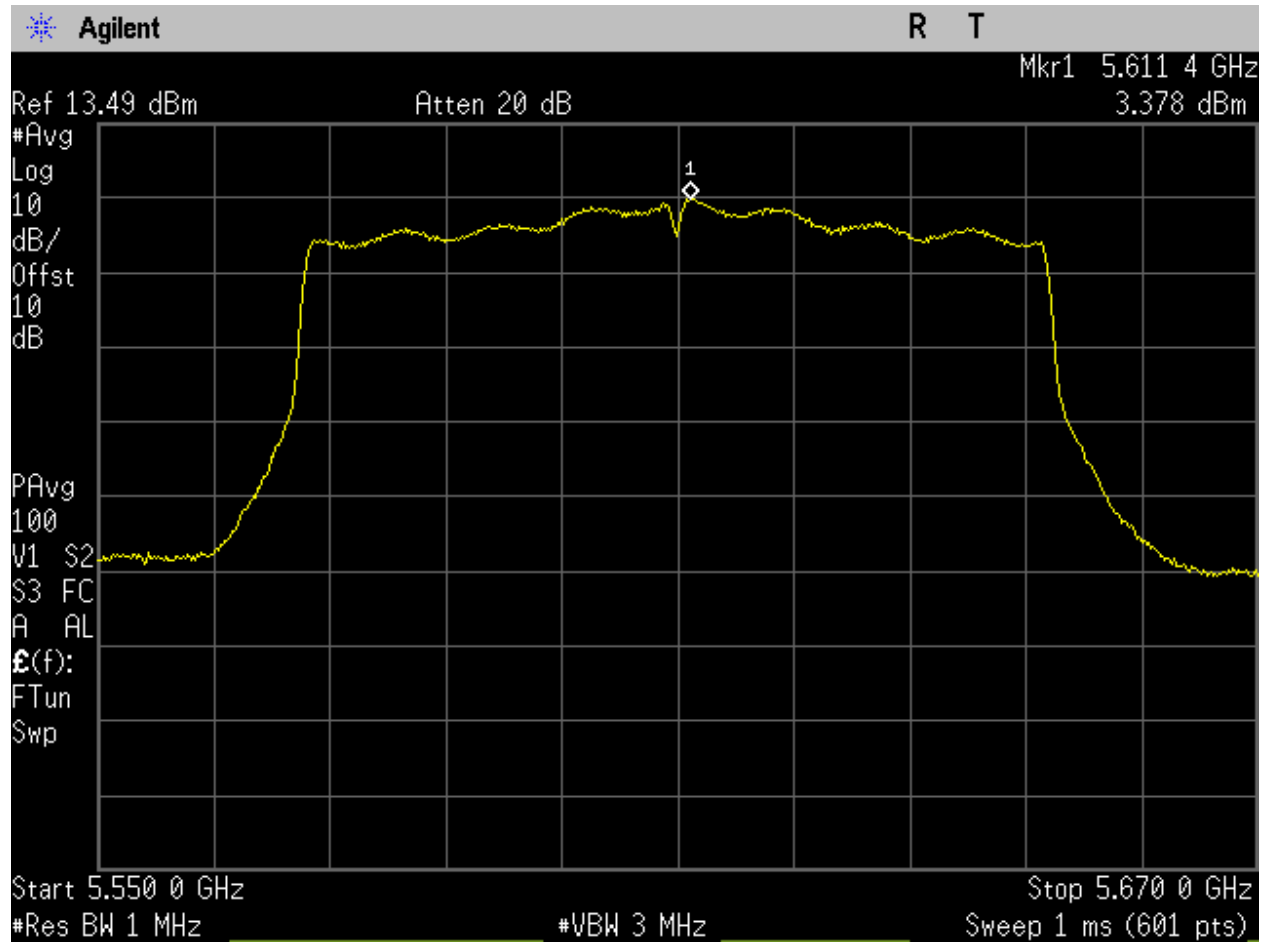


Figure 382: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Spectral Density_Port 2.

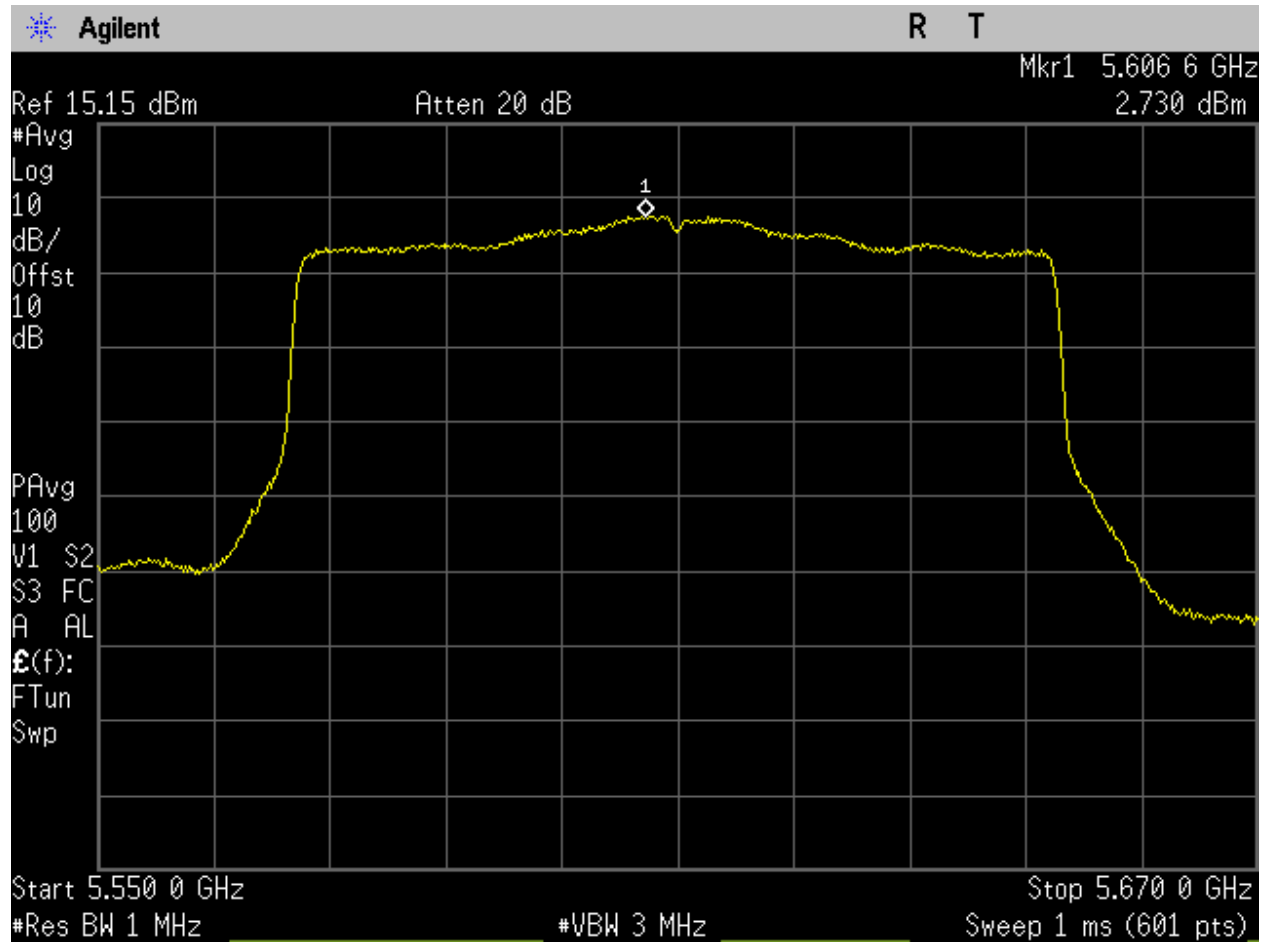


Figure 383: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Spectral Density_Port 1.

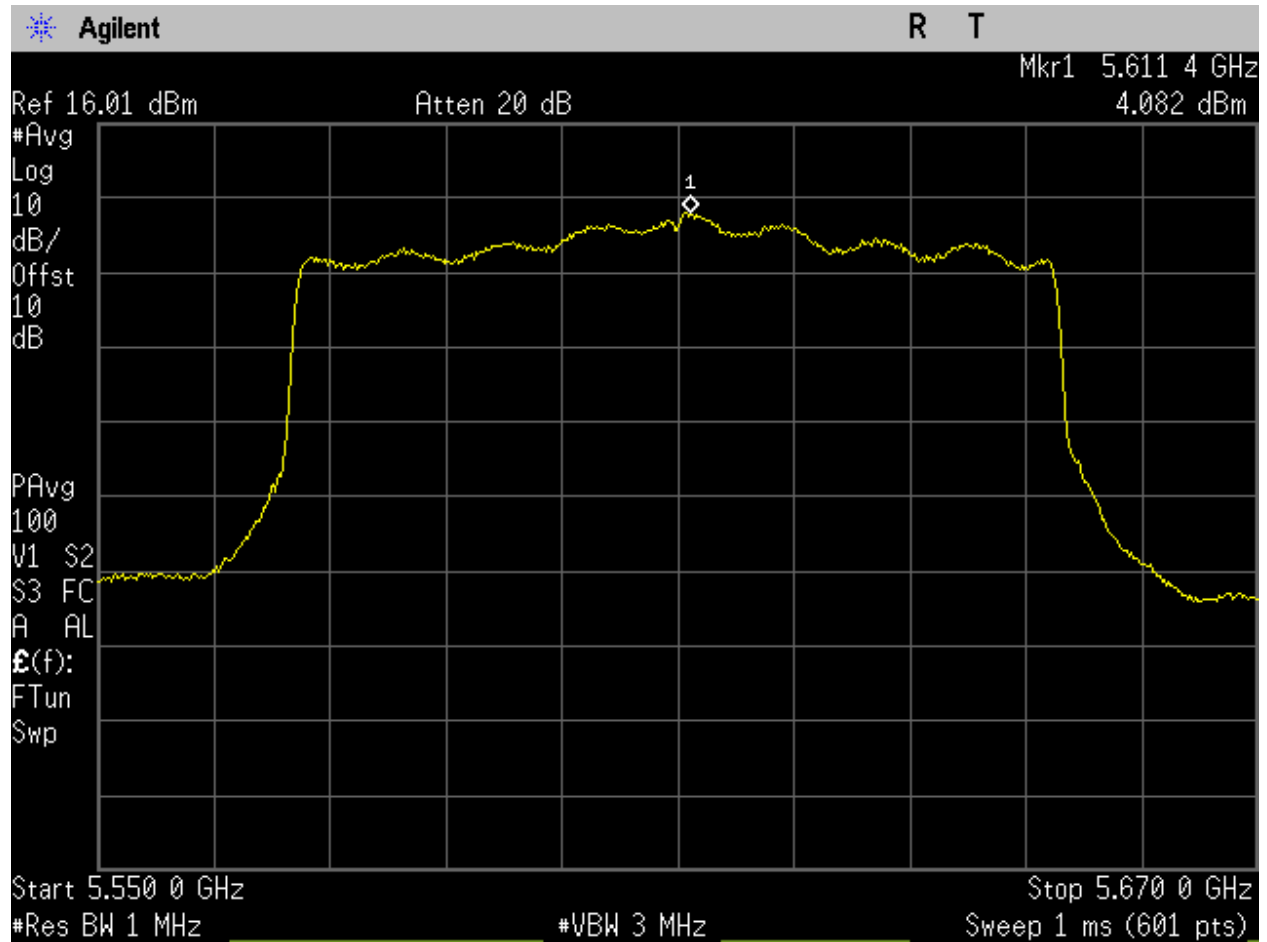


Figure 384: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Spectral Density_Port 2.

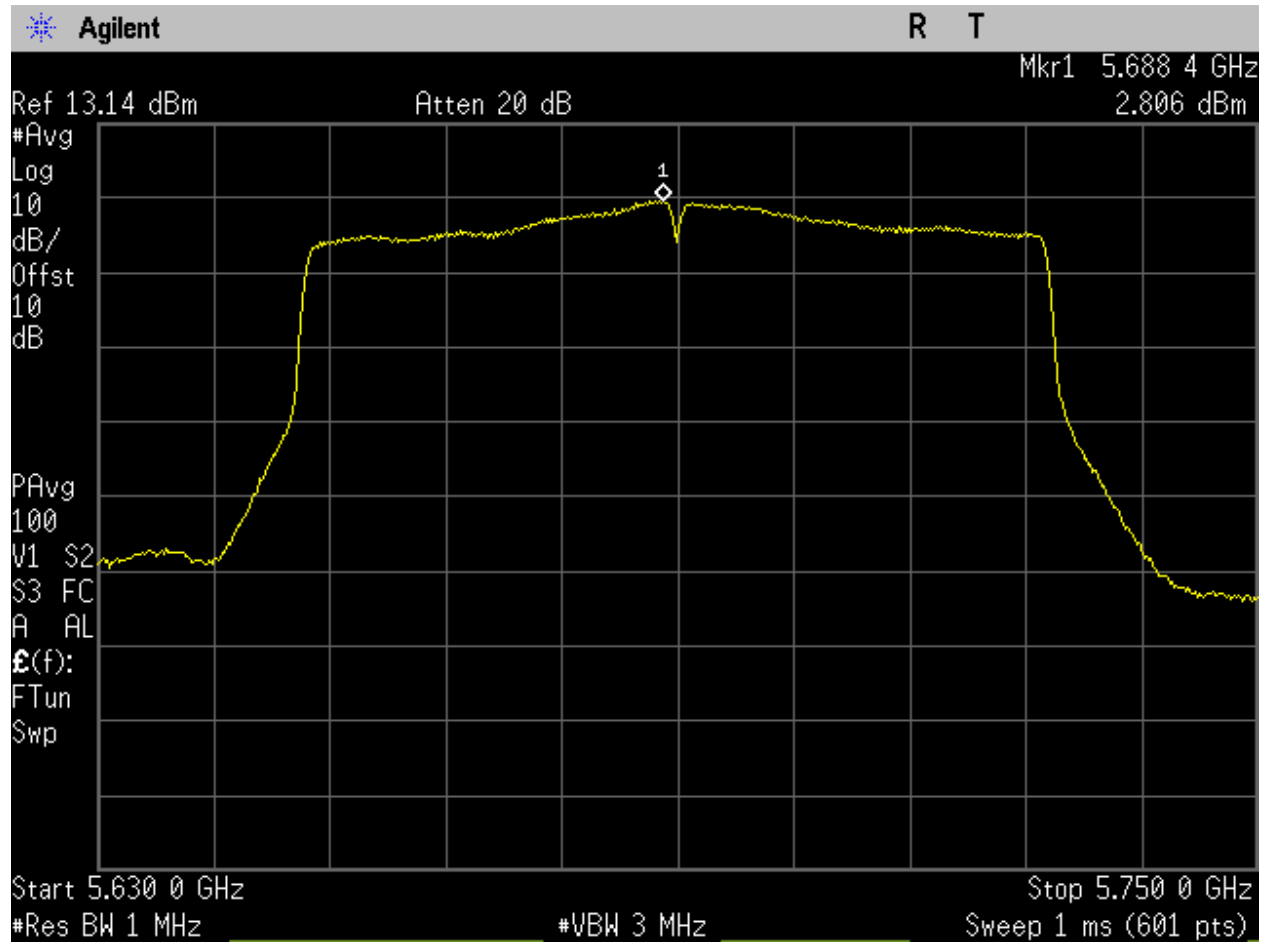


Figure 385: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Spectral Density_Port 1.

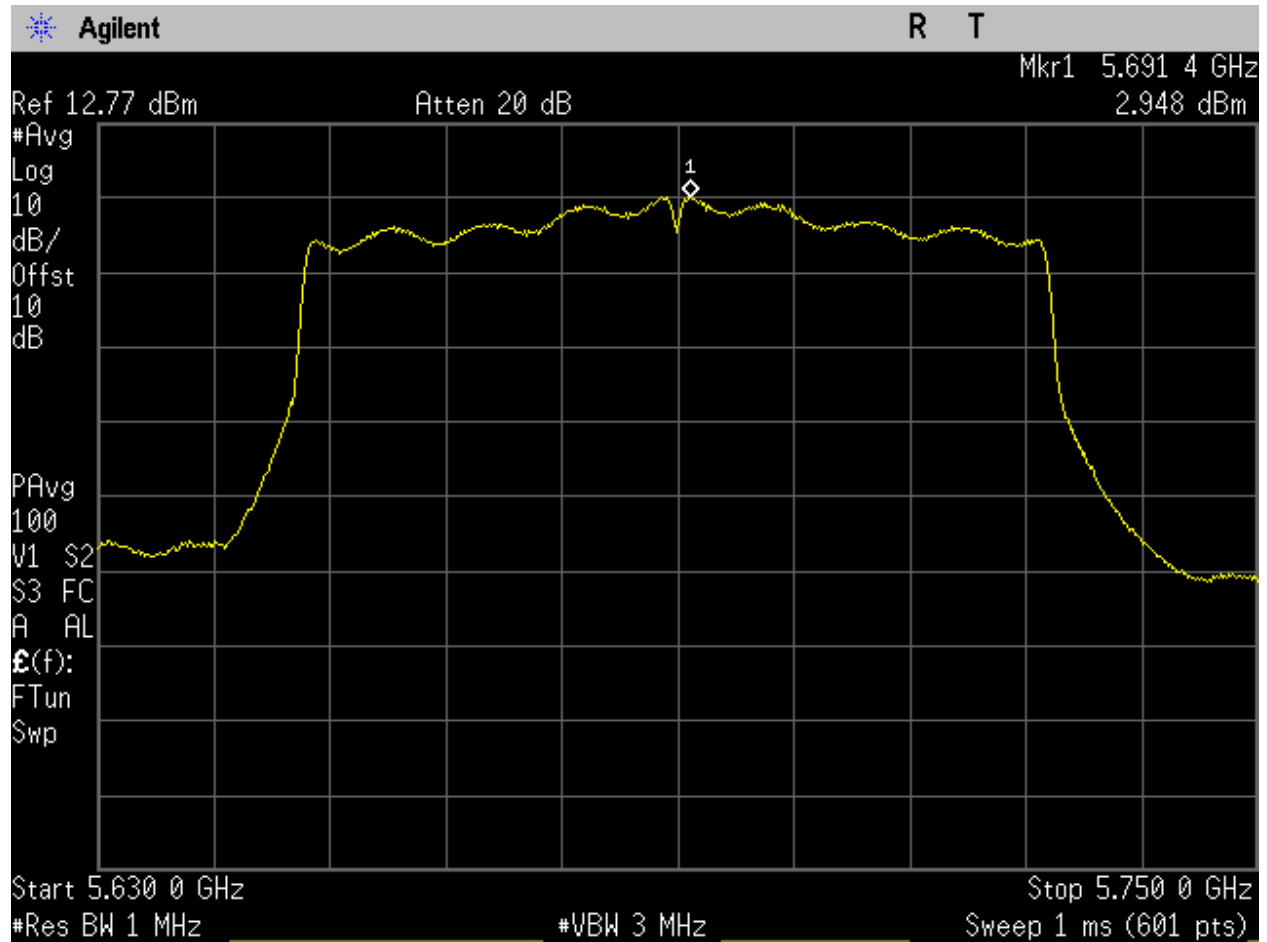


Figure 386: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Spectral Density_Port 2.

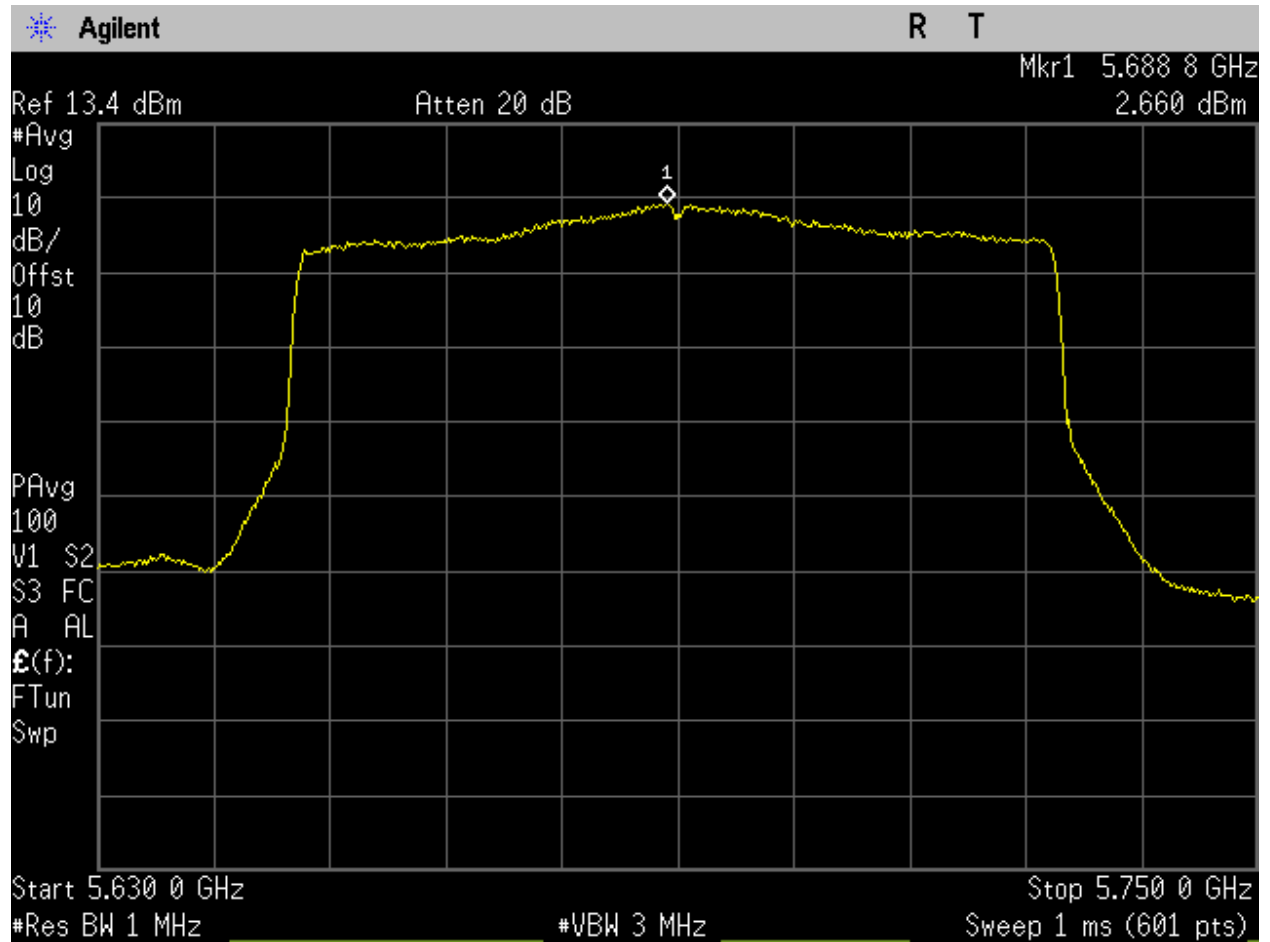


Figure 387: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ax-mode_Spectral Density_Port 1.

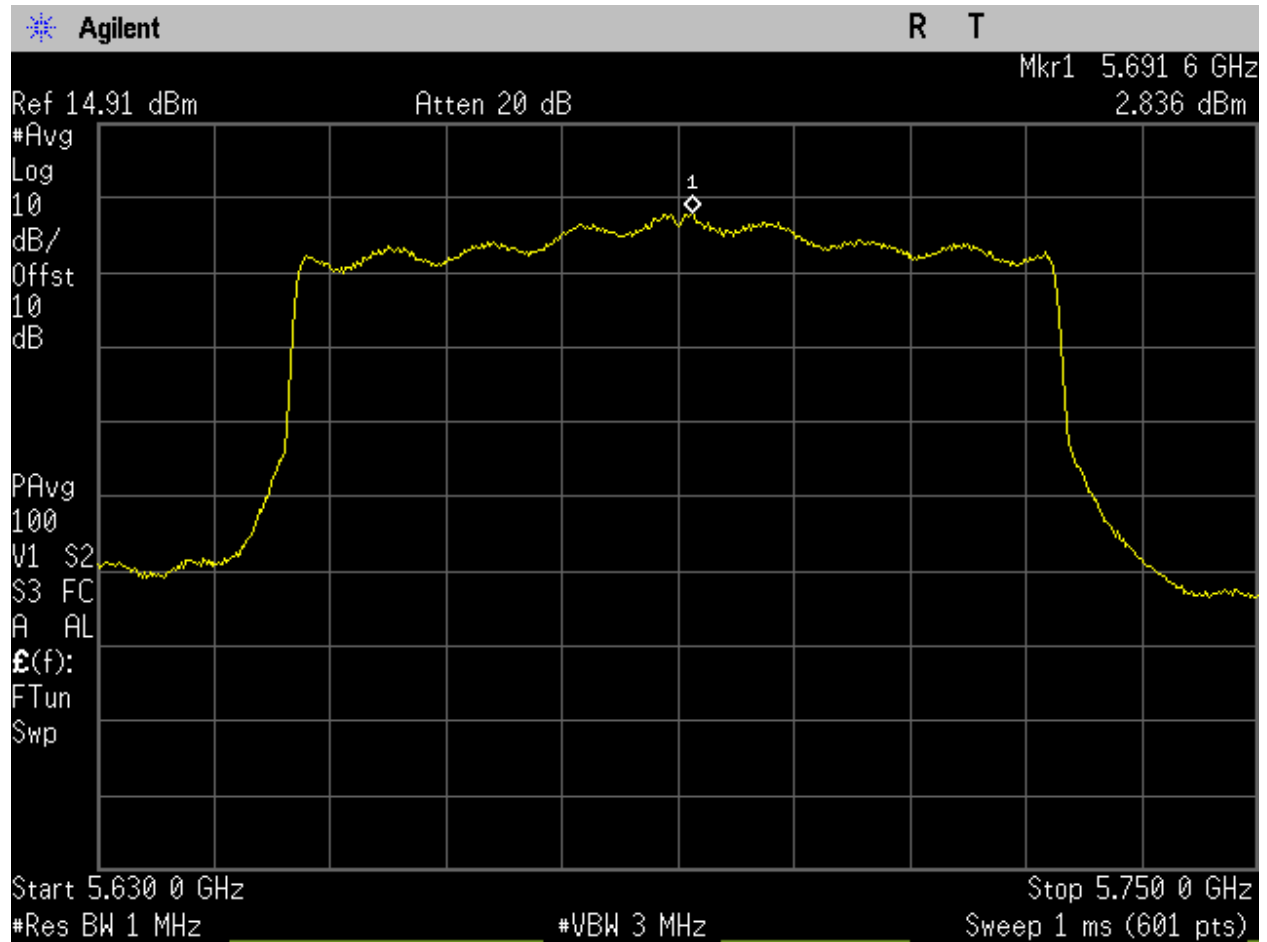


Figure 388: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ax-mode_Spectral Density_Port 2.

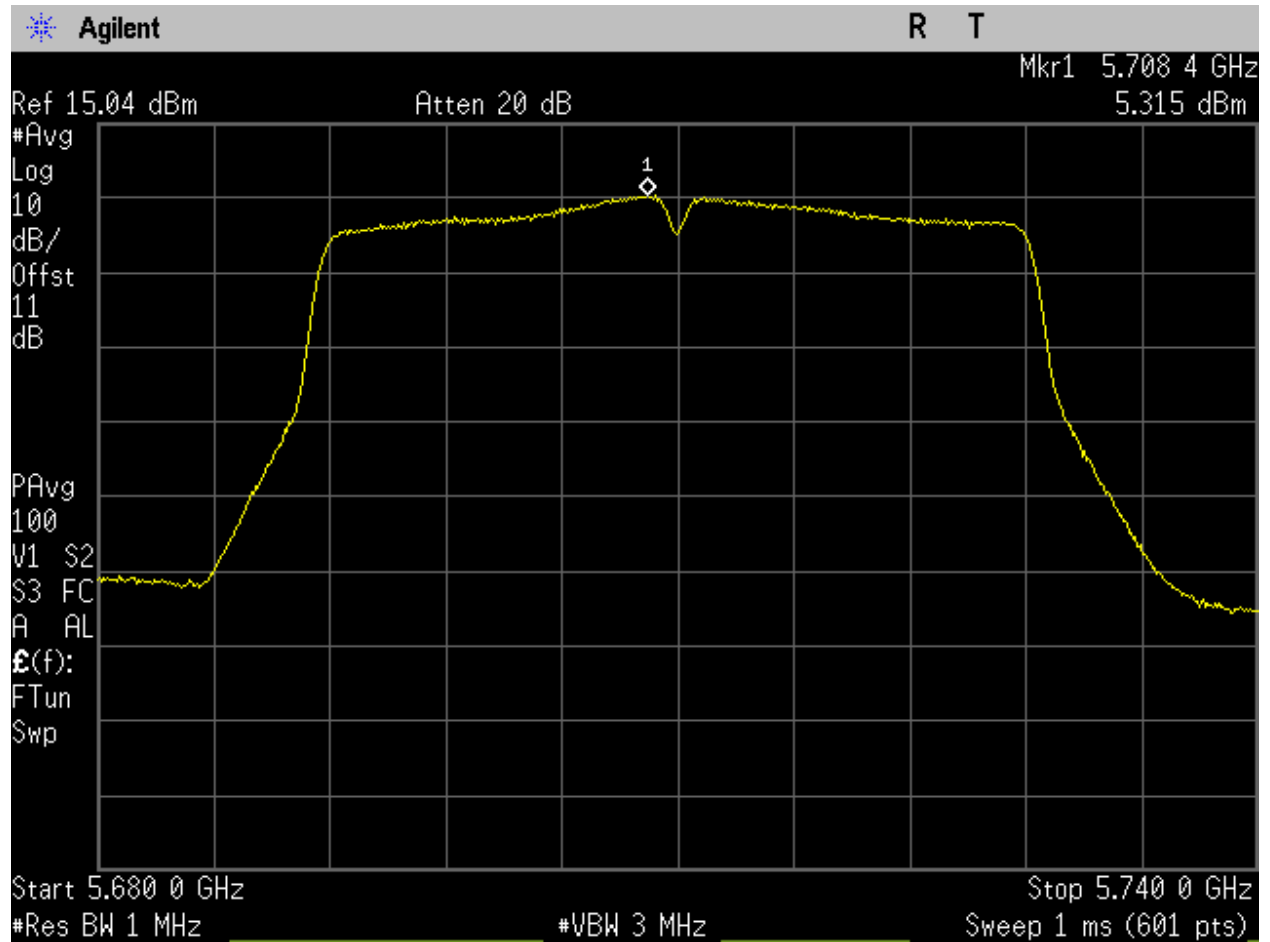


Figure 389: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Spectral Density_Port 1.

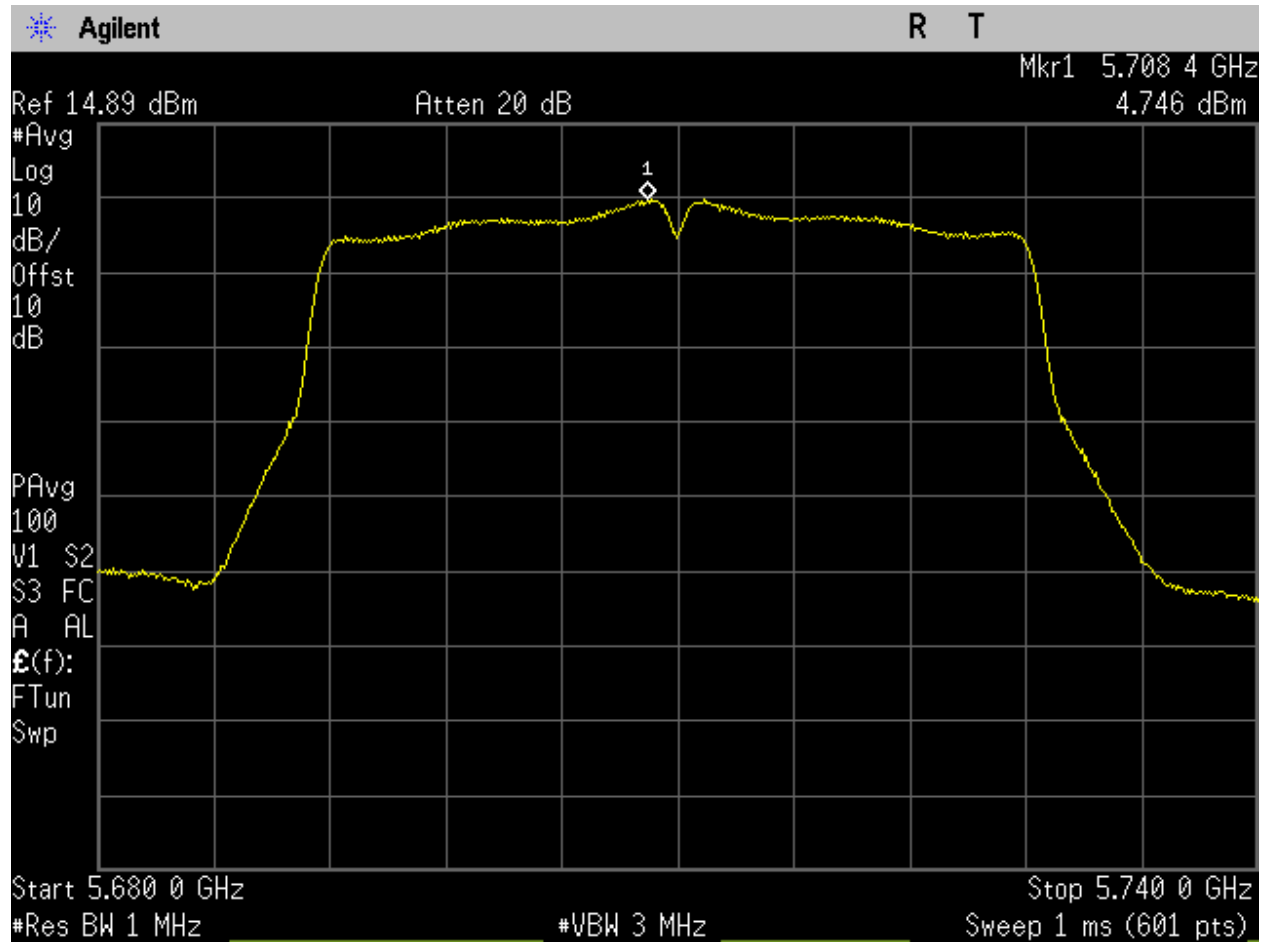


Figure 390: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Spectral Density_Port 2.

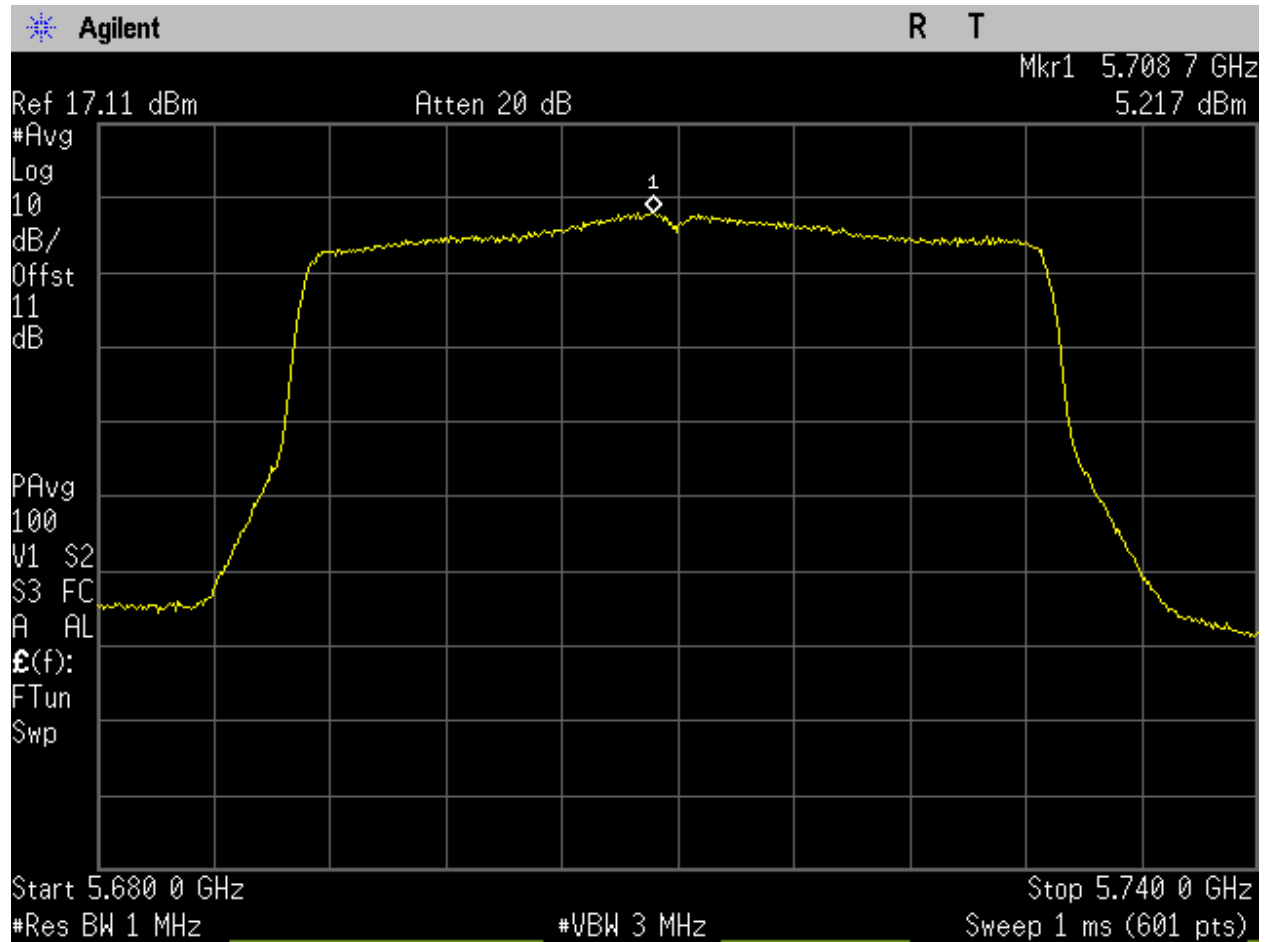


Figure 391: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ax-mode_Spectral Density_Port 1.

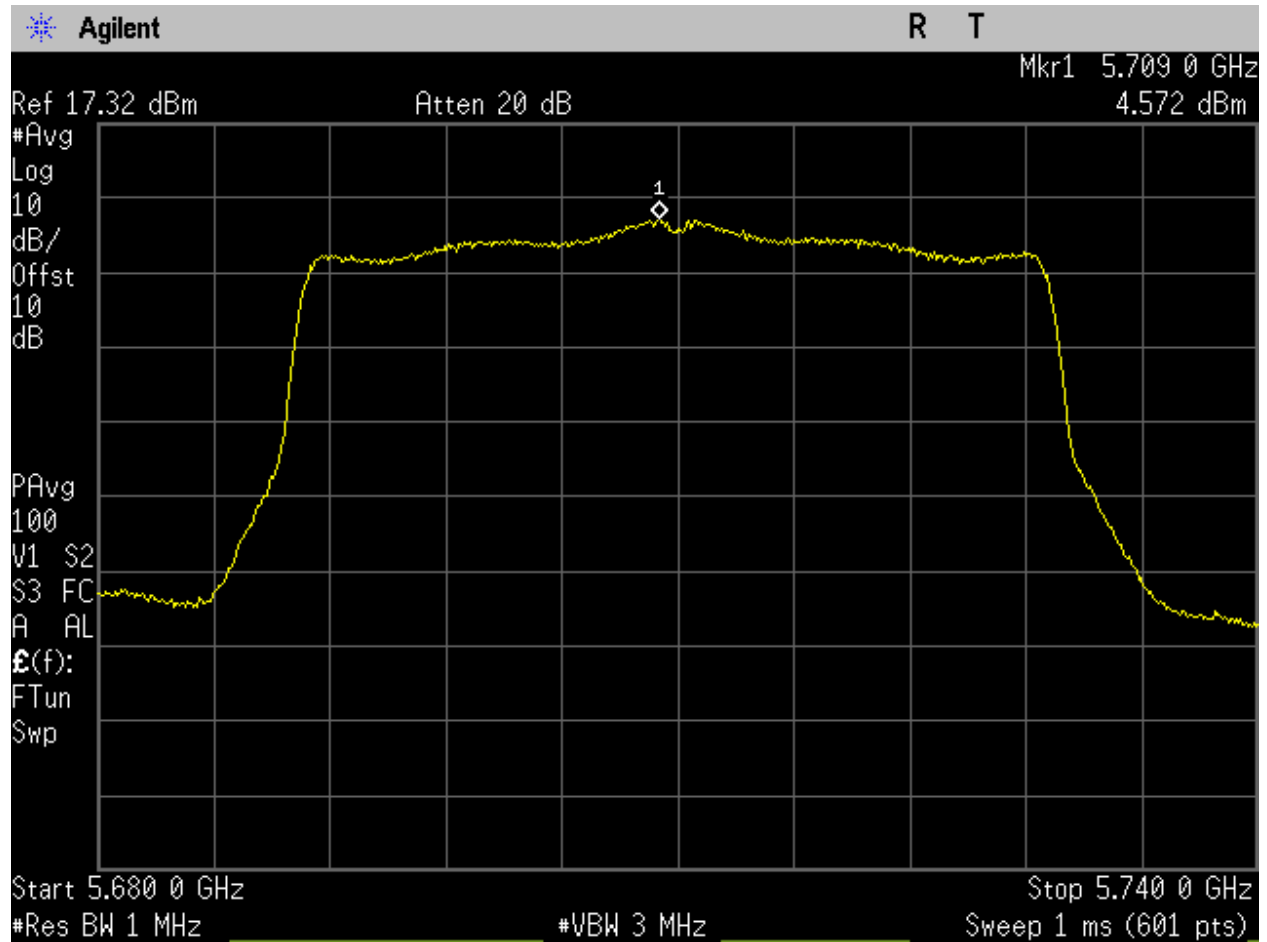


Figure 392: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ax-mode_Spectral Density_Port 2.

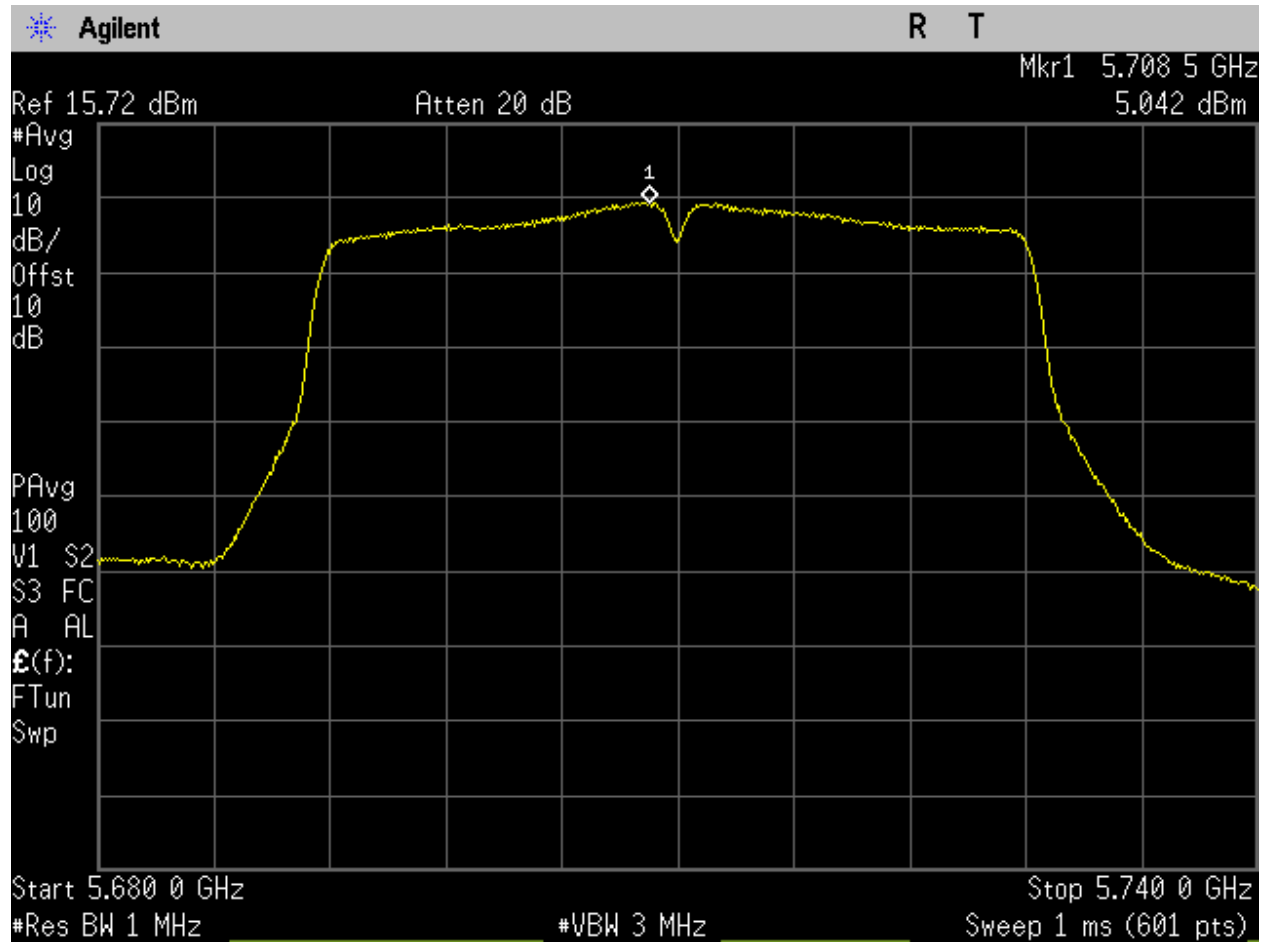


Figure 393: U-NII-2C_5710MHz_High Ch_142_40MHz BW_n-mode_Spectral Density_Port 1.

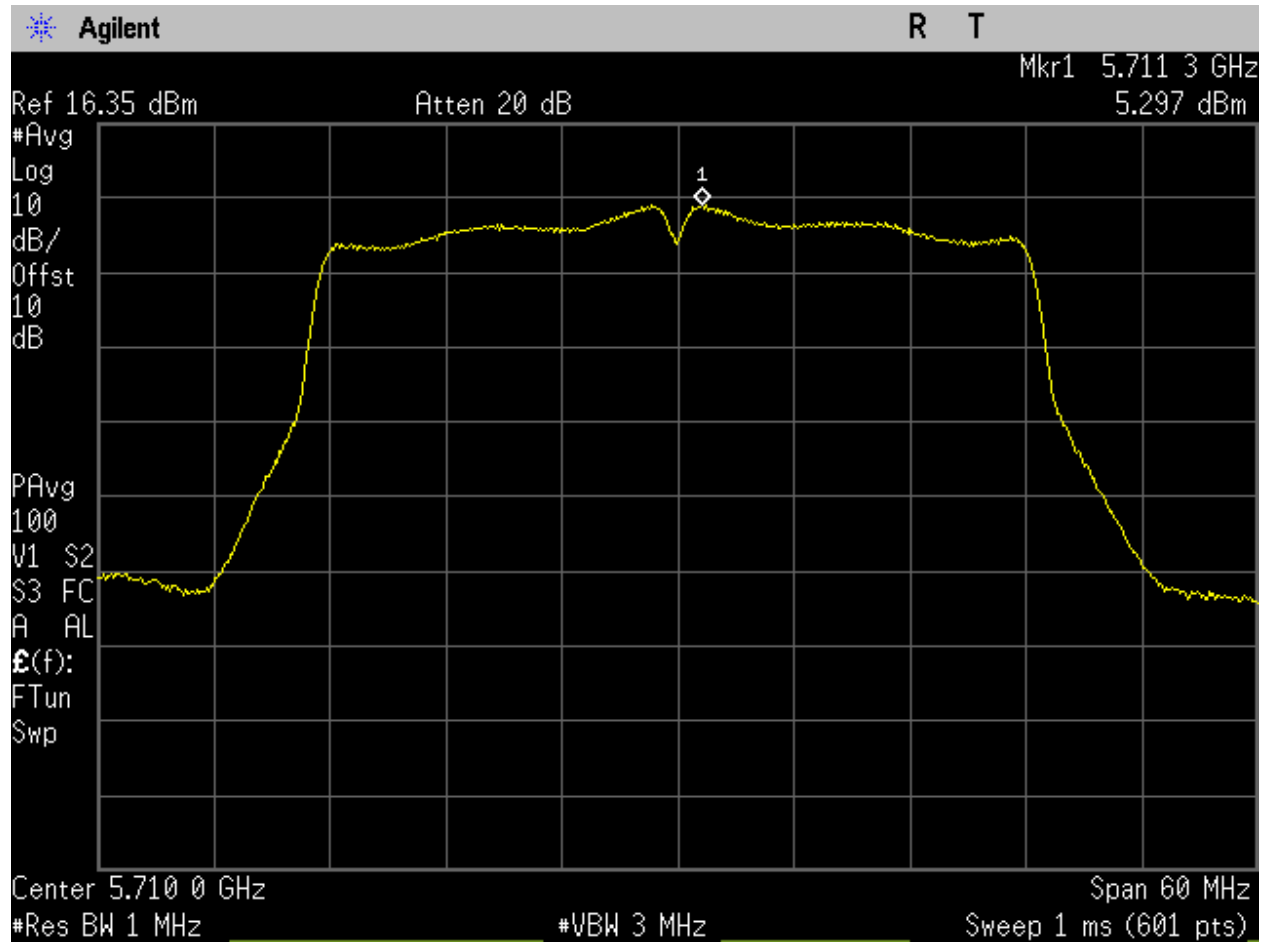


Figure 394: U-NII-2C_5710MHz_High Ch_142_40MHz BW_n-mode_Spectral Density_Port 2.

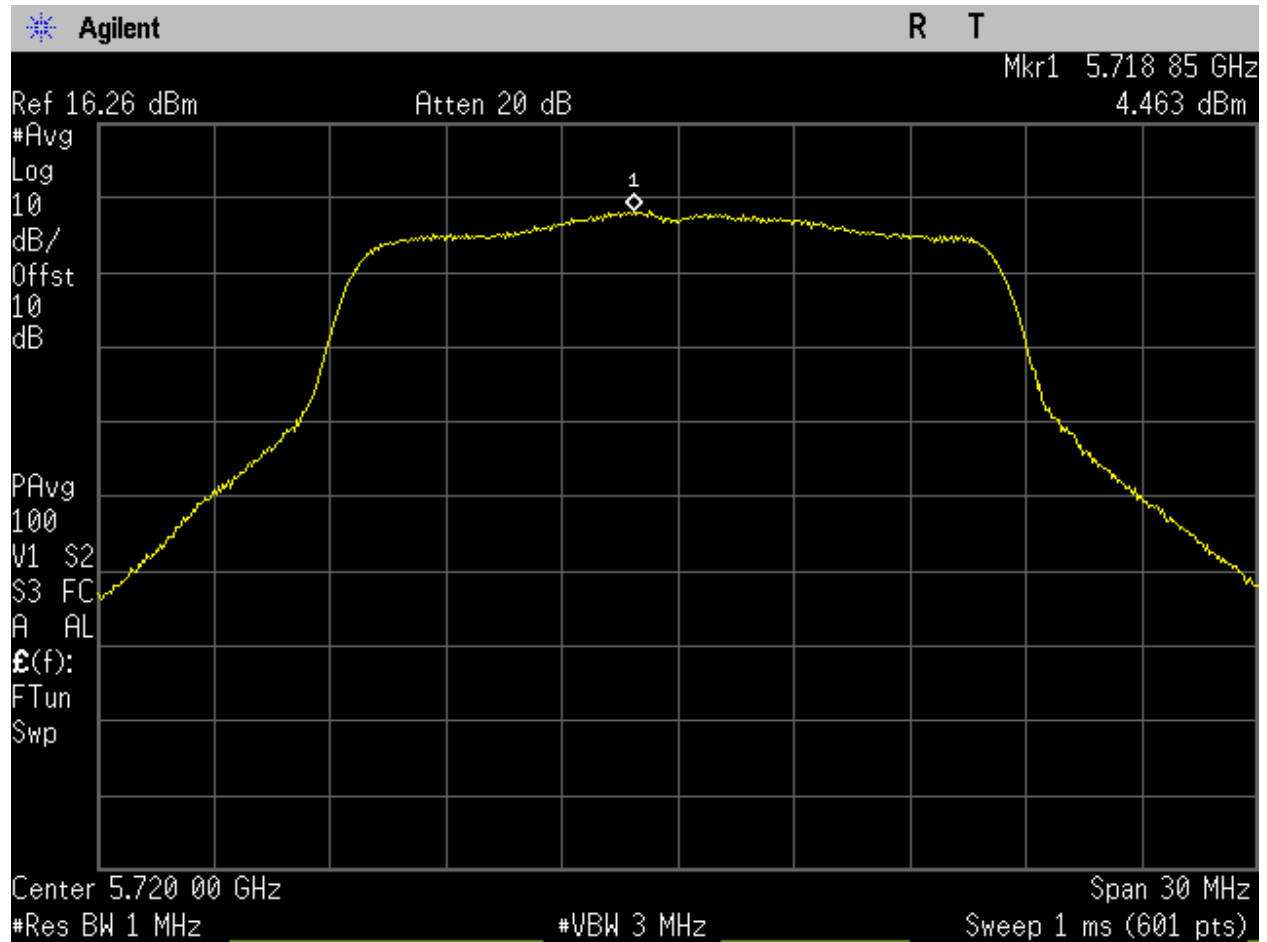


Figure 395: U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Spectral Density_Port 1.

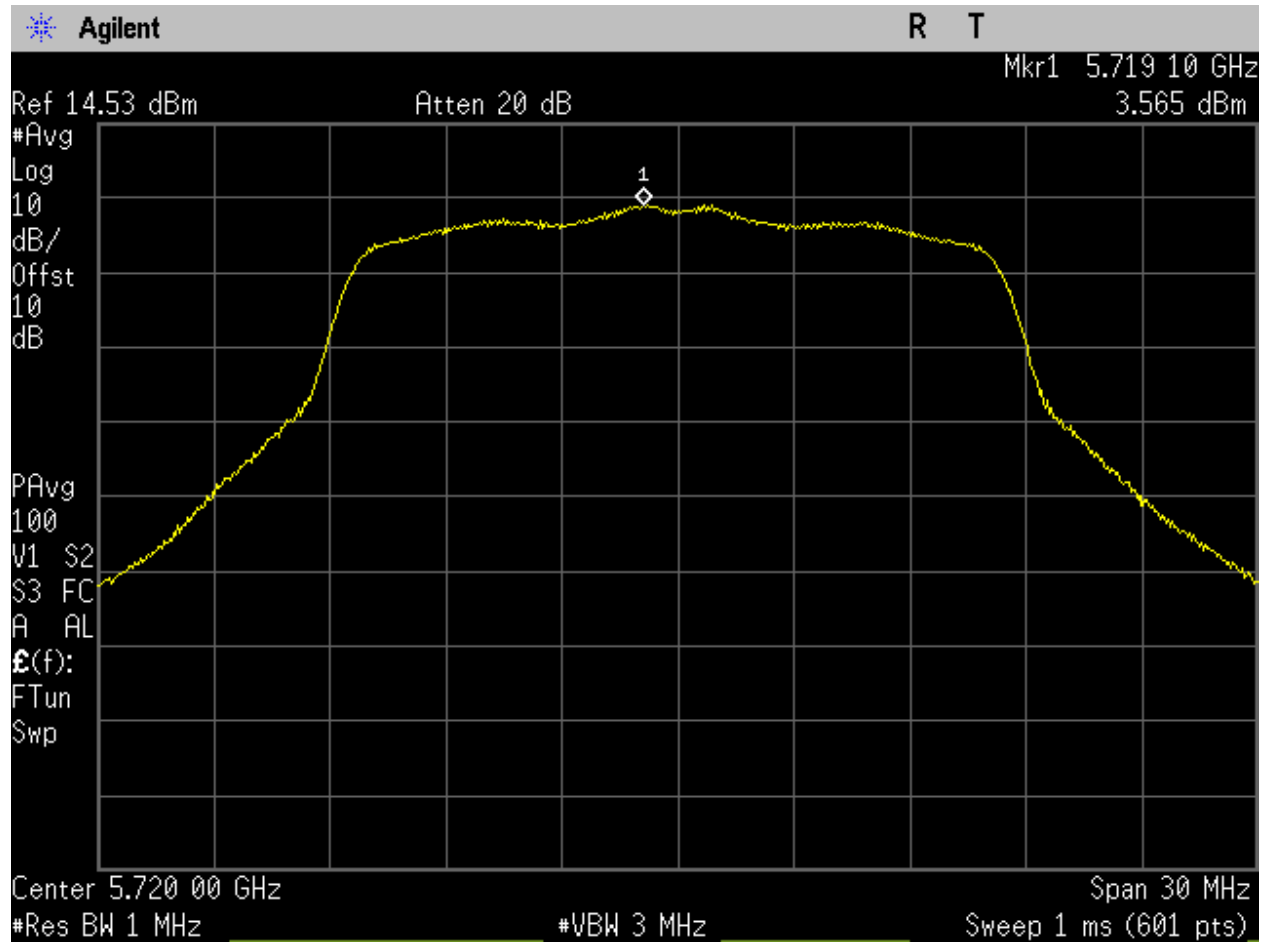


Figure 396: U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Spectral Density_Port 2.

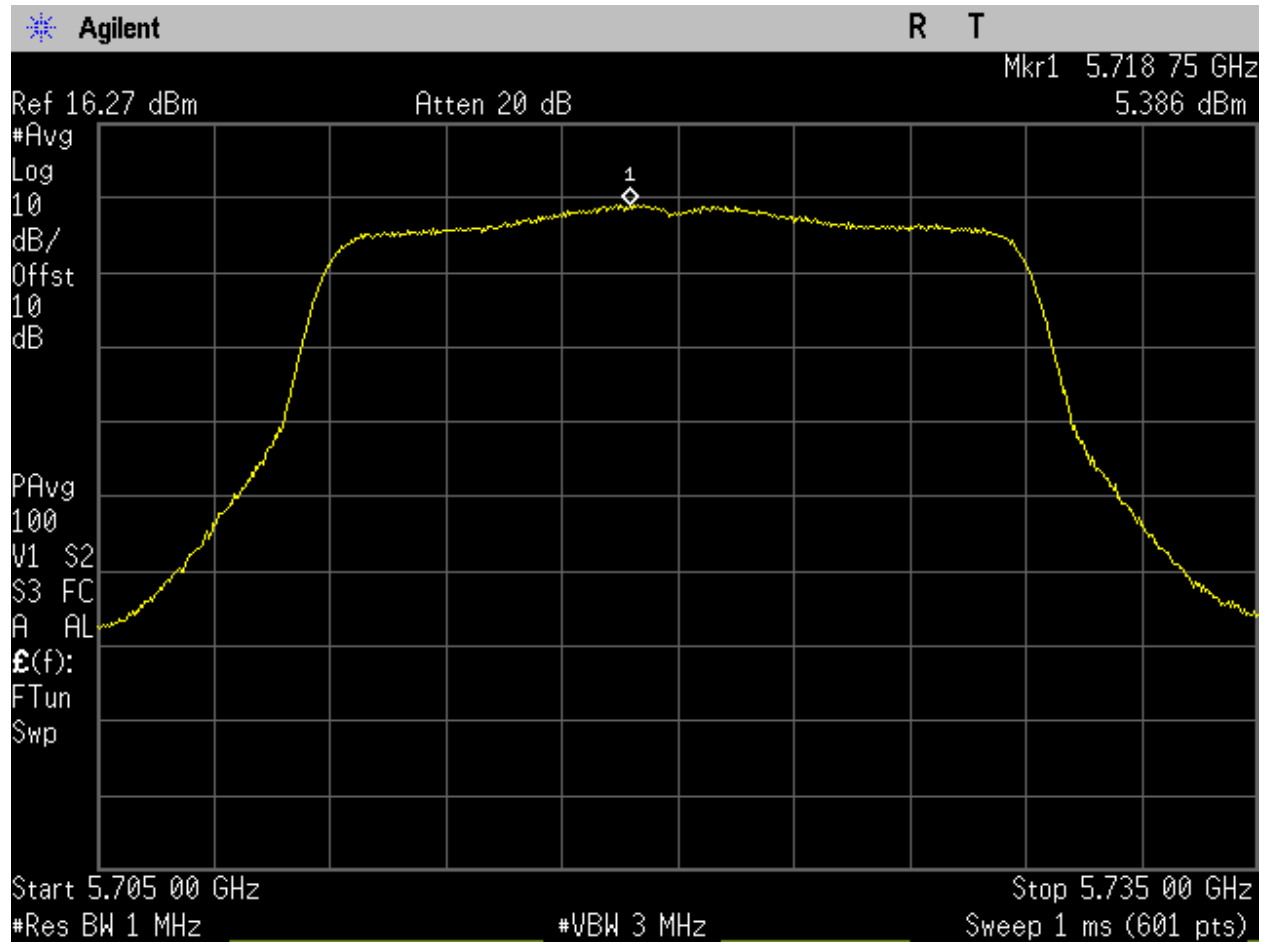


Figure 397: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Spectral Density_Port 1.

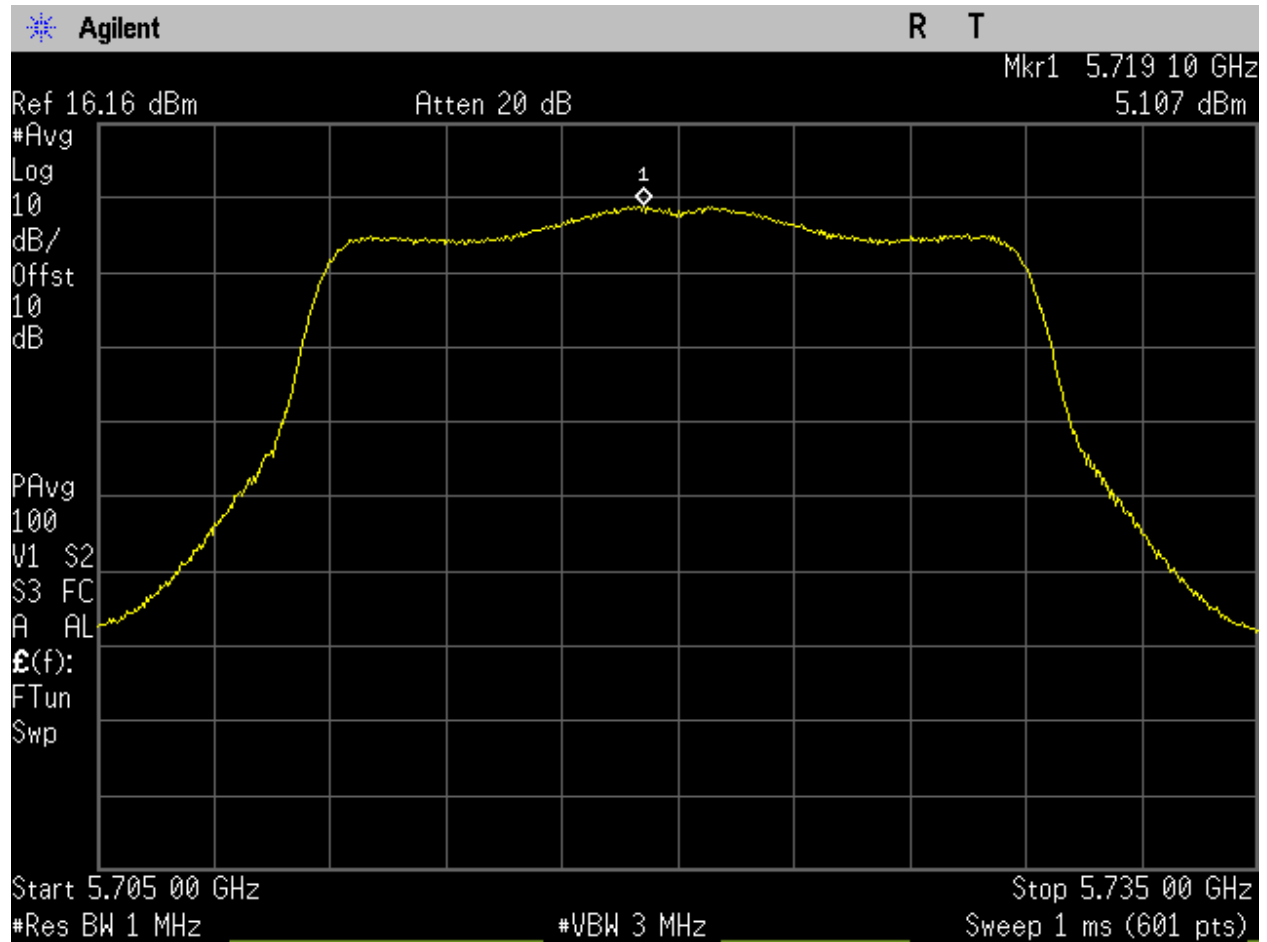


Figure 398: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Spectral Density_Port 2.

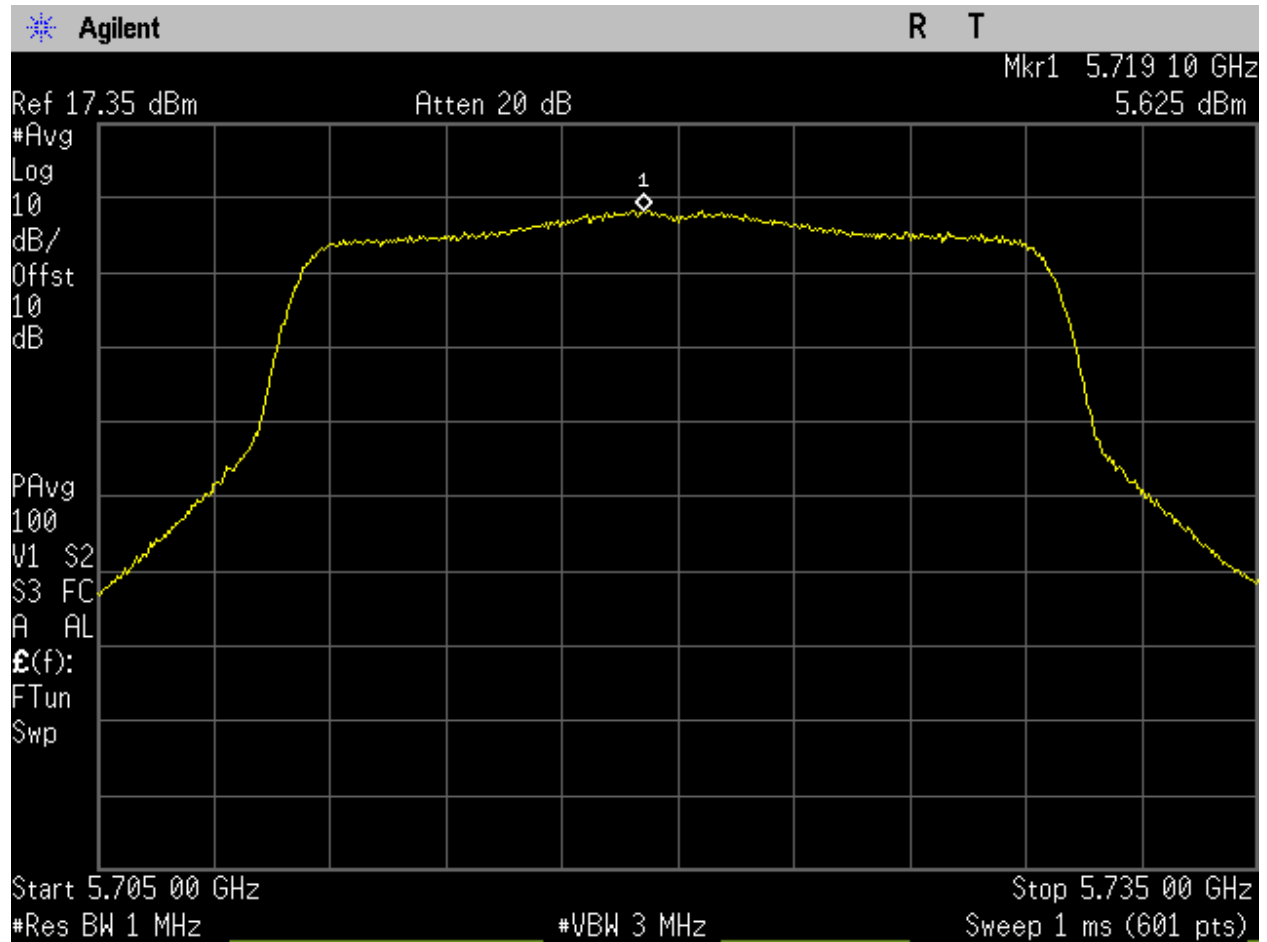


Figure 399: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ax-mode_Spectral Density_Port 1.

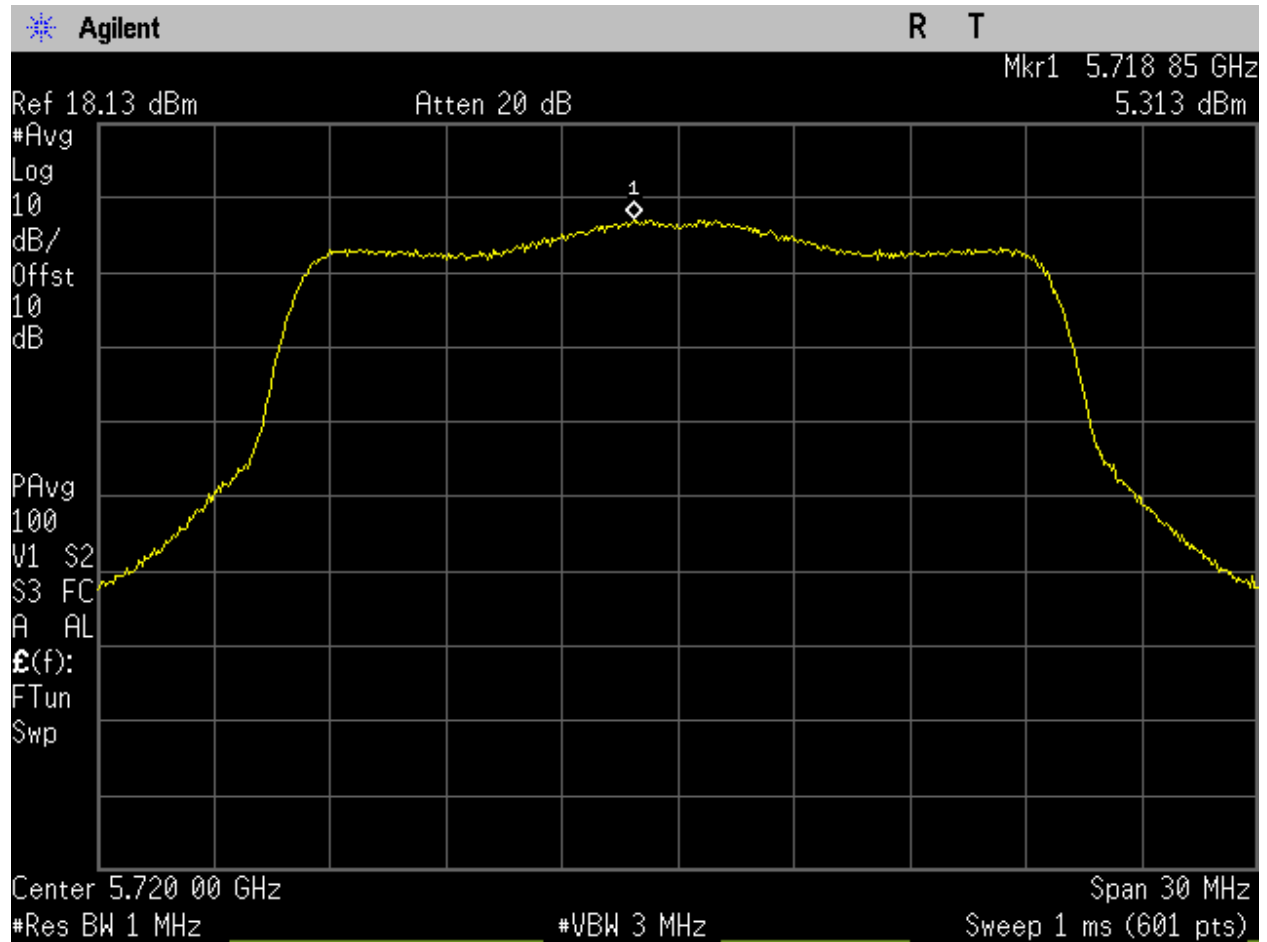


Figure 400: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ax-mode_Spectral Density_Port 2.

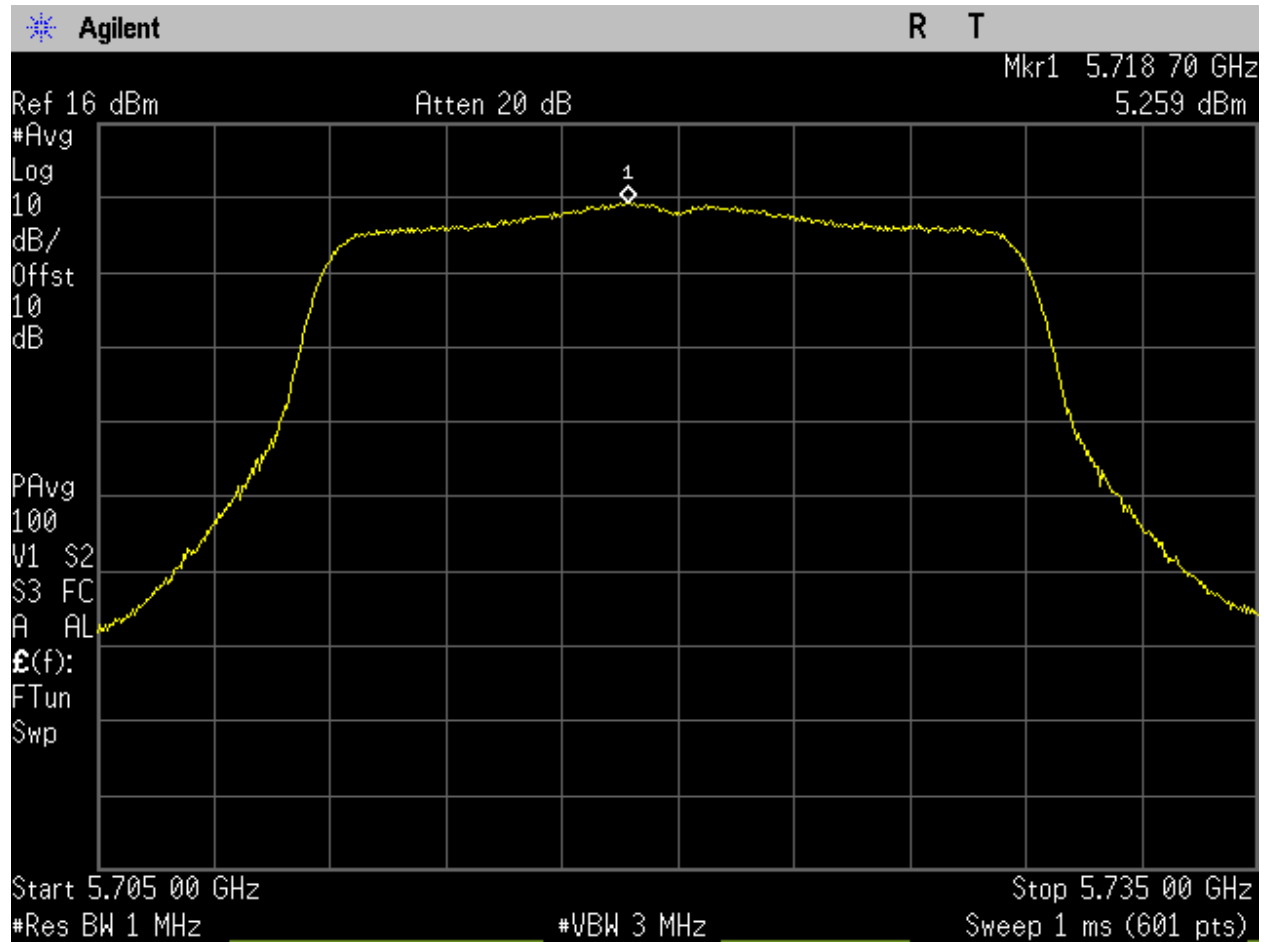


Figure 401: U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Spectral Density_Port 1.

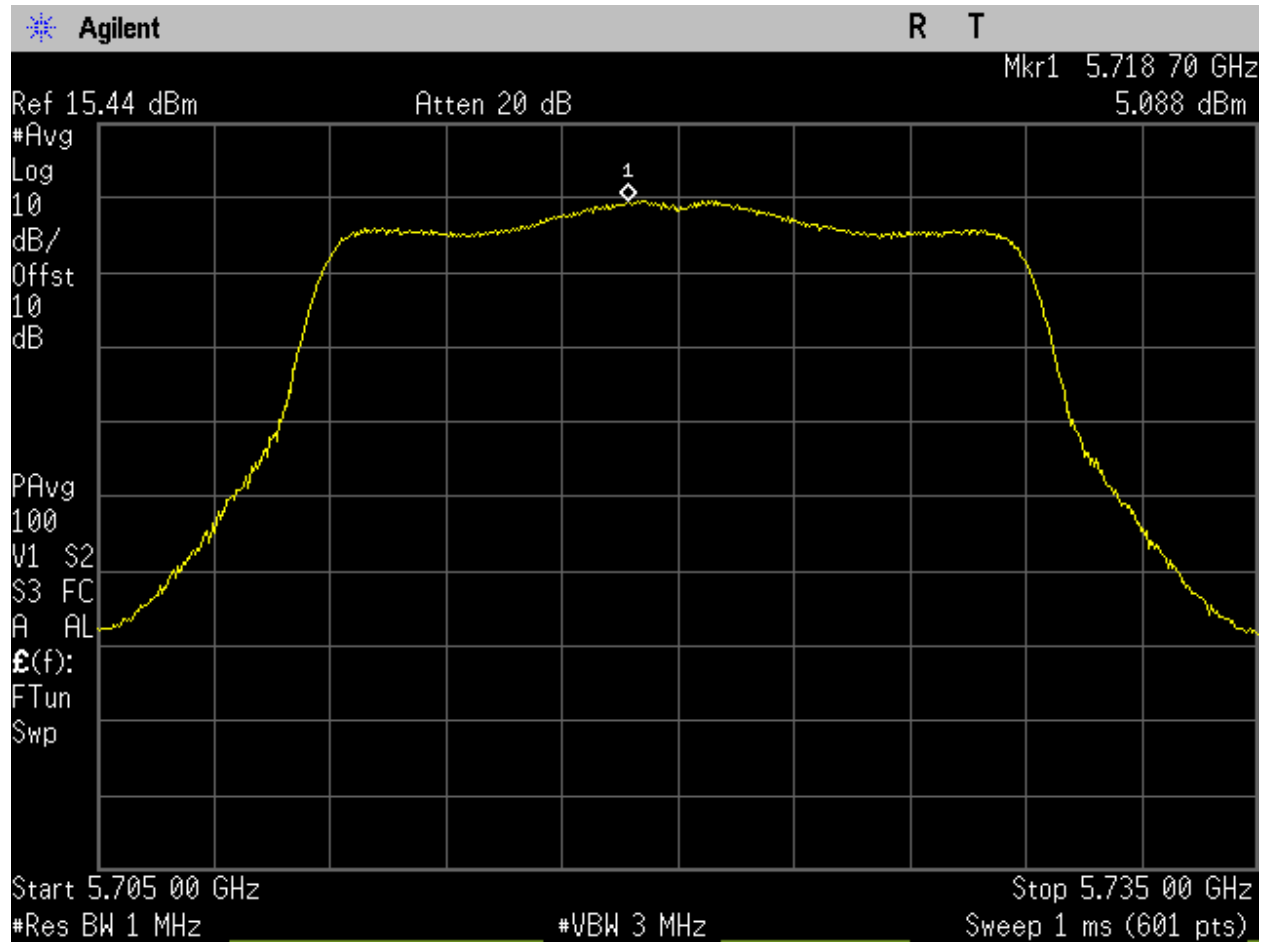


Figure 402: U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Spectral Density_Port 2.

§15.407(b)(2 – 3) & (9 – 10) Undesirable Emissions

FCC 15.209 Conducted

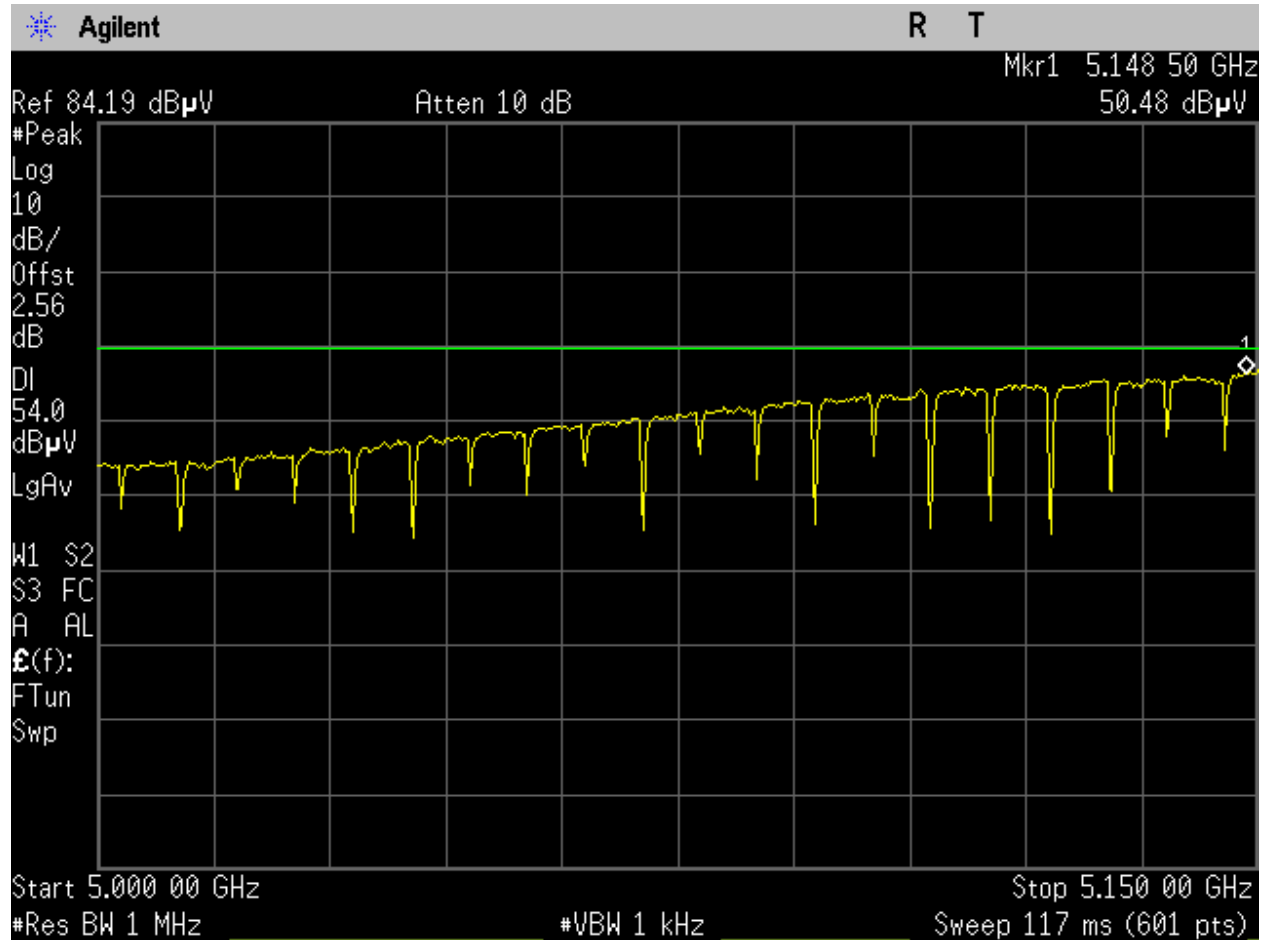


Figure 403: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 1.

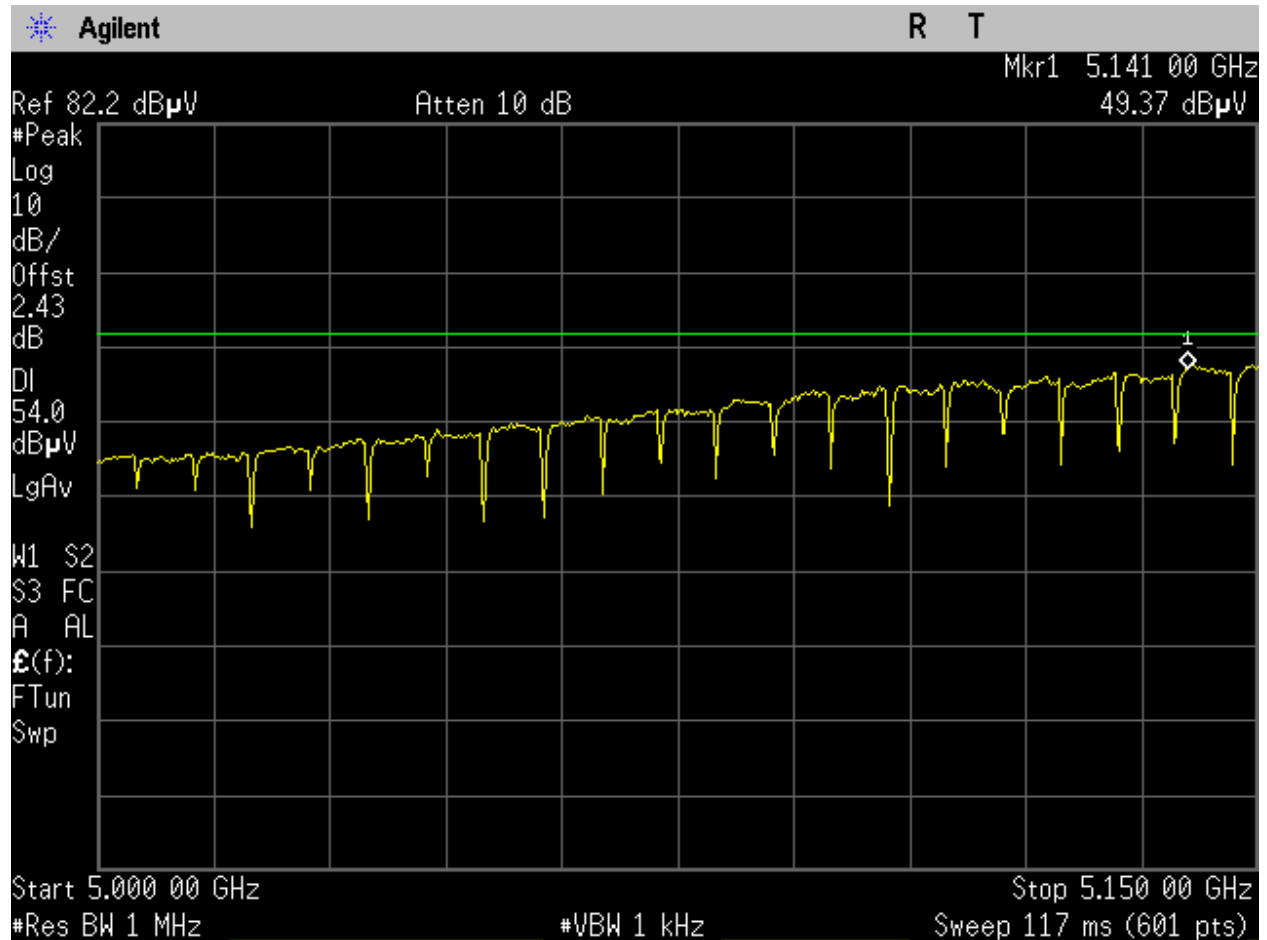


Figure 404: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 2.

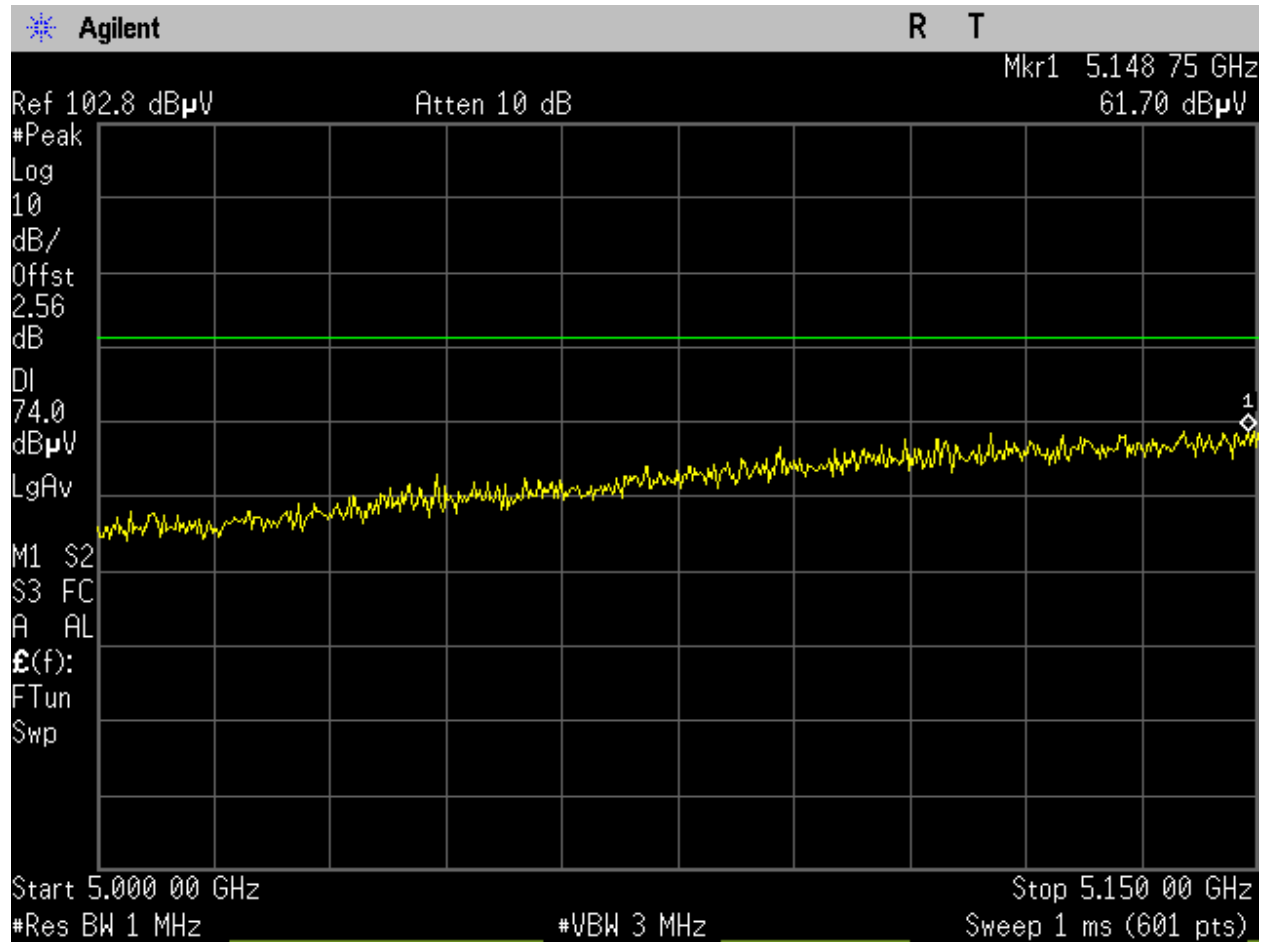


Figure 405: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 1.

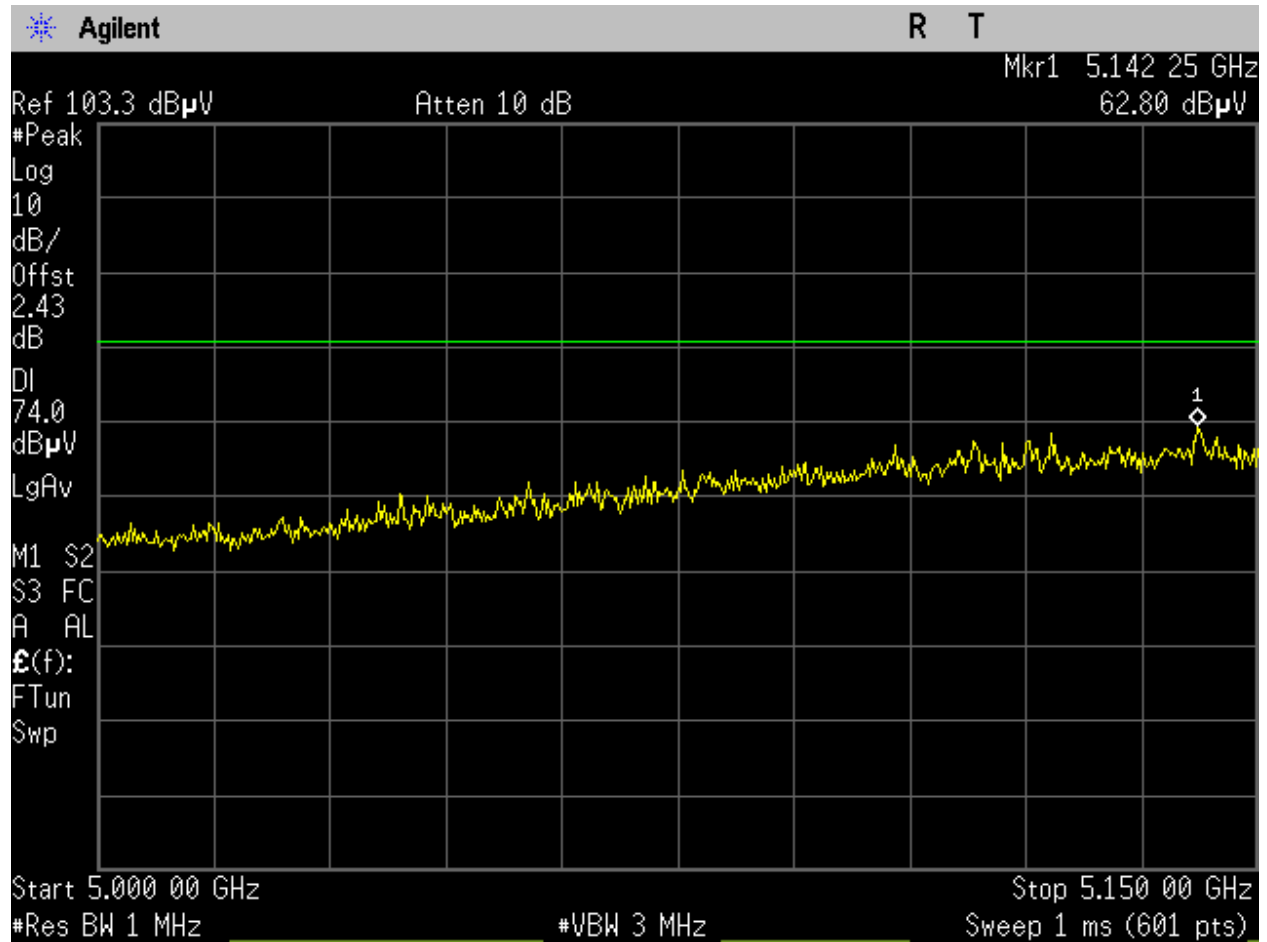


Figure 406: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 2.

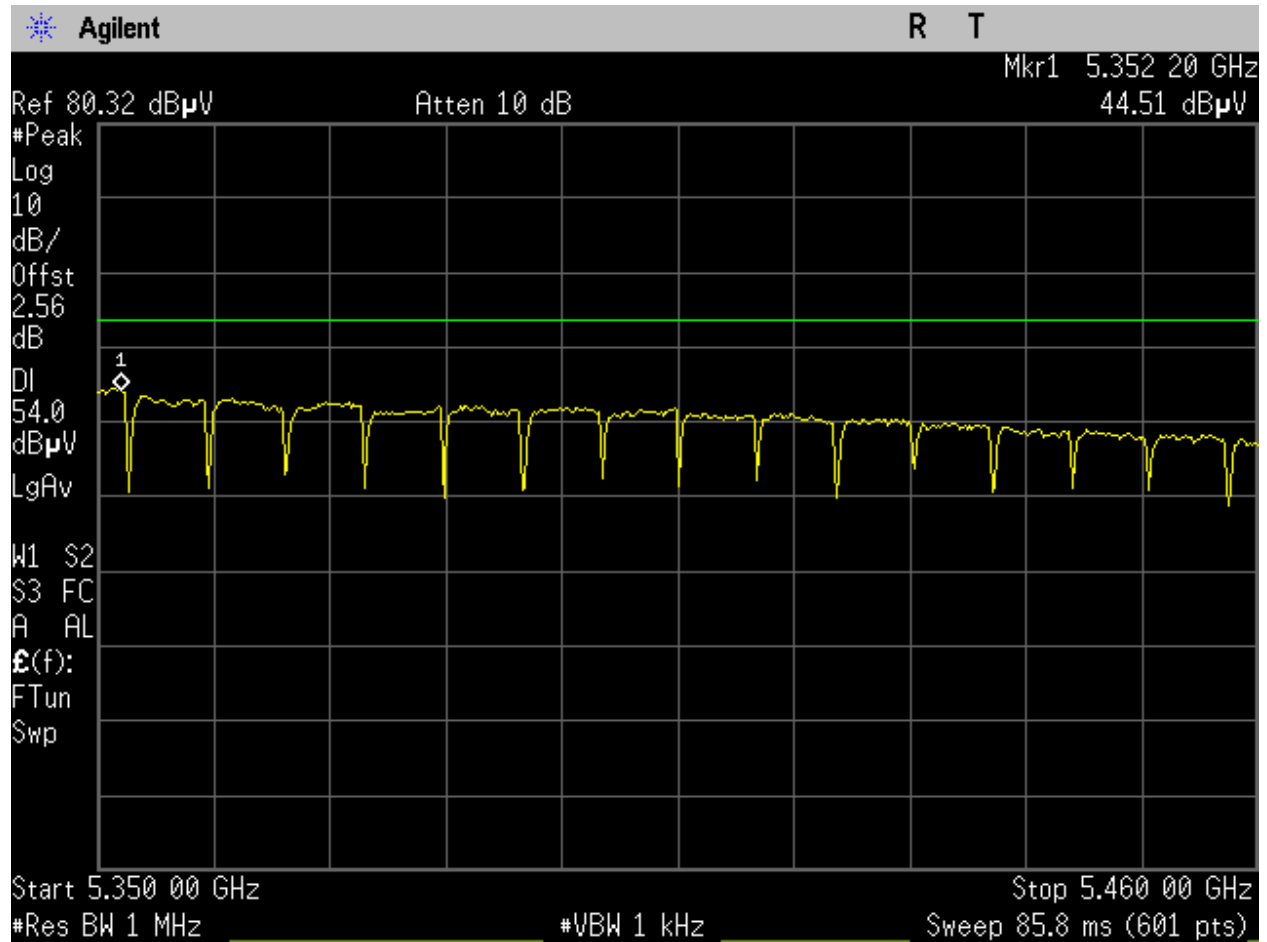


Figure 407: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 1.

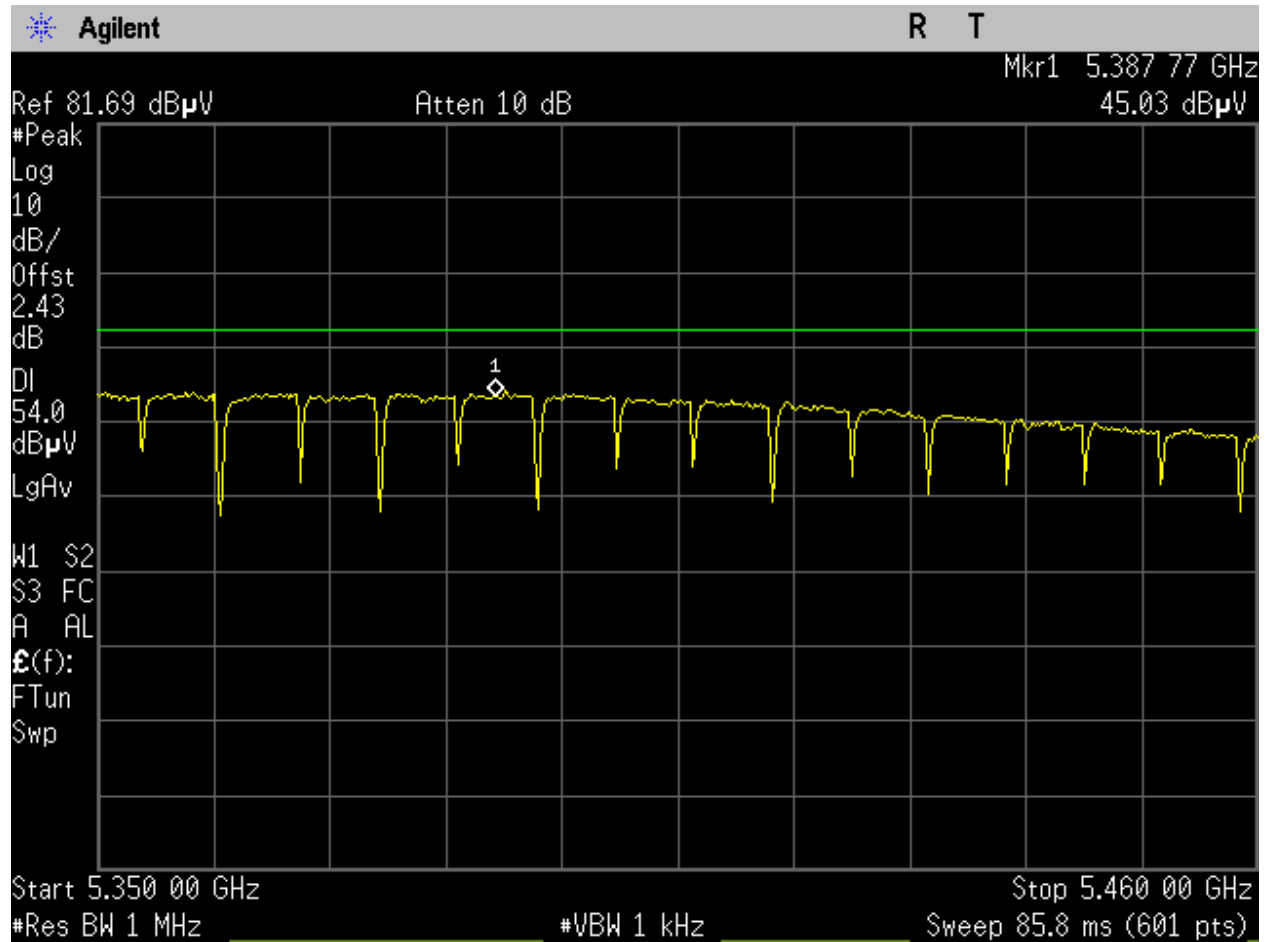


Figure 408: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 2.

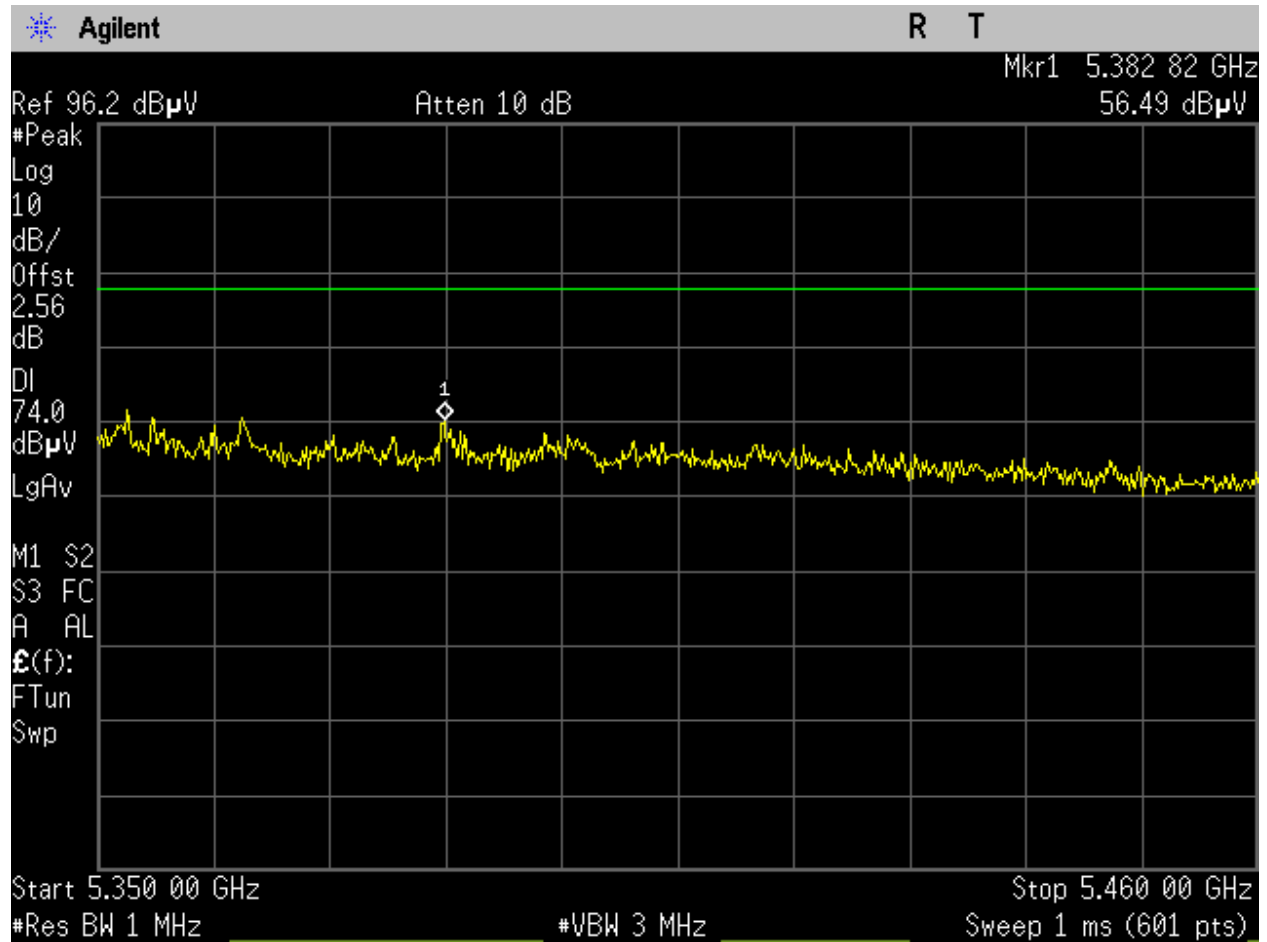


Figure 409: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 1.

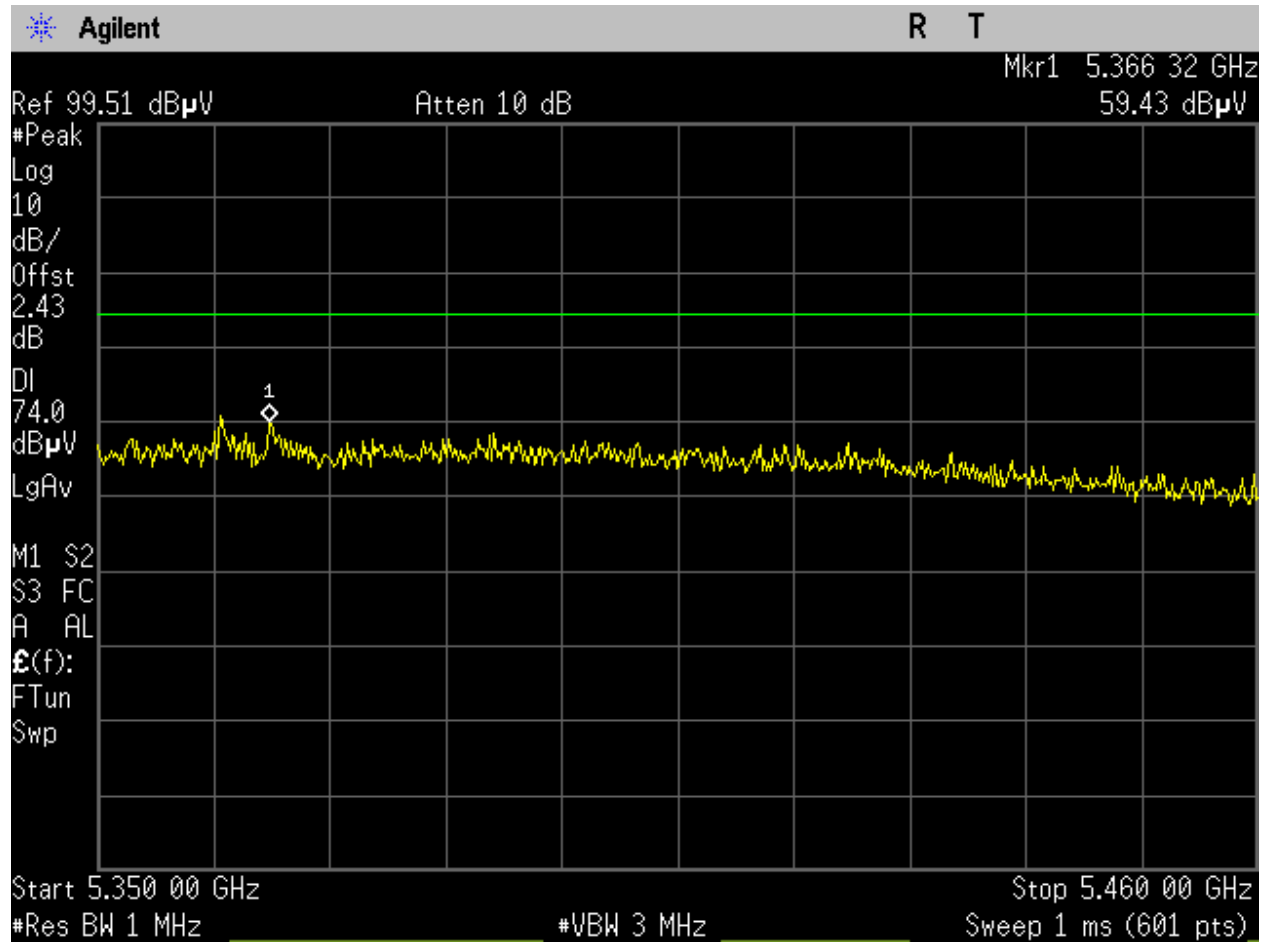


Figure 410: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 2.

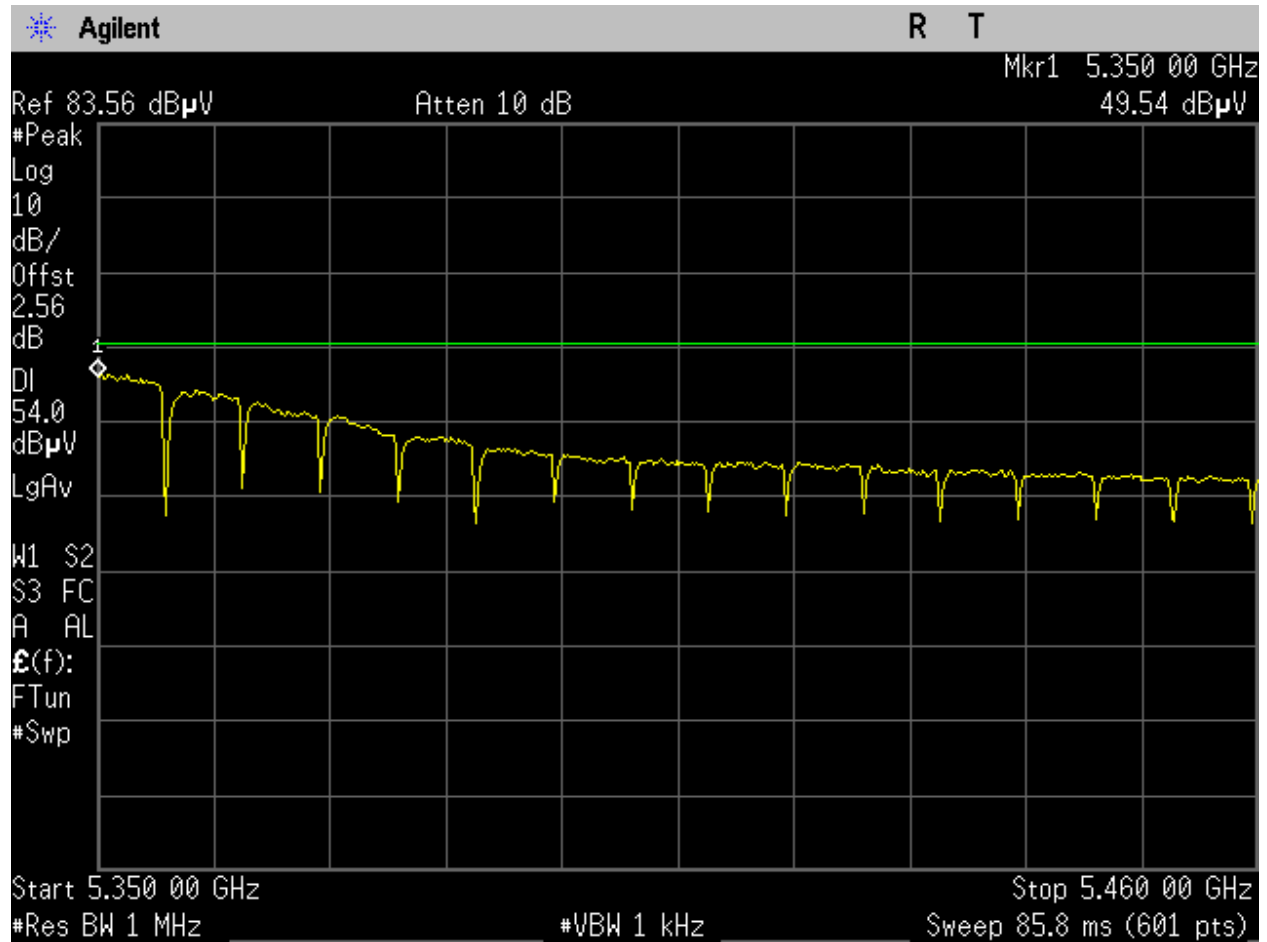


Figure 411: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 1.

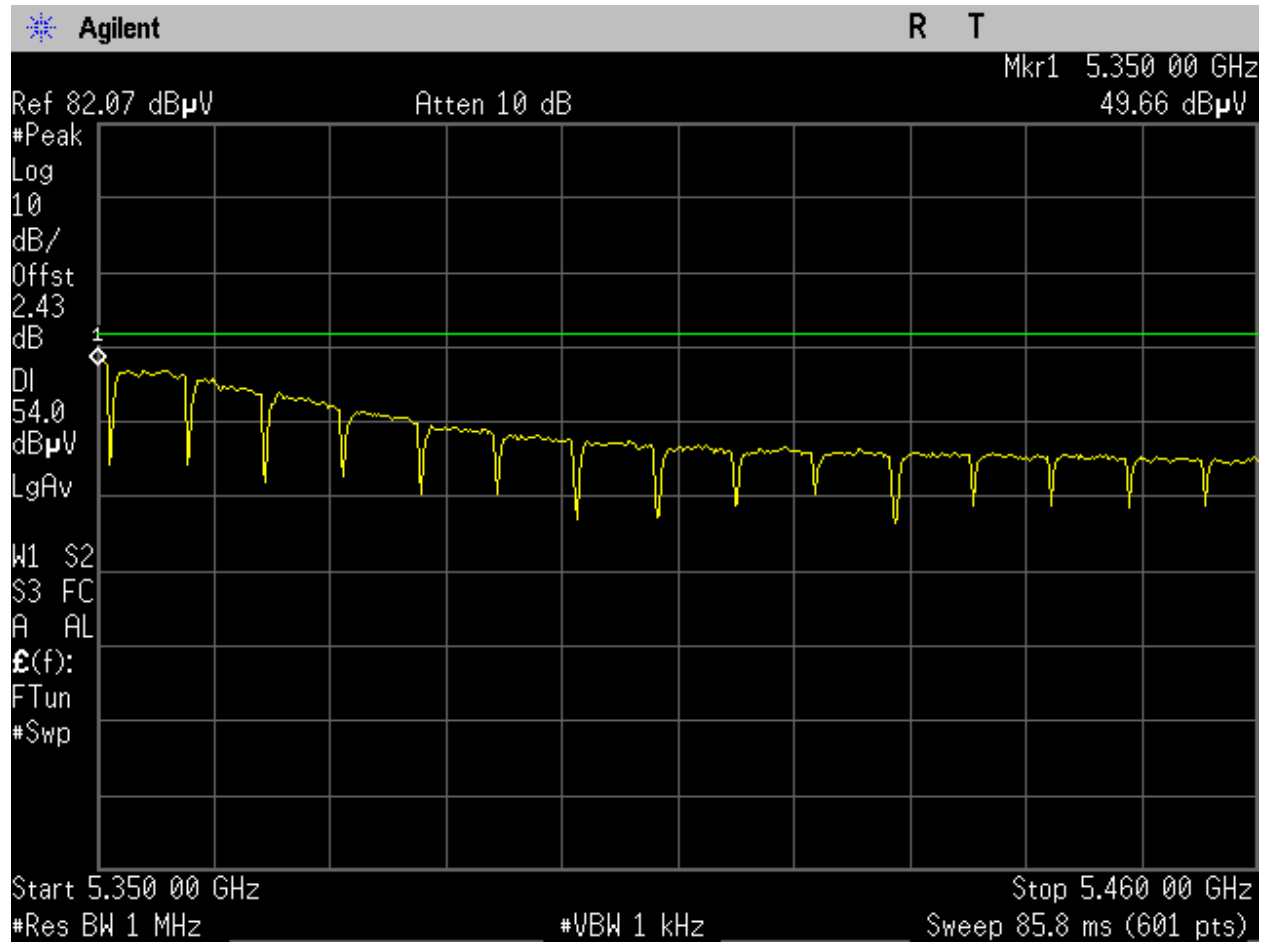


Figure 412: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 2.

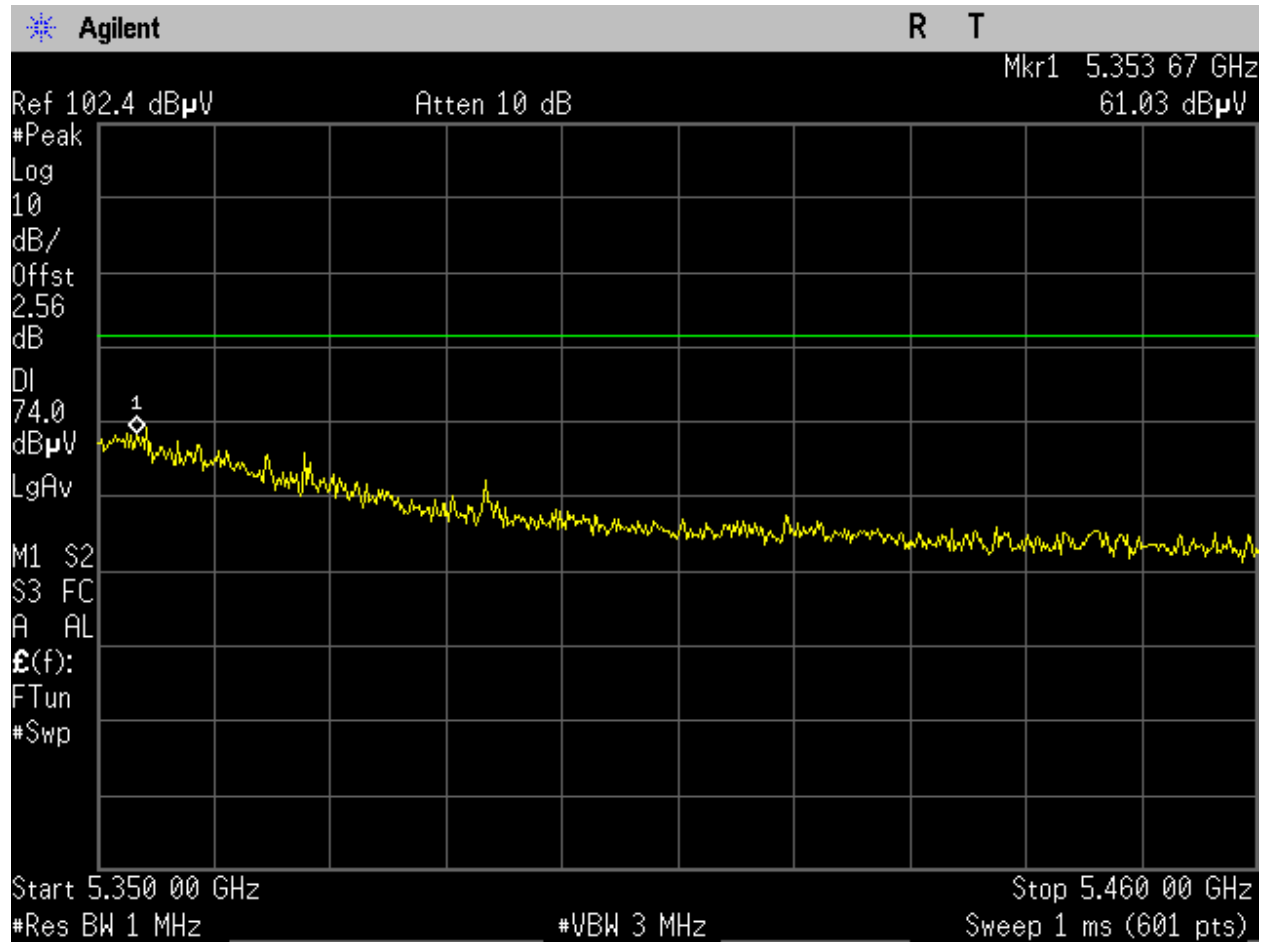


Figure 413: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 1.

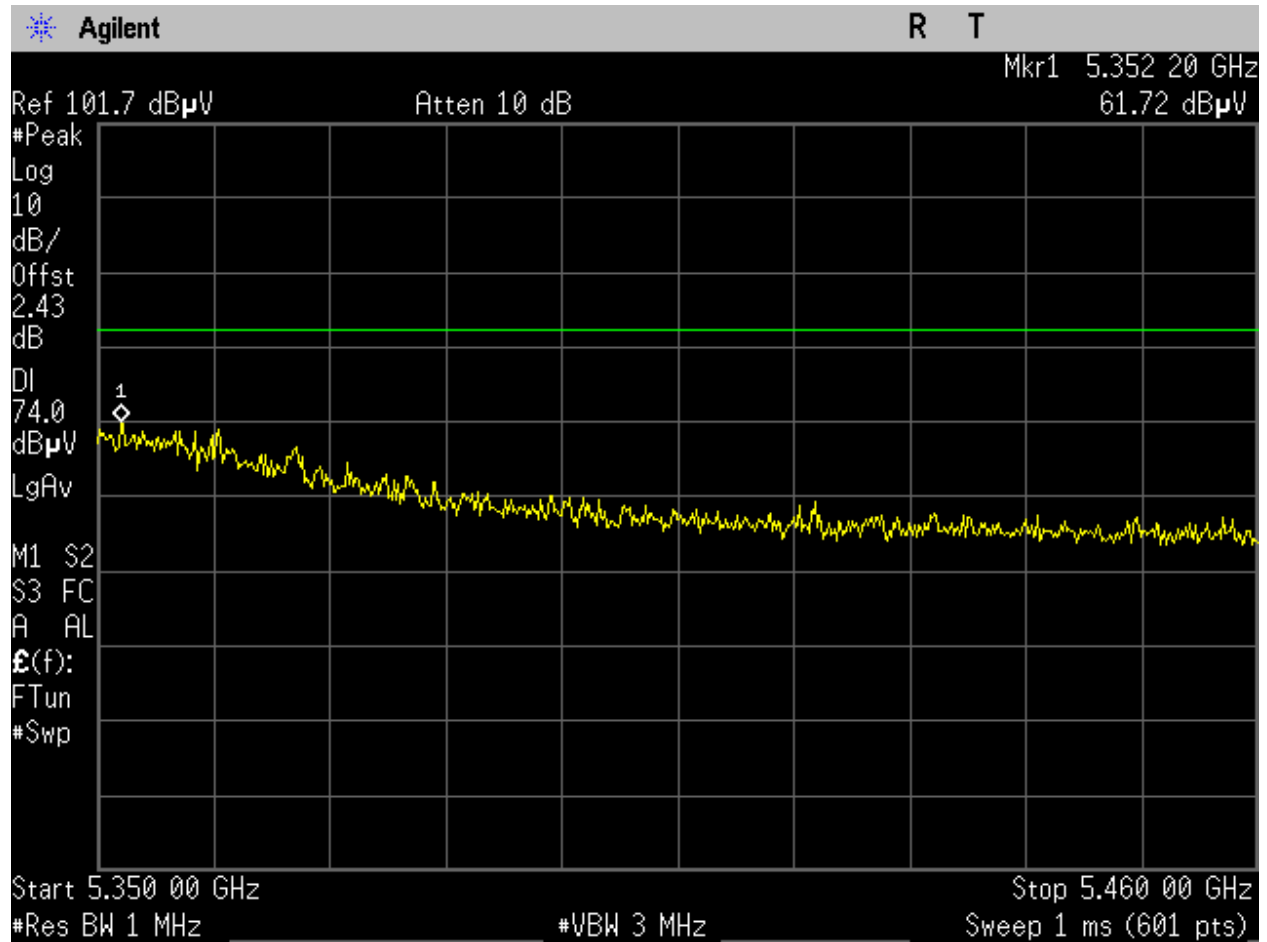


Figure 414: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 2.

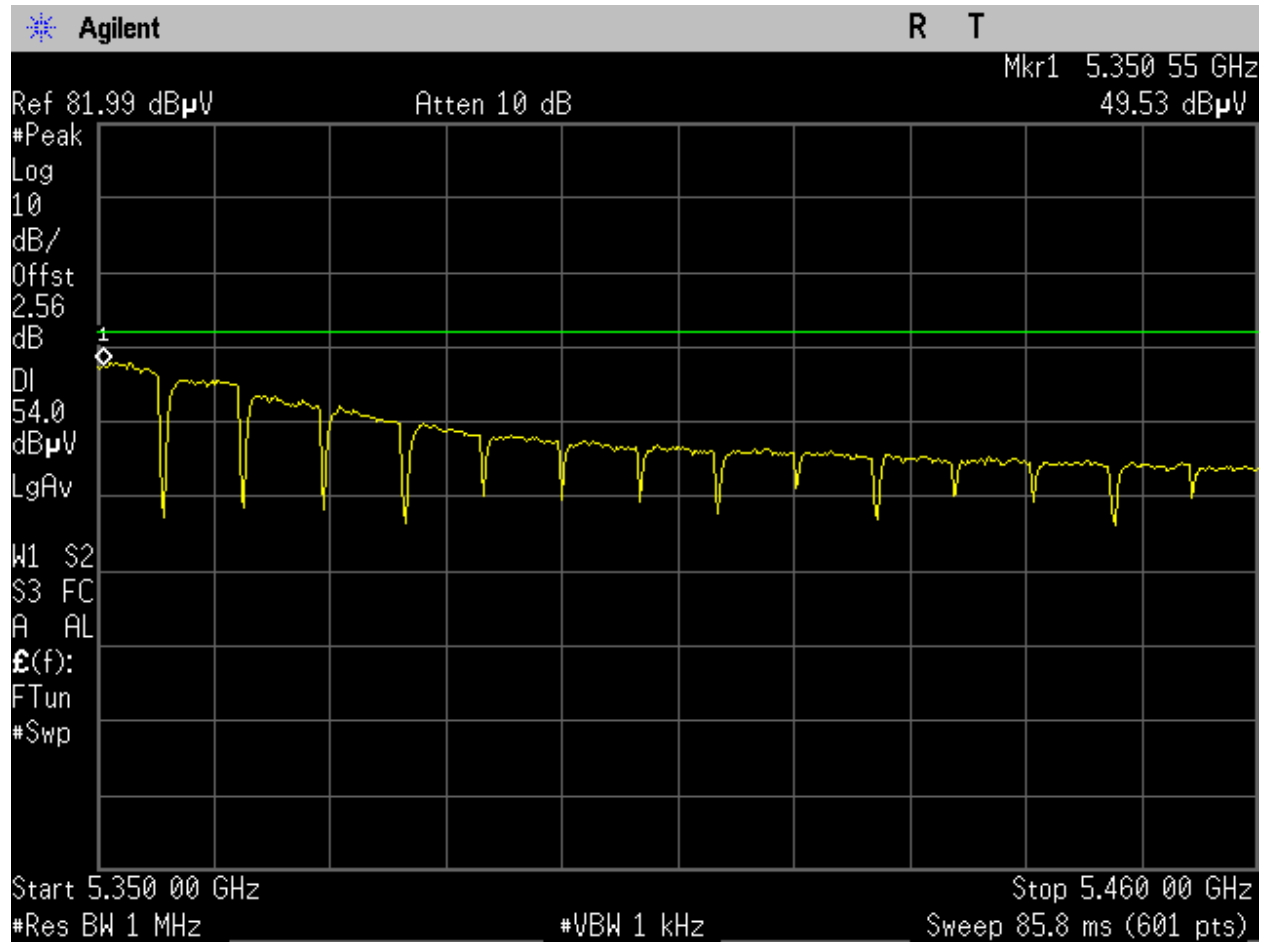


Figure 415: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_Upper Band Edge Avg_Port 1.

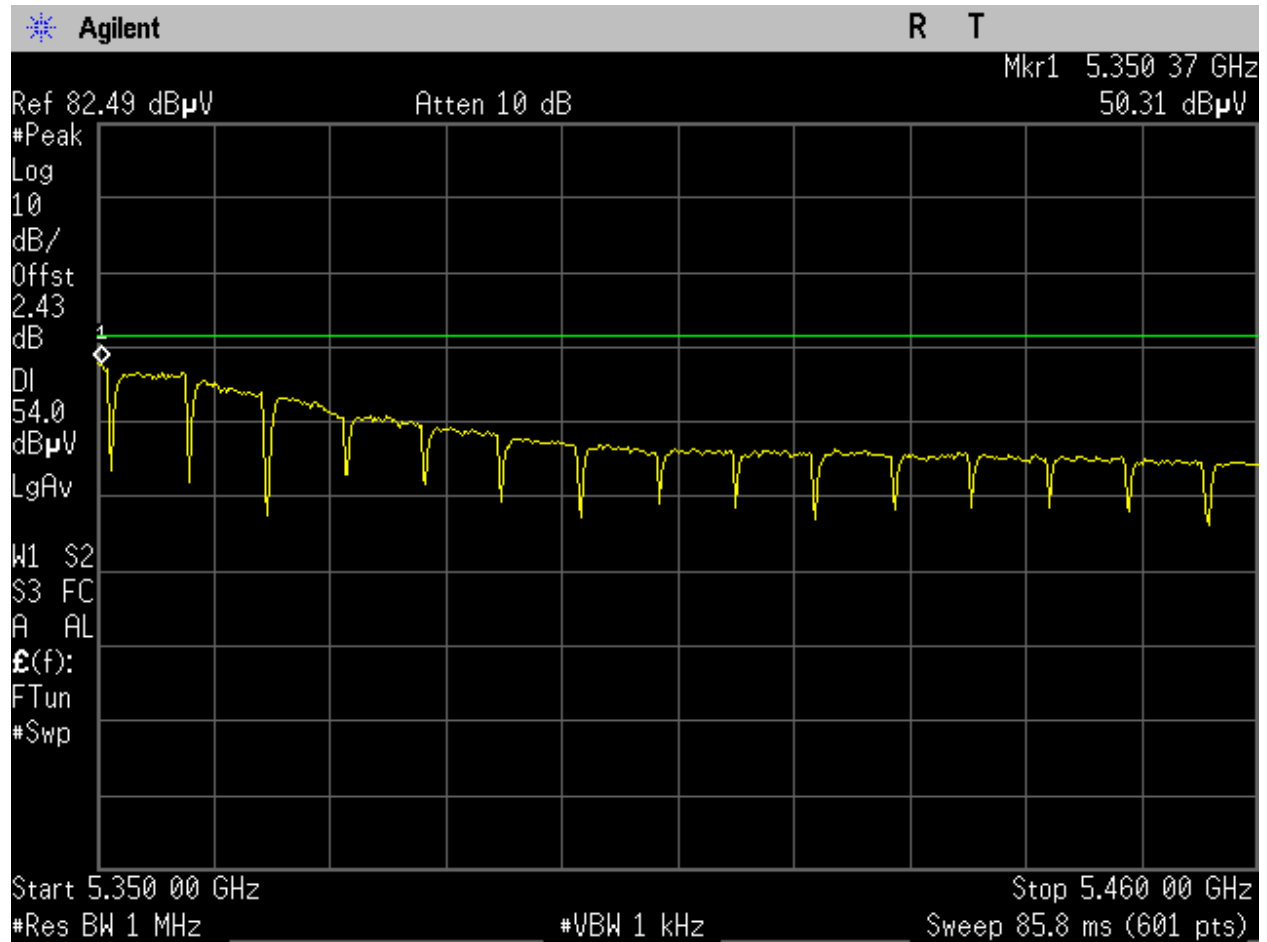


Figure 416: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_Upper Band Edge Avg_Port 2.

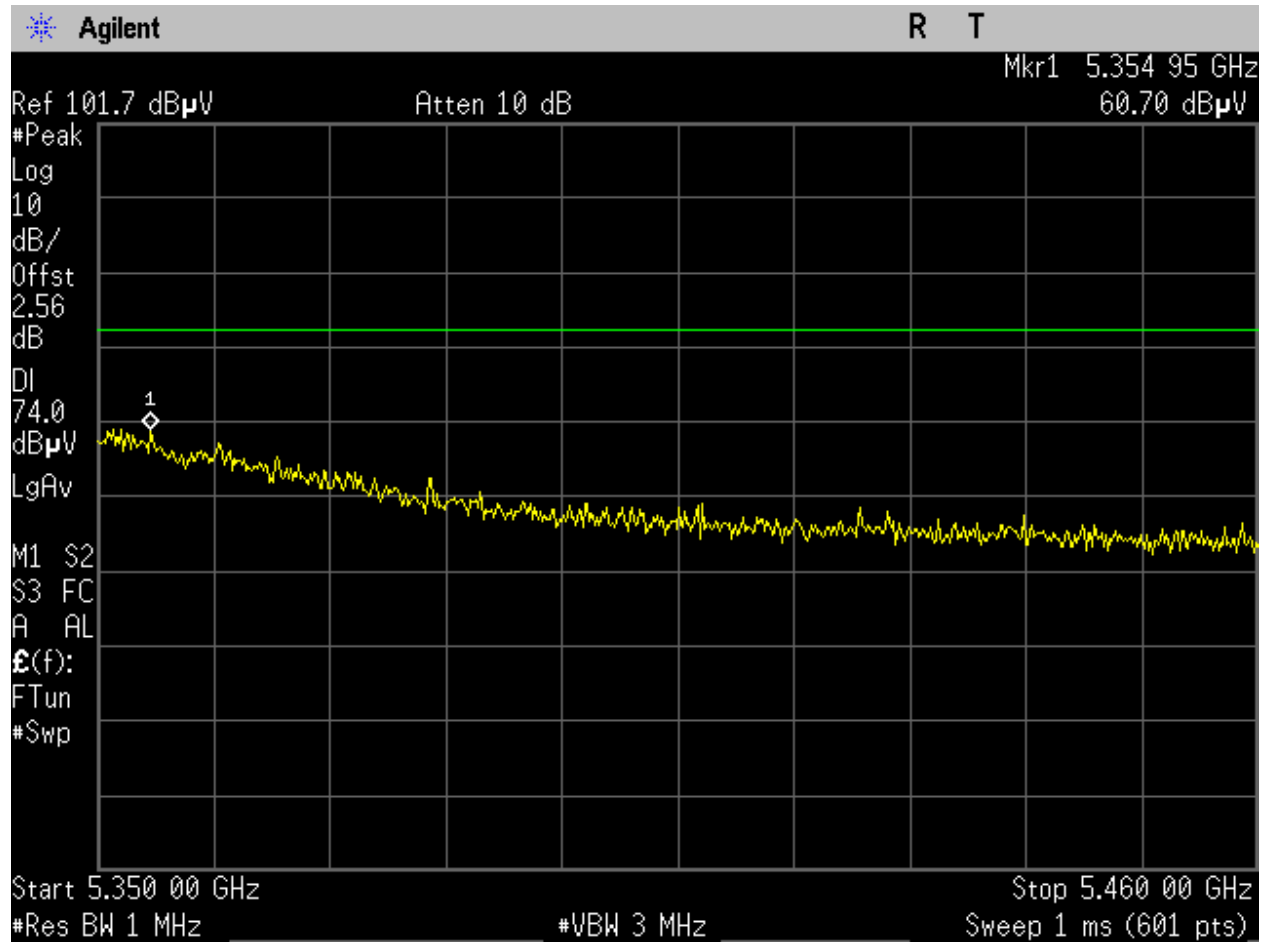


Figure 417: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_Upper Band Edge Peak_Port 1.

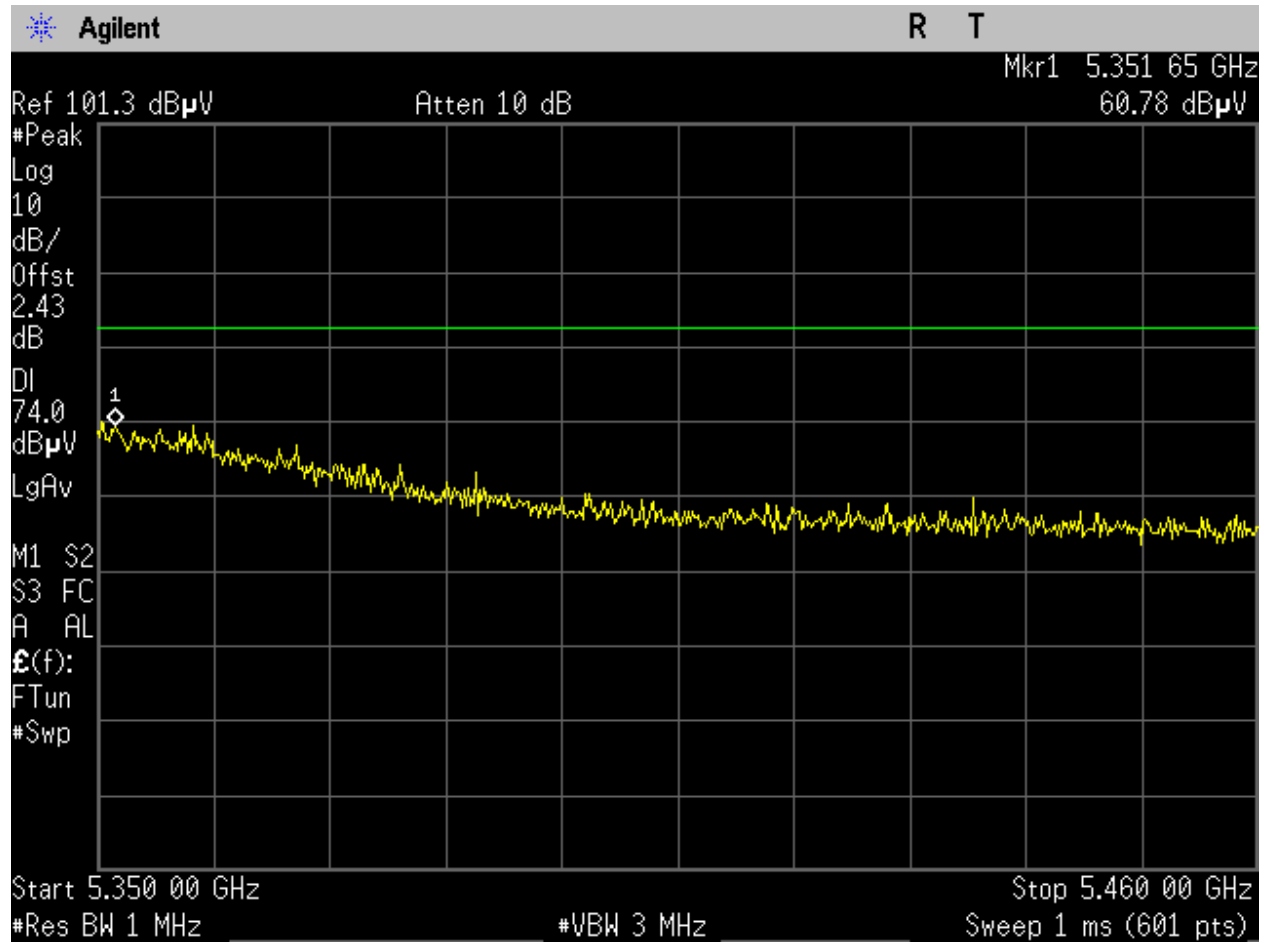


Figure 418: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_Upper Band Edge Peak_Port 2.

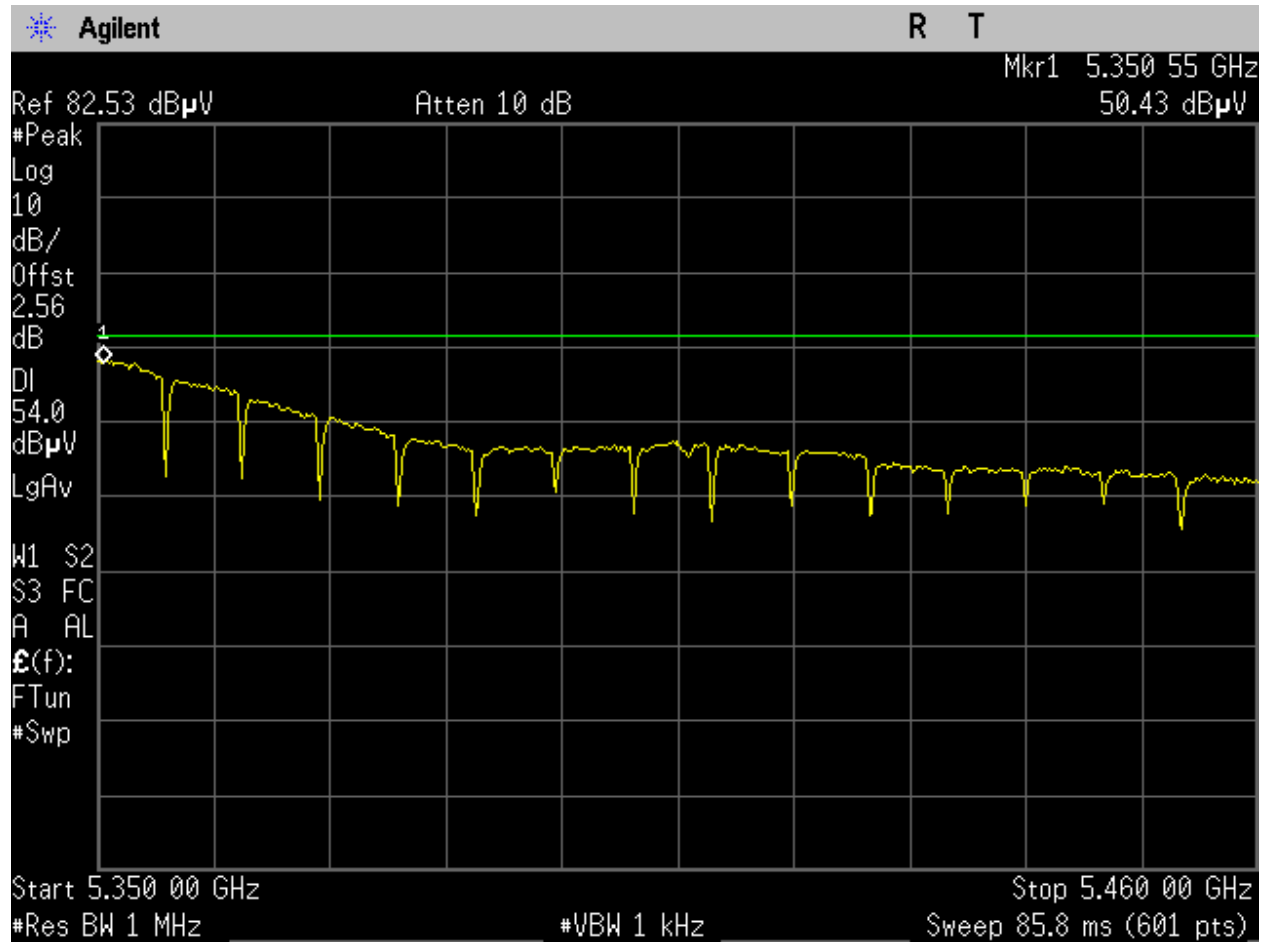


Figure 419: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 1.

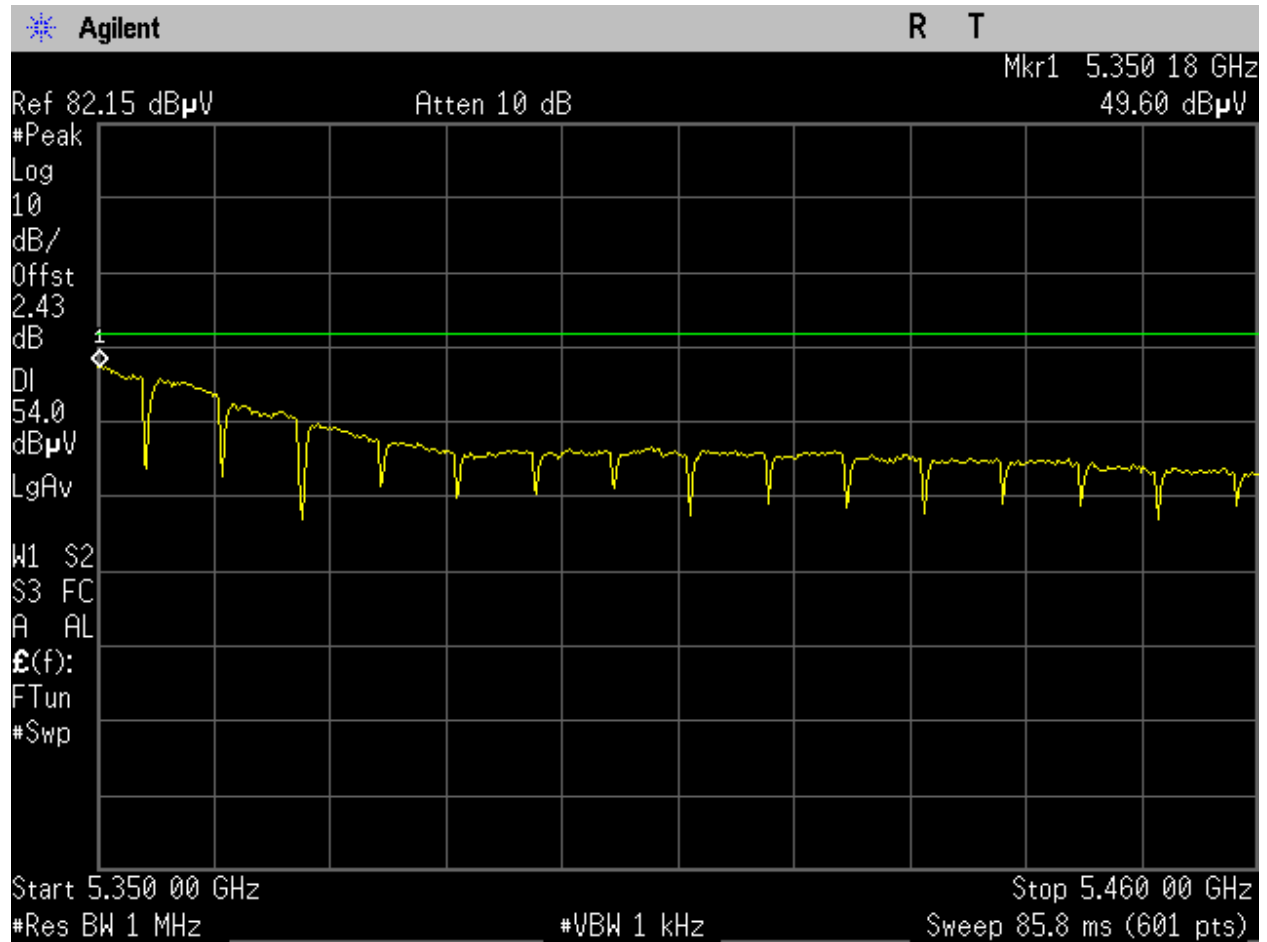


Figure 420: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 2.

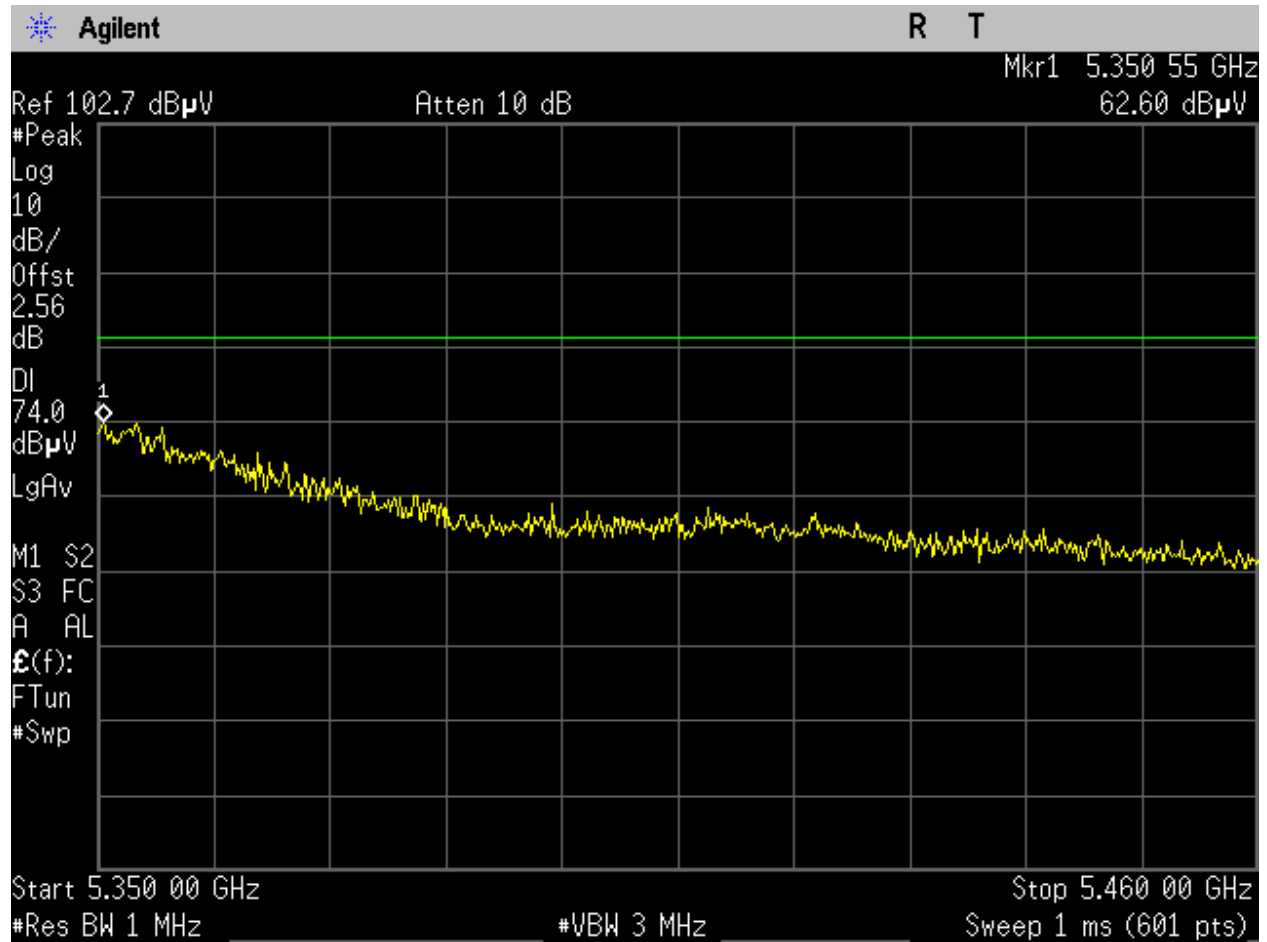


Figure 421: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 1.

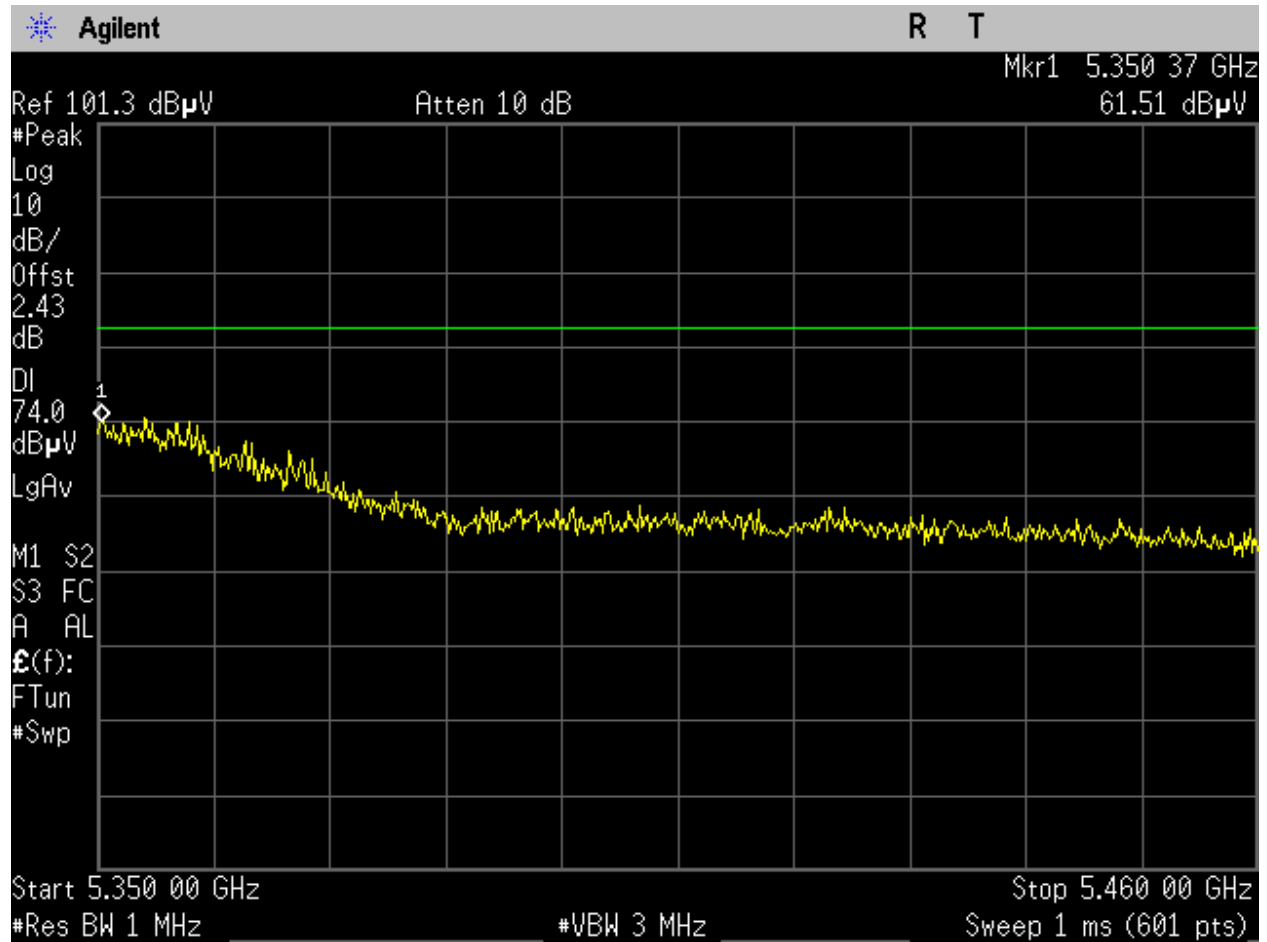


Figure 422: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 2.

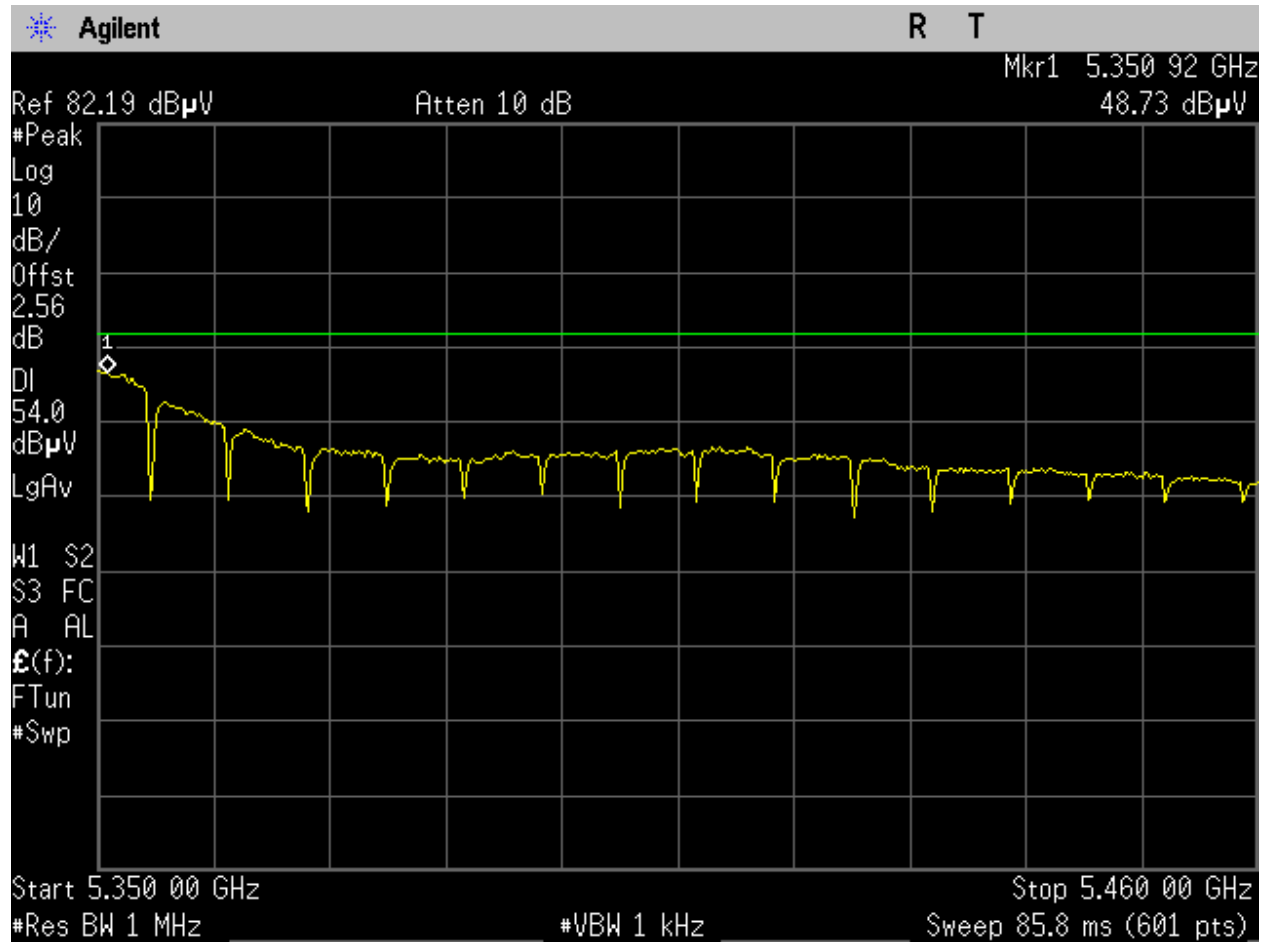


Figure 423: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_Upper Band Edge Avg_Port 1.

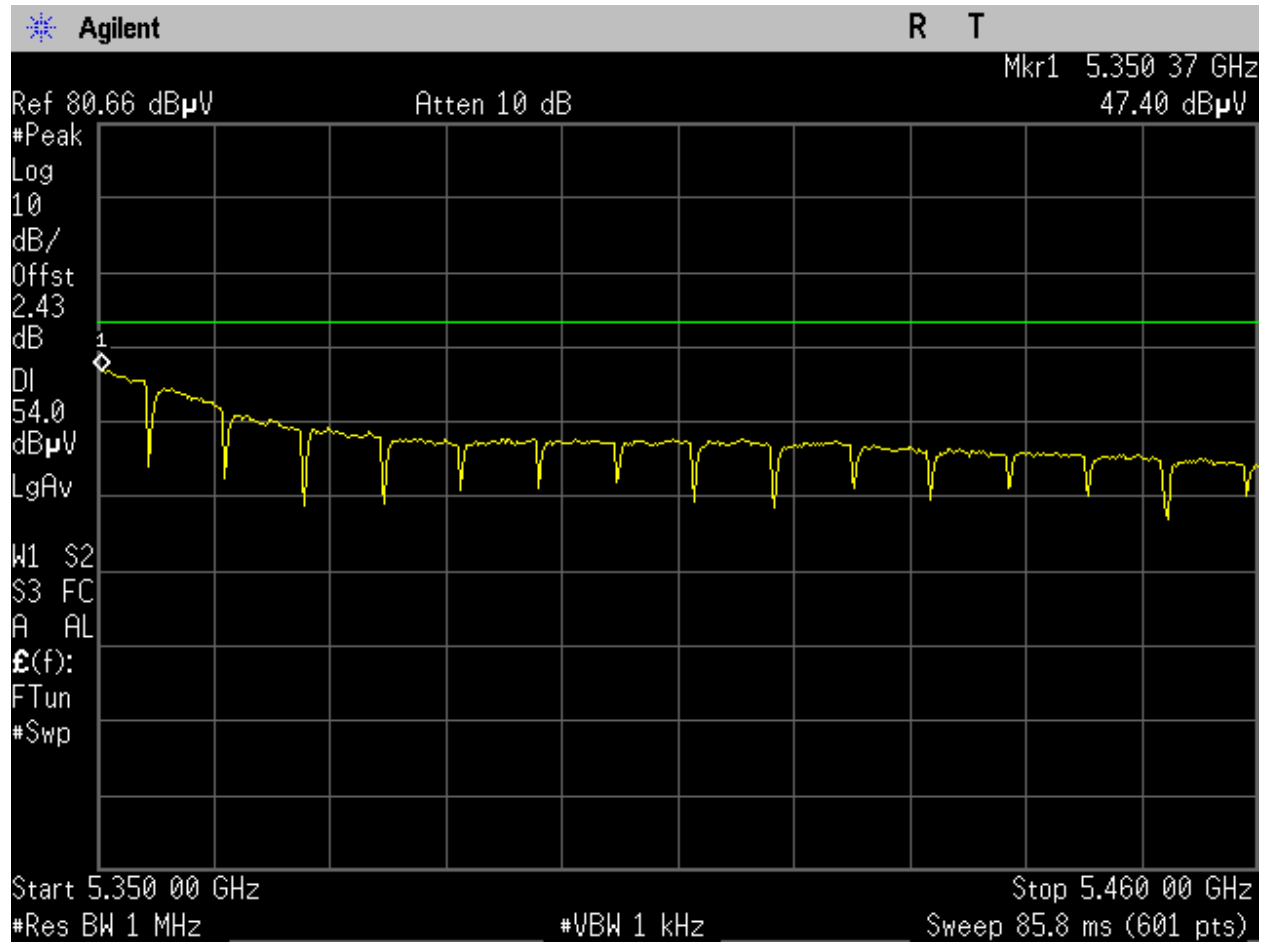


Figure 424: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_Upper Band Edge Avg_Port 2.

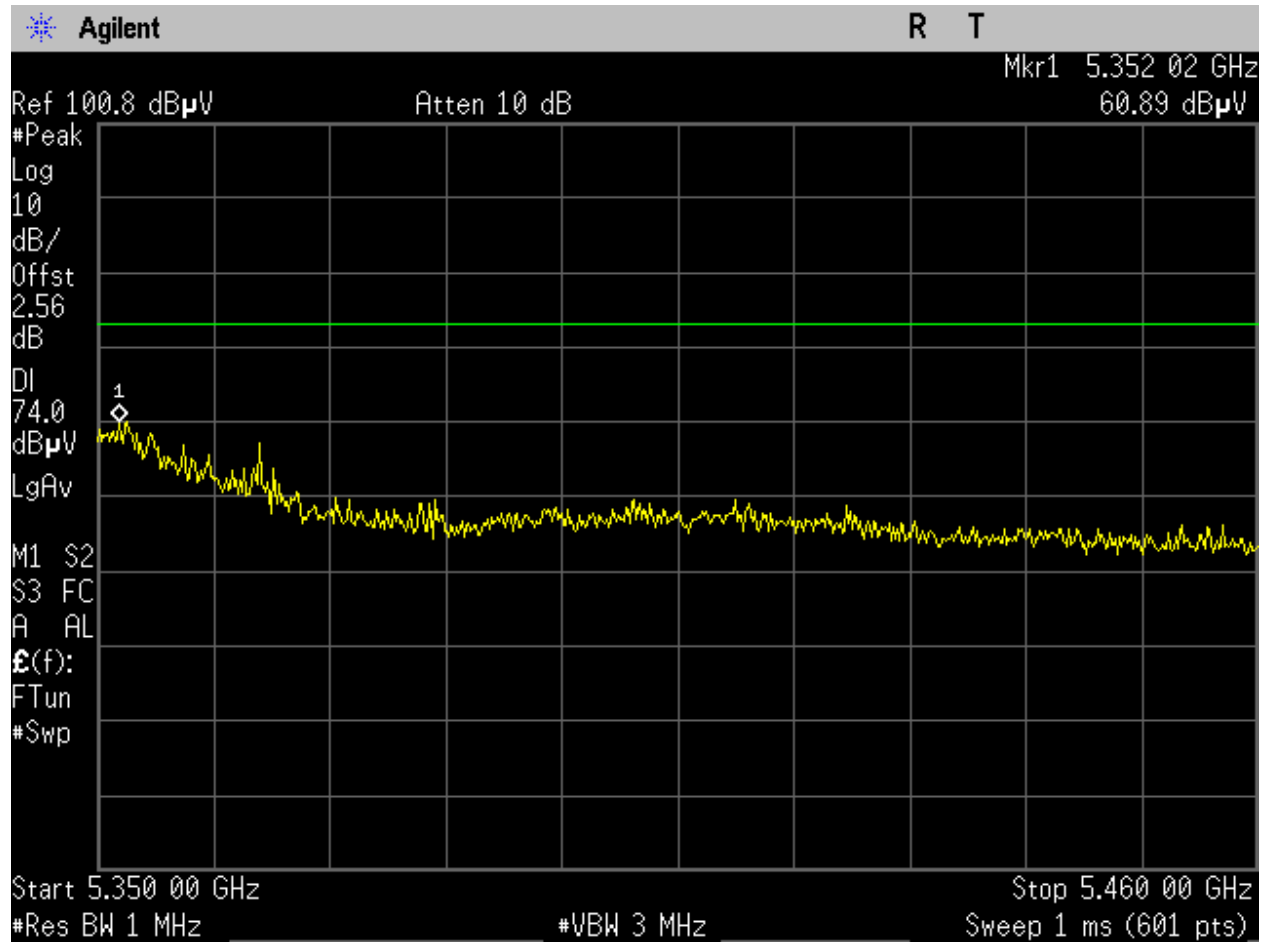


Figure 425: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_Upper Band Edge Peak_Port 1.

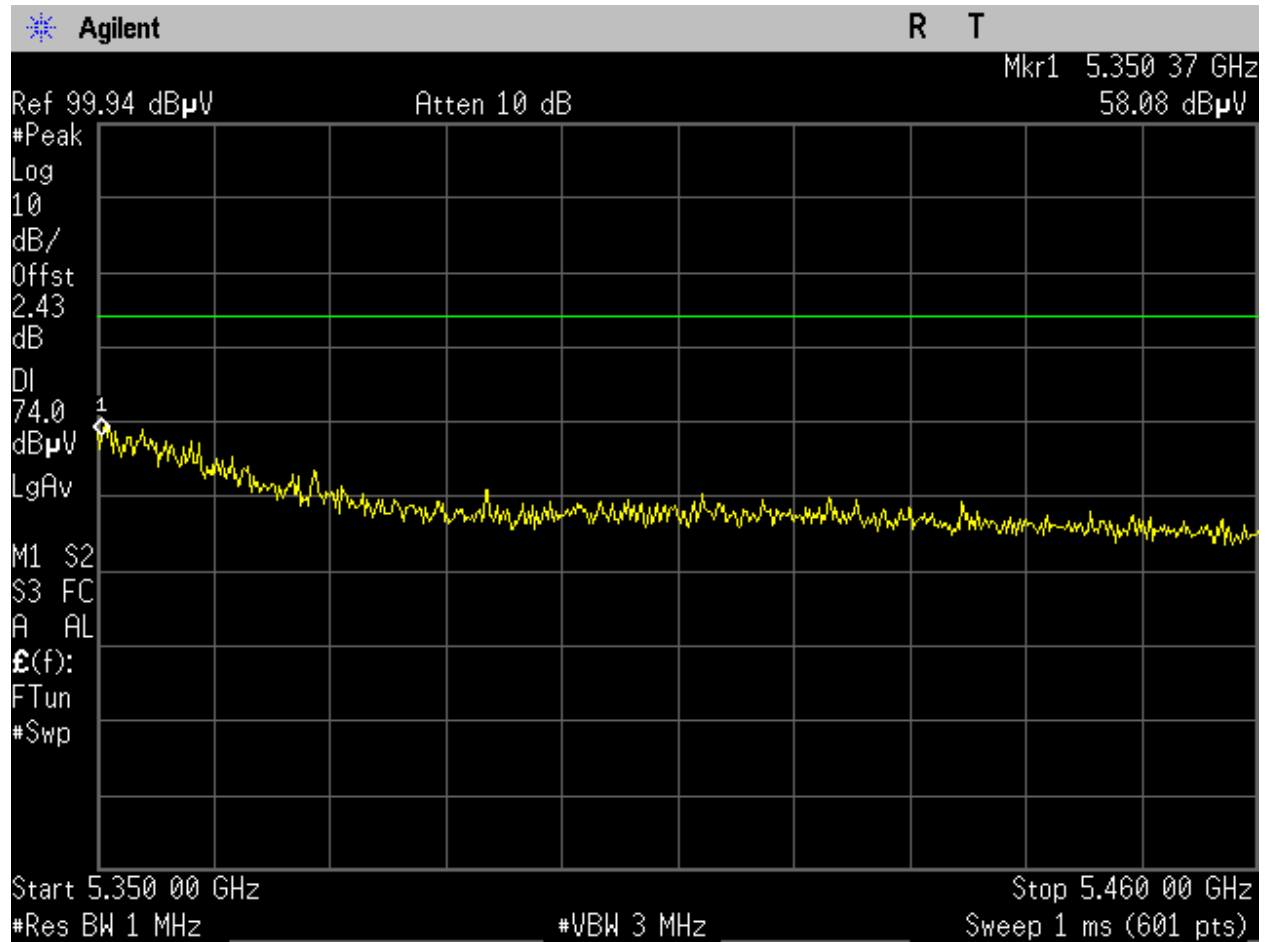


Figure 426: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_Upper Band Edge Peak_Port 2.

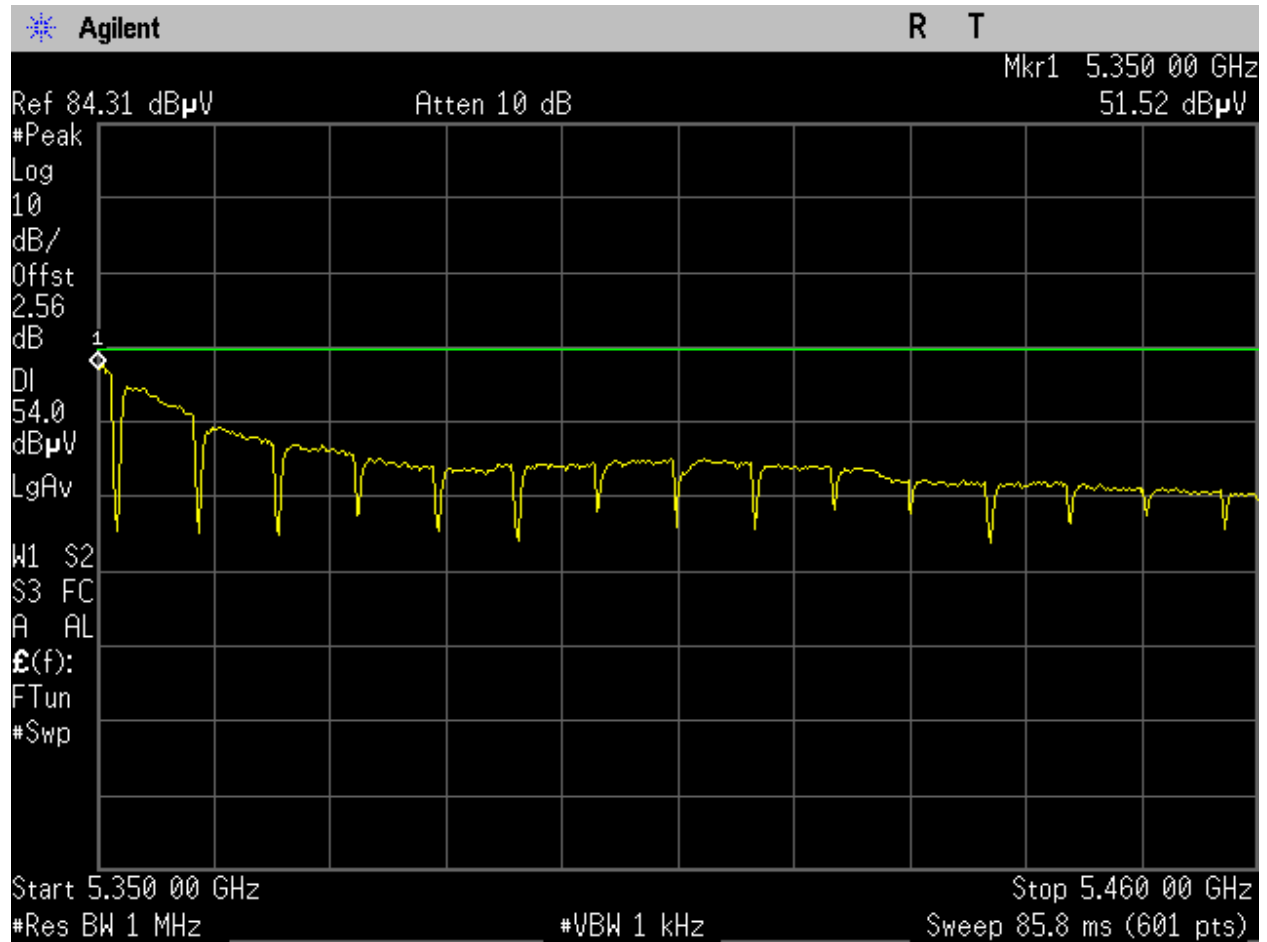


Figure 427: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_Upper Band Edge Avg_Port 1.

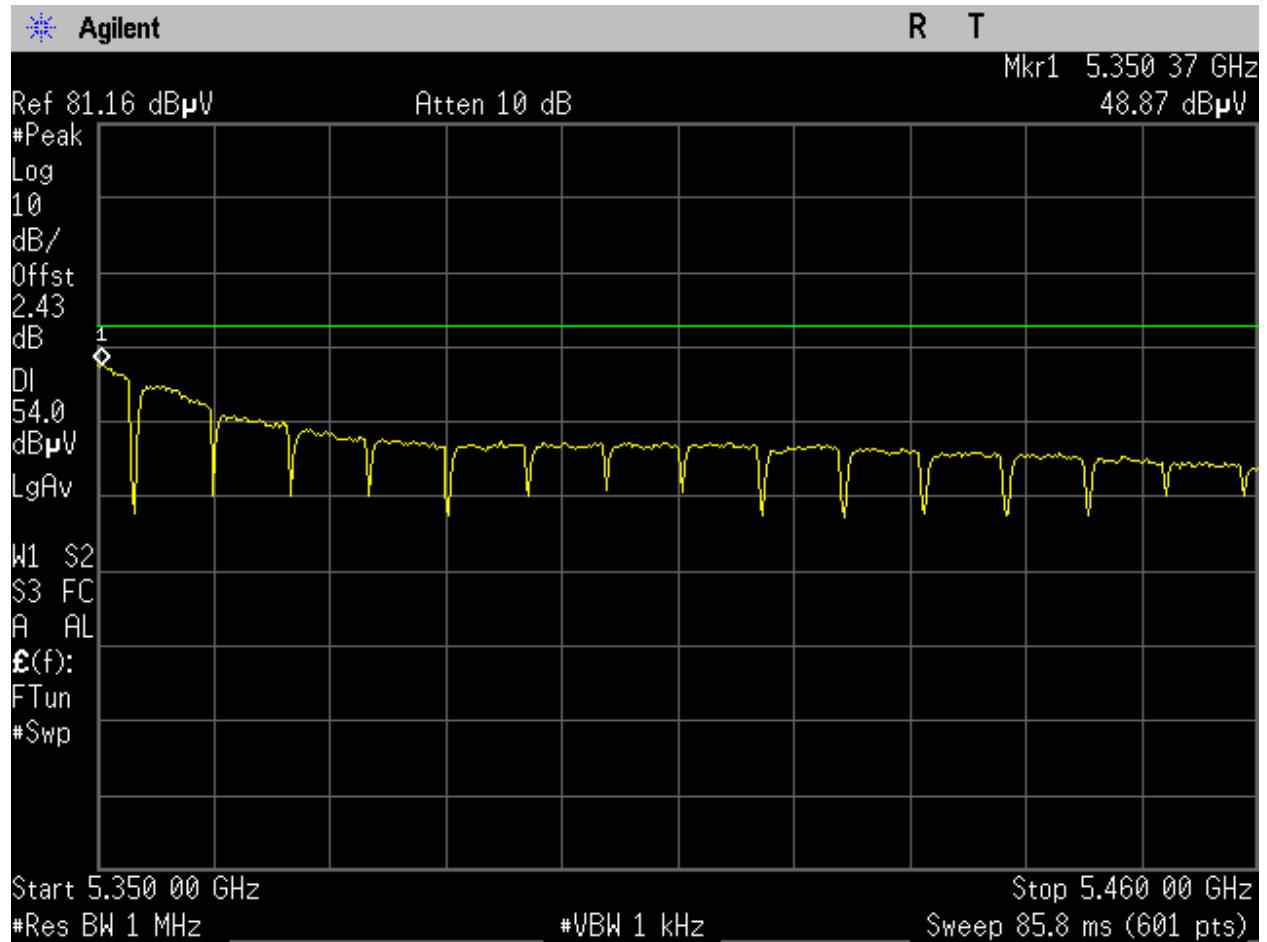


Figure 428: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_Upper Band Edge Avg_Port 2.

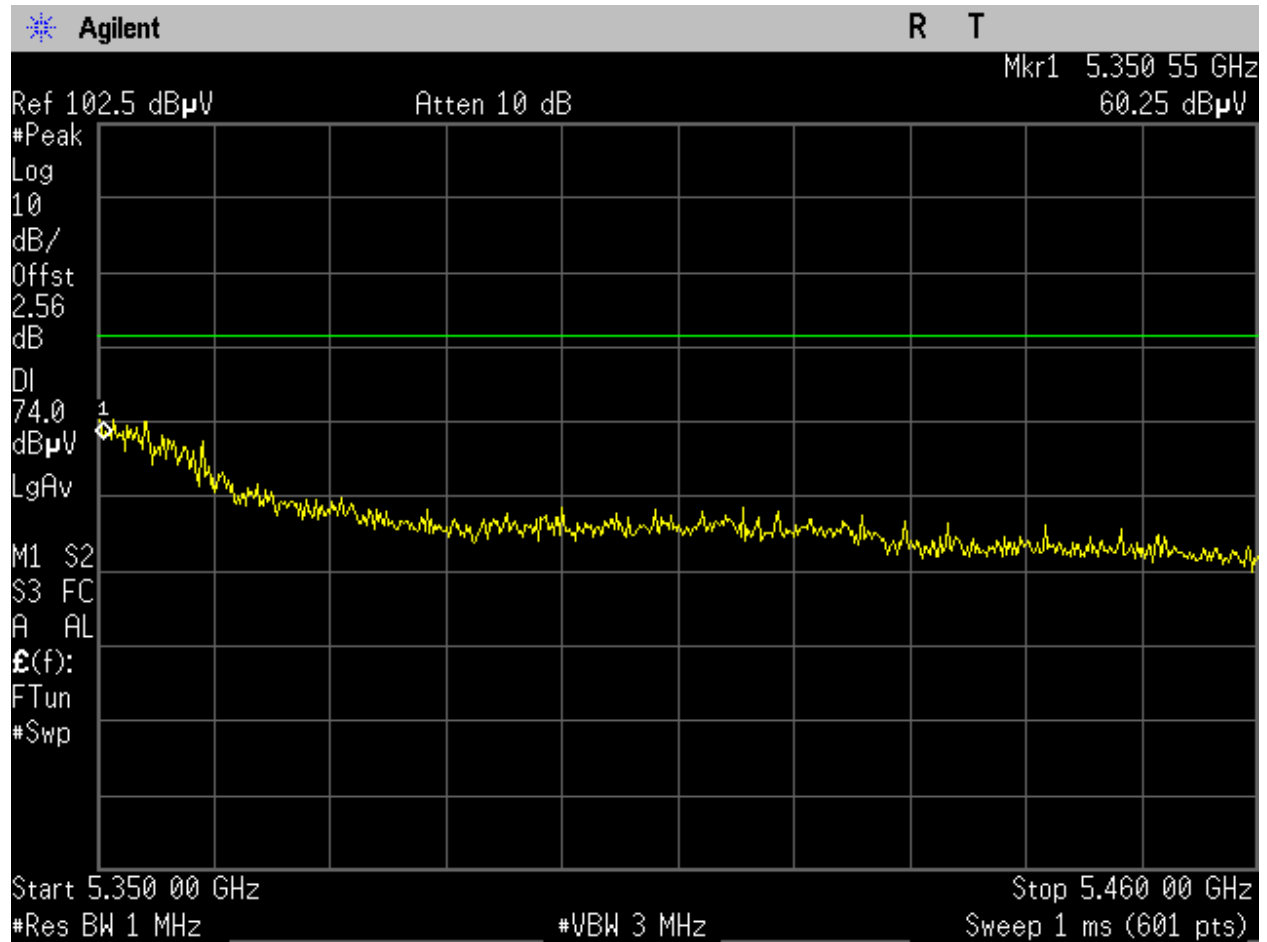


Figure 429: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_Upper Band Edge Peak_Port 1.

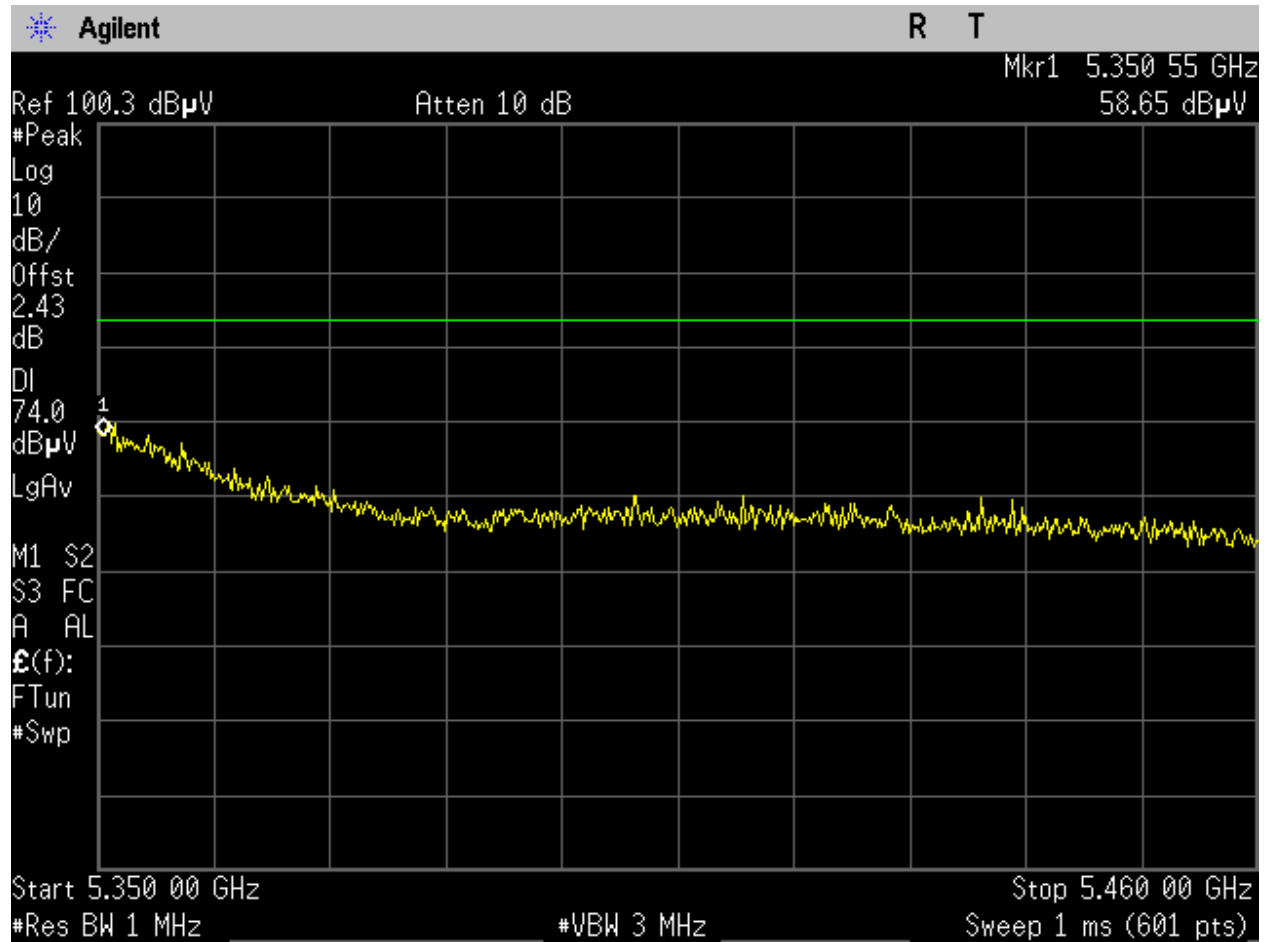


Figure 430: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_Upper Band Edge Peak_Port 2.

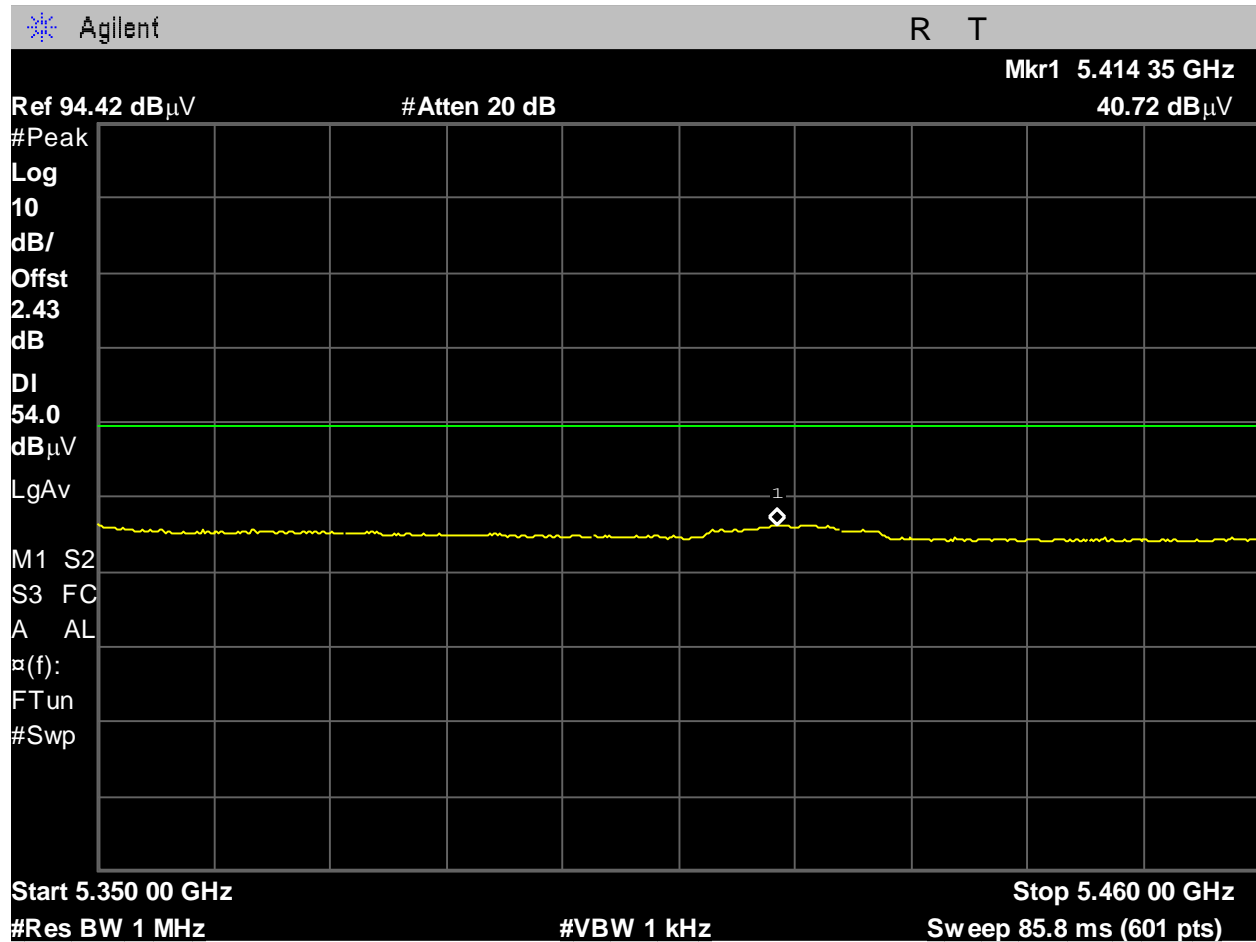


Figure 431: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_Upper Band Edge Avg_Port 1.

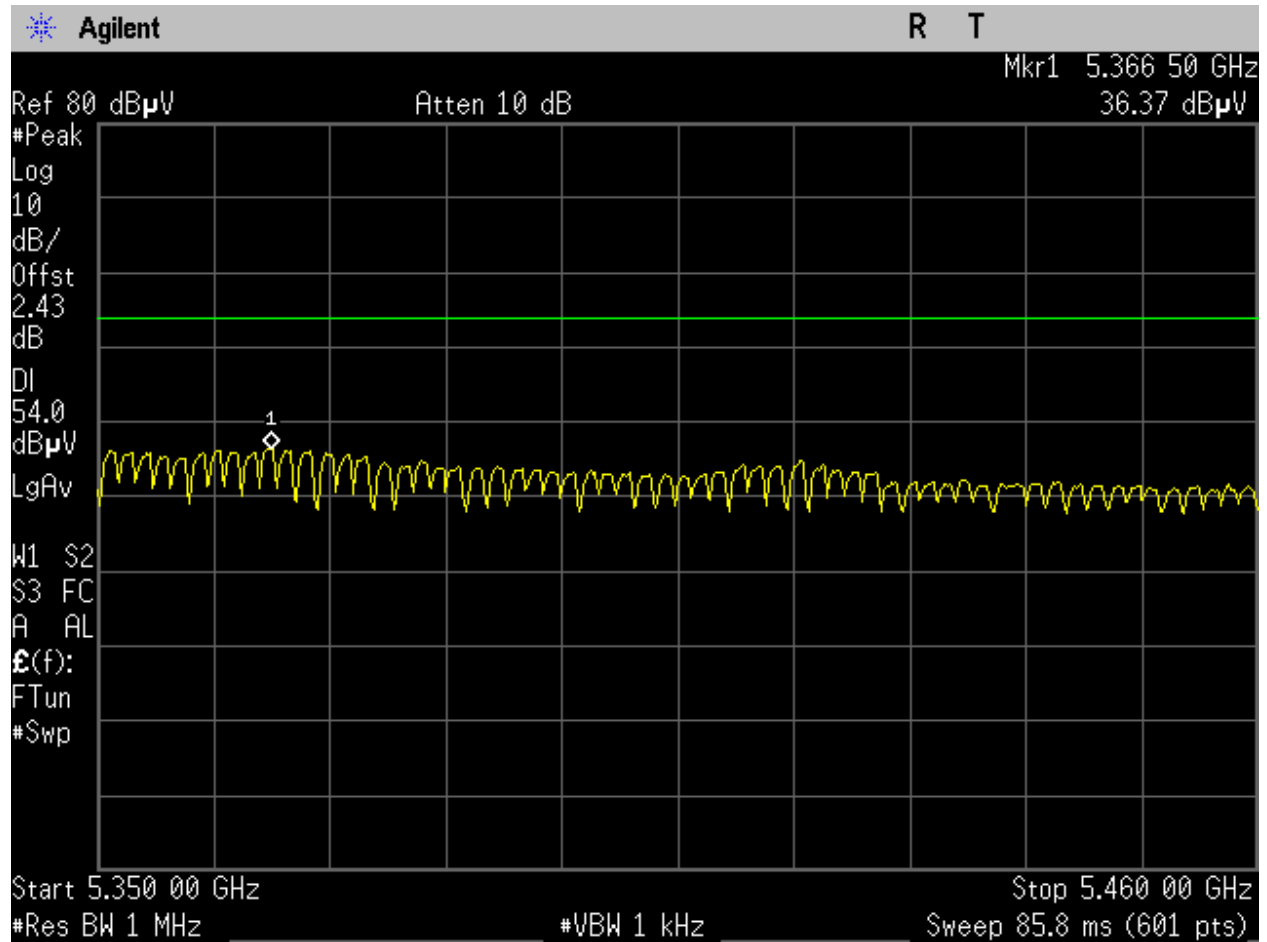


Figure 432: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_Upper Band Edge Avg_Port 2.

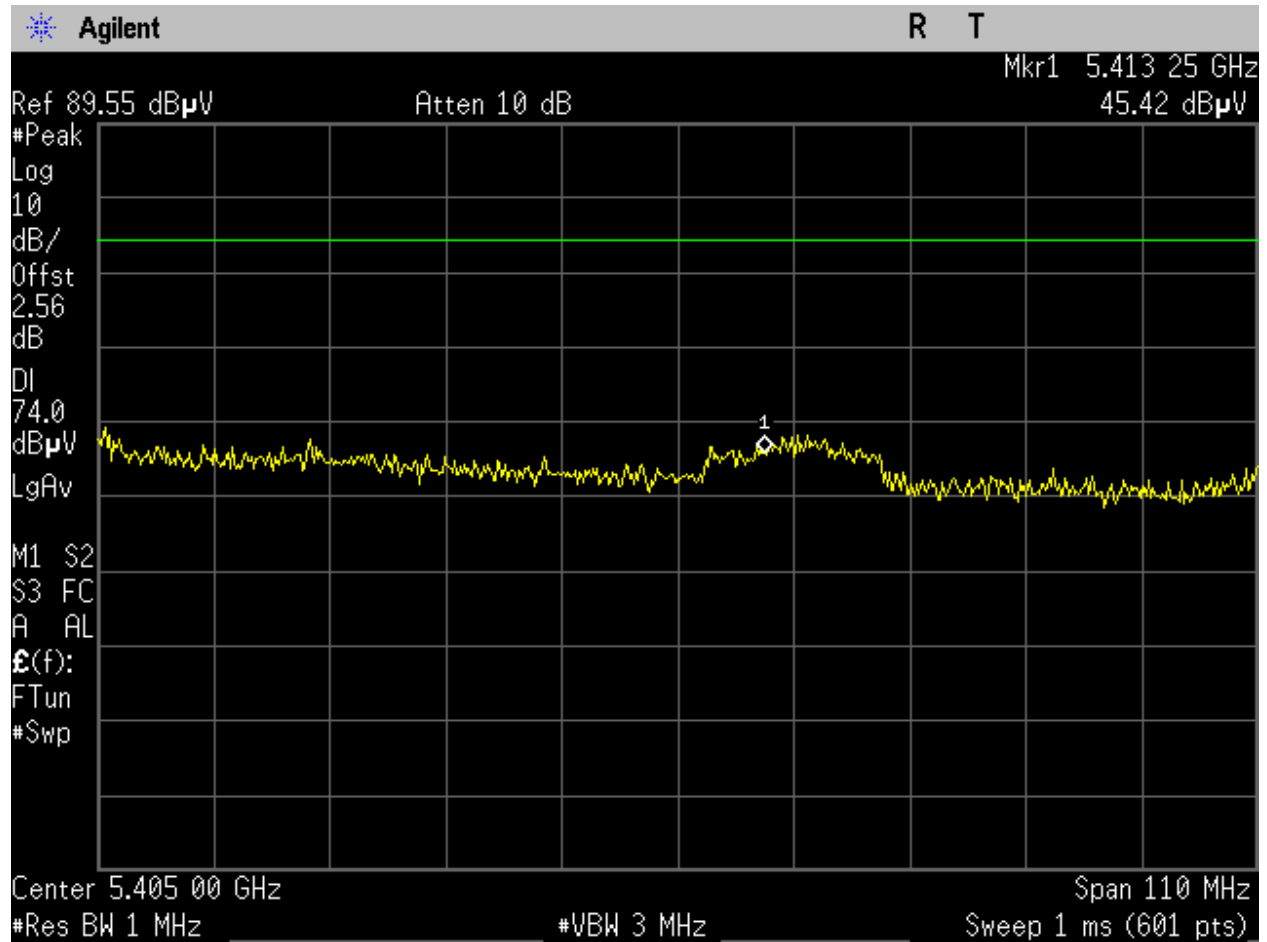


Figure 433: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_Upper Band Edge Peak_Port 1.

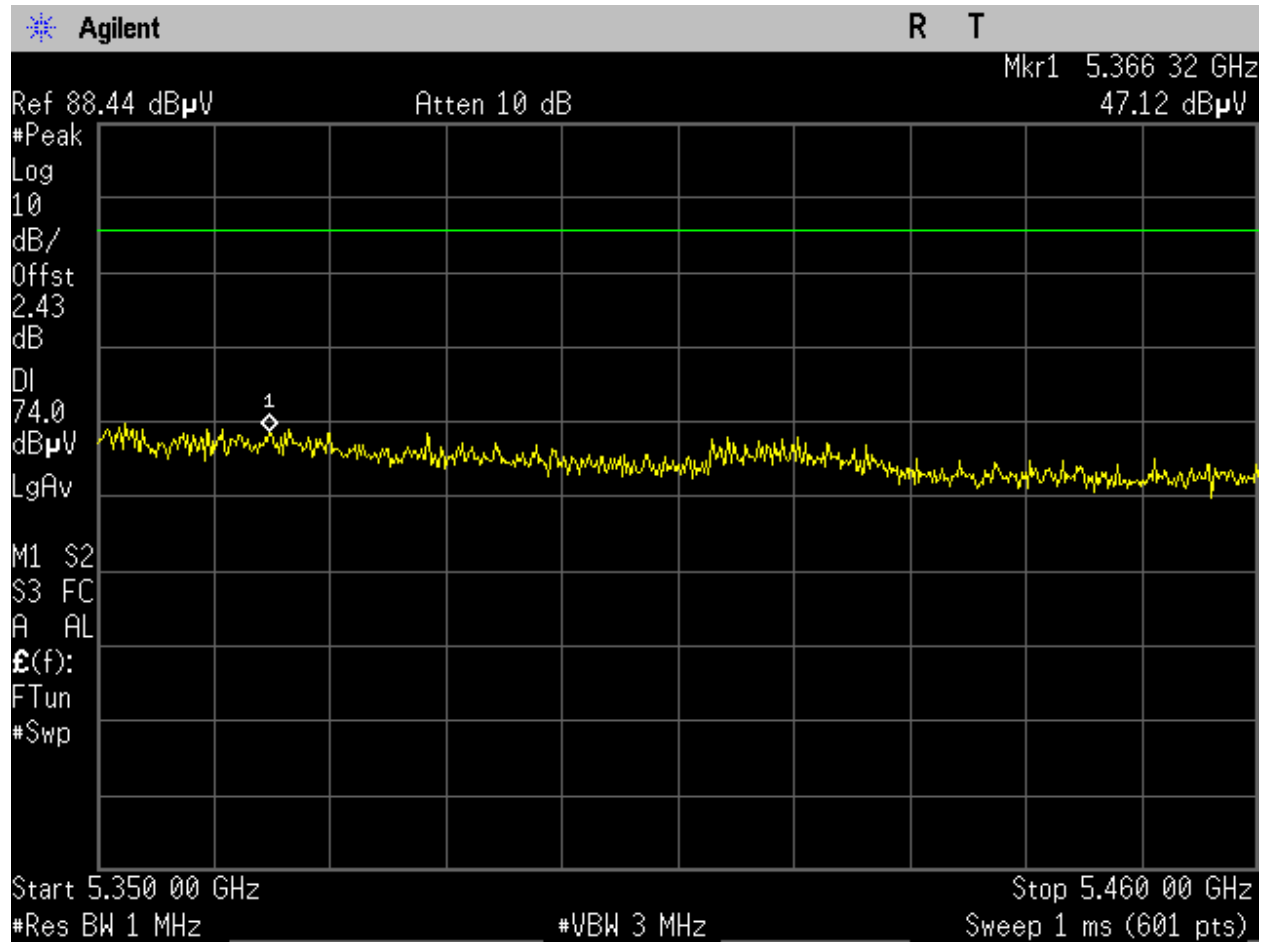


Figure 434: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_Upper Band Edge Peak_Port 2.

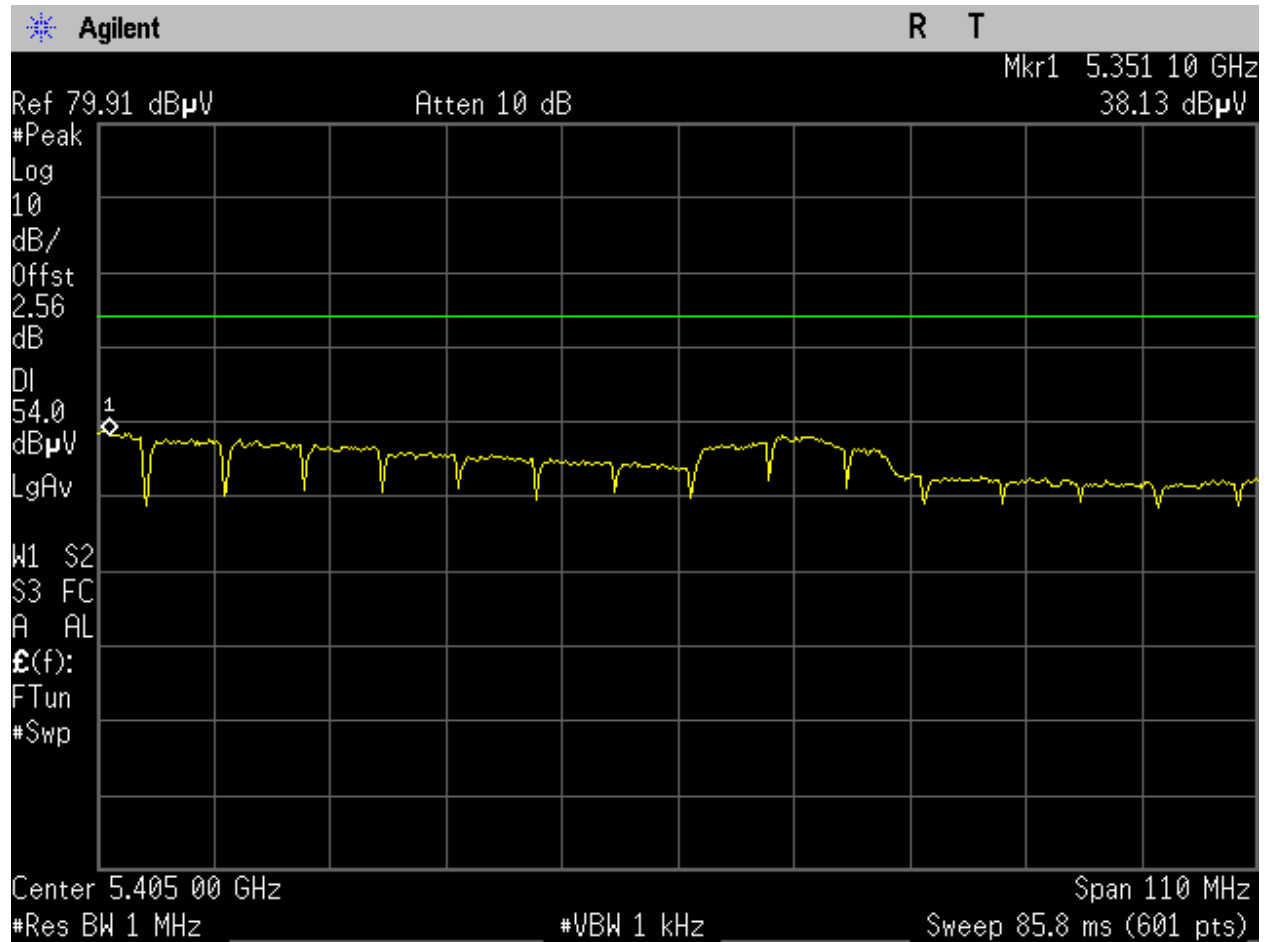


Figure 435: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 1.

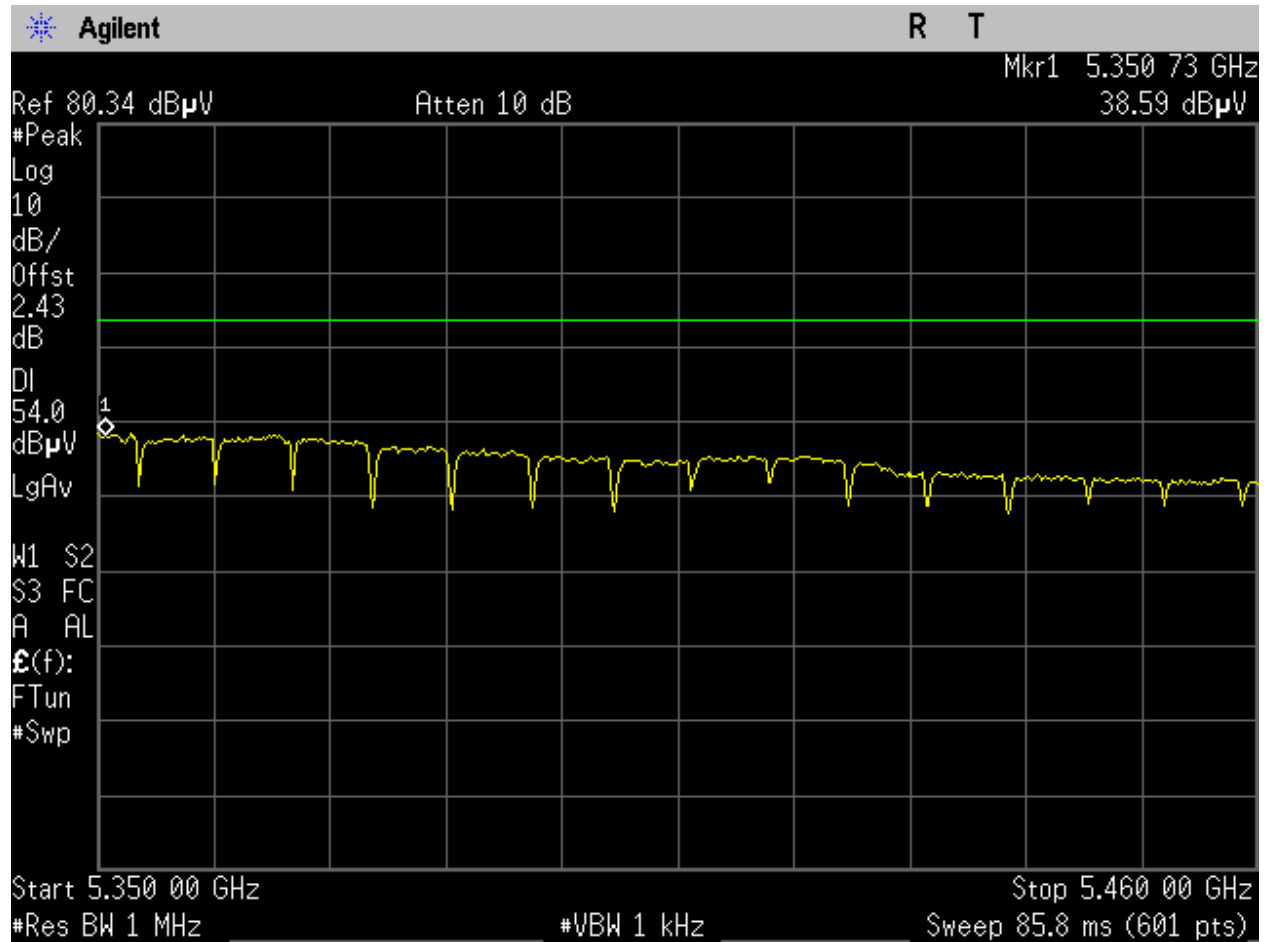


Figure 436: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 2.

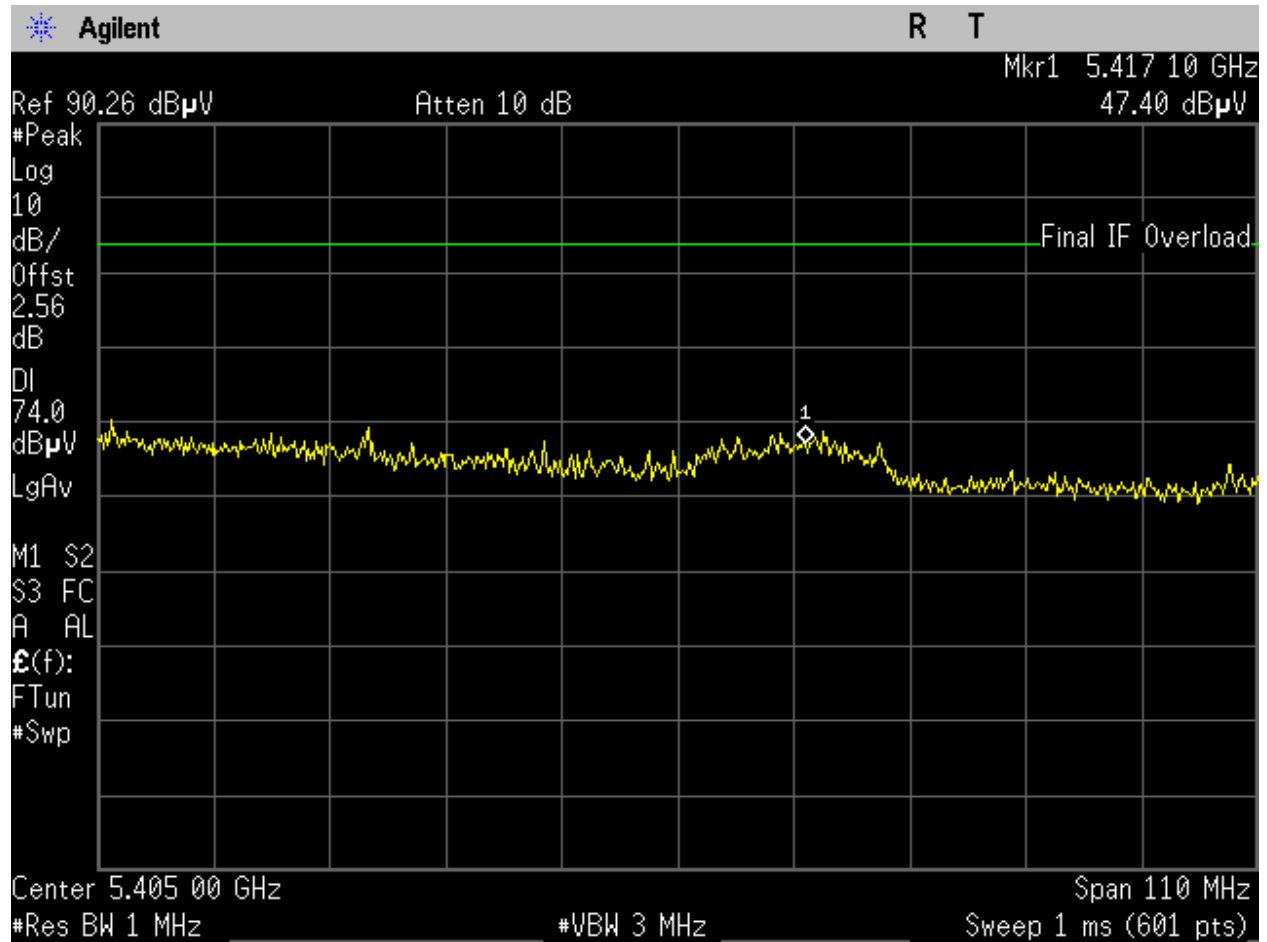


Figure 437: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 1.

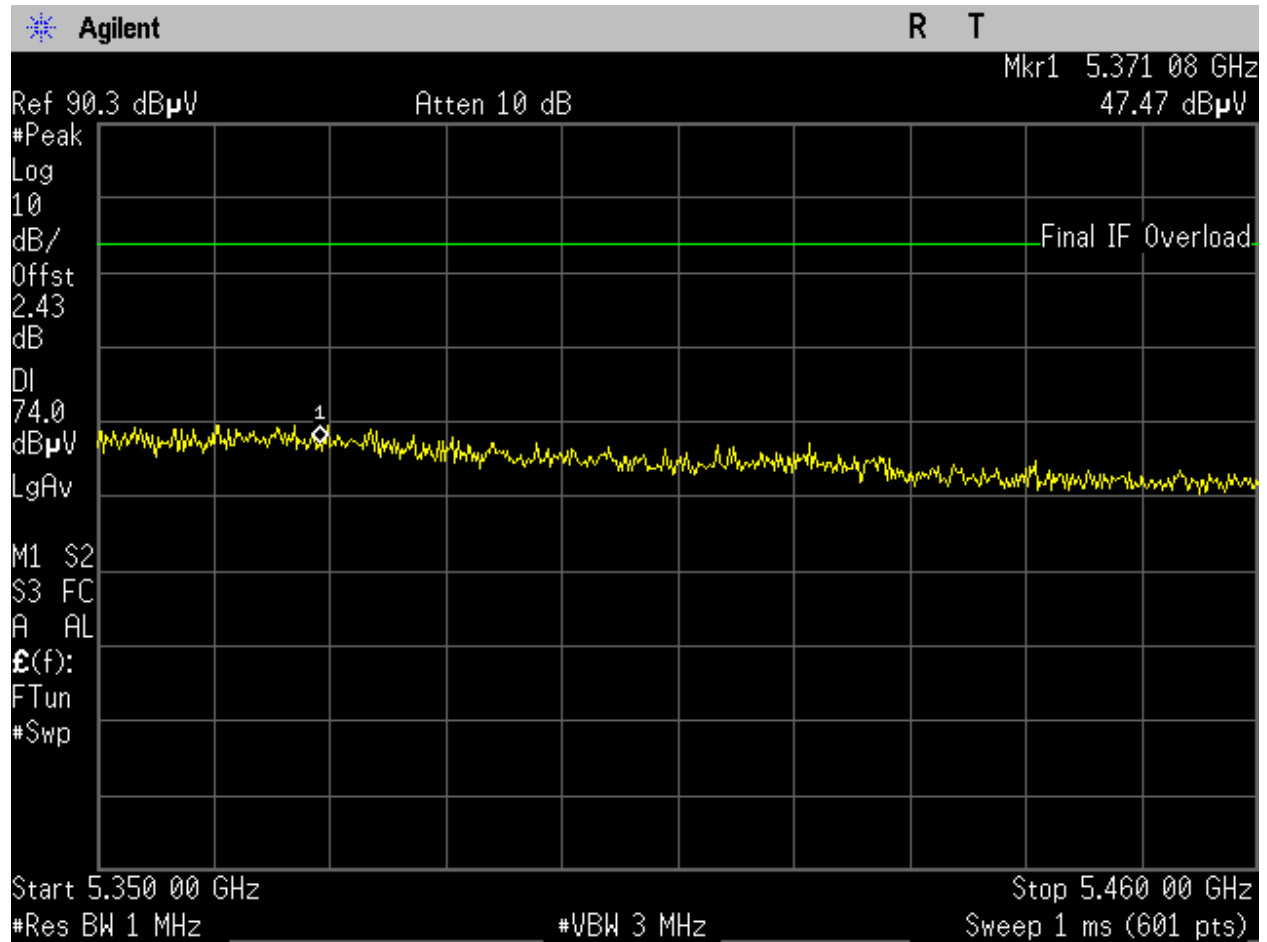


Figure 438: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 2.

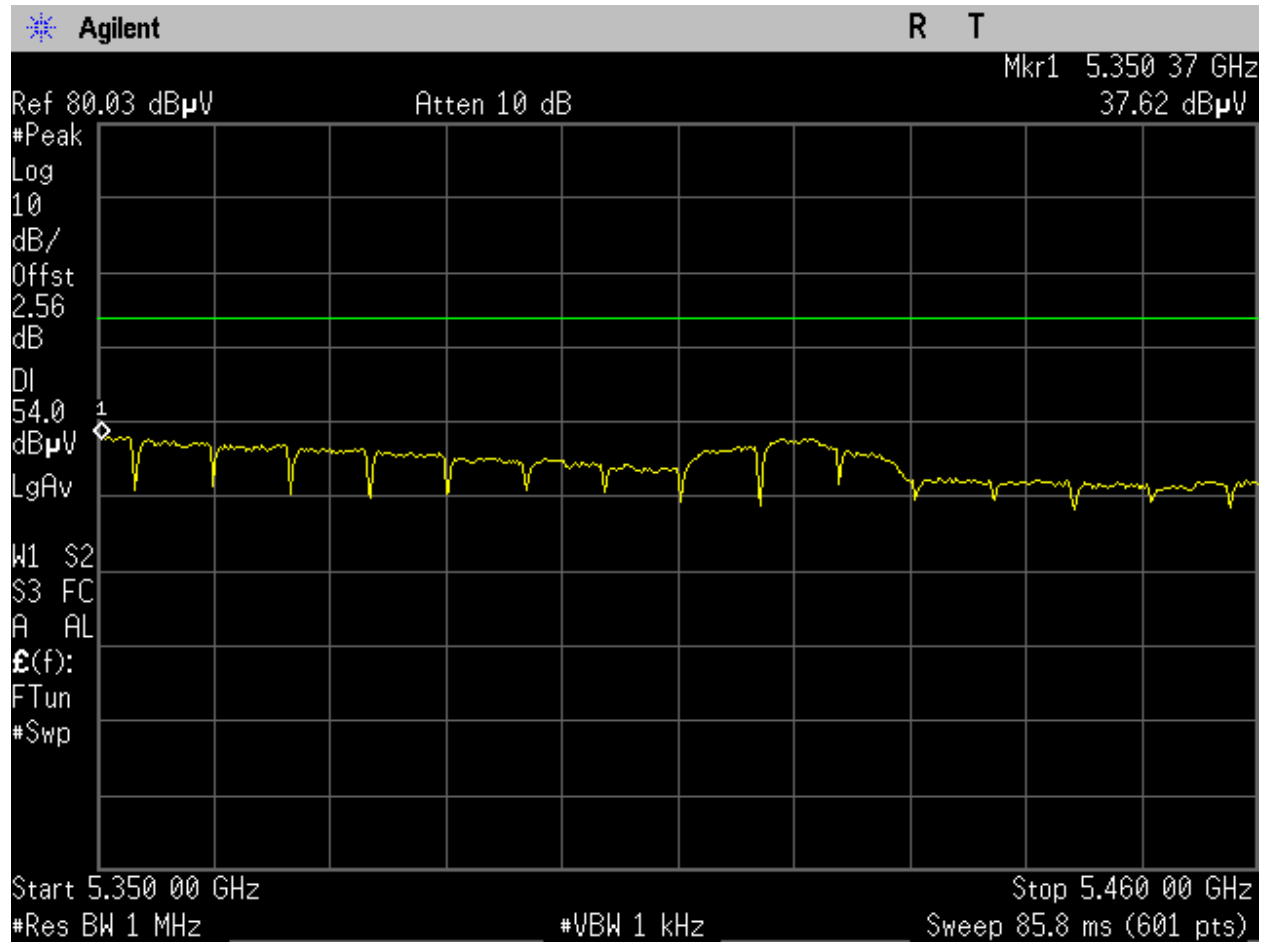


Figure 439: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_Upper Band Edge Avg_Port 1.

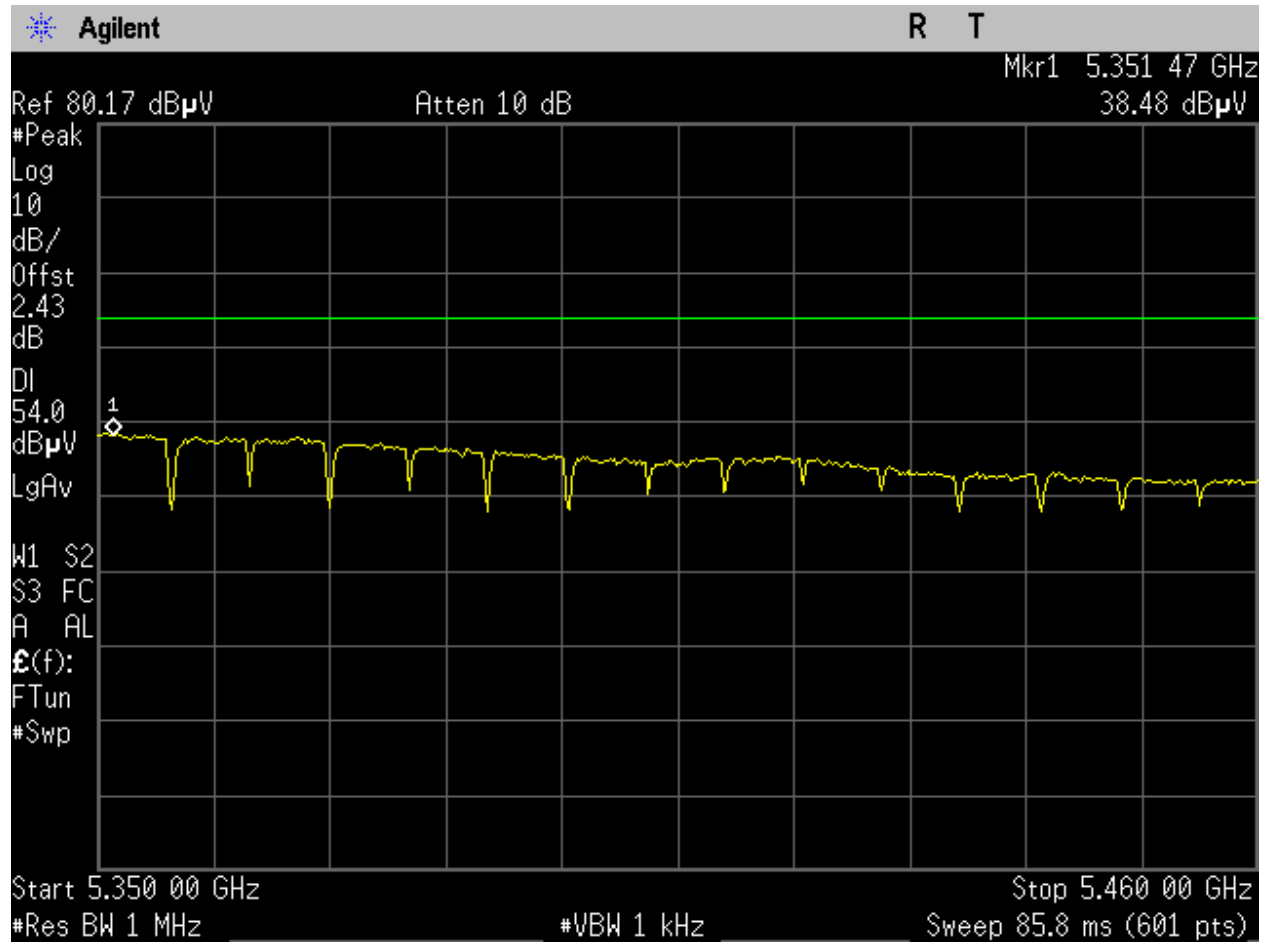


Figure 440: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_Upper Band Edge Avg_Port 2.

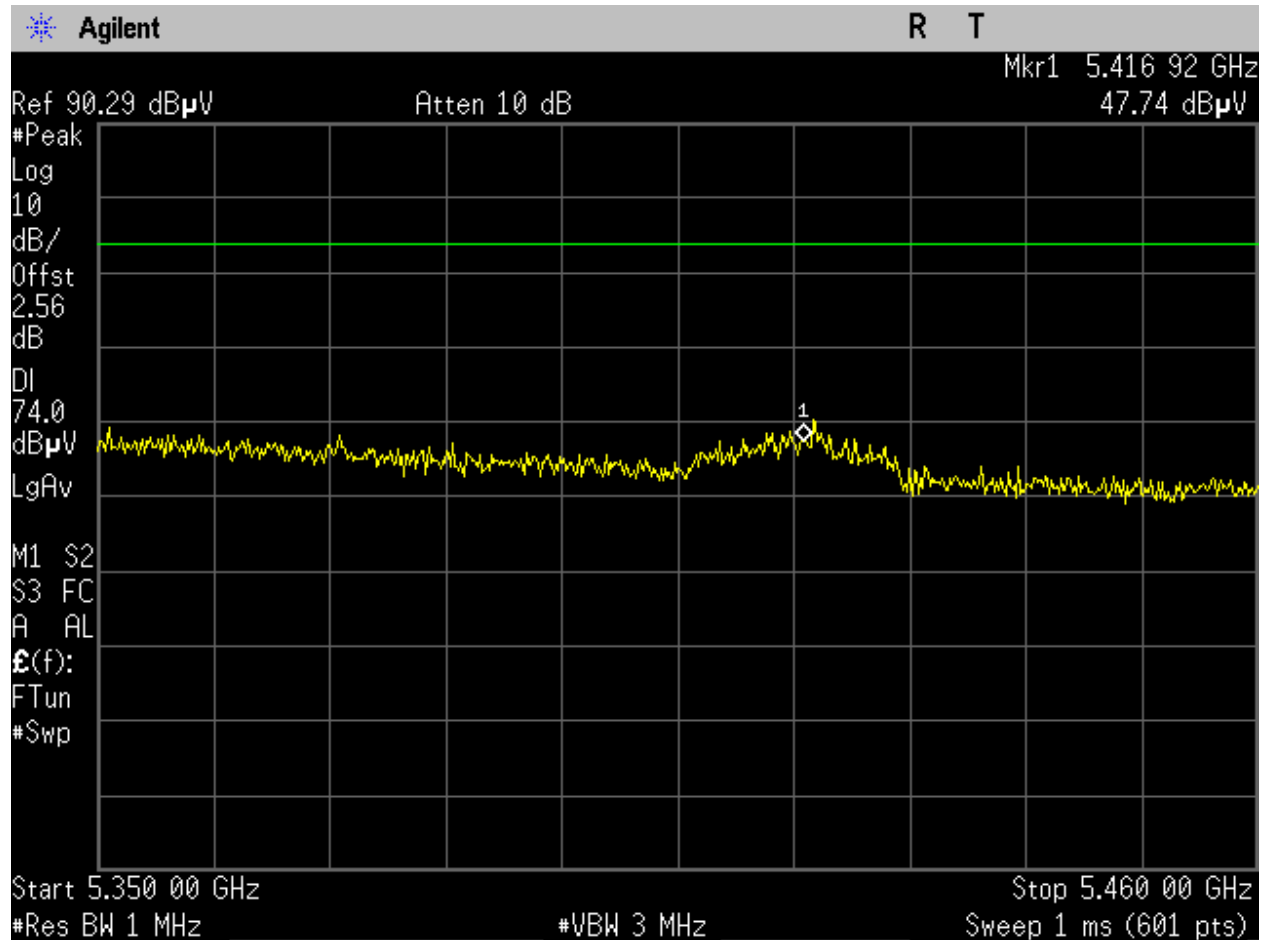


Figure 441: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_Upper Band Edge Peak_Port 1.

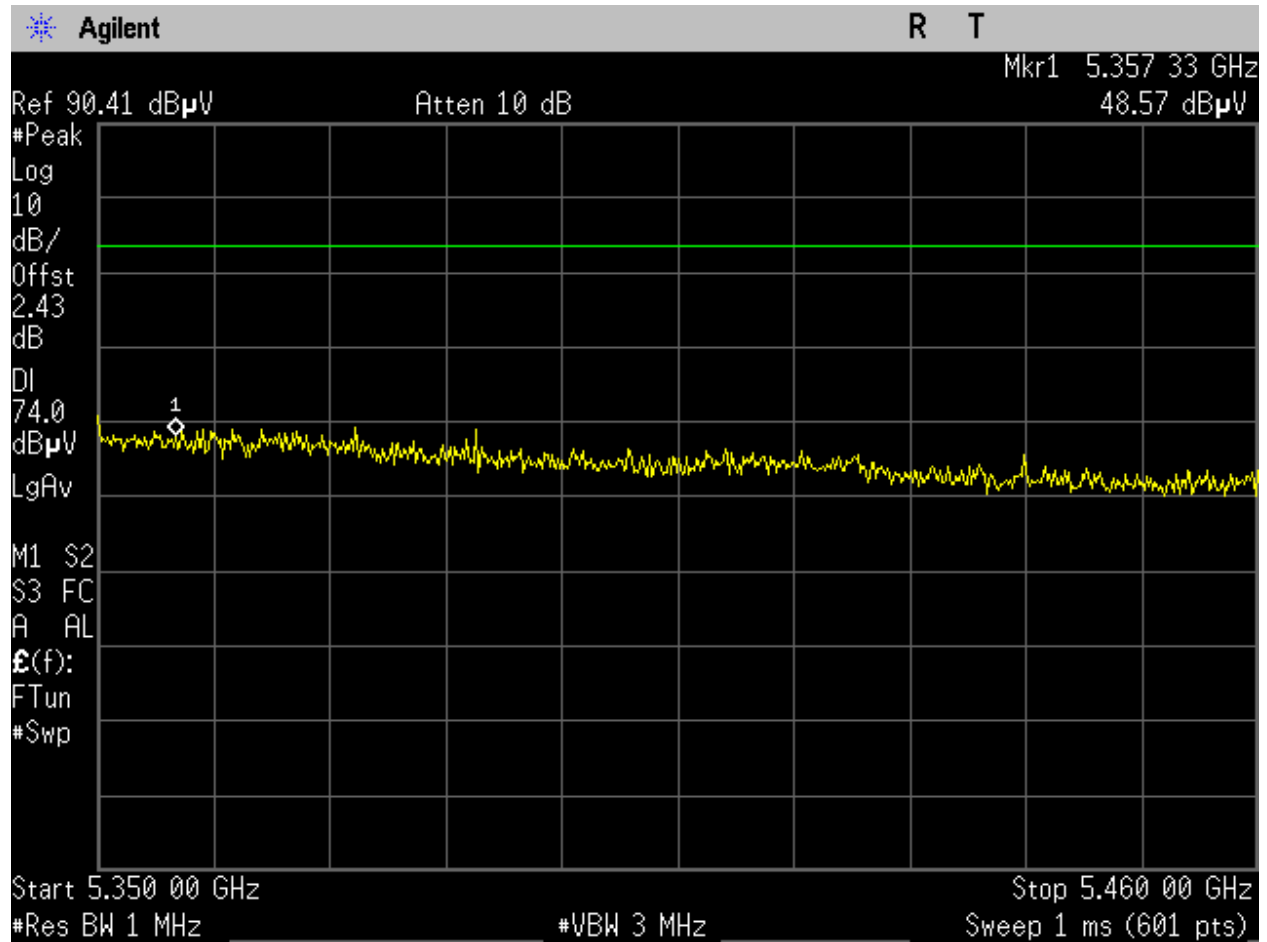


Figure 442: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_Upper Band Edge Peak_Port 2.

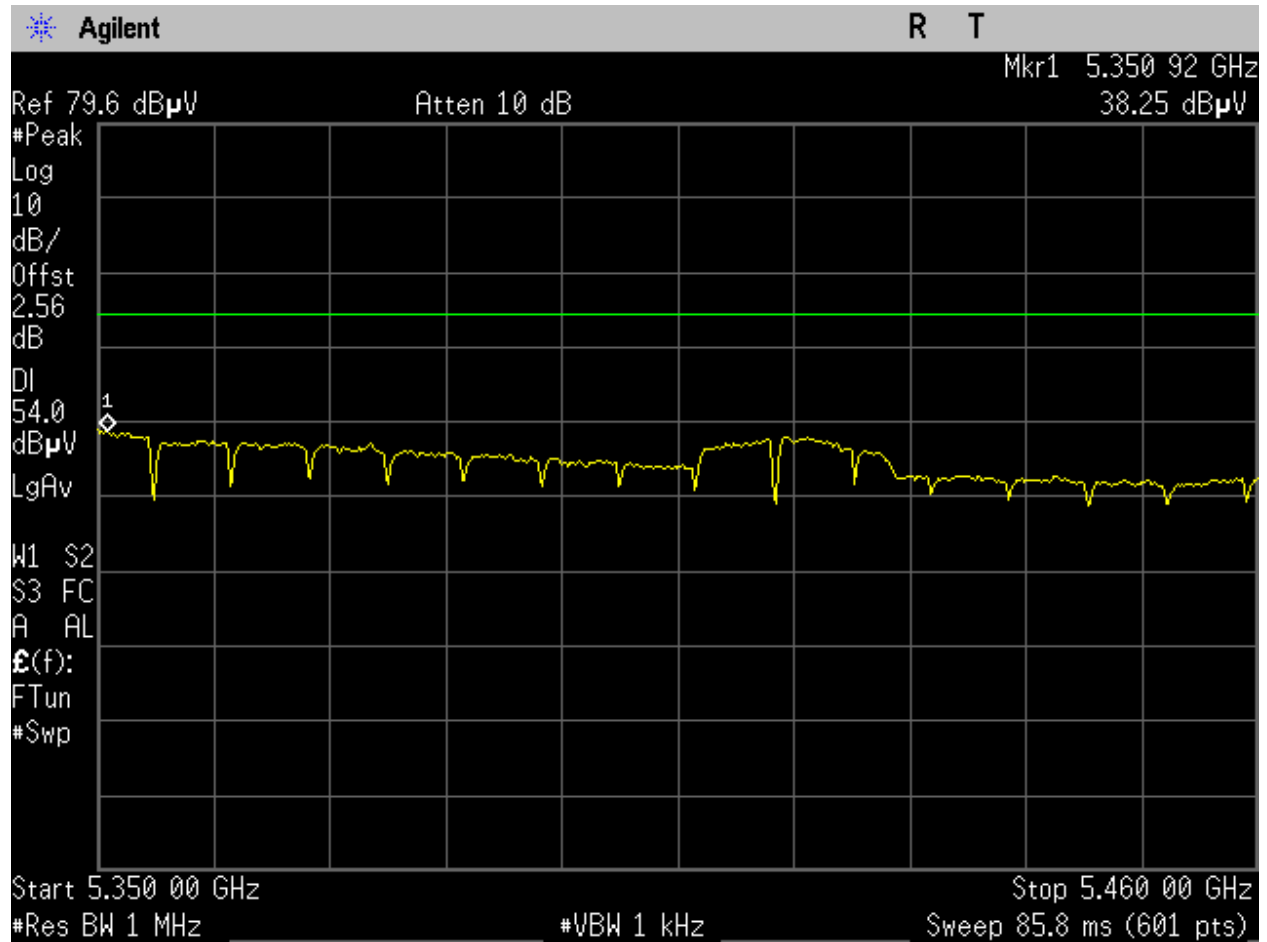


Figure 443: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_Upper Band Edge Avg_Port 1.

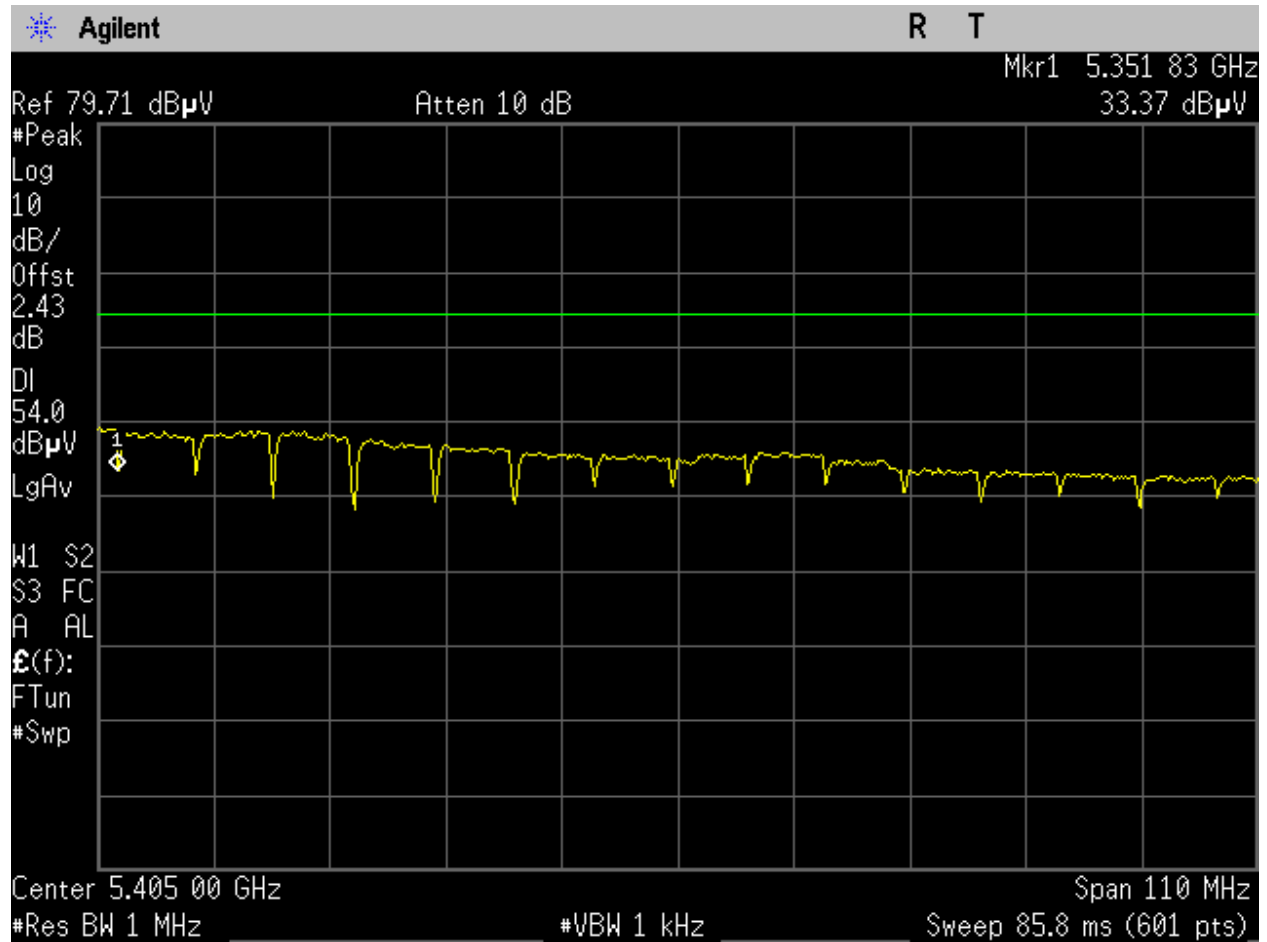


Figure 444: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_Upper Band Edge Avg_Port 2.

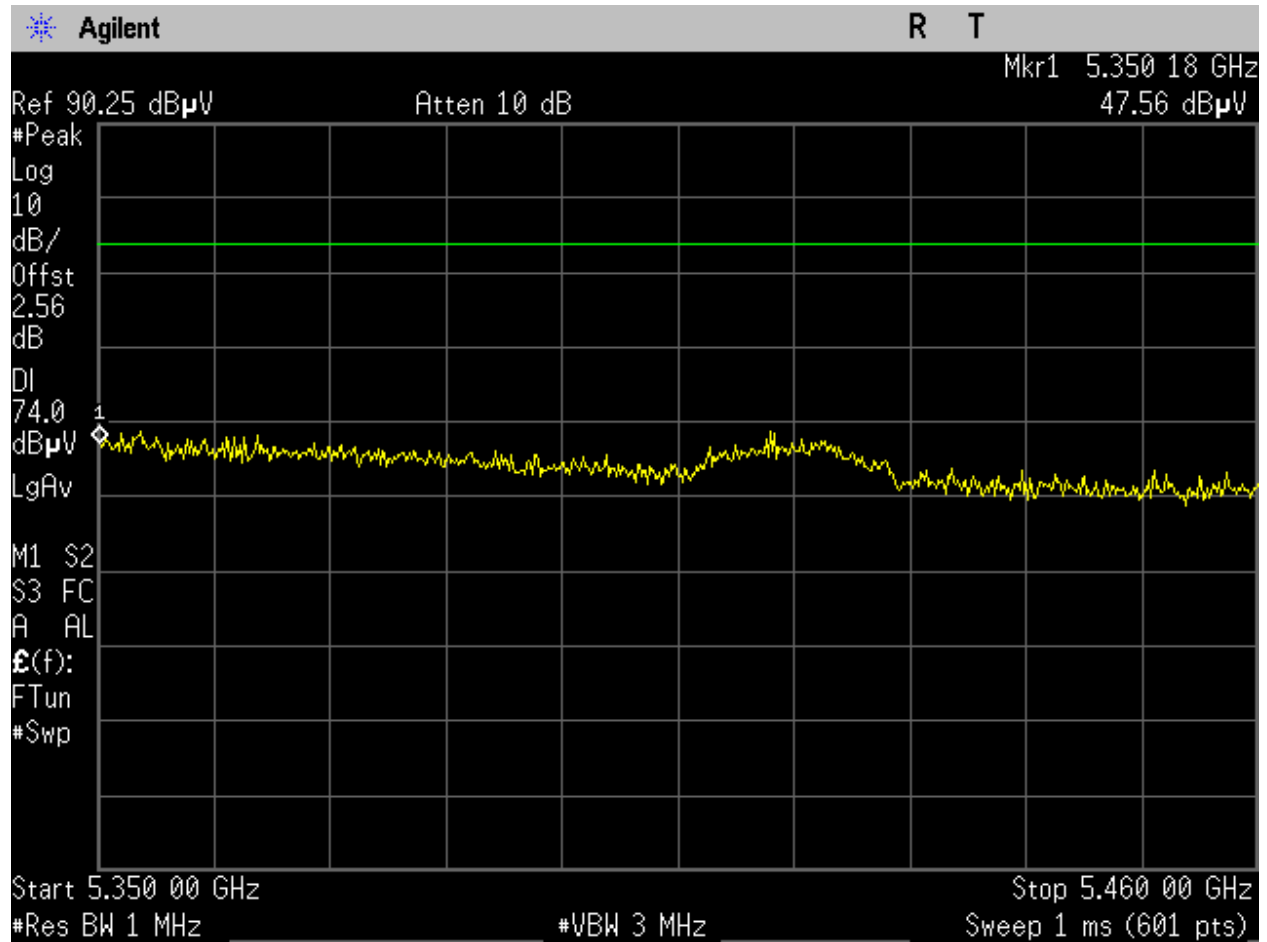


Figure 445: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_Upper Band Edge Peak_Port 1.

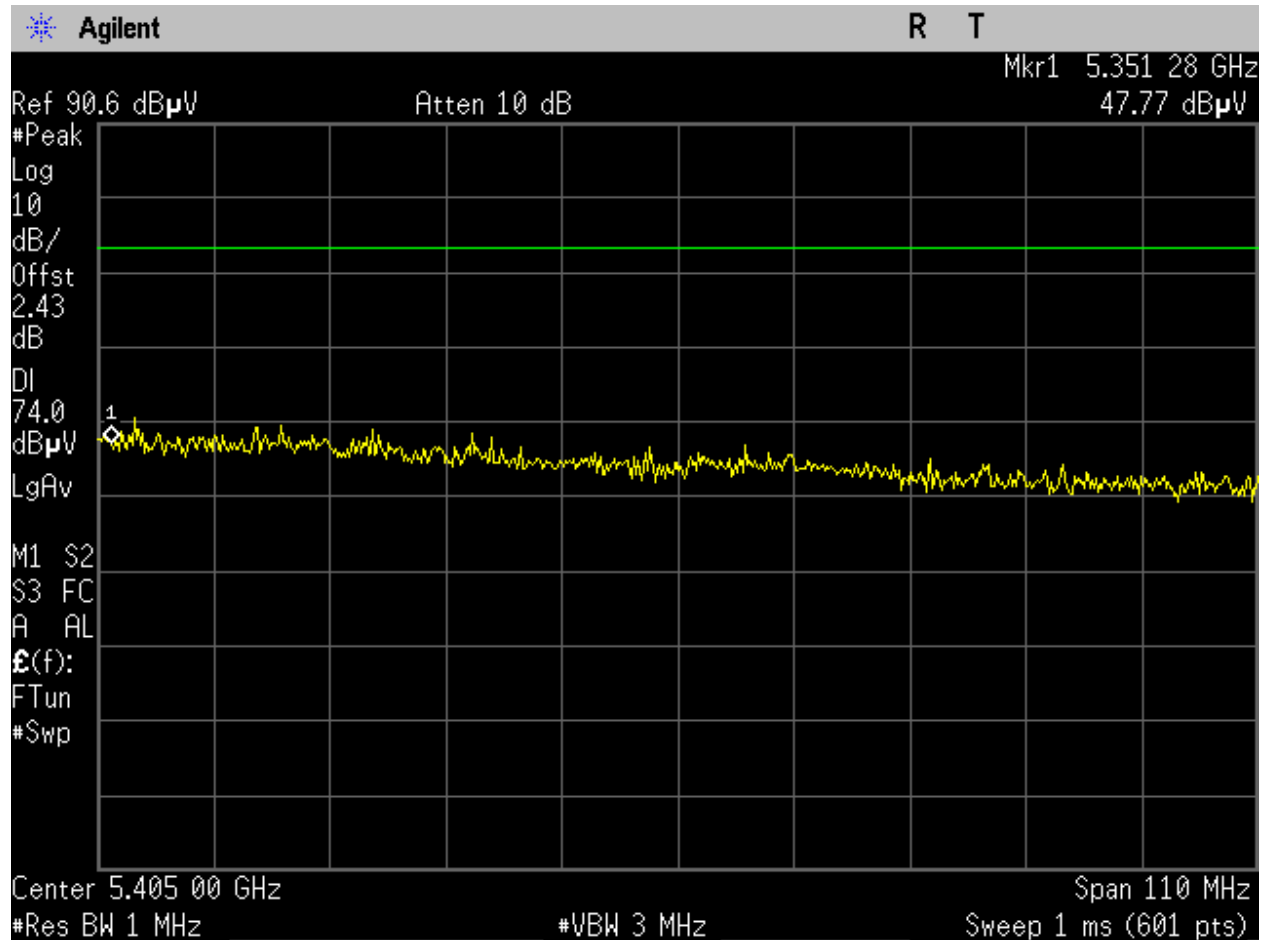


Figure 446: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_Upper Band Edge Peak_Port 2.

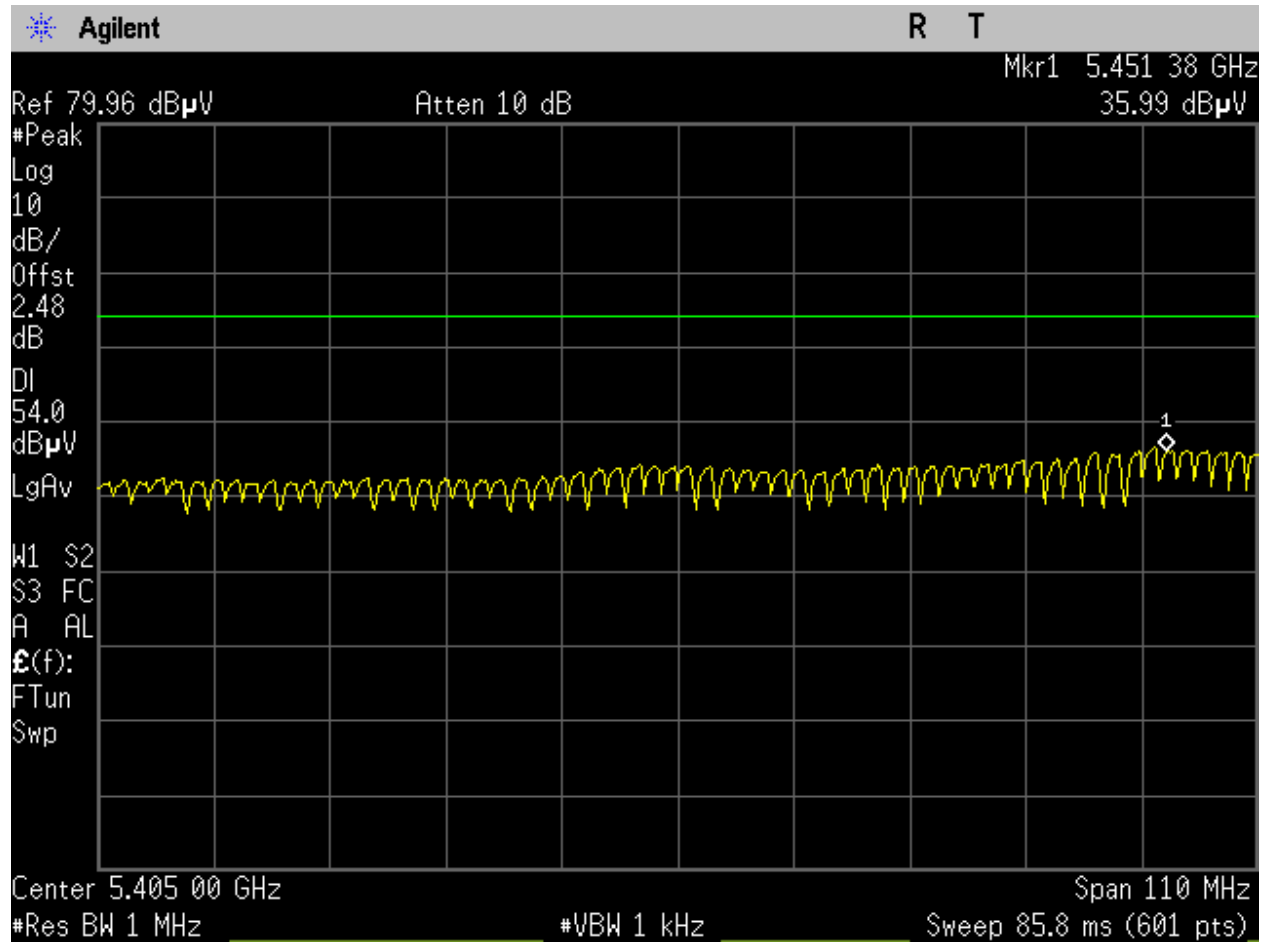


Figure 447: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_Lower Band Edge Avg_Port 1.

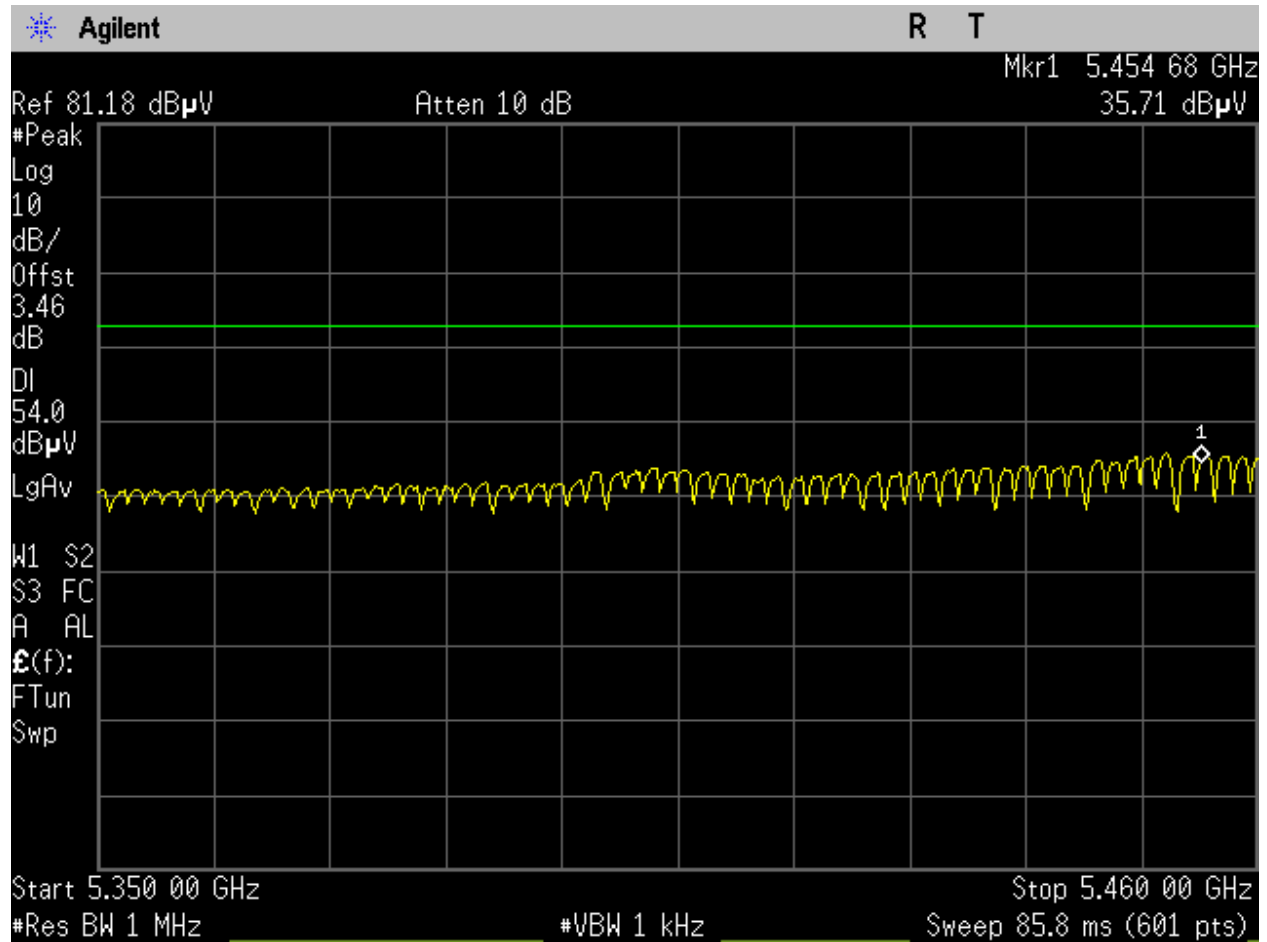


Figure 448: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_Lower Band Edge Avg_Port 2.

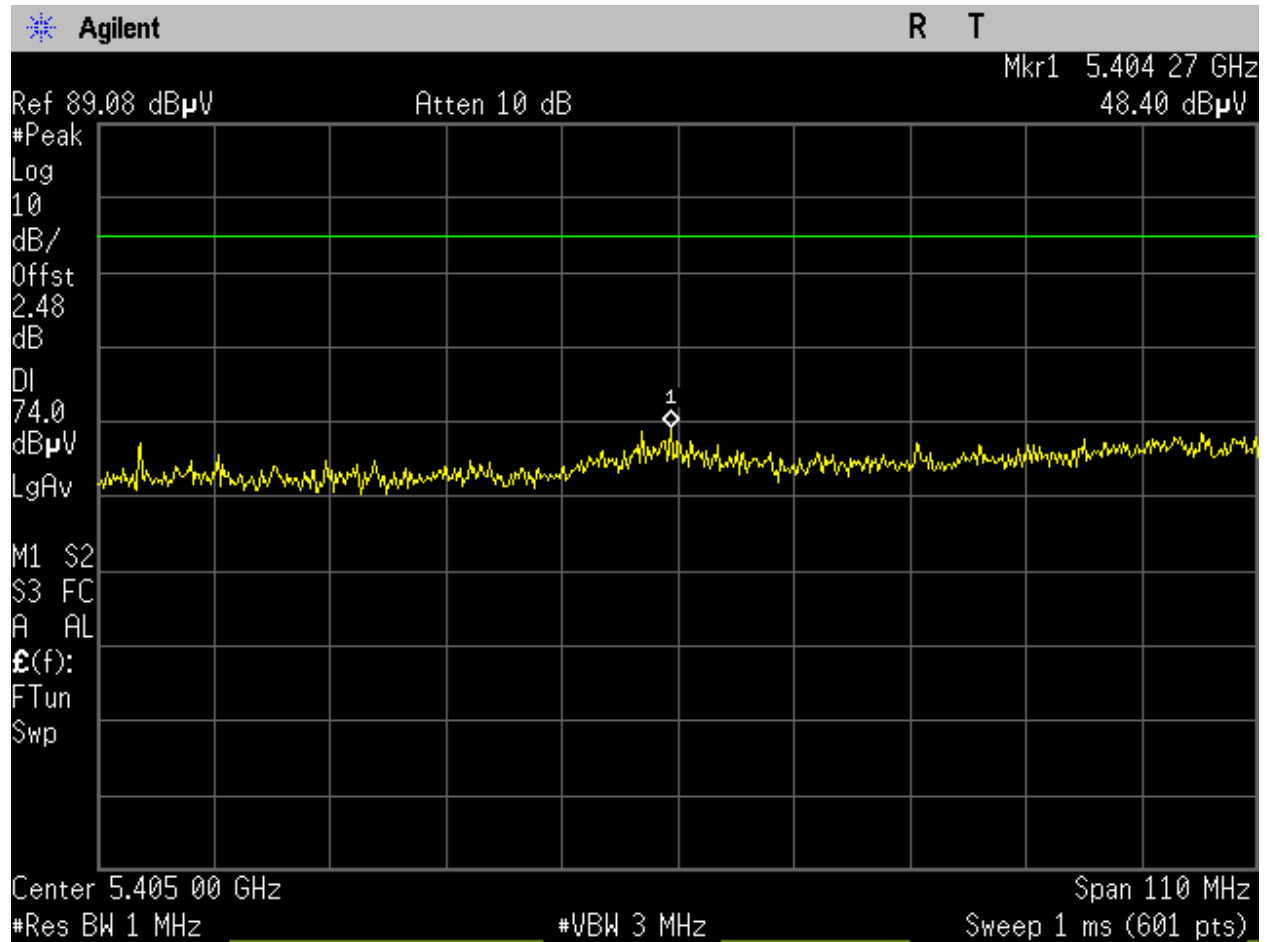


Figure 449: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_Lower Band Edge Peak_Port 1.

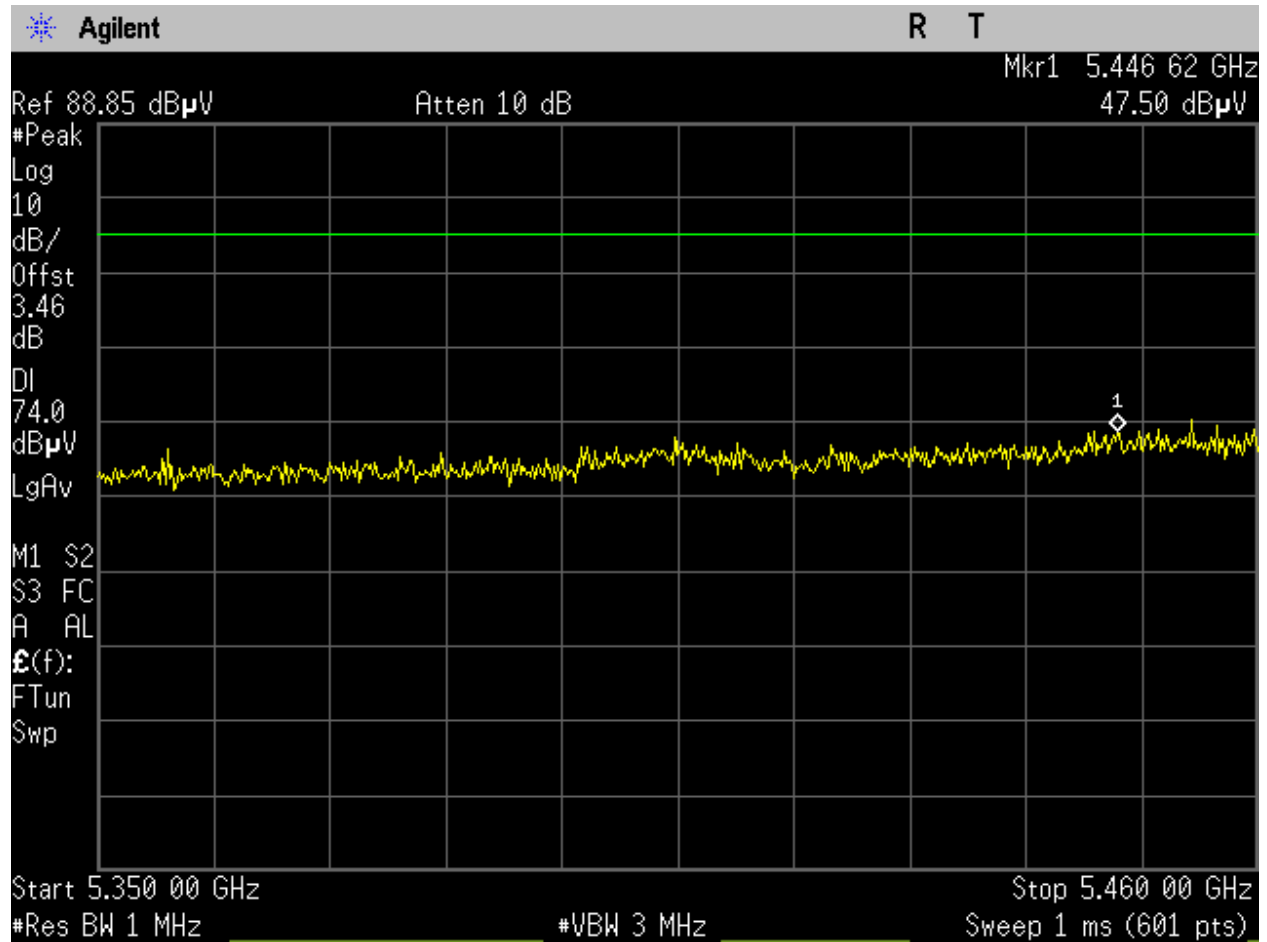


Figure 450: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_Lower Band Edge Peak_Port 2.

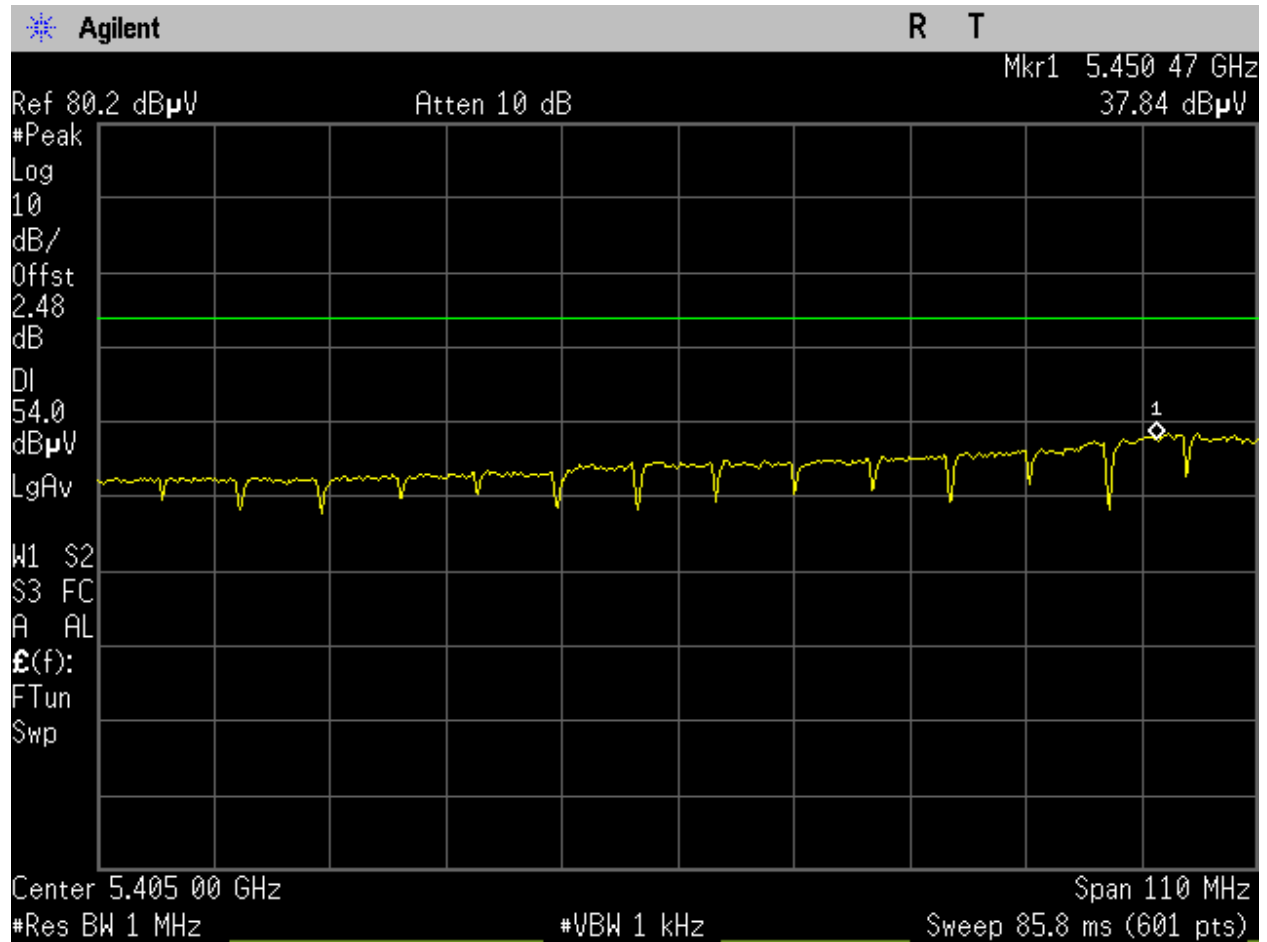


Figure 451: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 1.

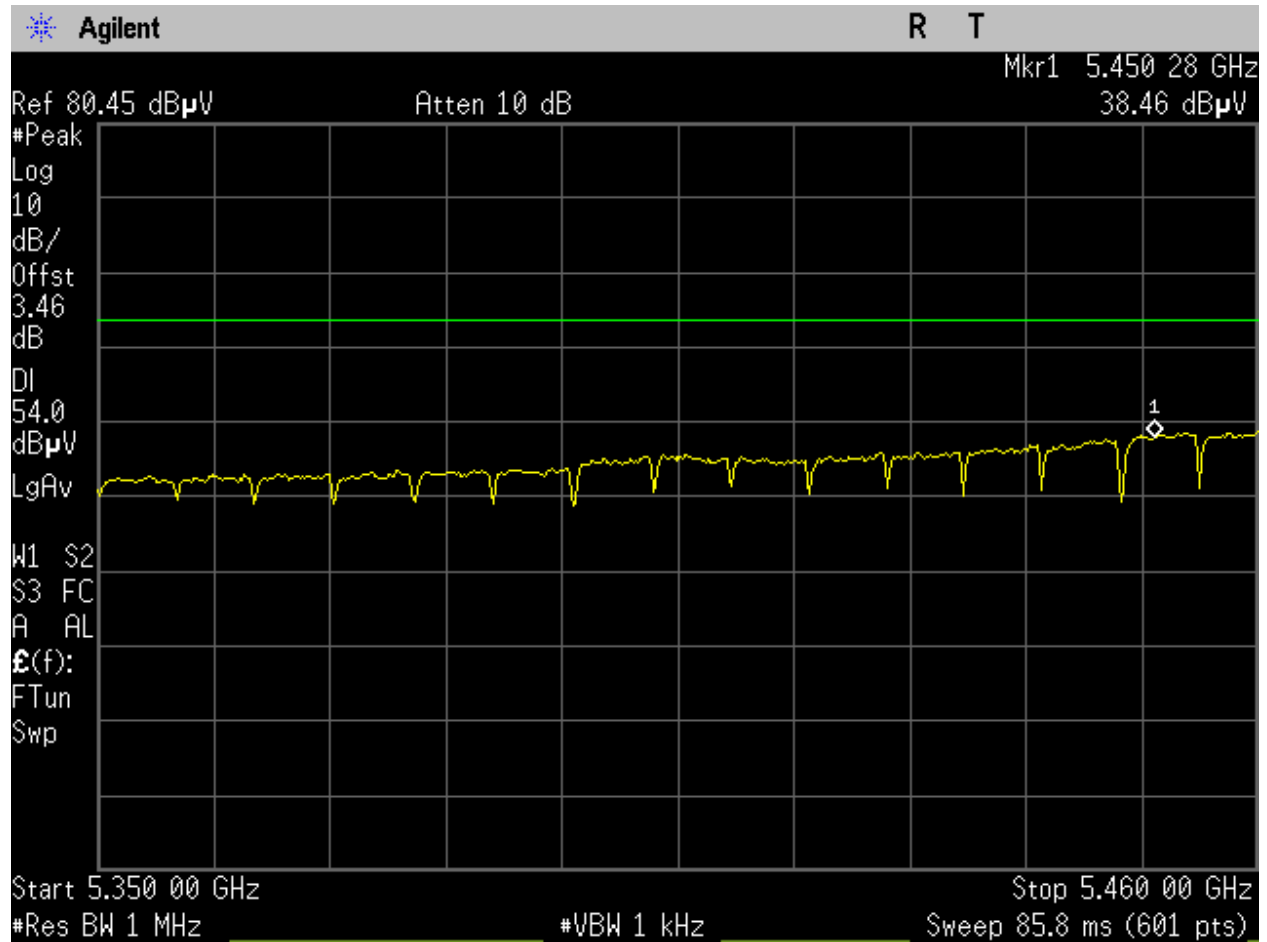


Figure 452: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 2.

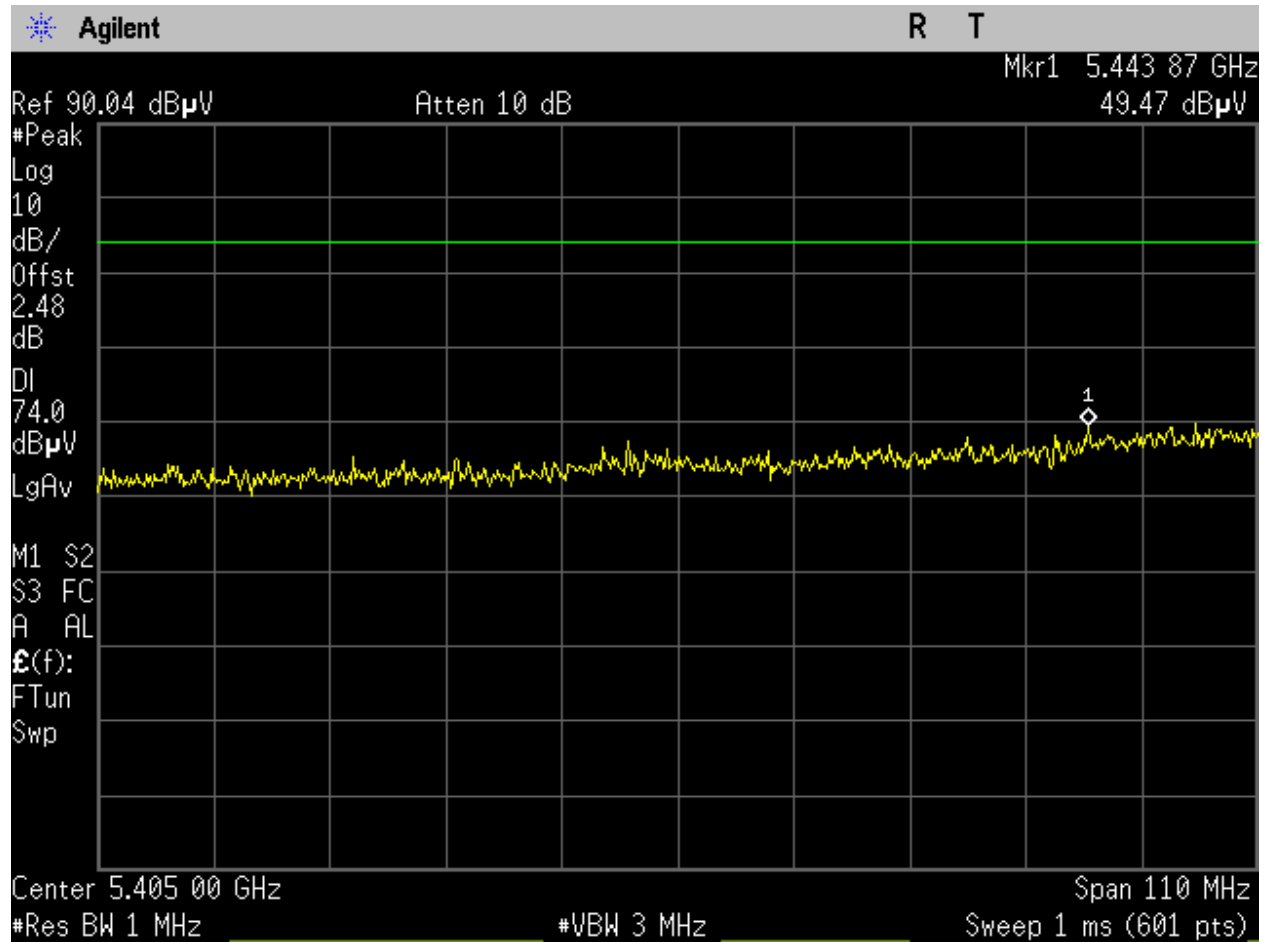


Figure 453: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 1.

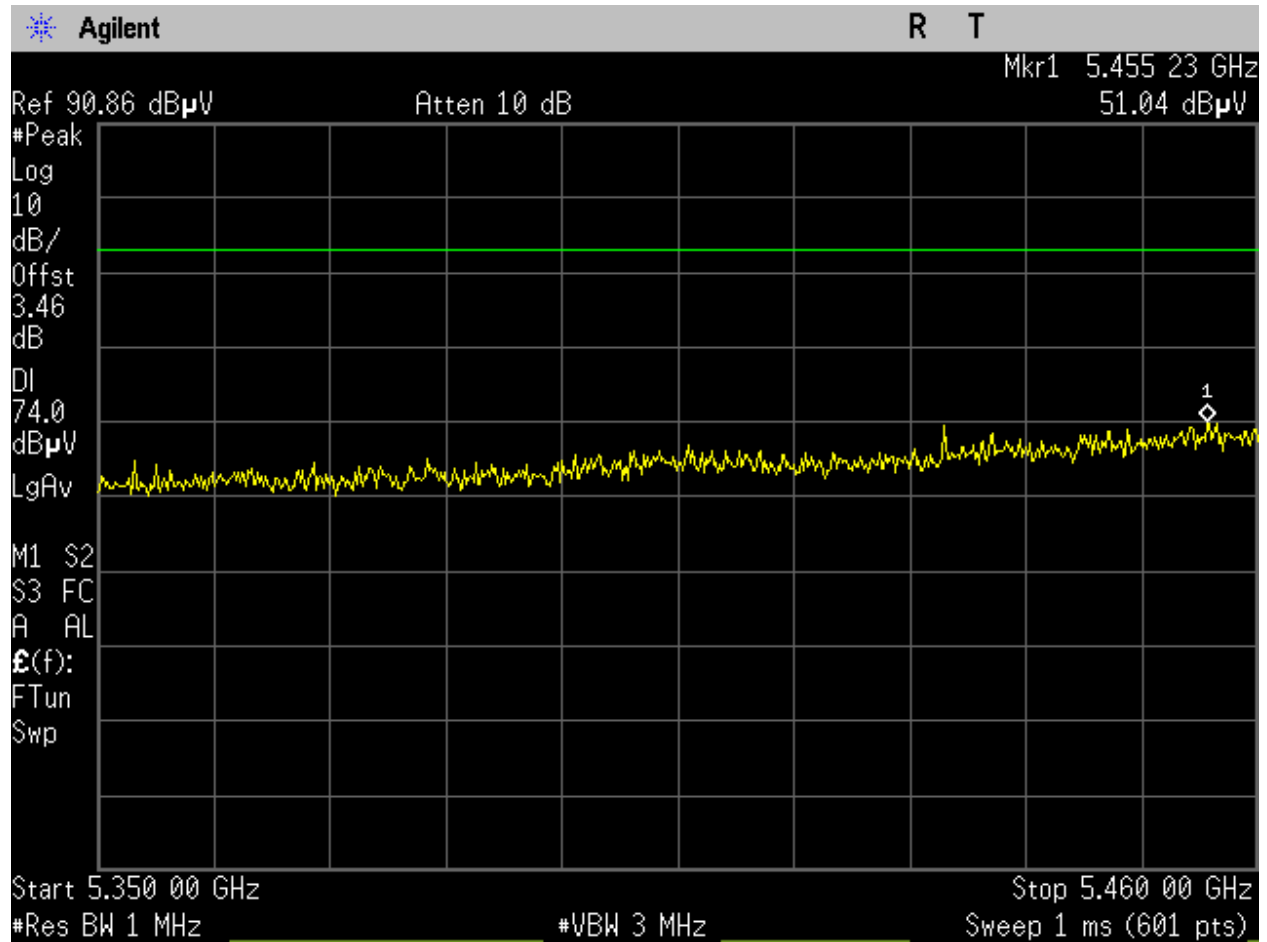


Figure 454: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 2.

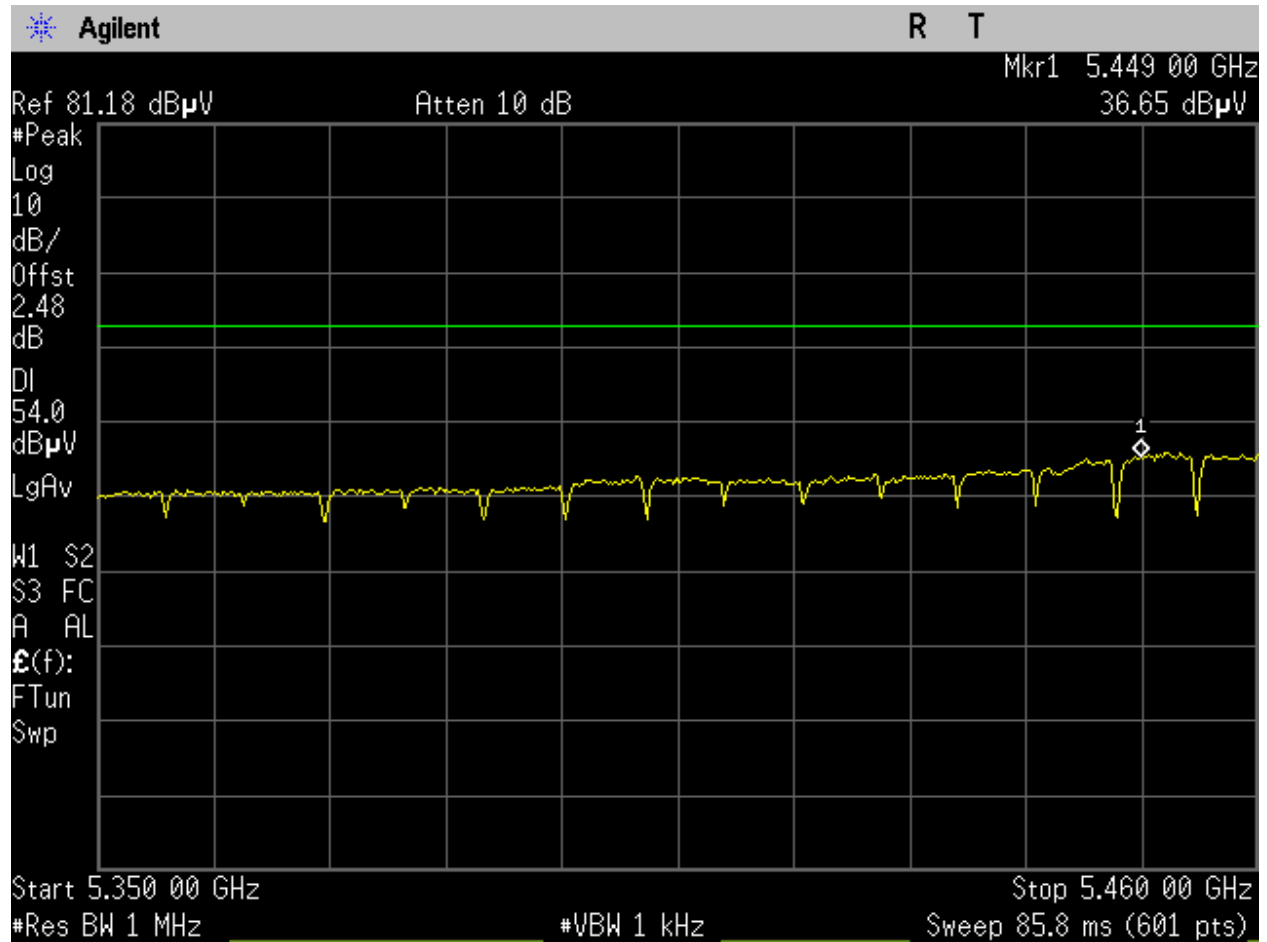


Figure 455: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 1.

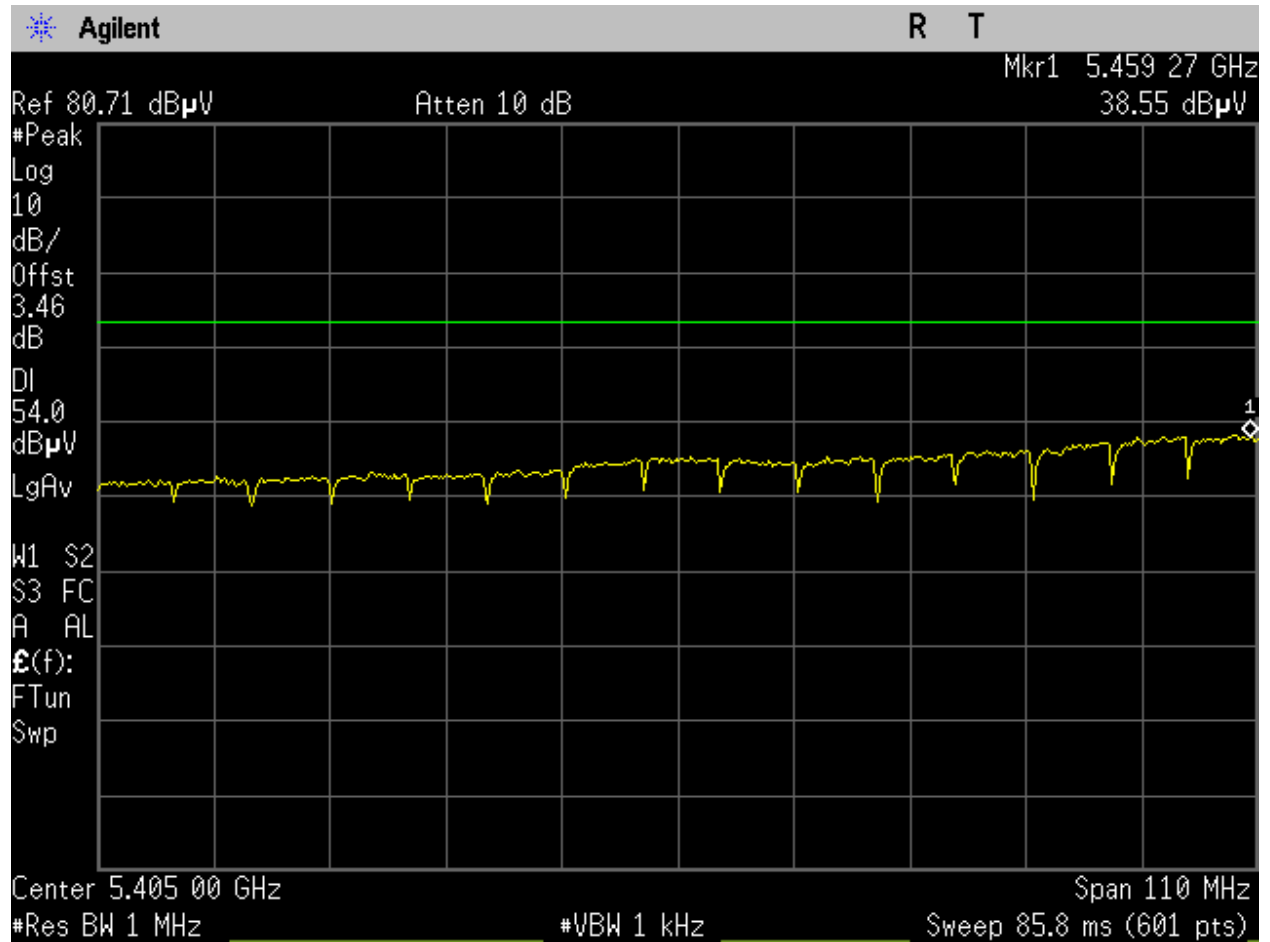


Figure 456: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 2.

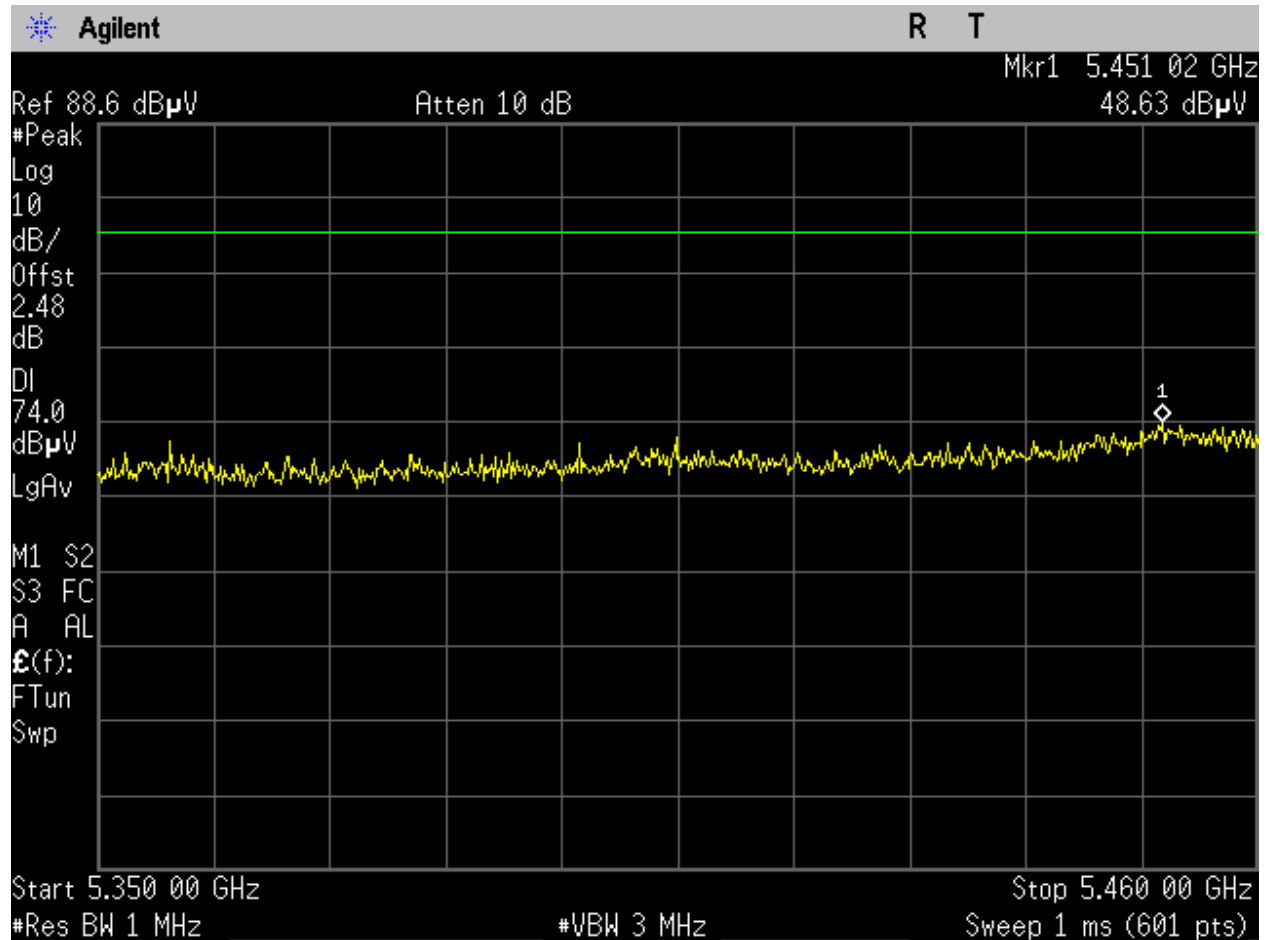


Figure 457: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 1.

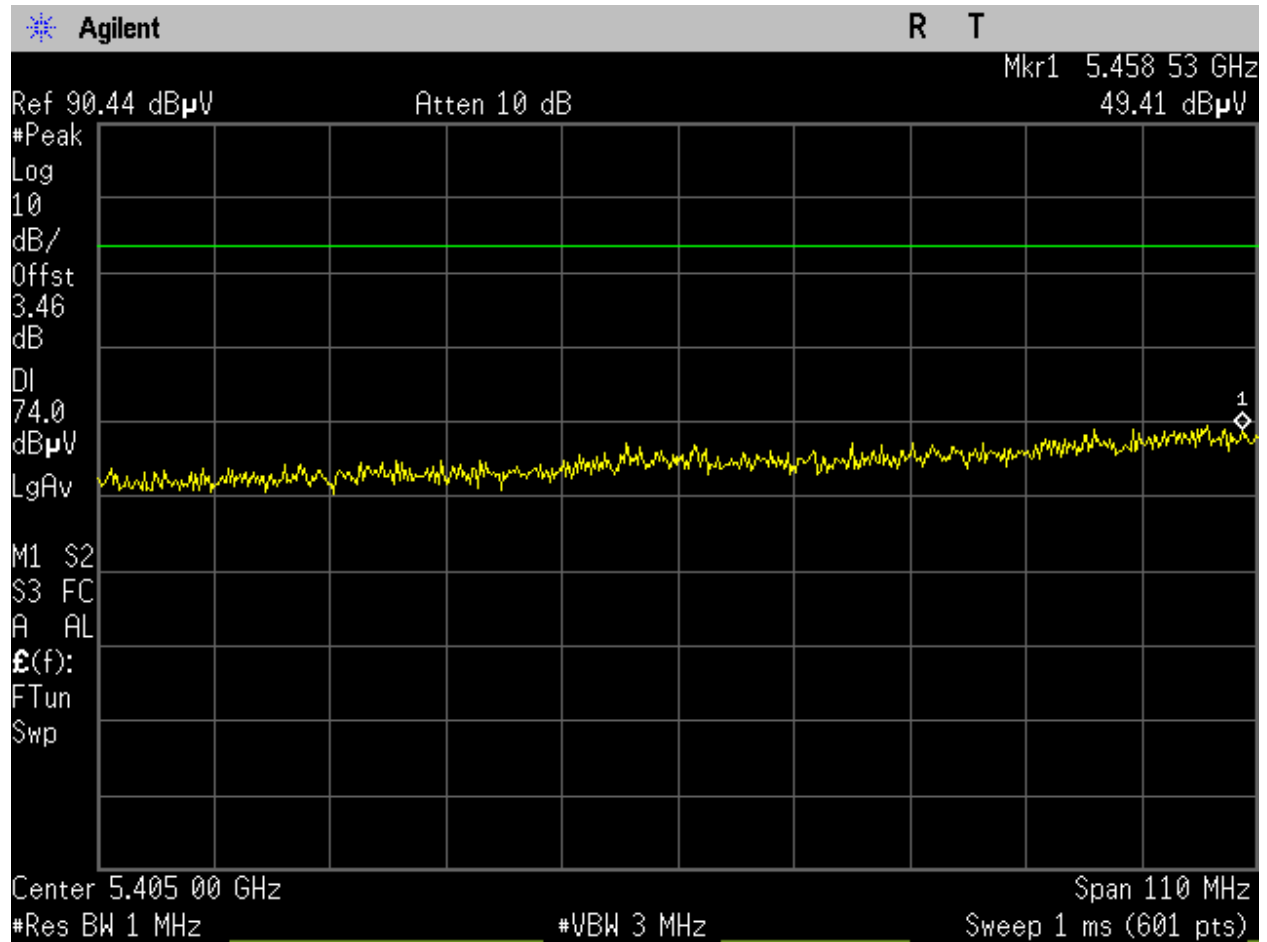


Figure 458: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 2.

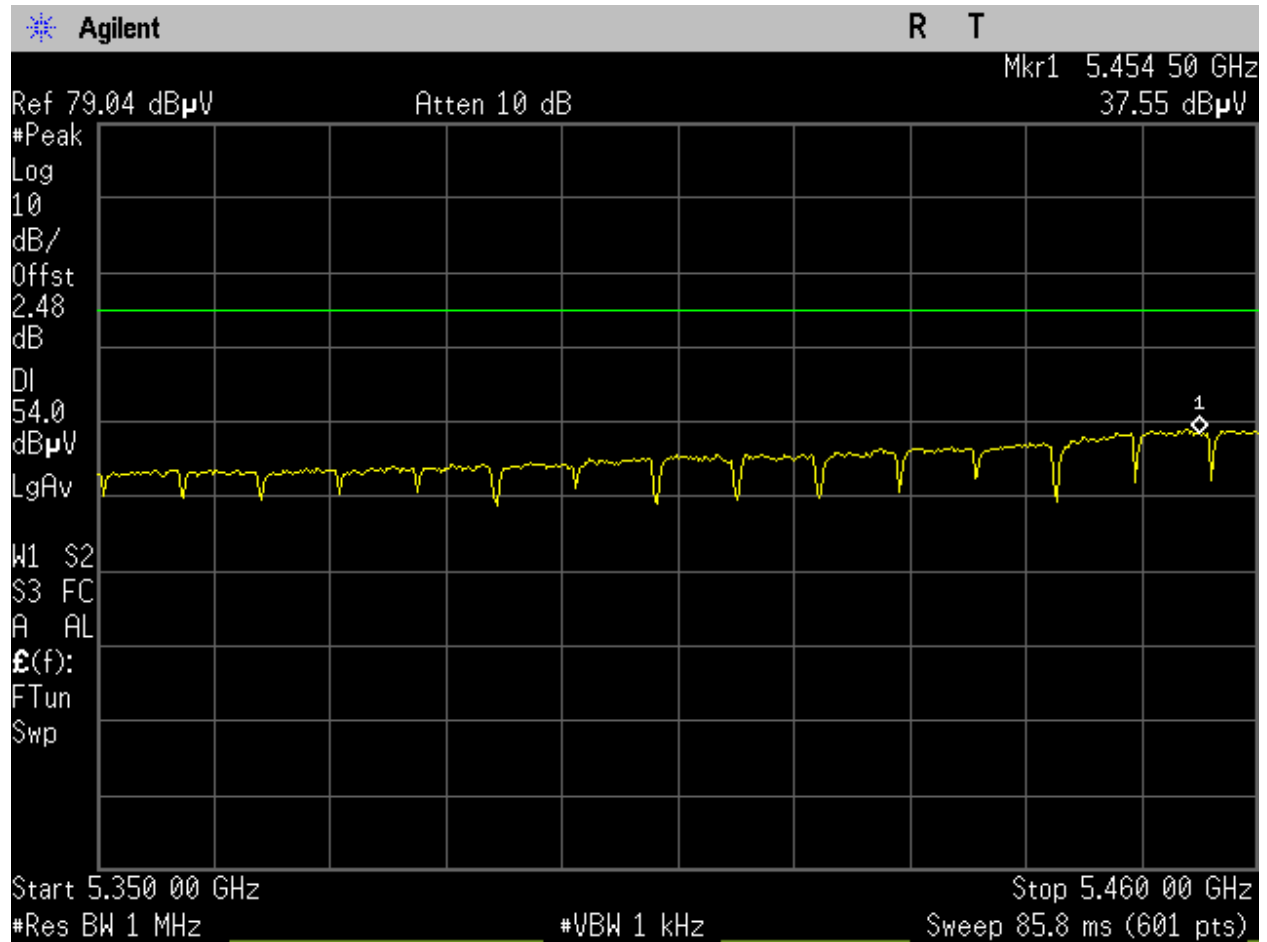


Figure 459: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_Lower Band Edge Avg_Port 1.

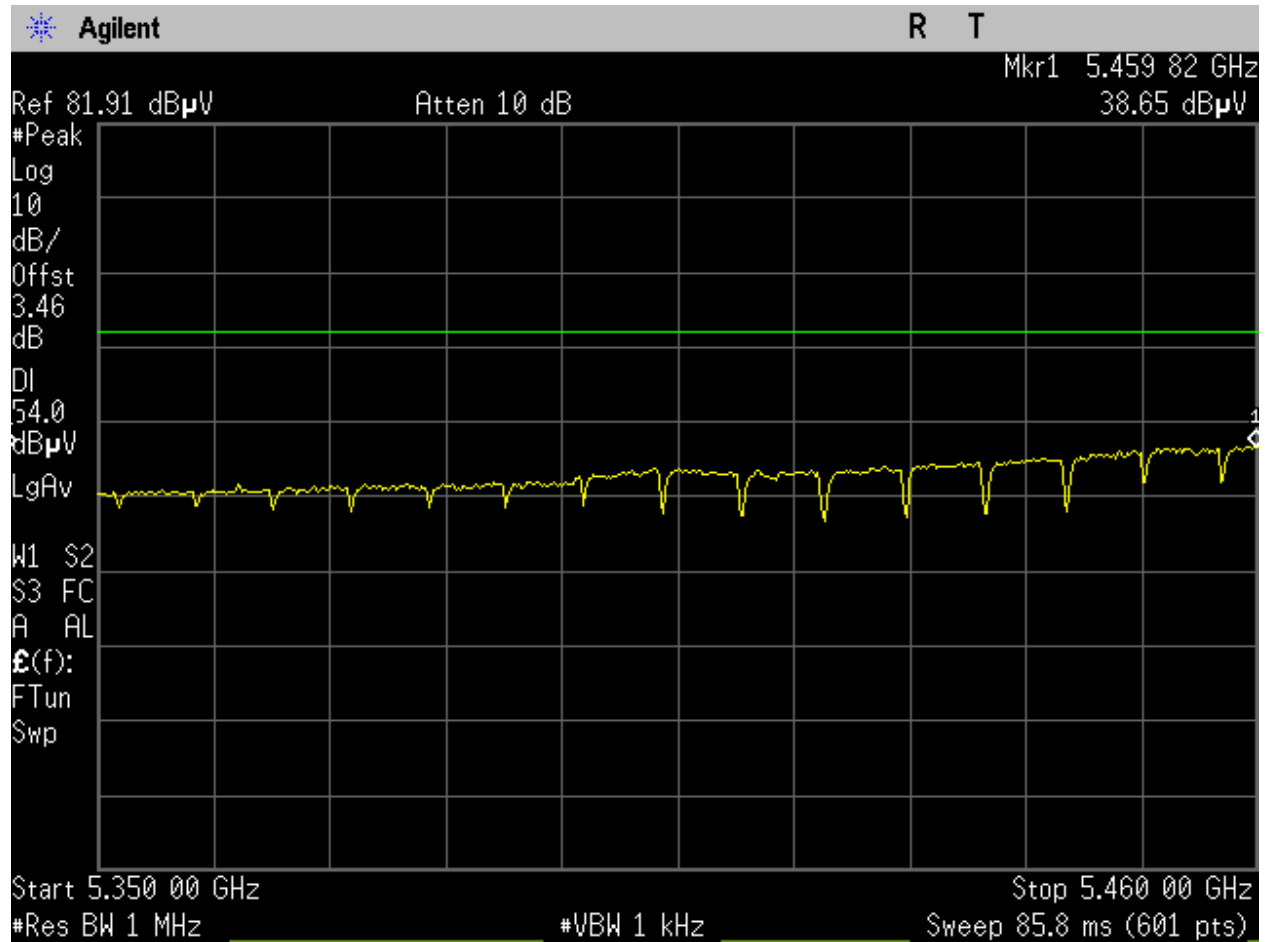


Figure 460: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_Lower Band Edge Avg_Port 2.

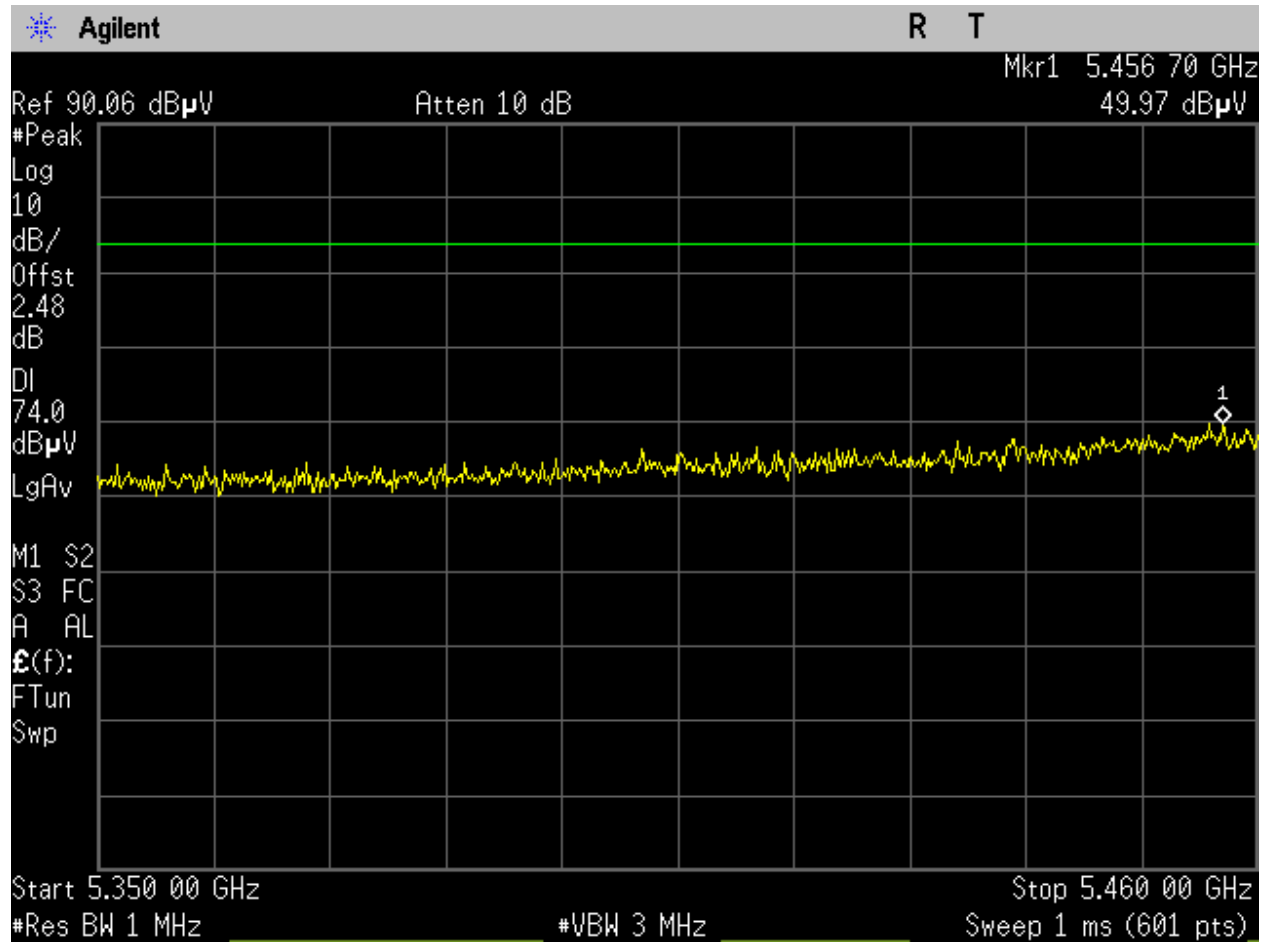


Figure 461: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_Lower Band Edge Peak_Port 1.

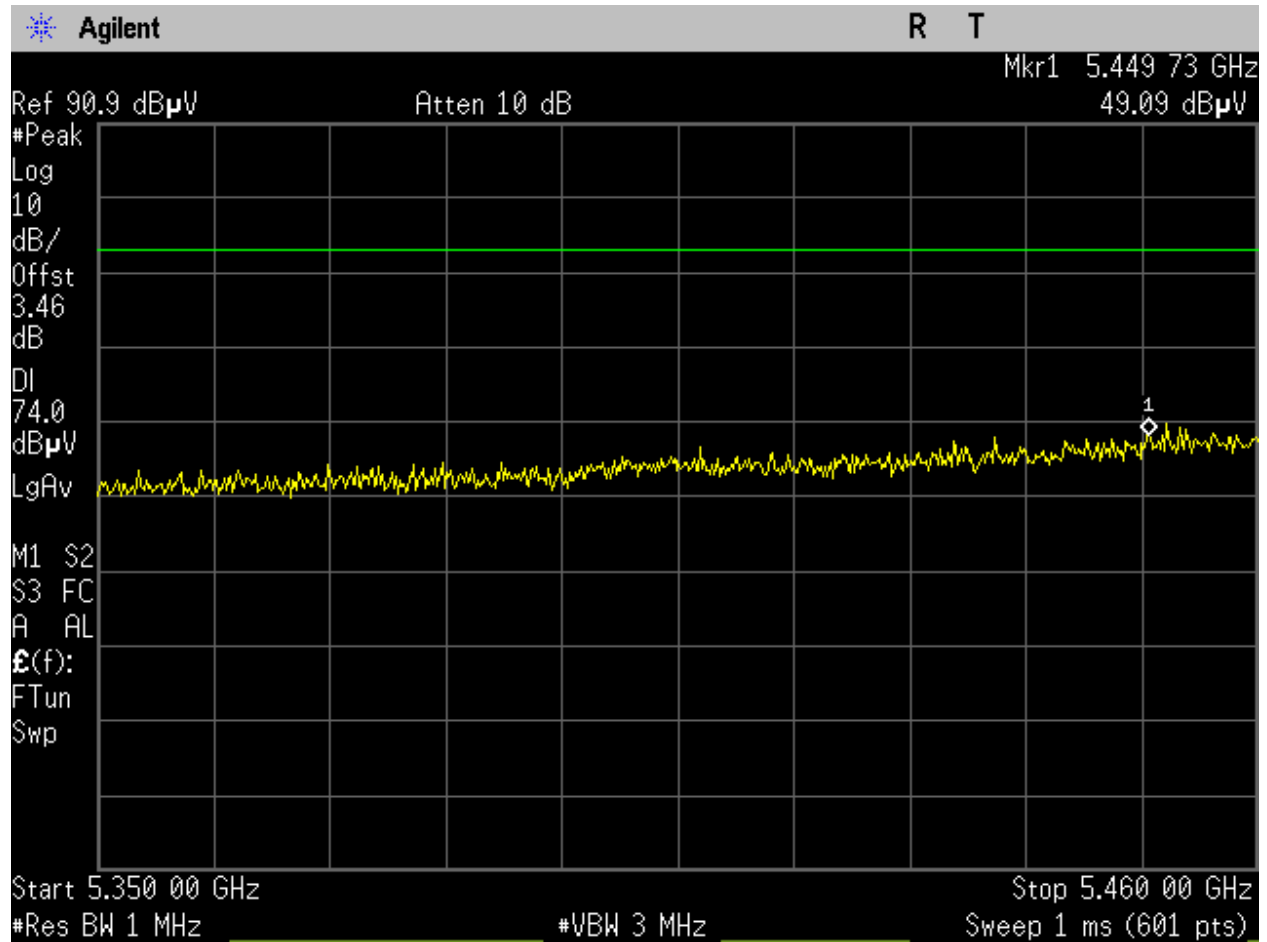


Figure 462: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_Lower Band Edge Peak_Port 2.

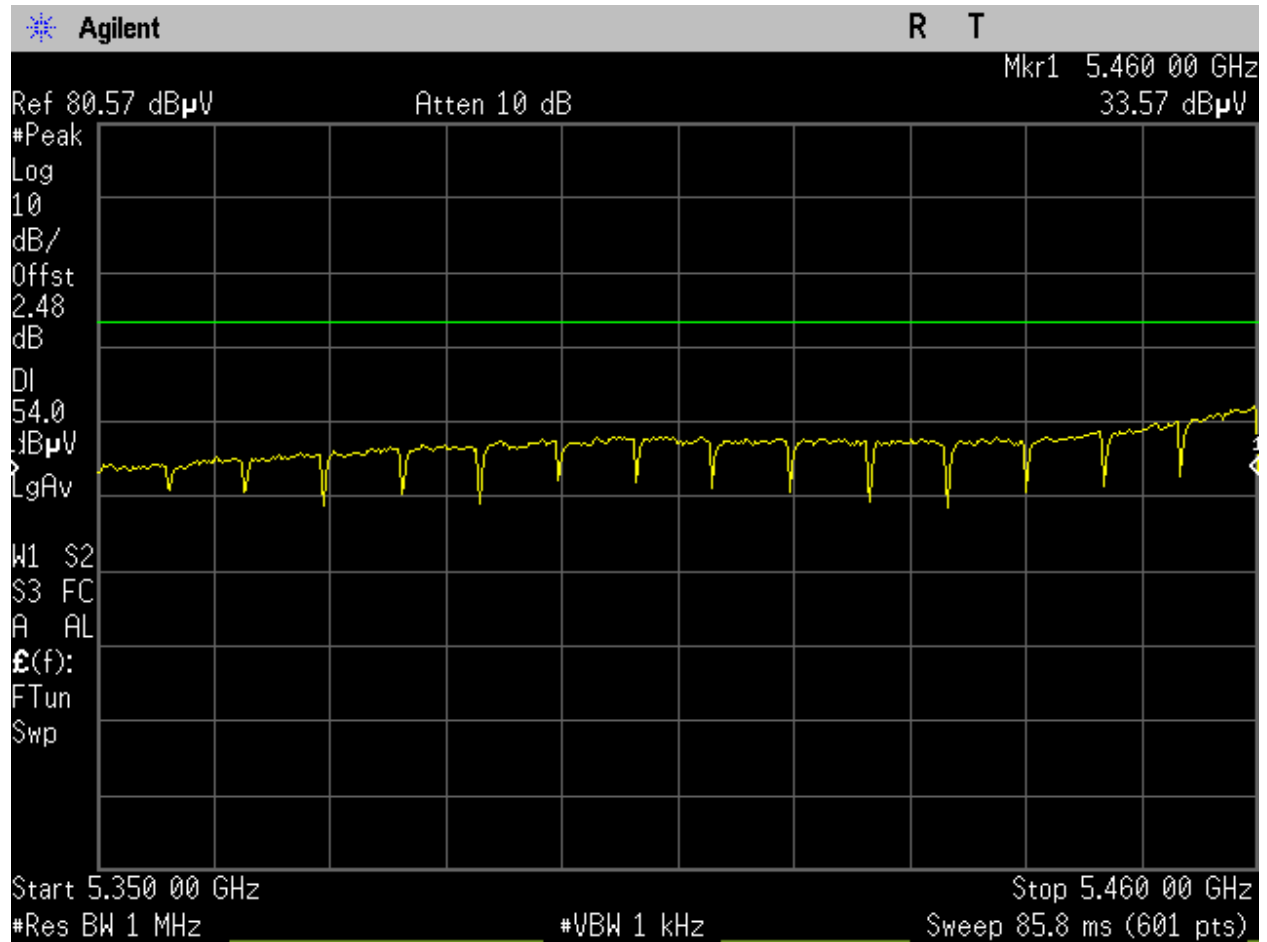


Figure 463: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 1.

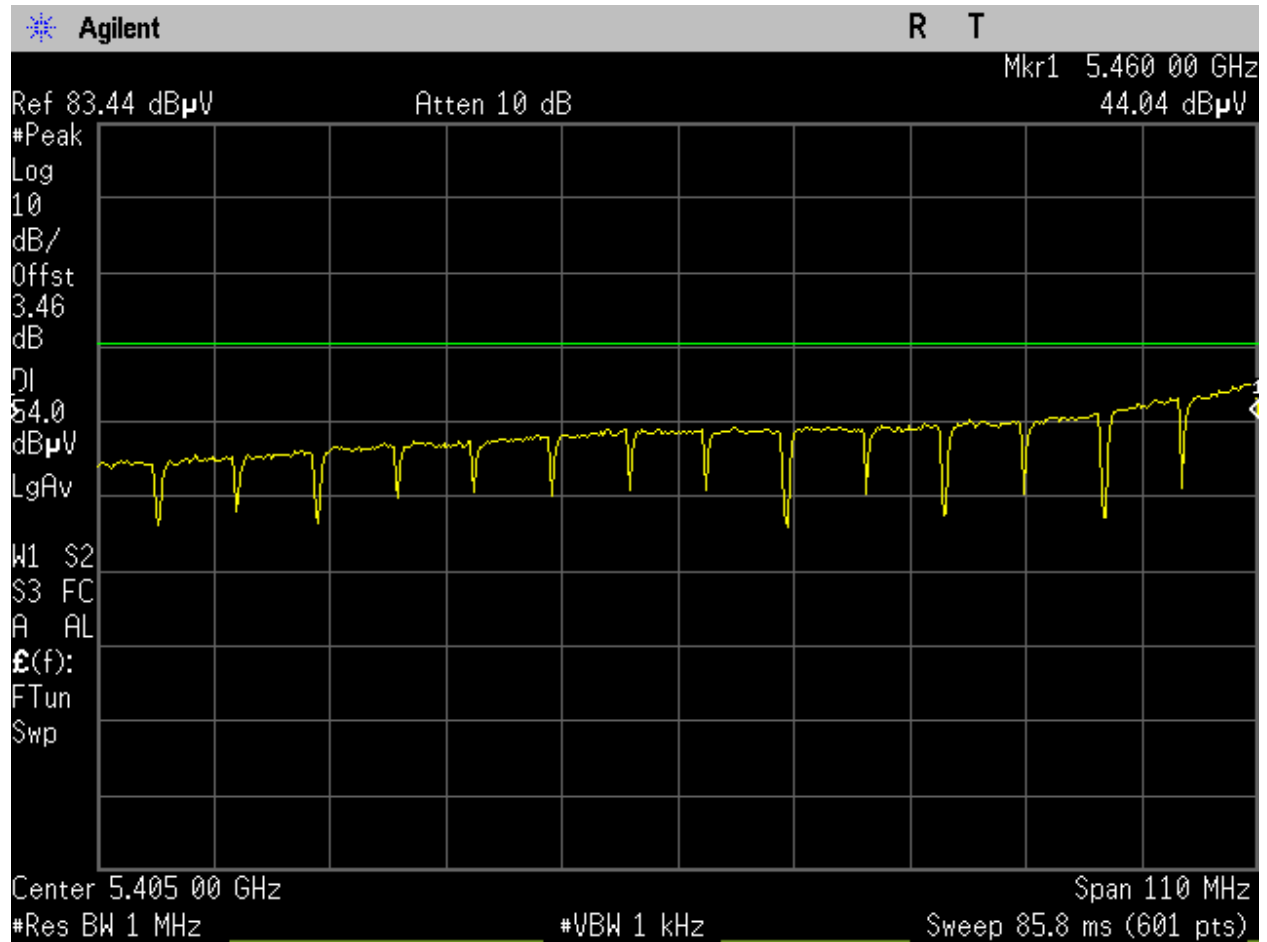


Figure 464: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 2.

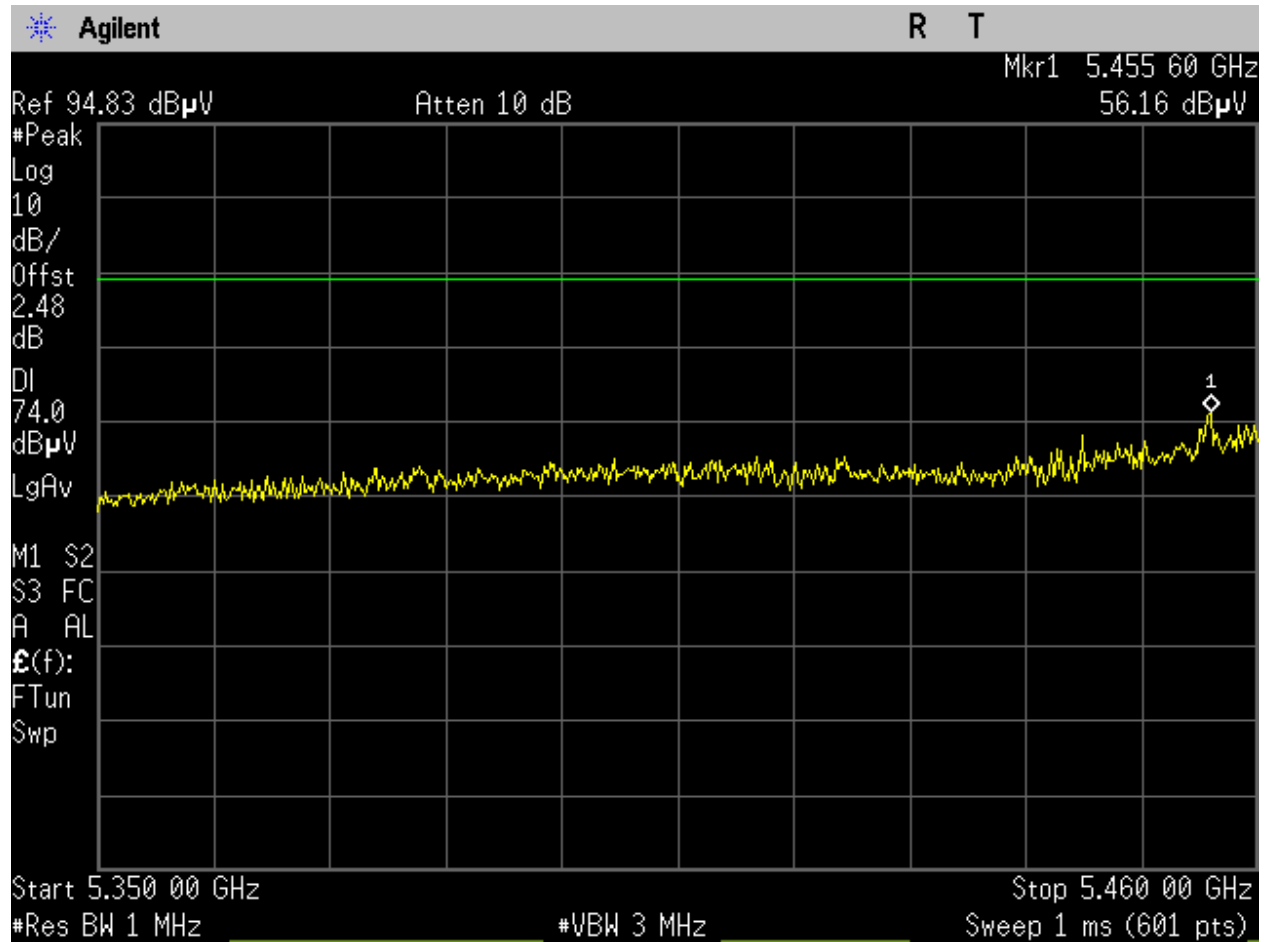


Figure 465: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 1.

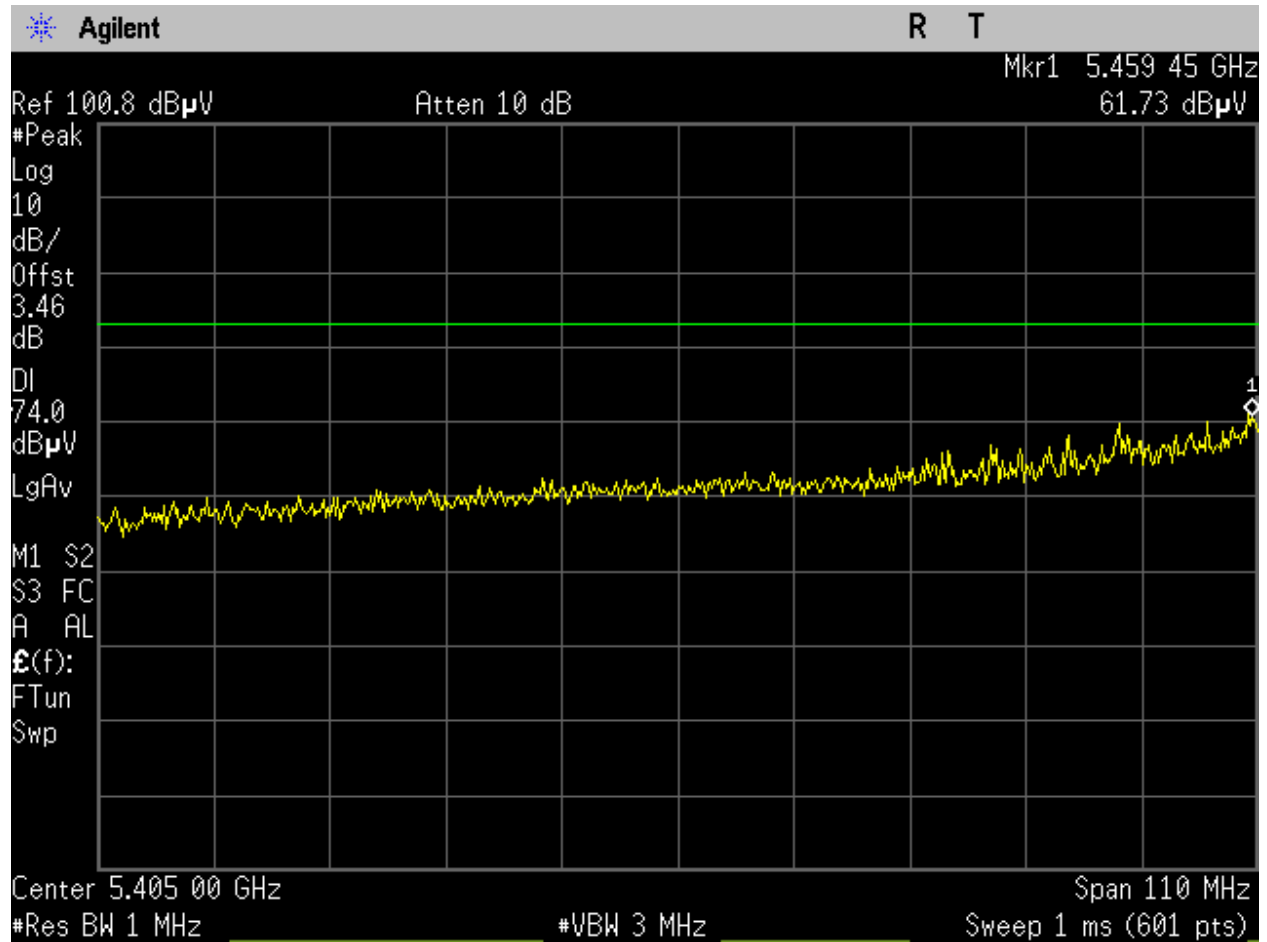


Figure 466: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 2.

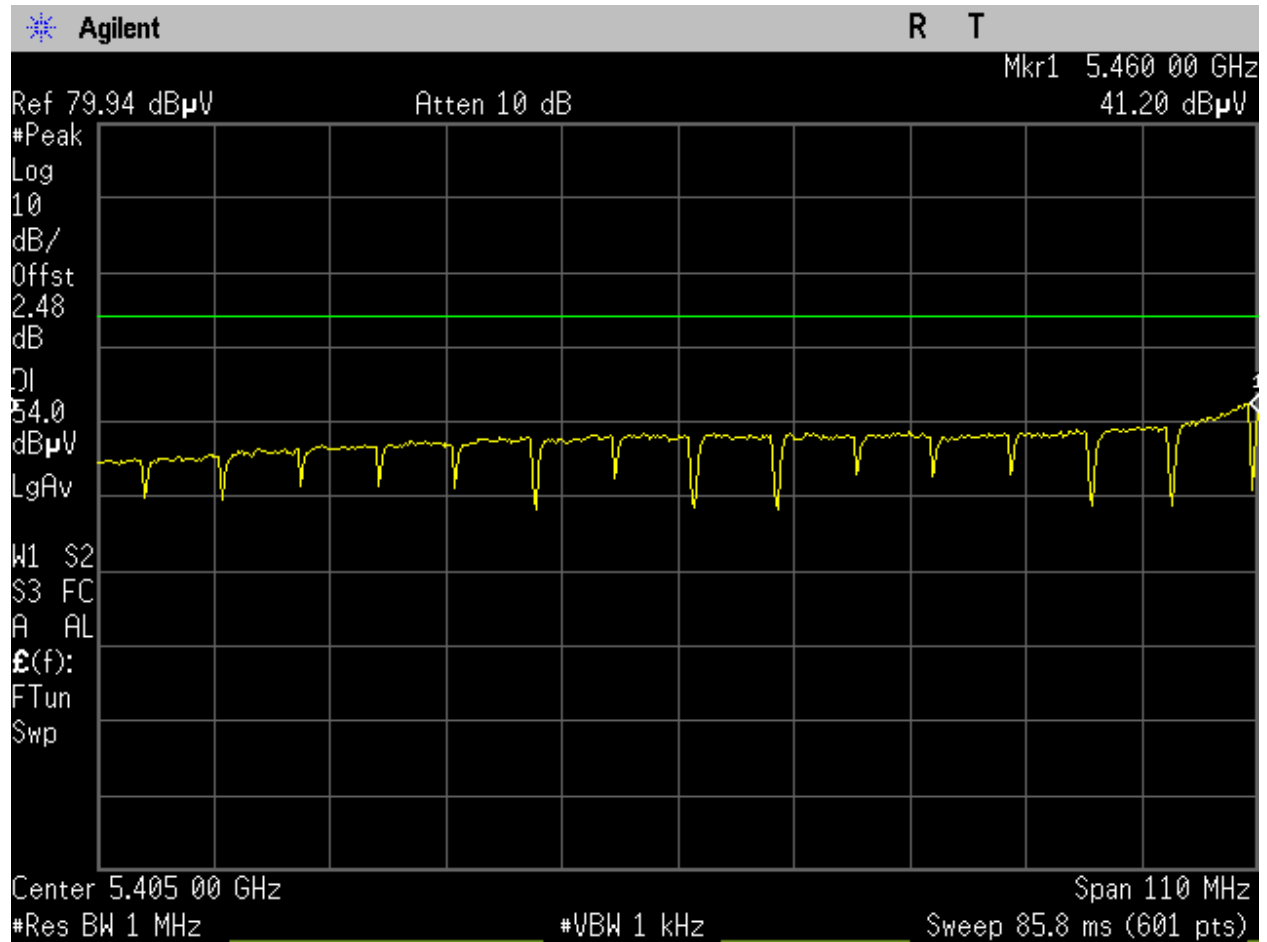


Figure 467: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 1.

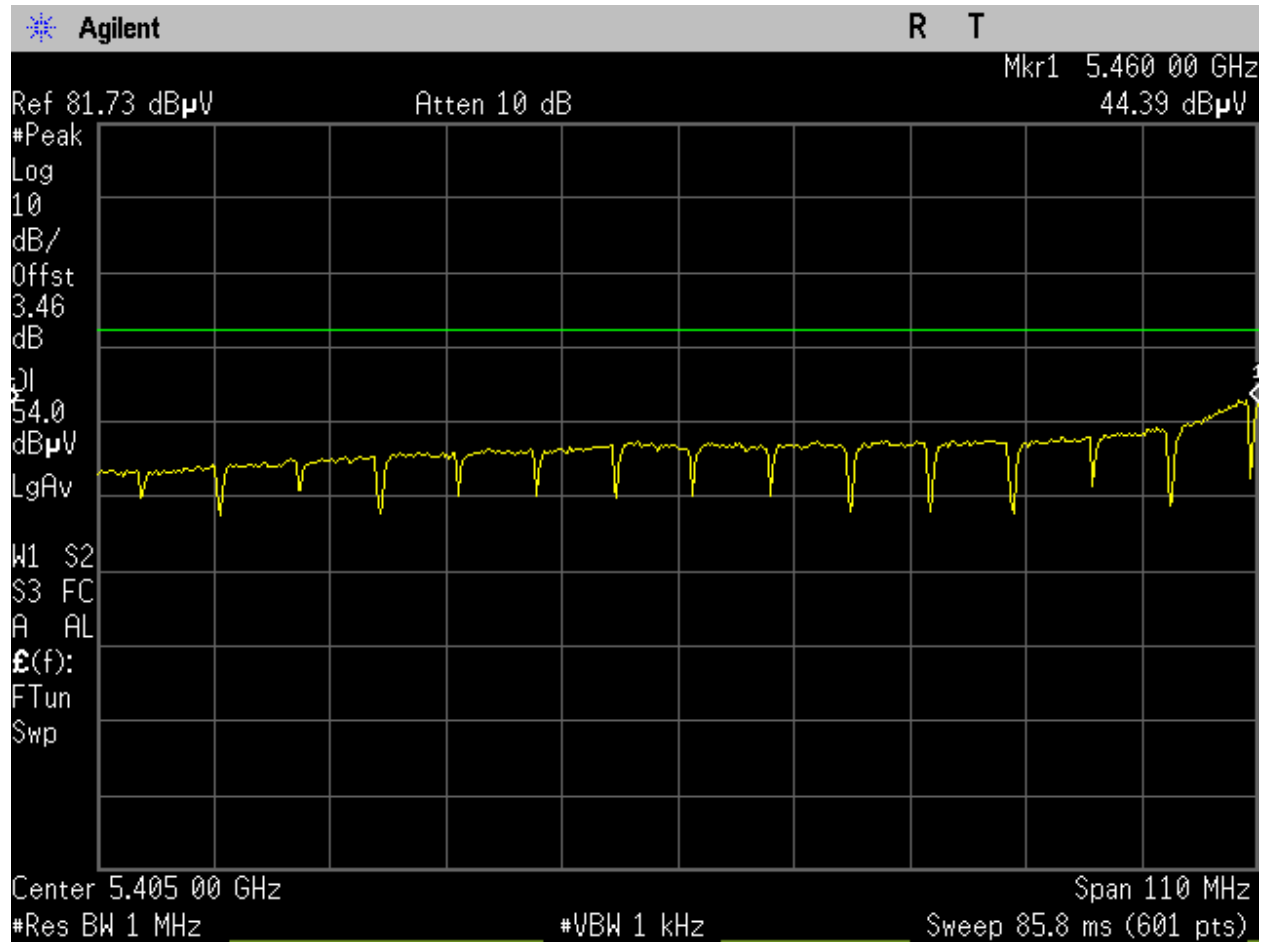


Figure 468: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 2.

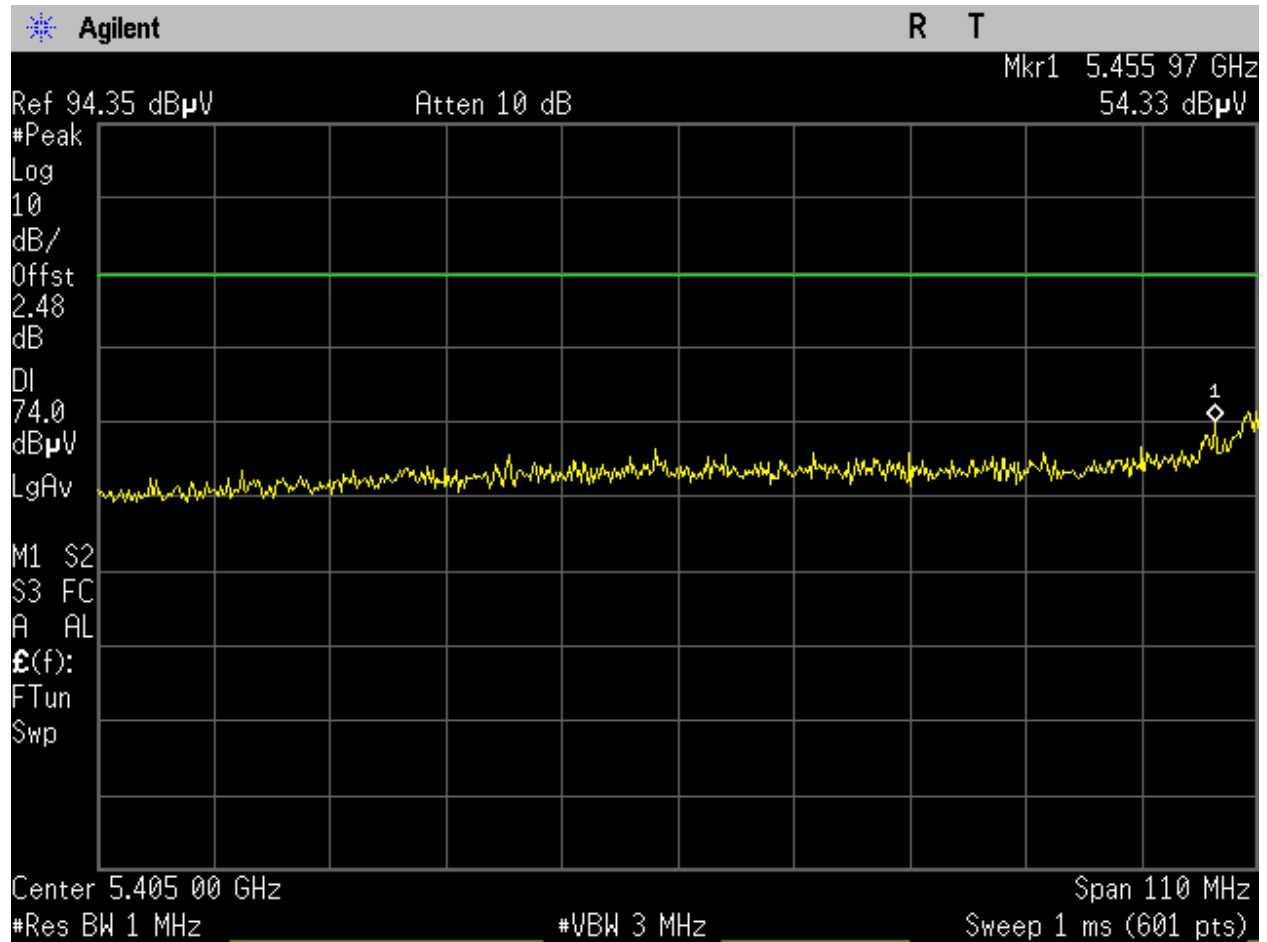


Figure 469: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 1.

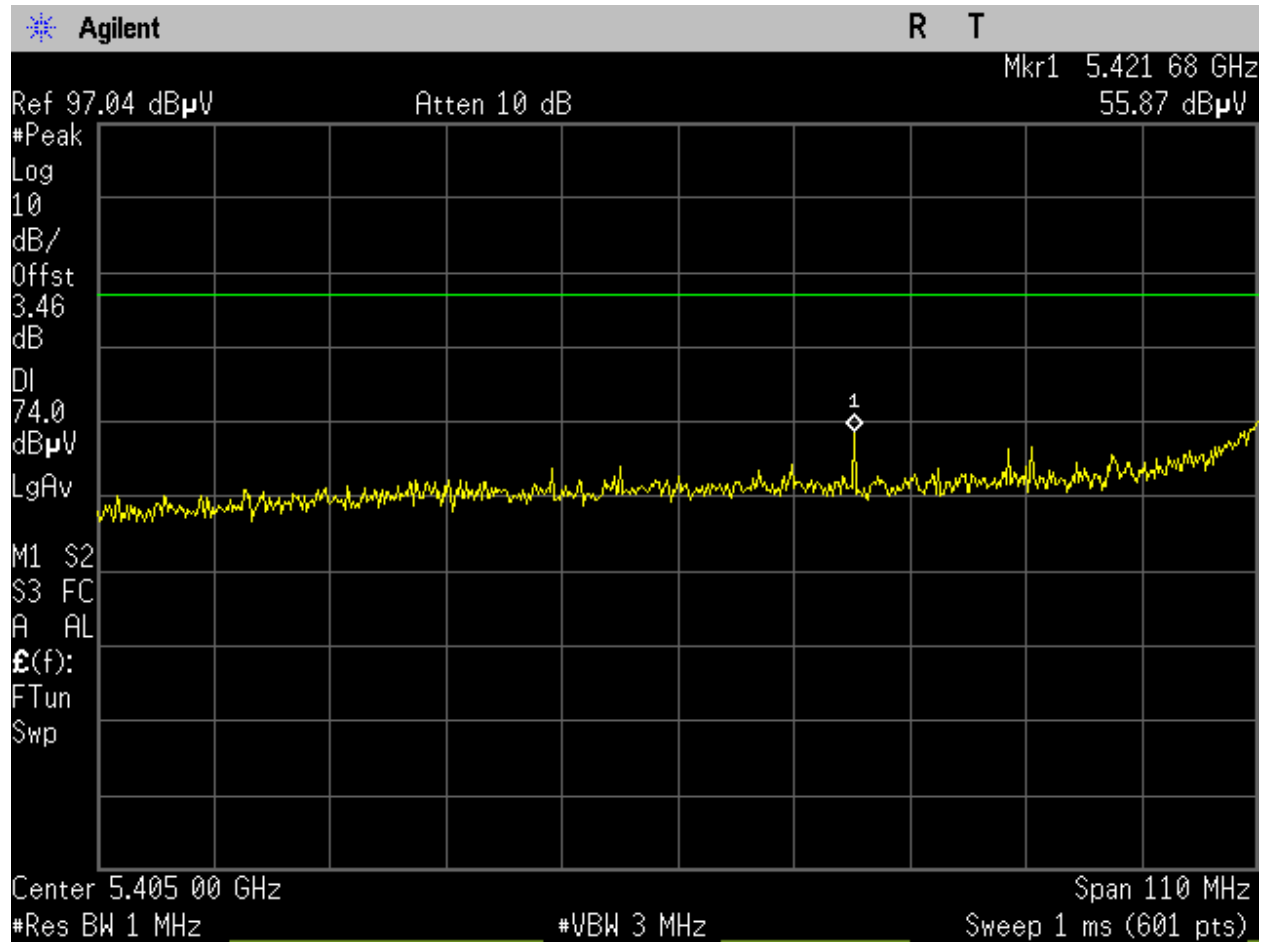


Figure 470: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 2.

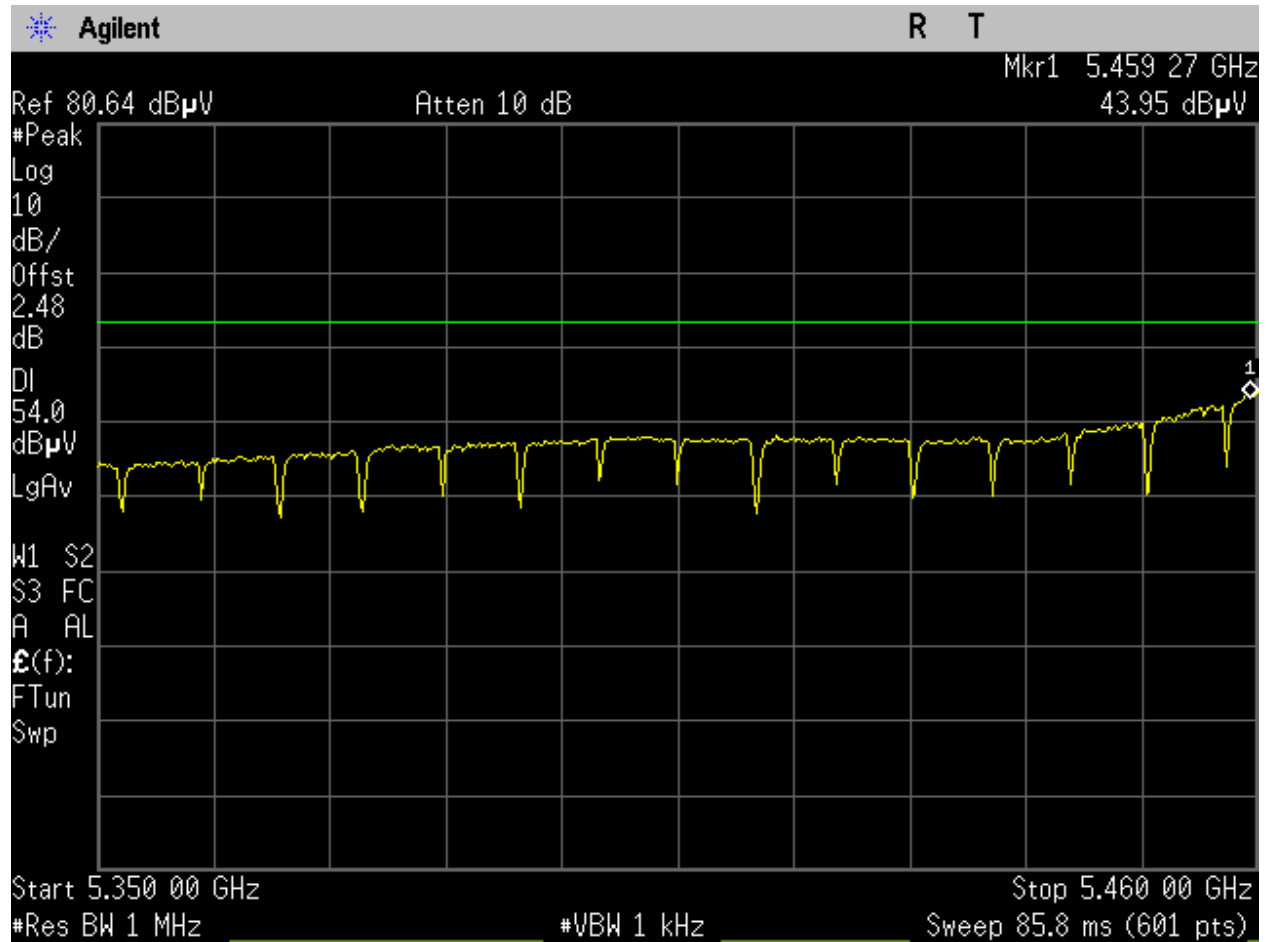


Figure 471: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_15.209_Lower Band Edge Avg_Port 1.

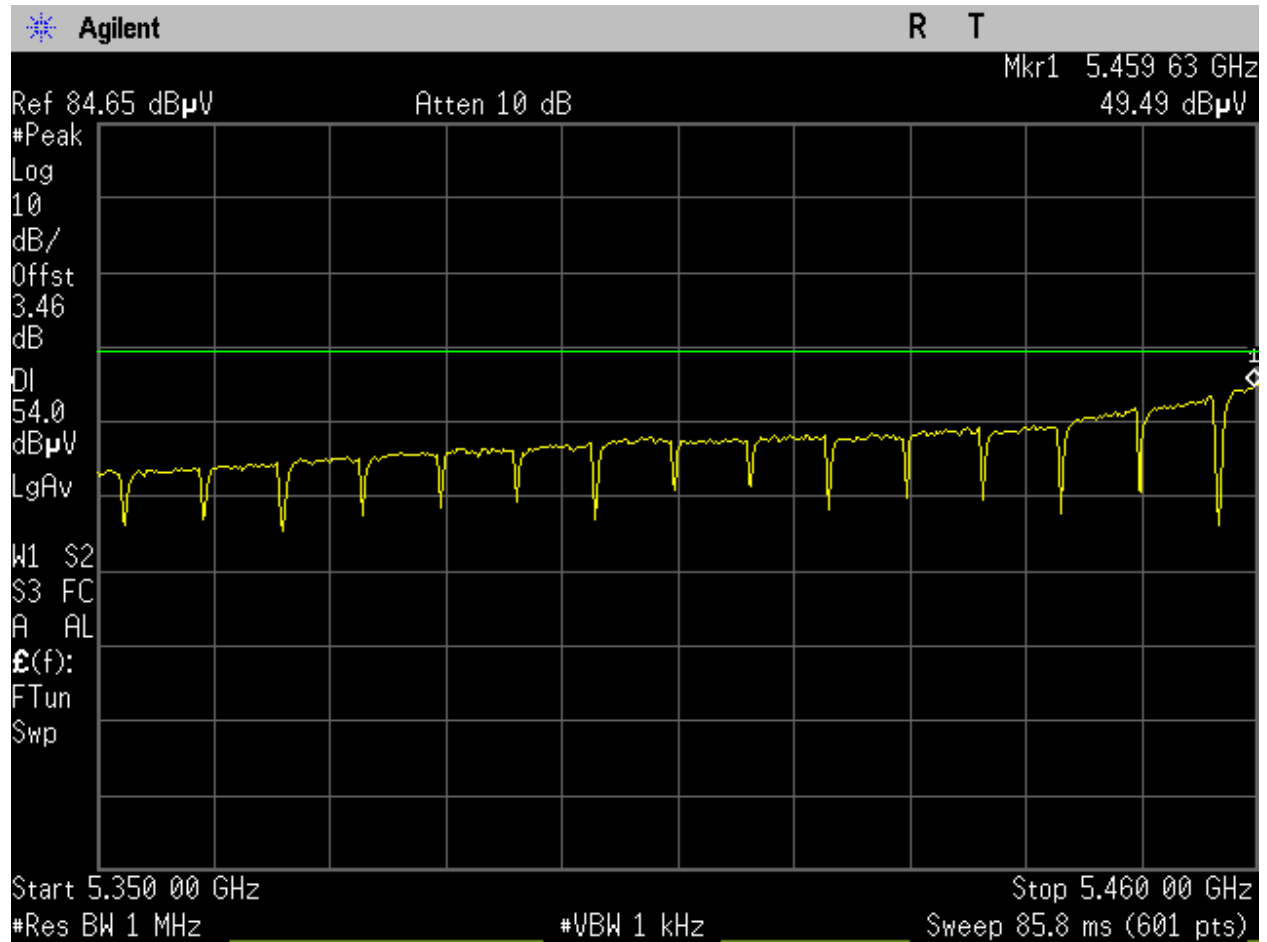


Figure 472: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_15.209_Lower Band Edge Avg_Port 2.

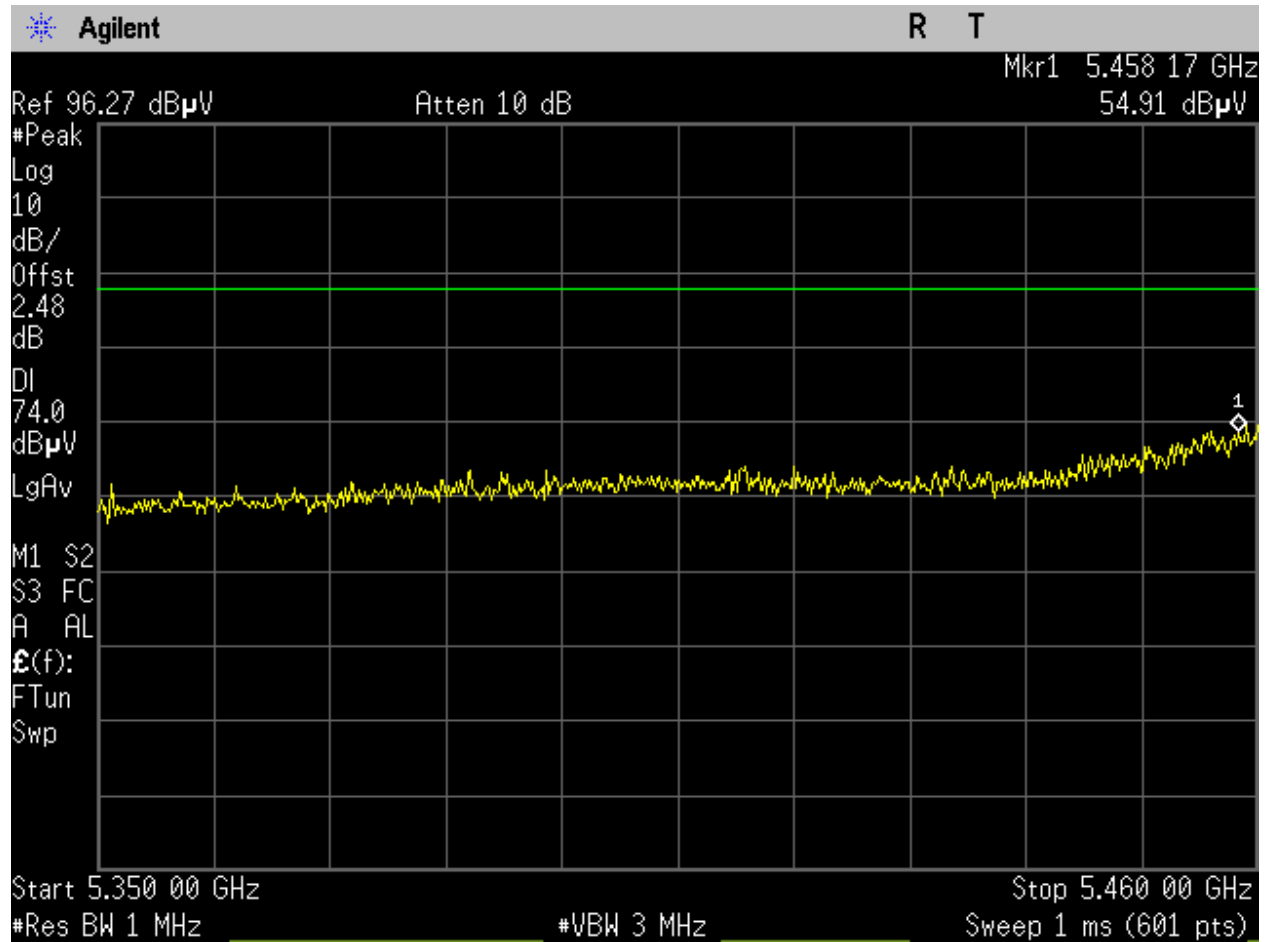


Figure 473: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_15.209_Lower Band Edge Peak_Port 1.

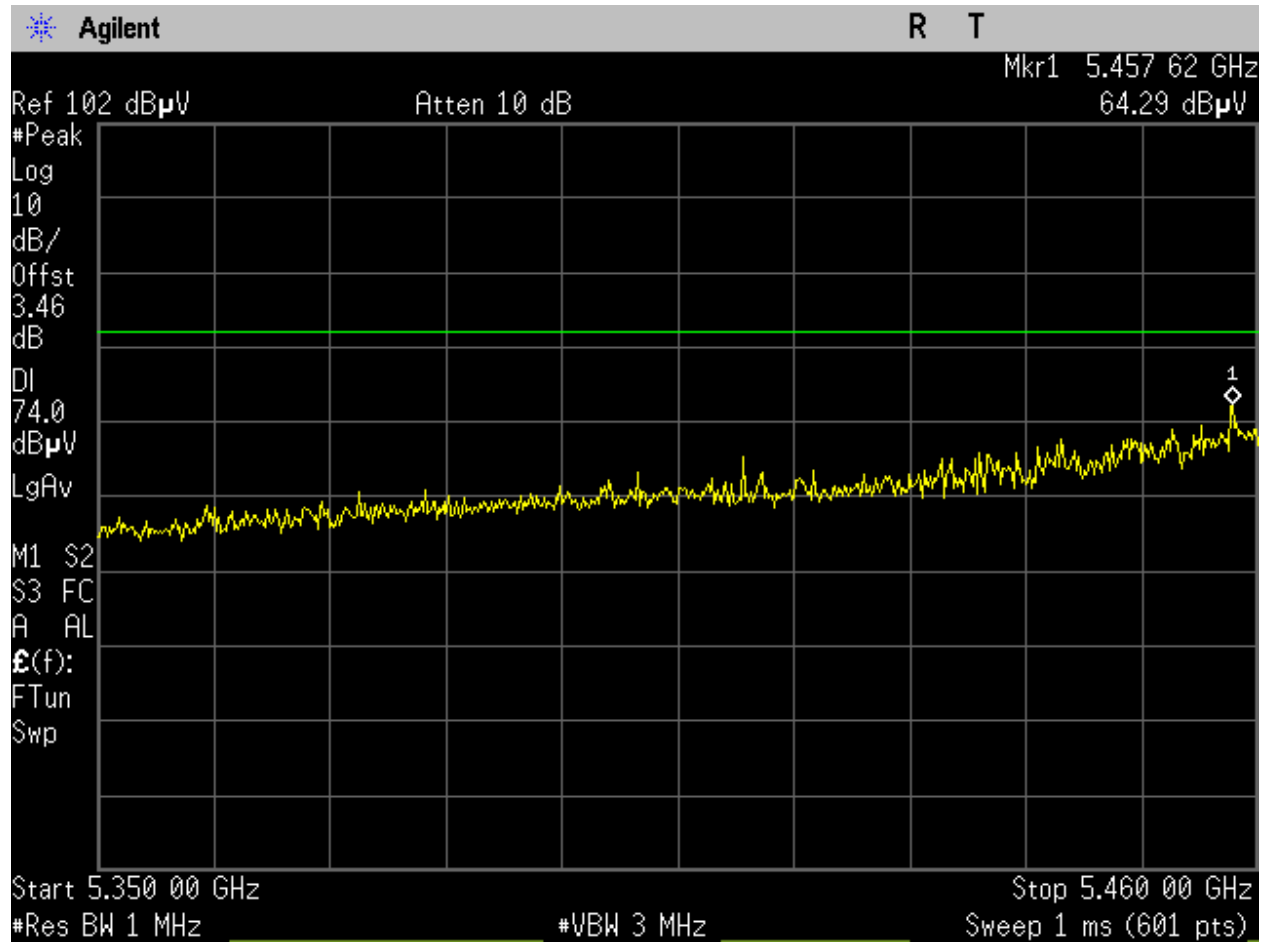


Figure 474: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_15.209_Lower Band Edge Peak_Port 2.

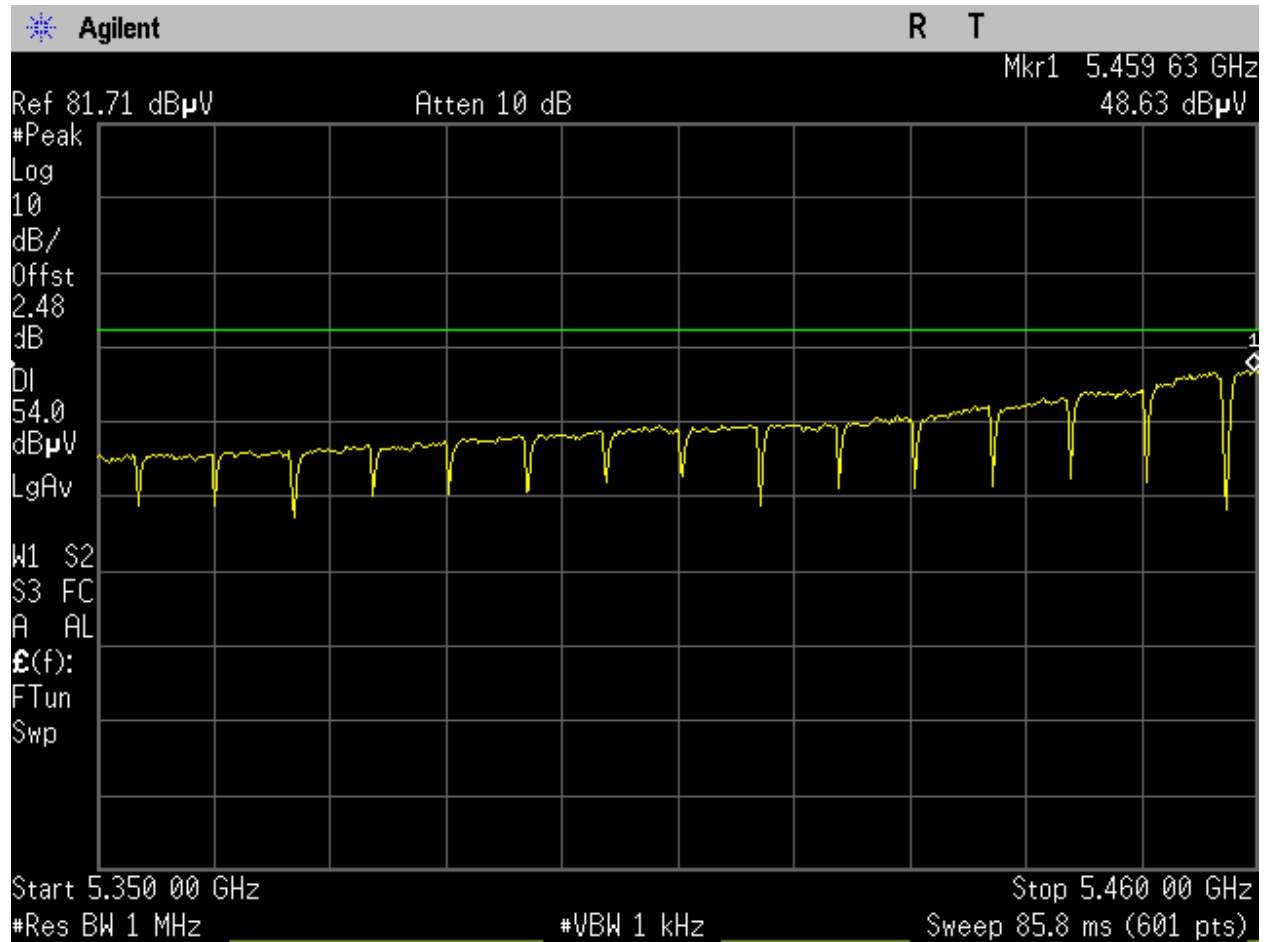


Figure 475: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 1.

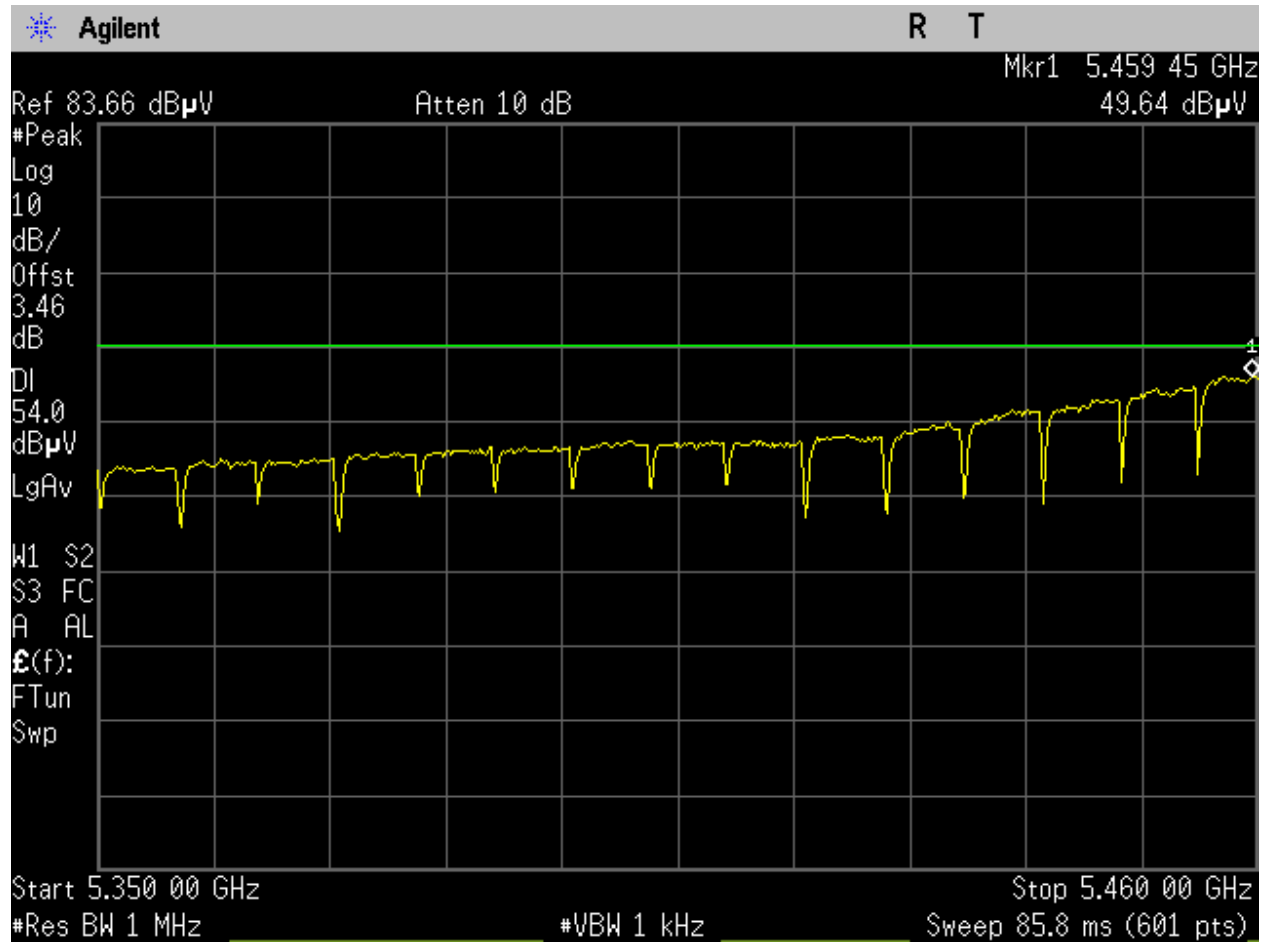


Figure 476: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 2.

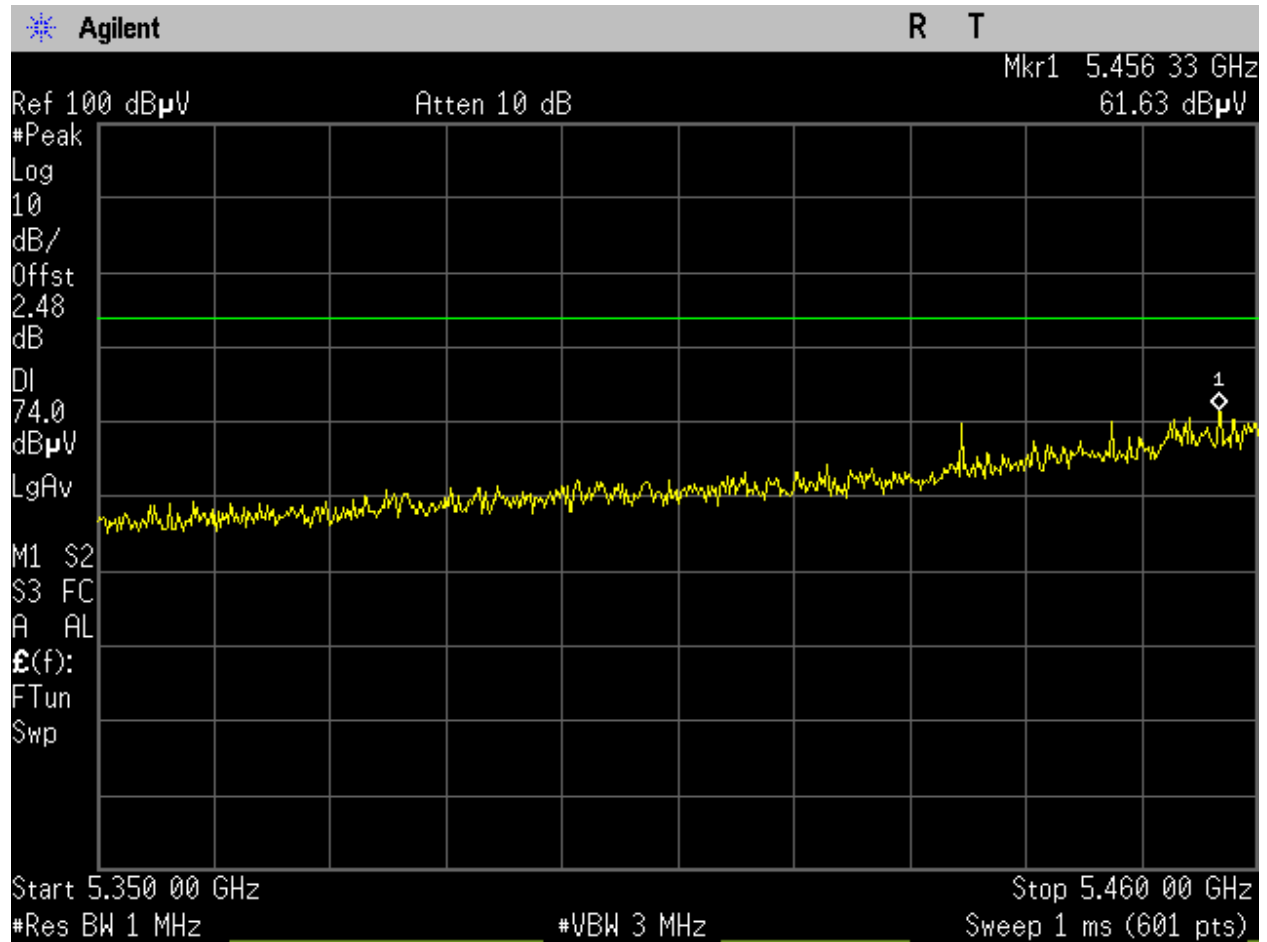


Figure 477: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 1.

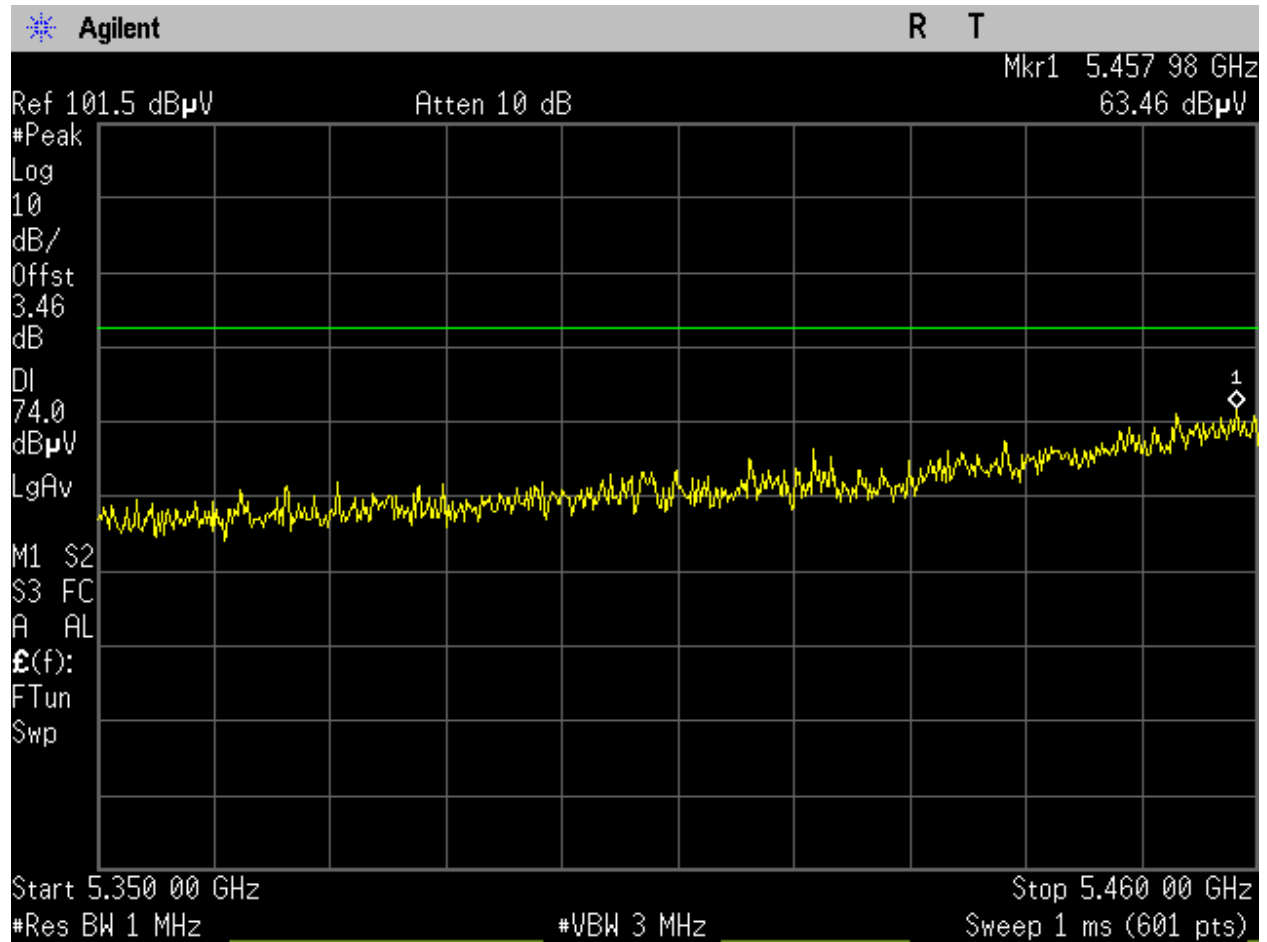


Figure 478: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 2.

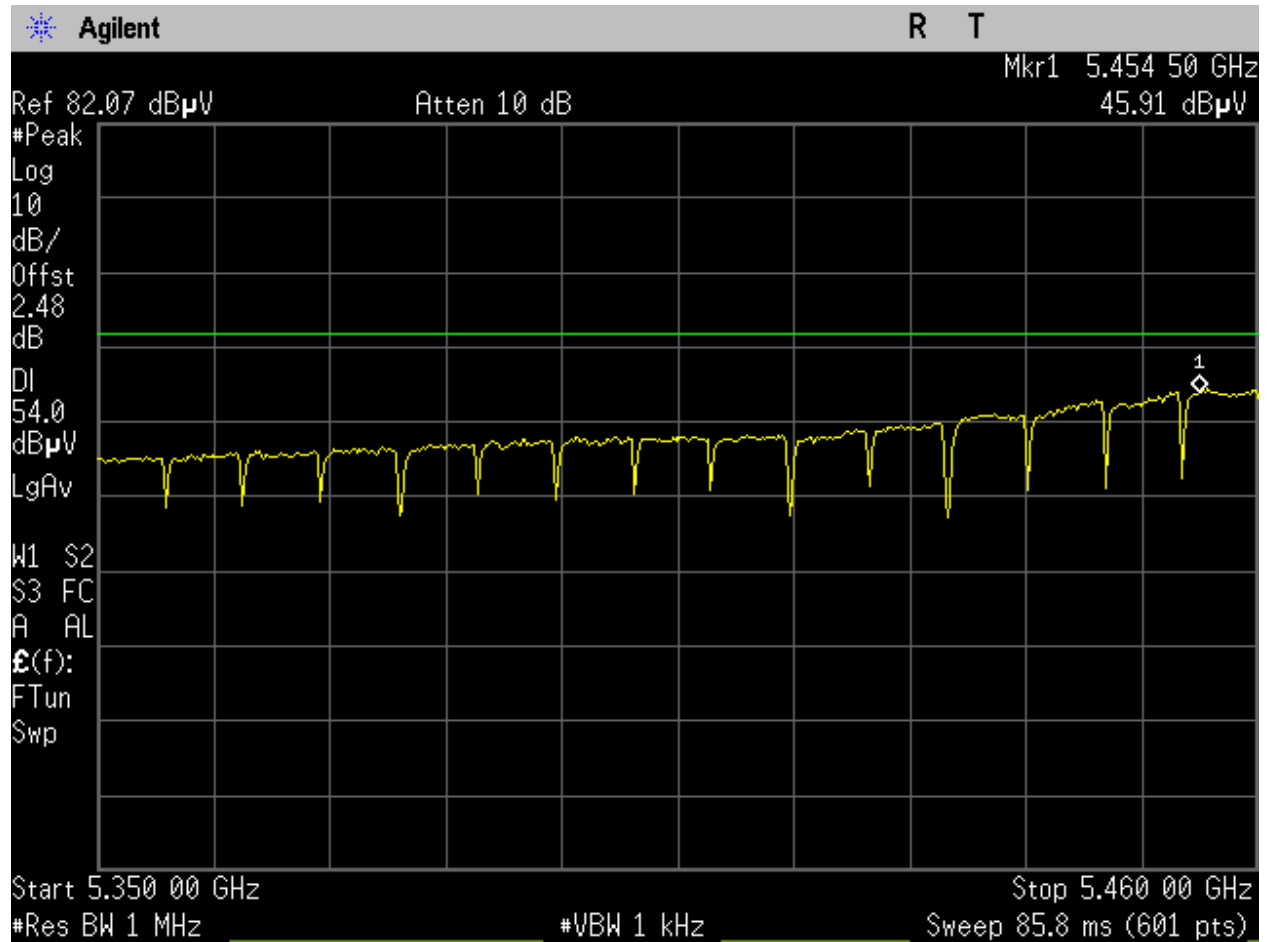


Figure 479: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 1.

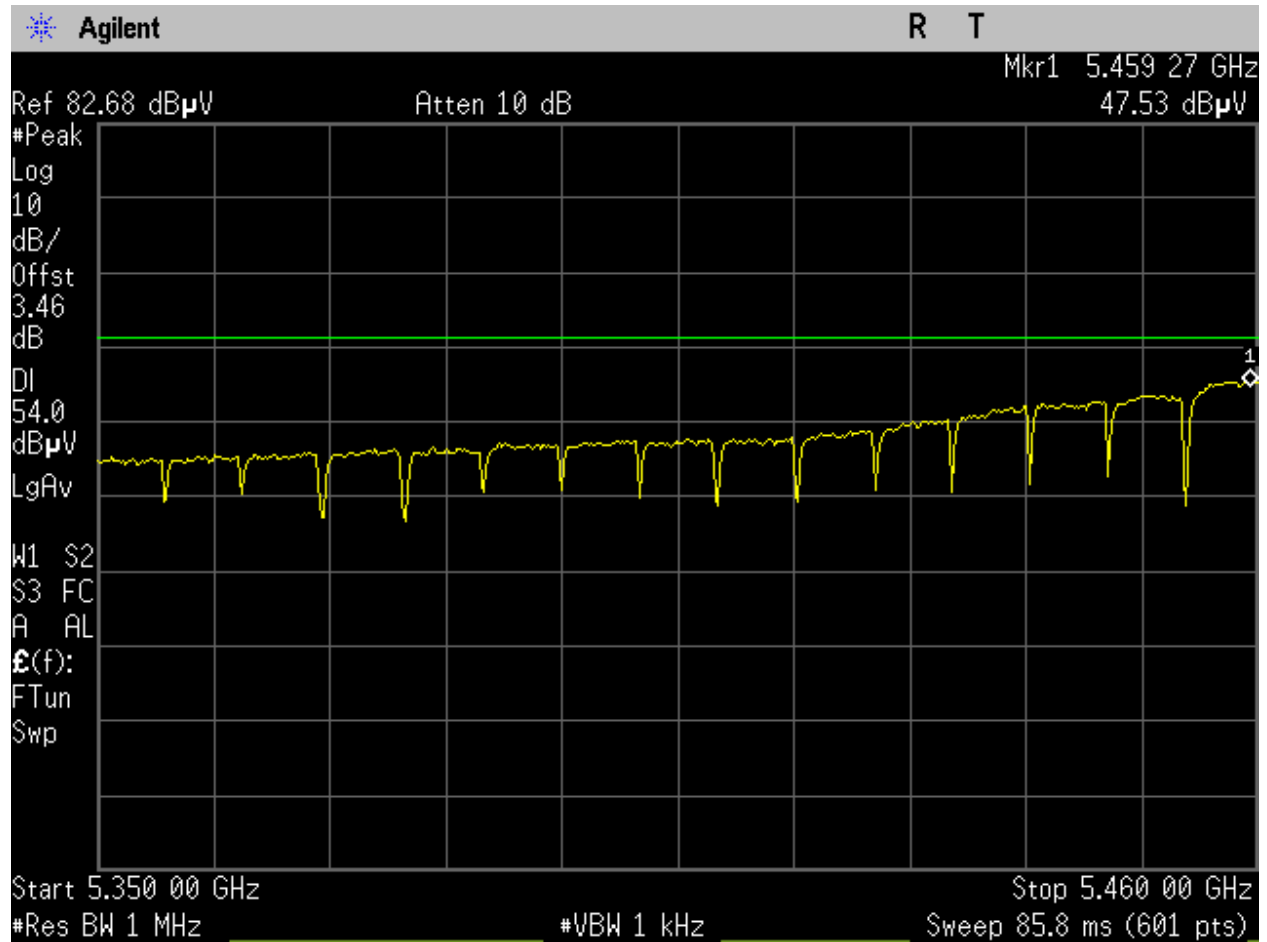


Figure 480: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 2.

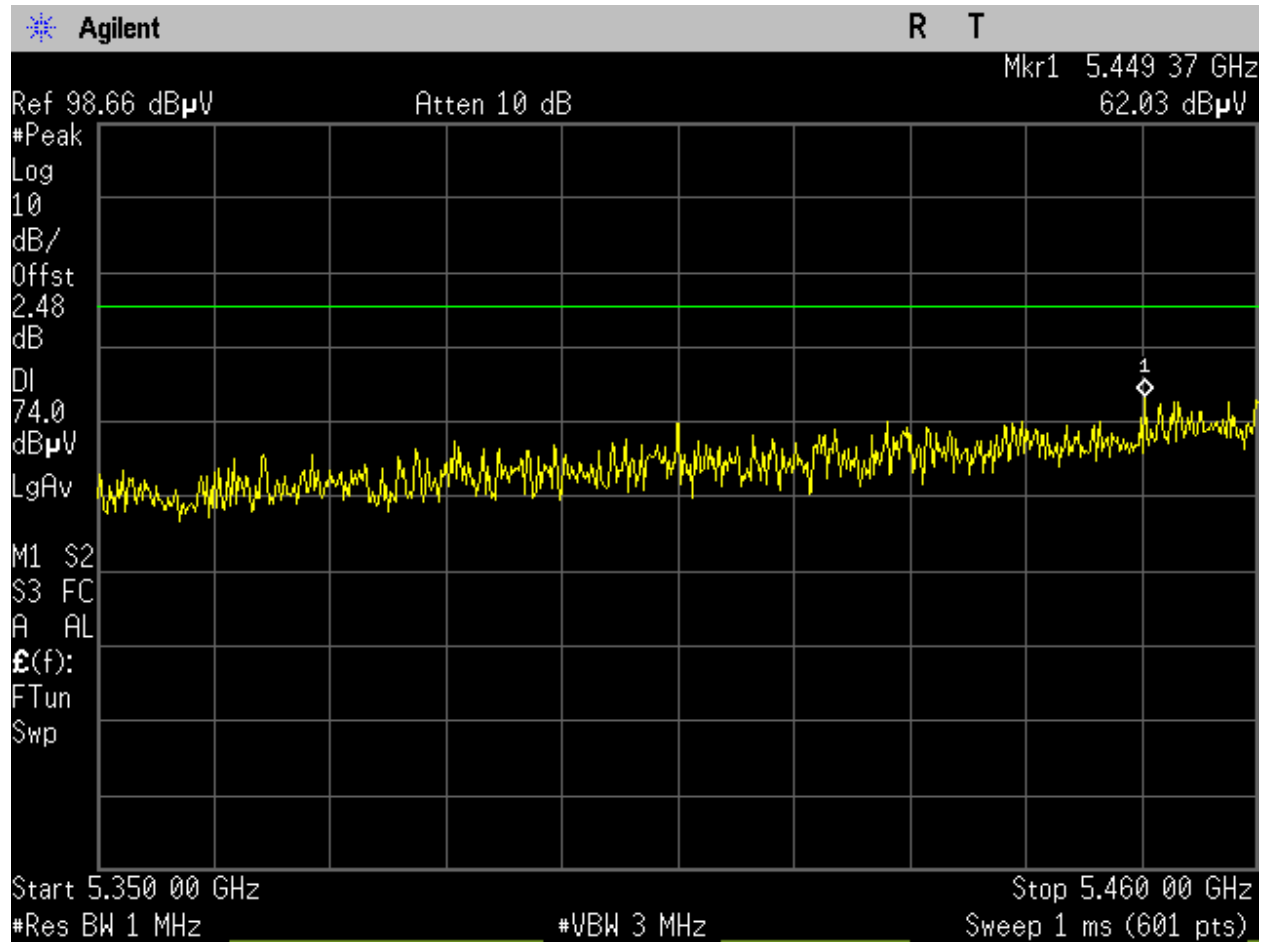


Figure 481: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 1.

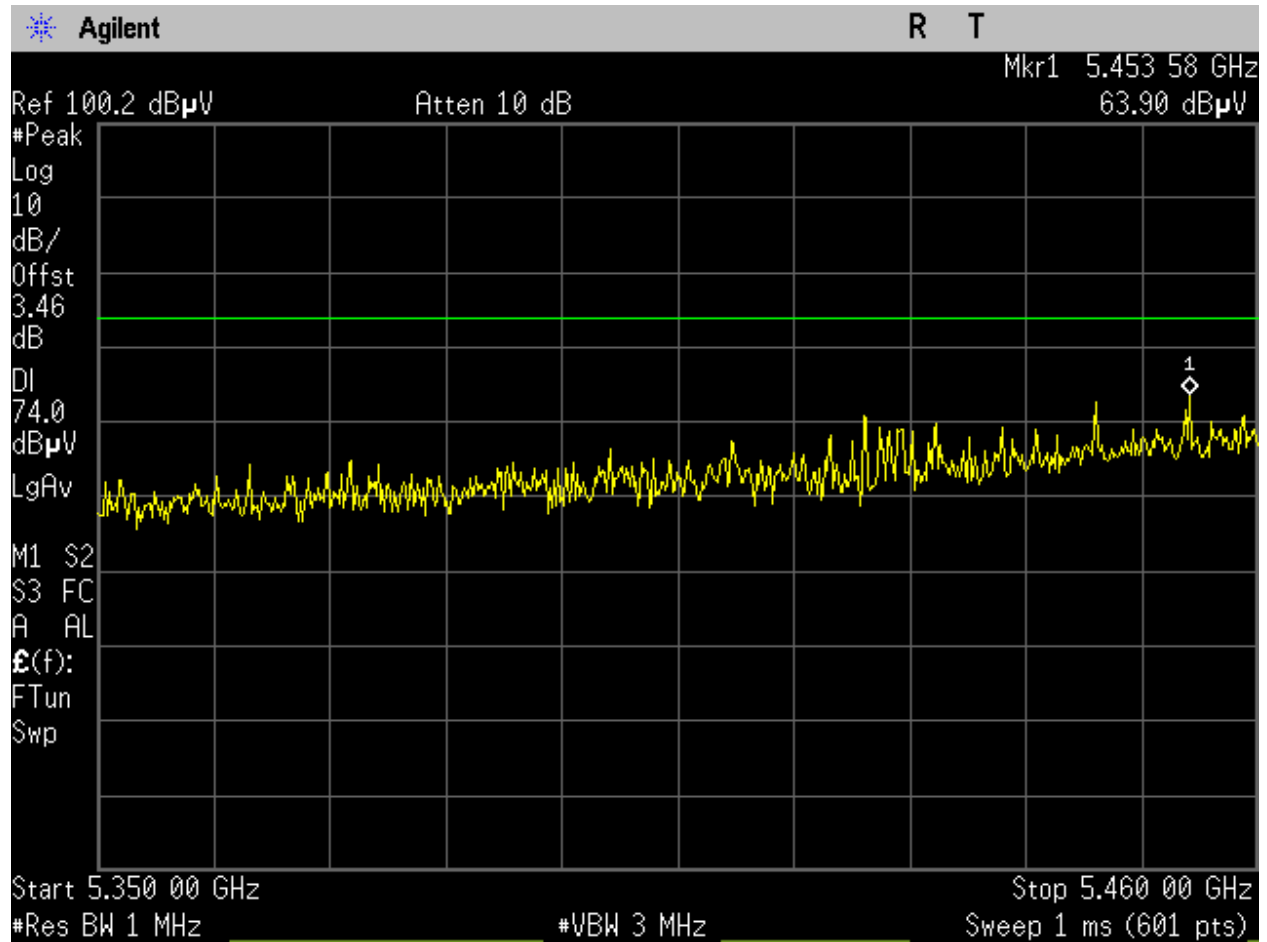


Figure 482: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 2.

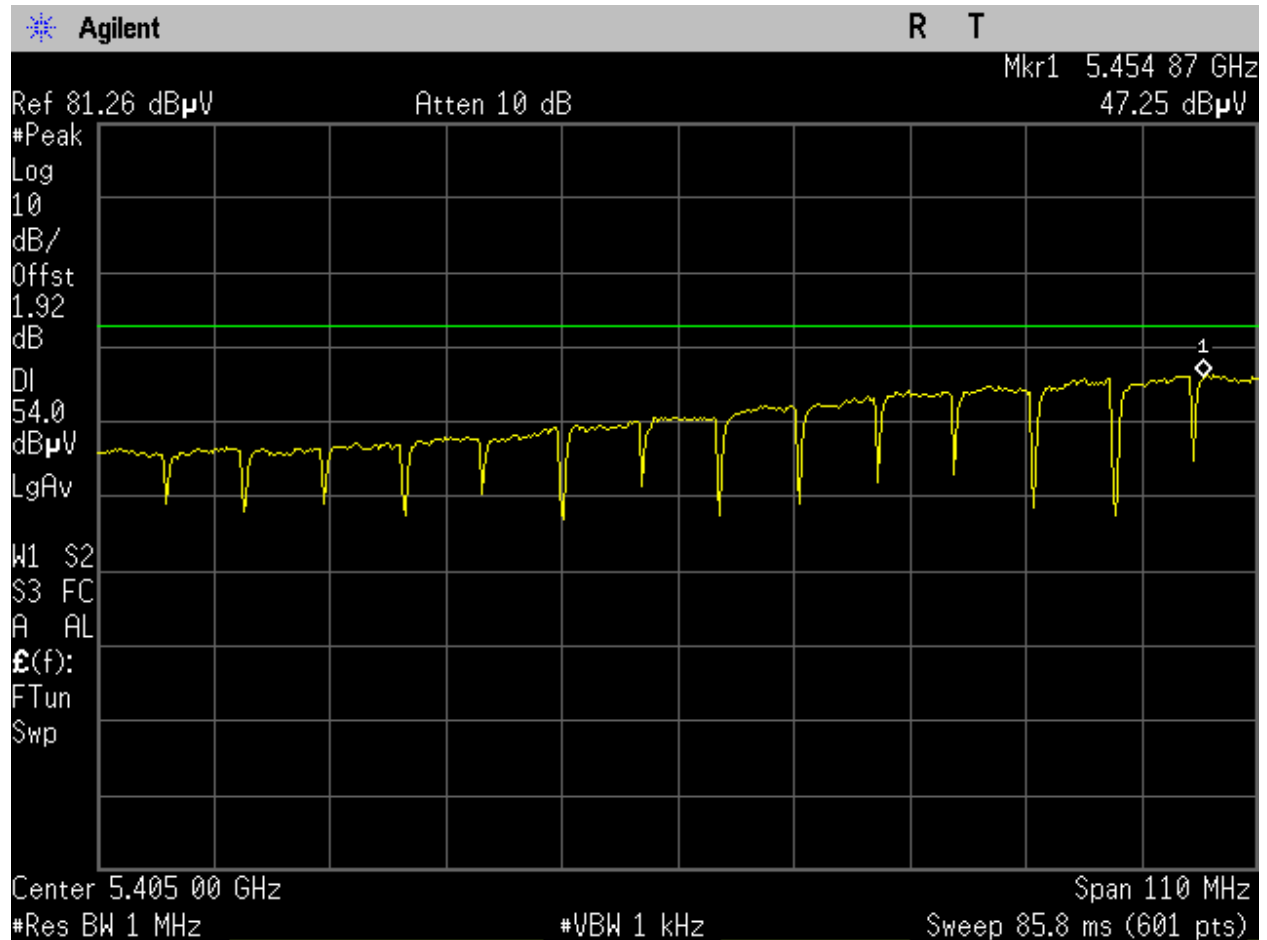


Figure 483: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 1.

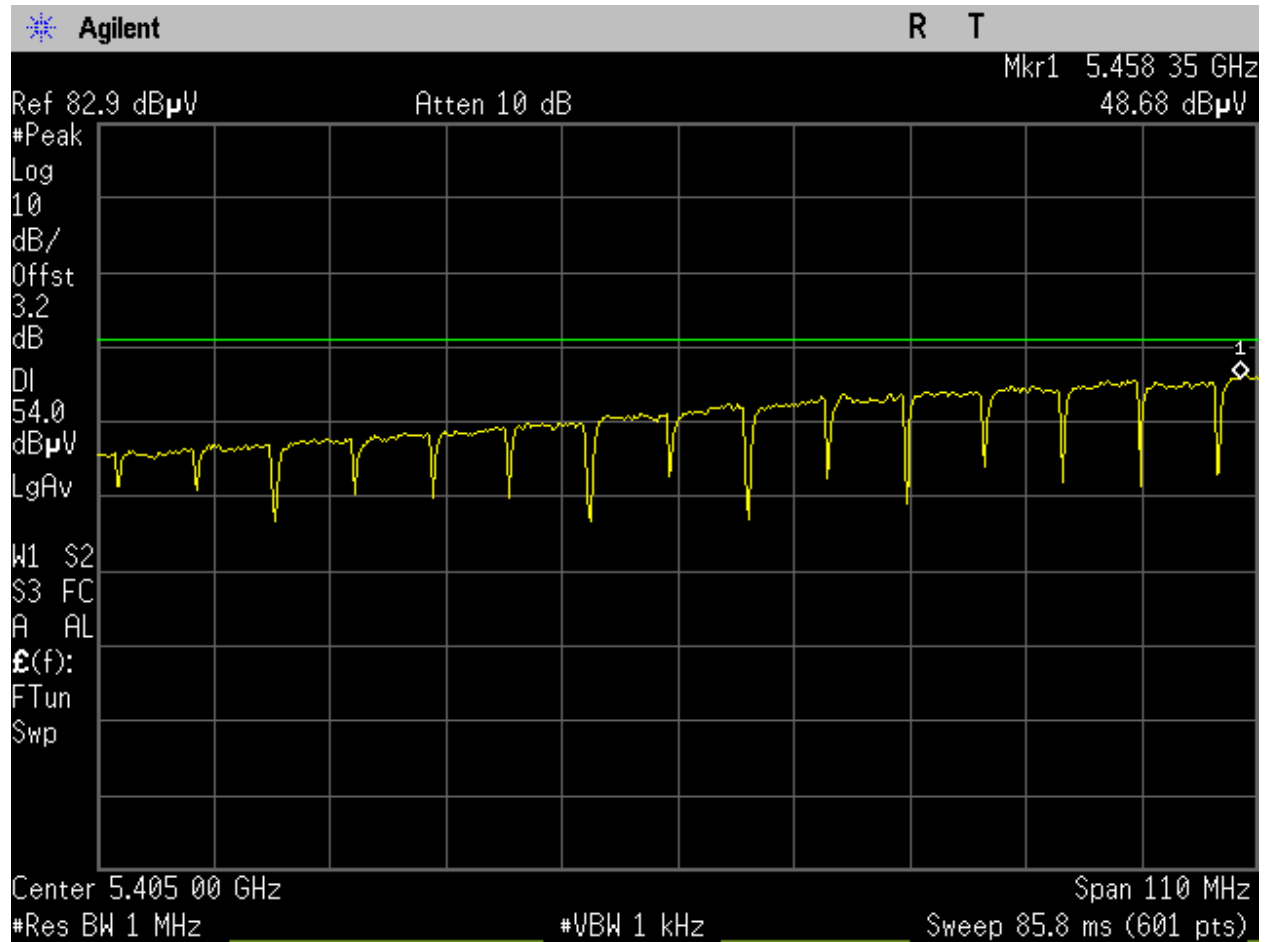


Figure 484: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 2.

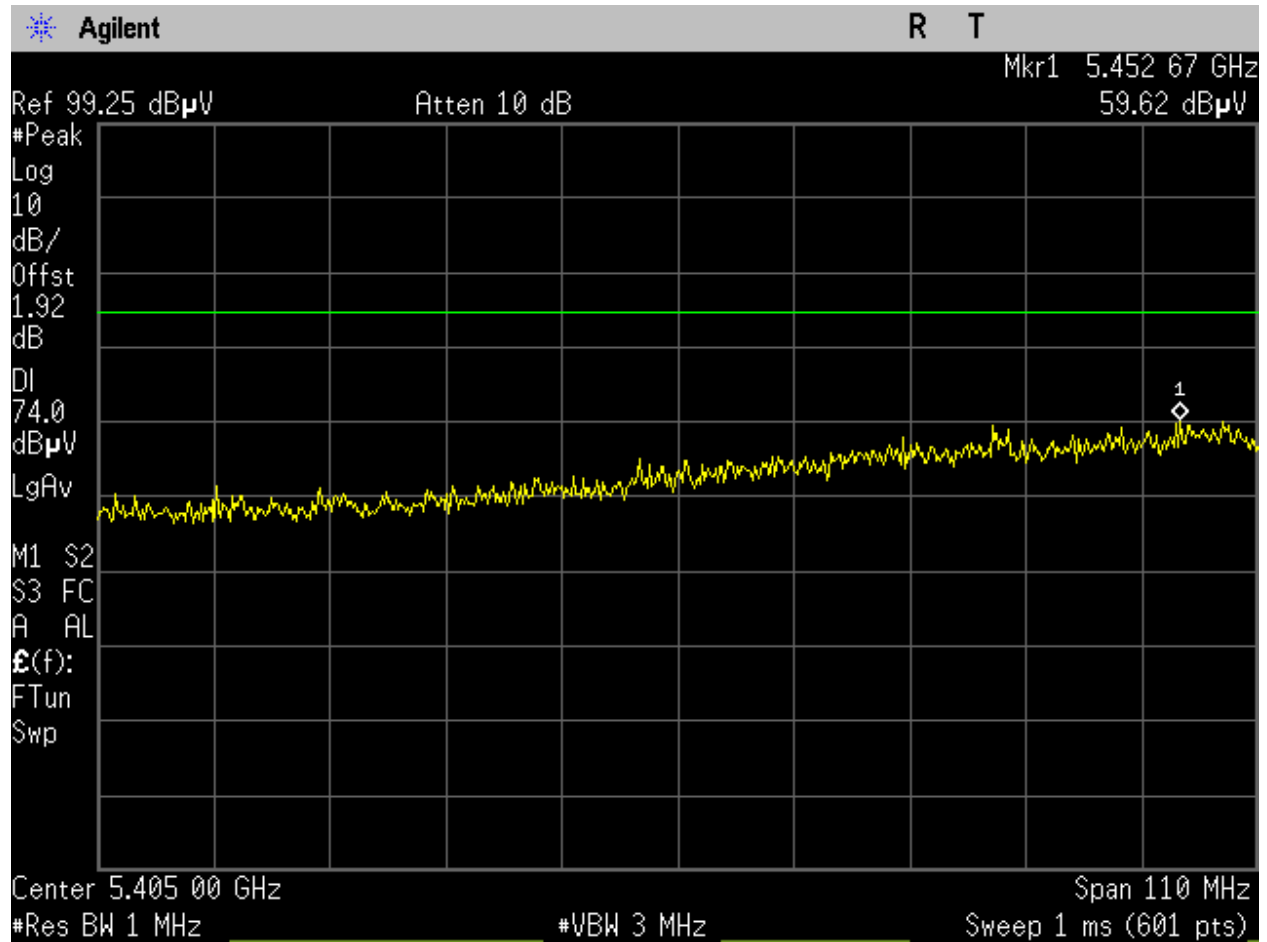


Figure 485: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 1.

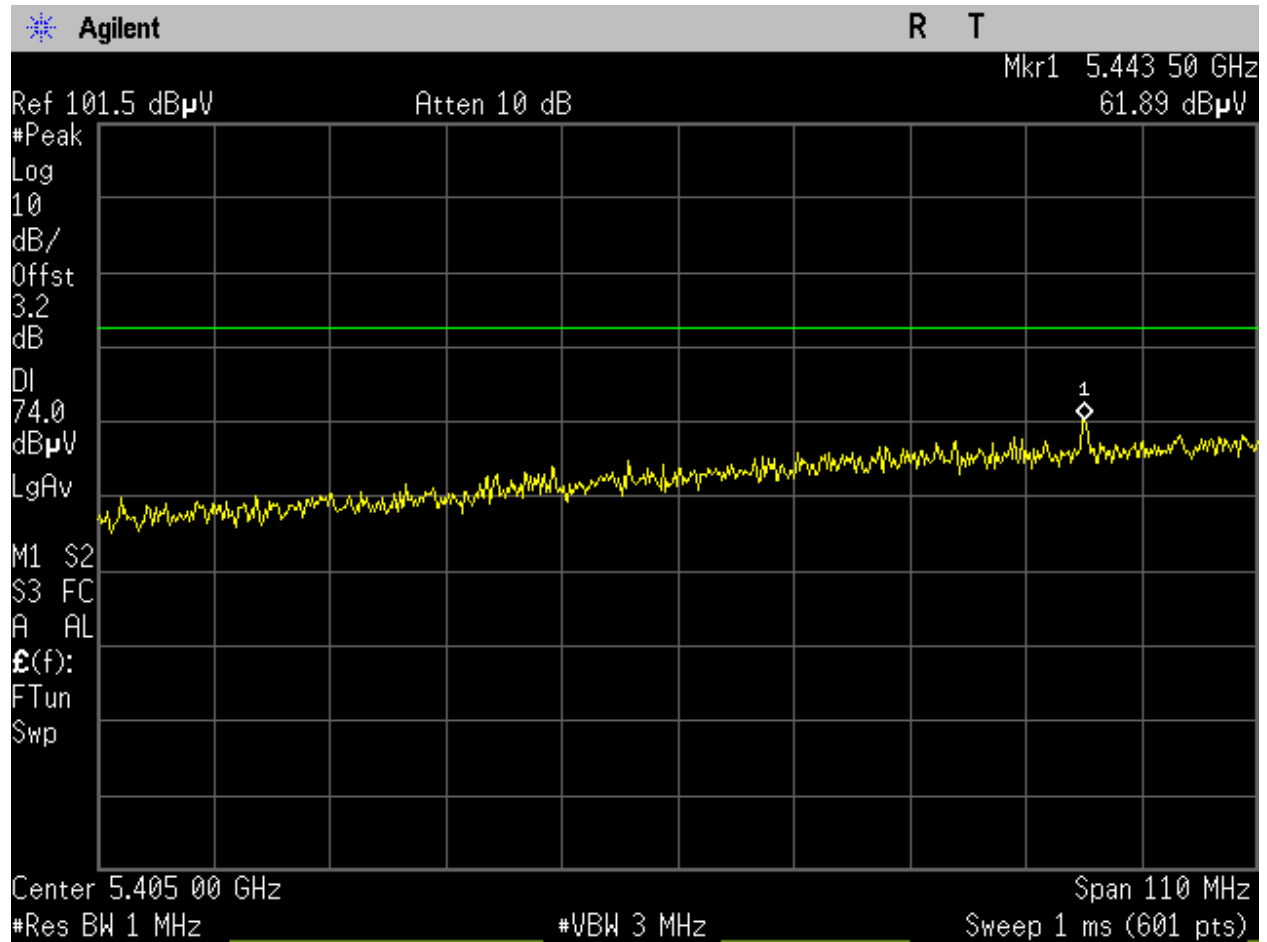


Figure 486: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 2.

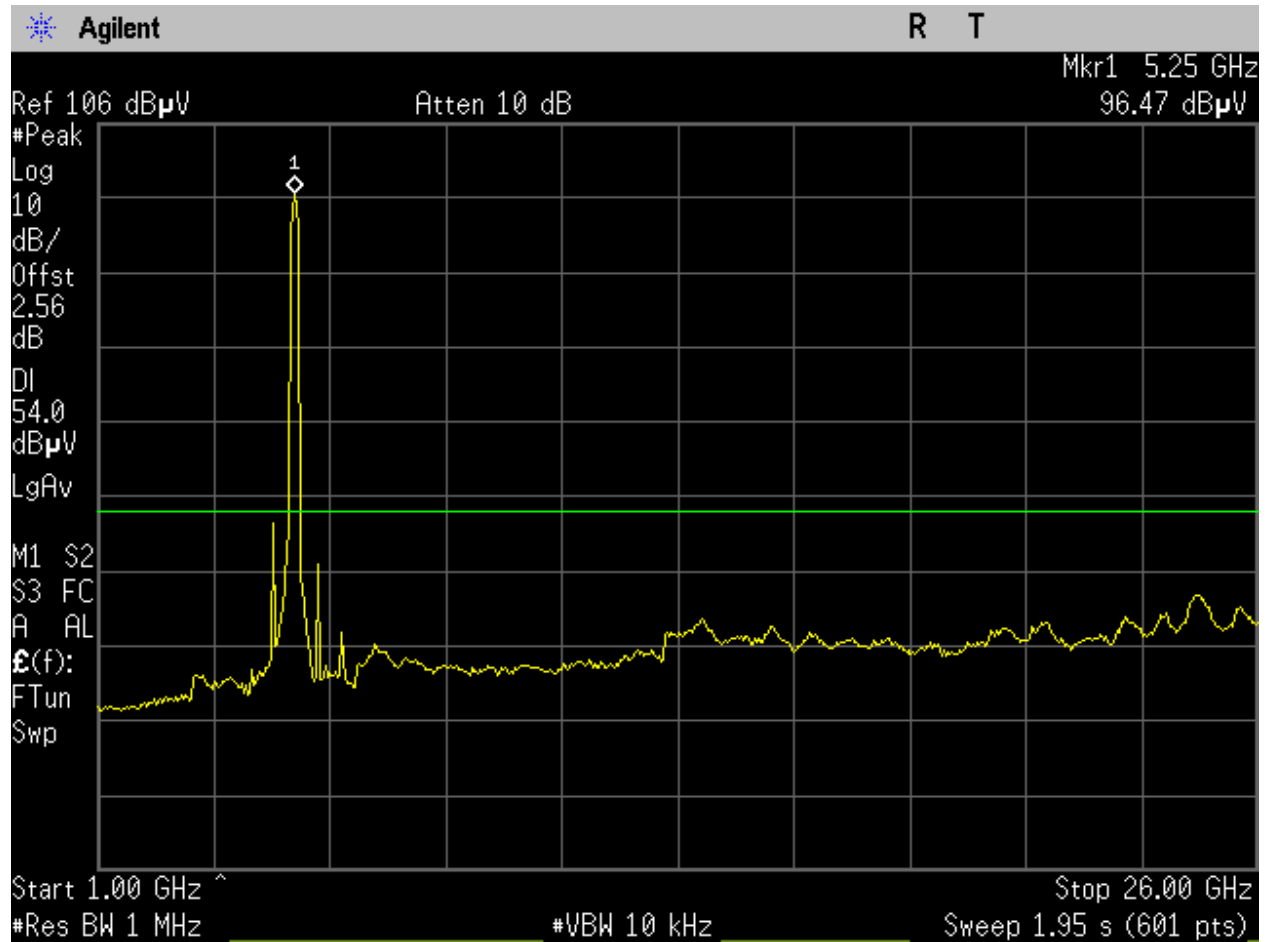


Figure 487: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

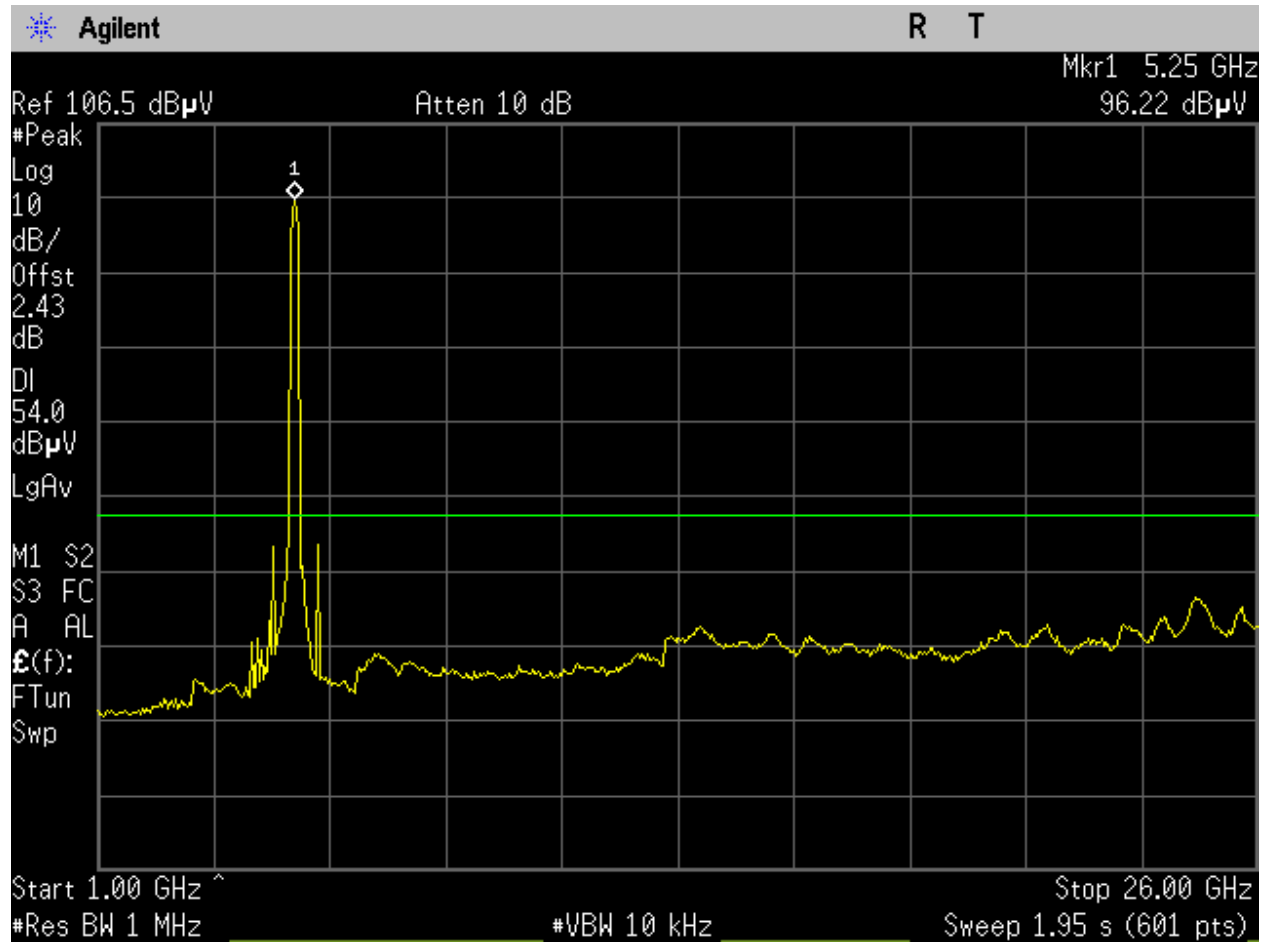


Figure 488: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

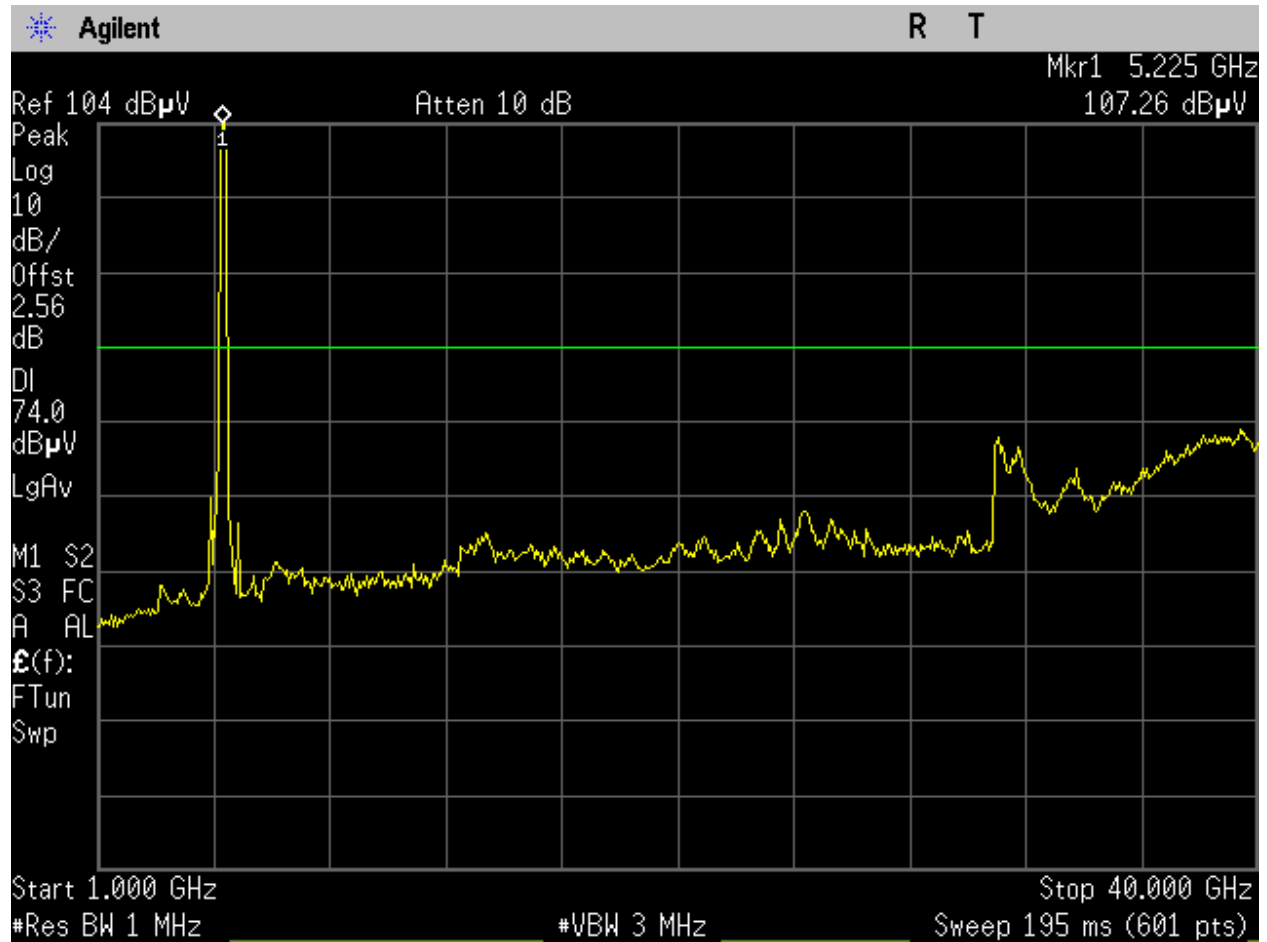


Figure 489: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 1.

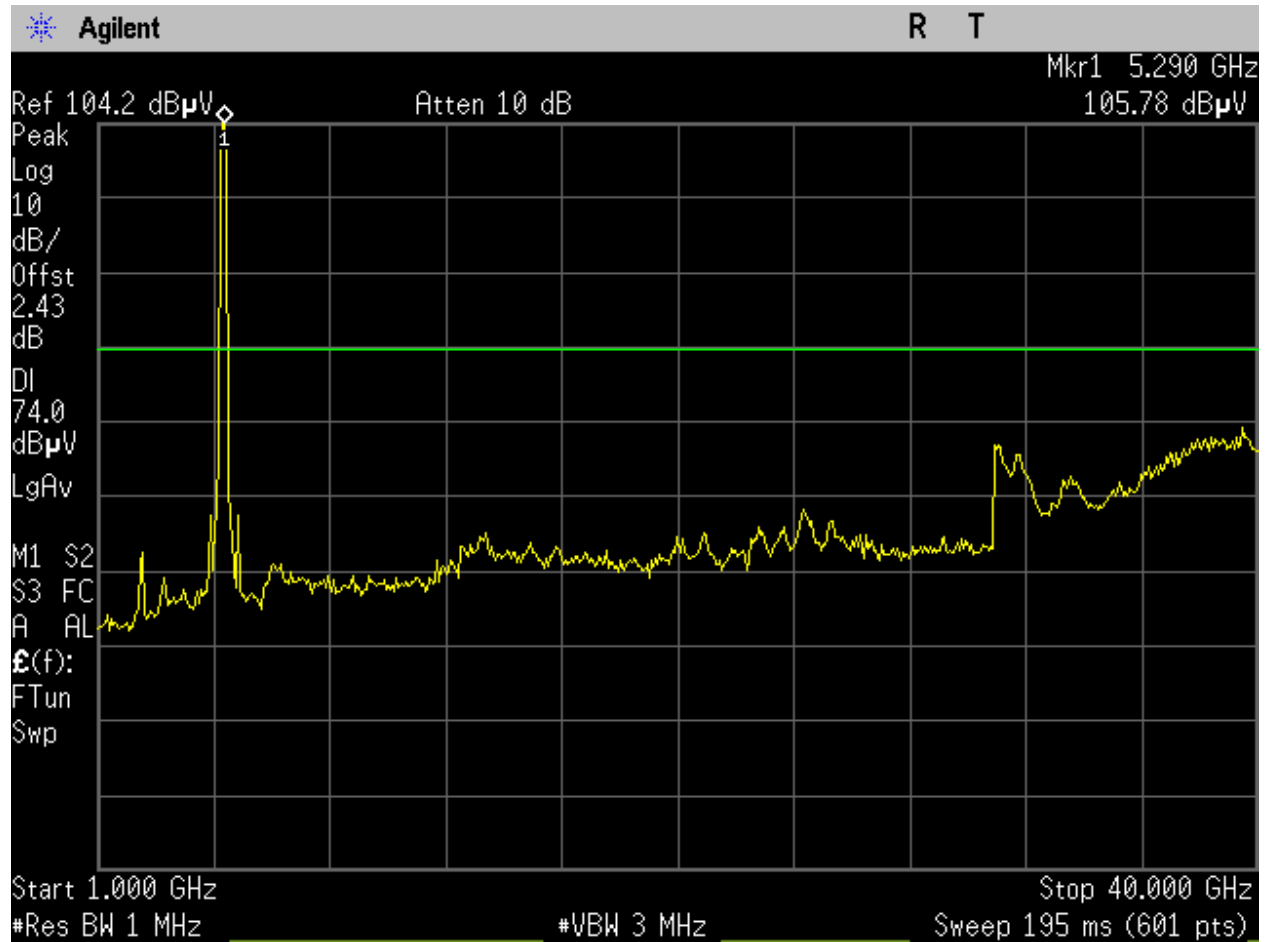


Figure 490: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 2.

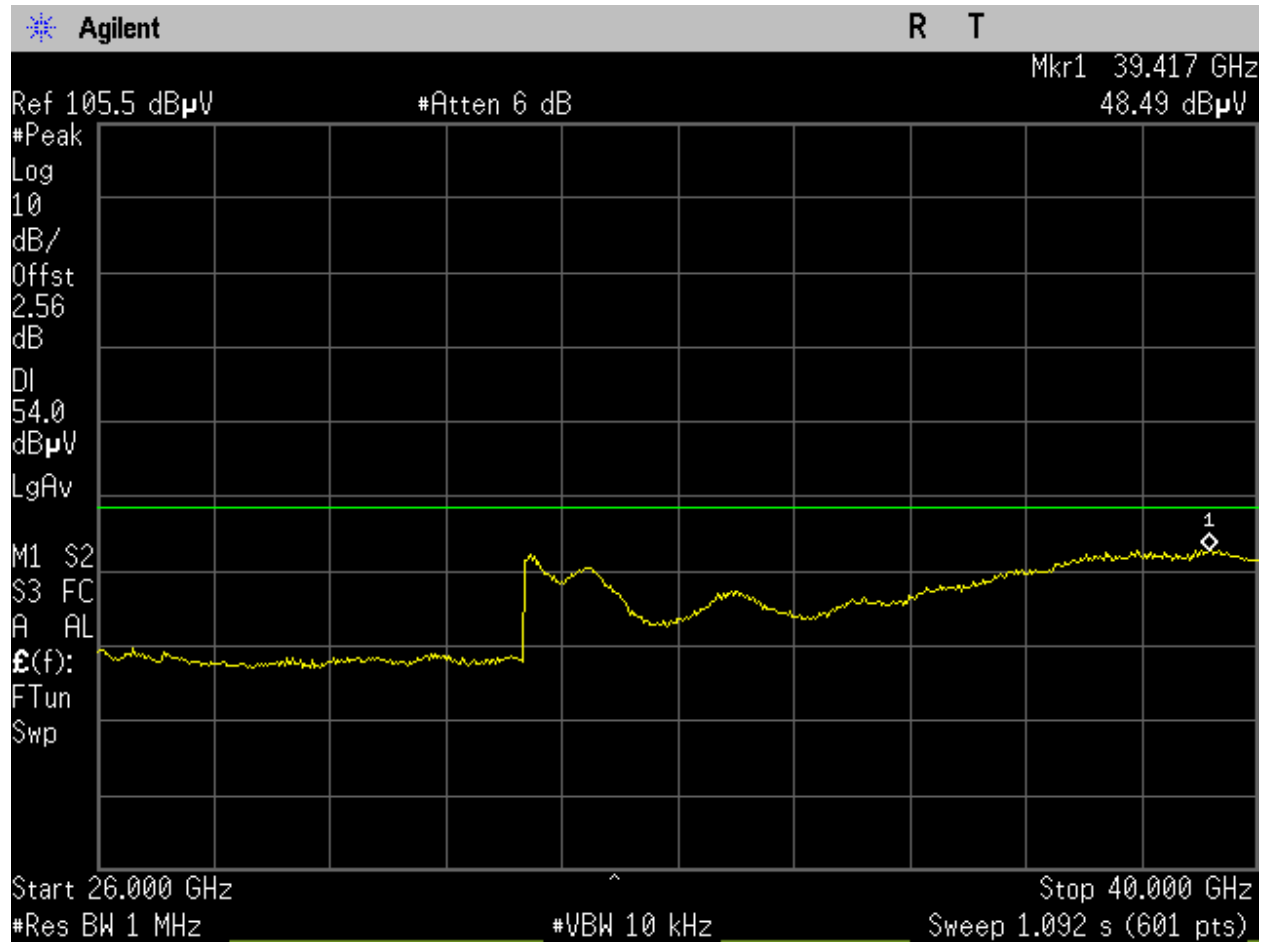


Figure 491: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

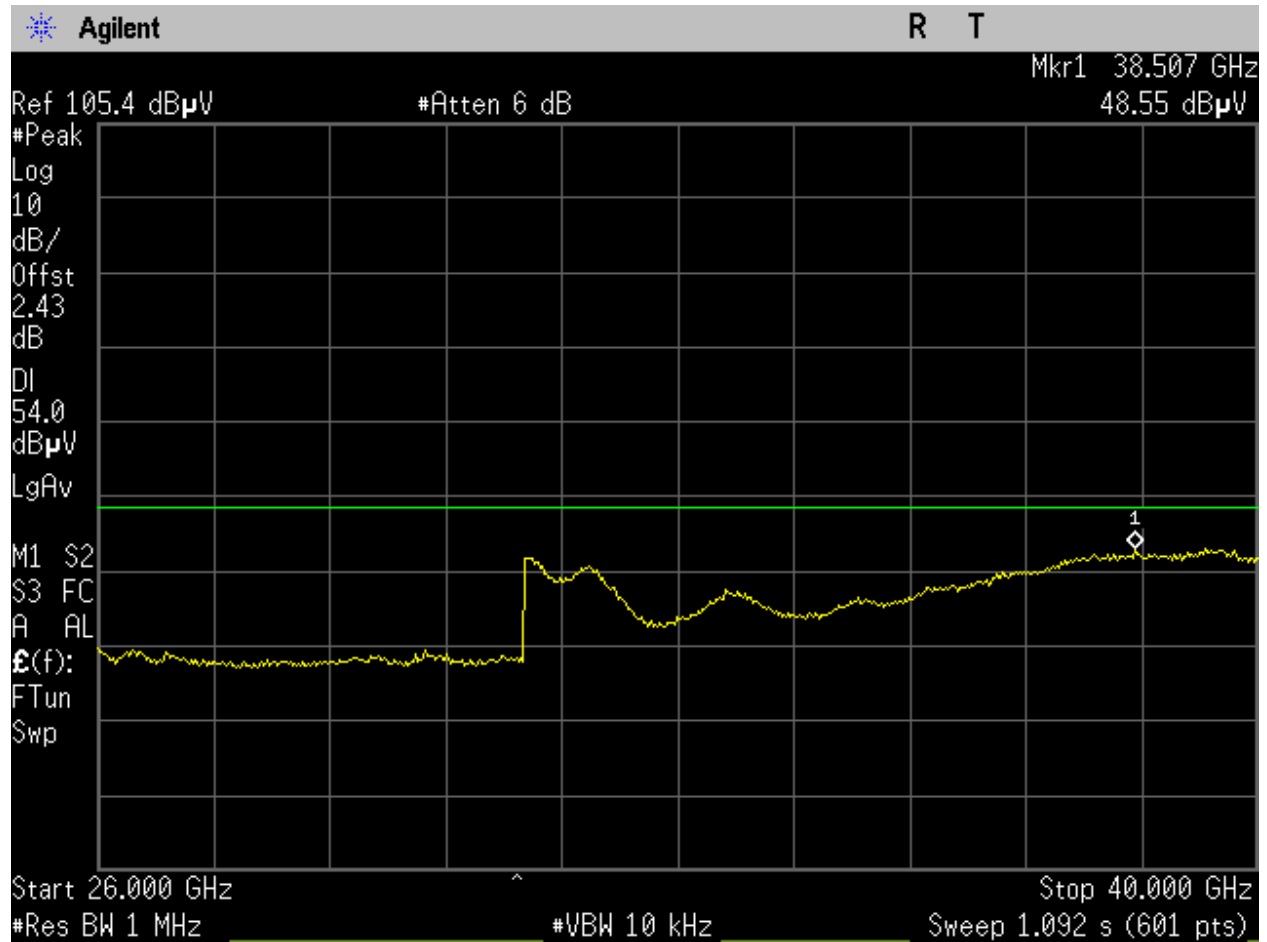


Figure 492: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

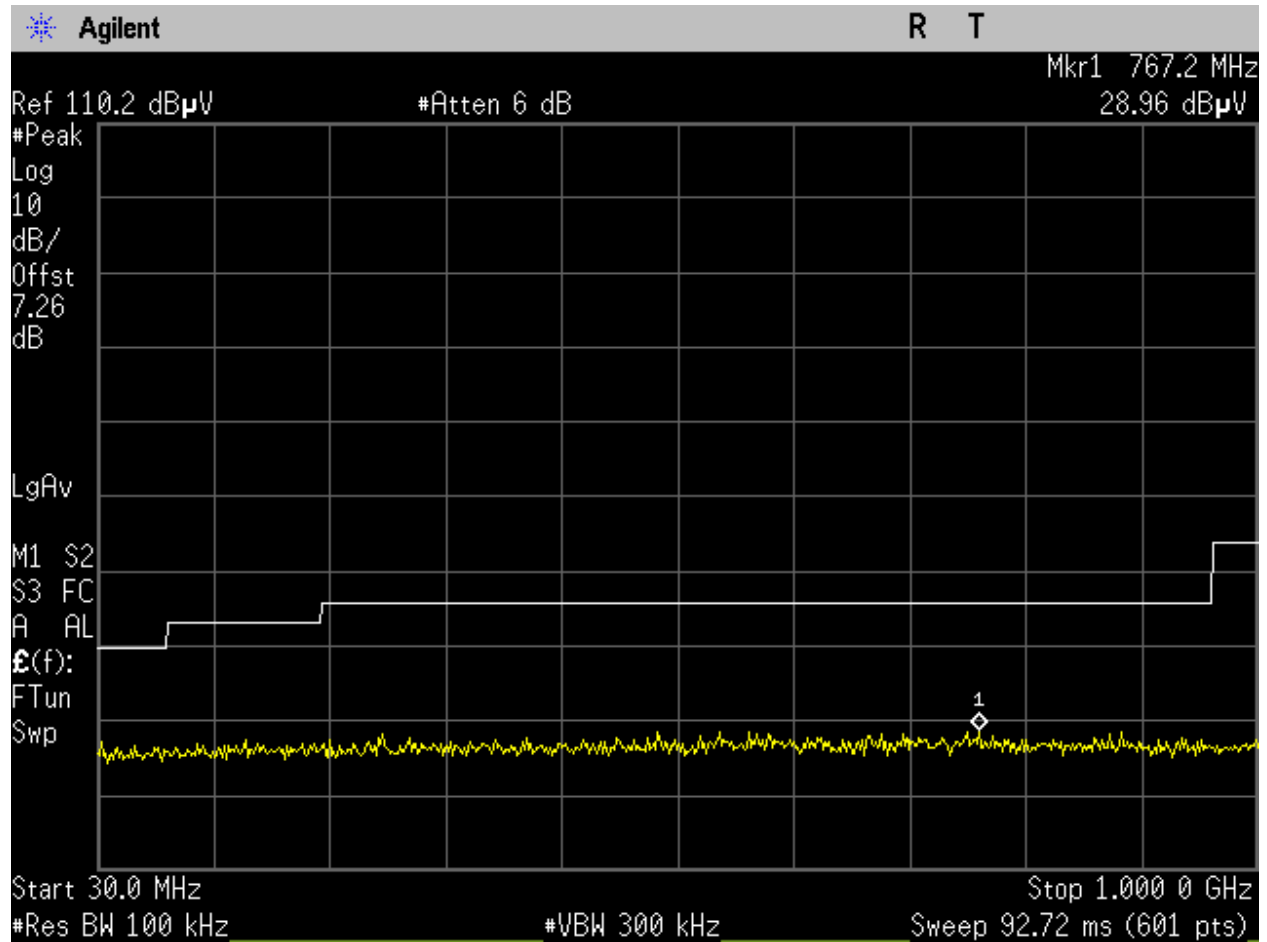


Figure 493: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

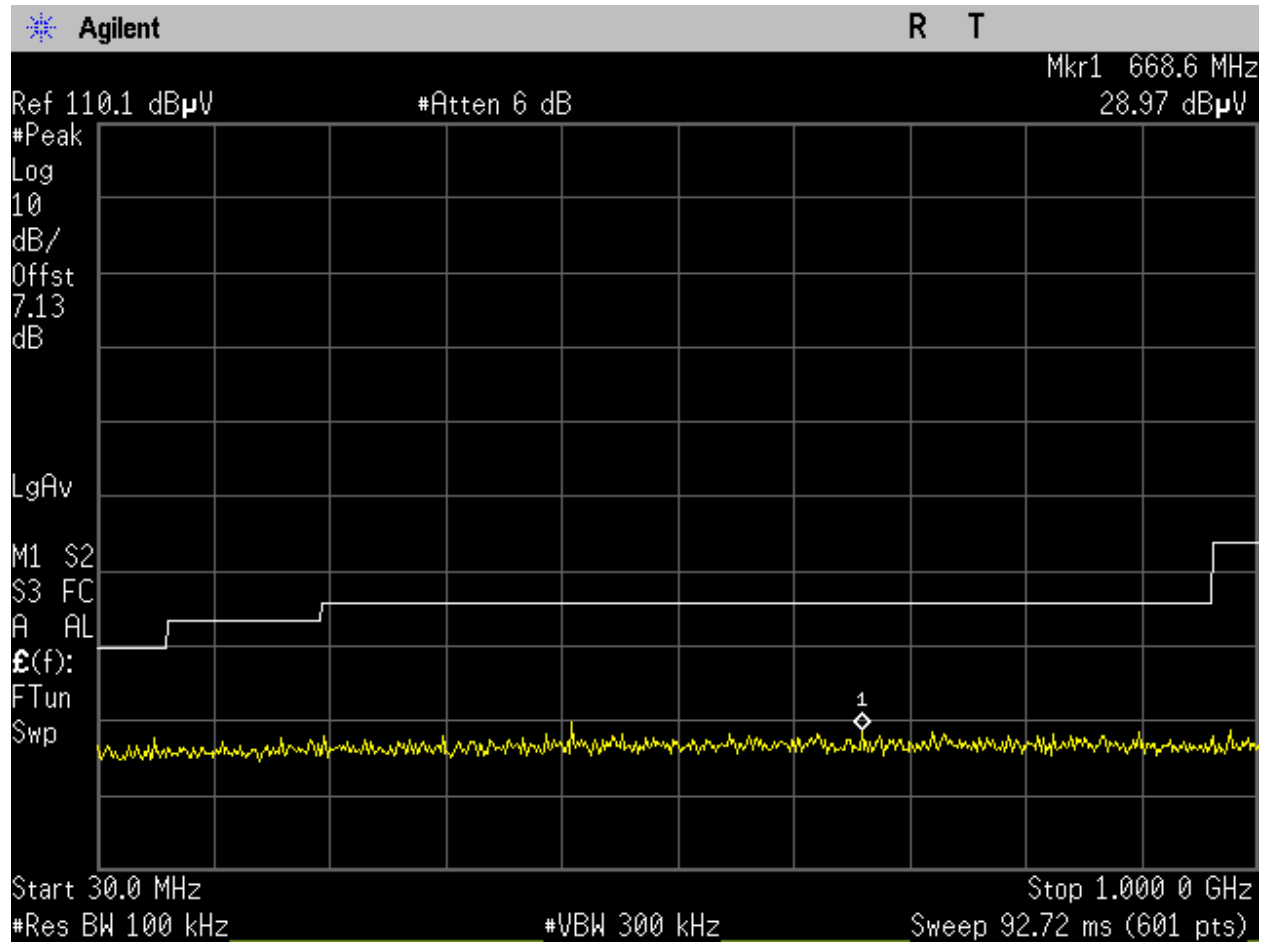


Figure 494: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

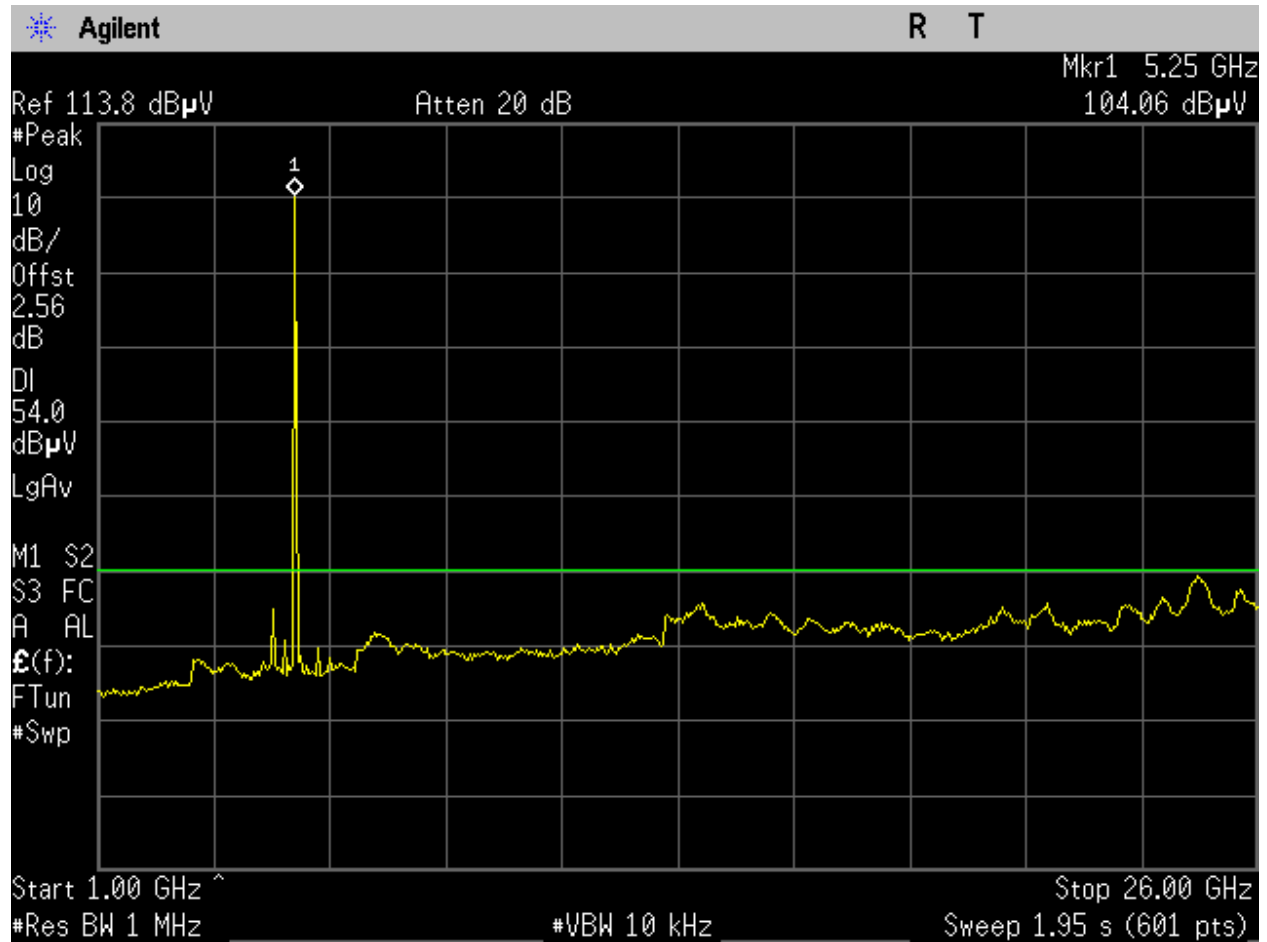


Figure 495: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_15.209_1-26GHz avg_Port 1.

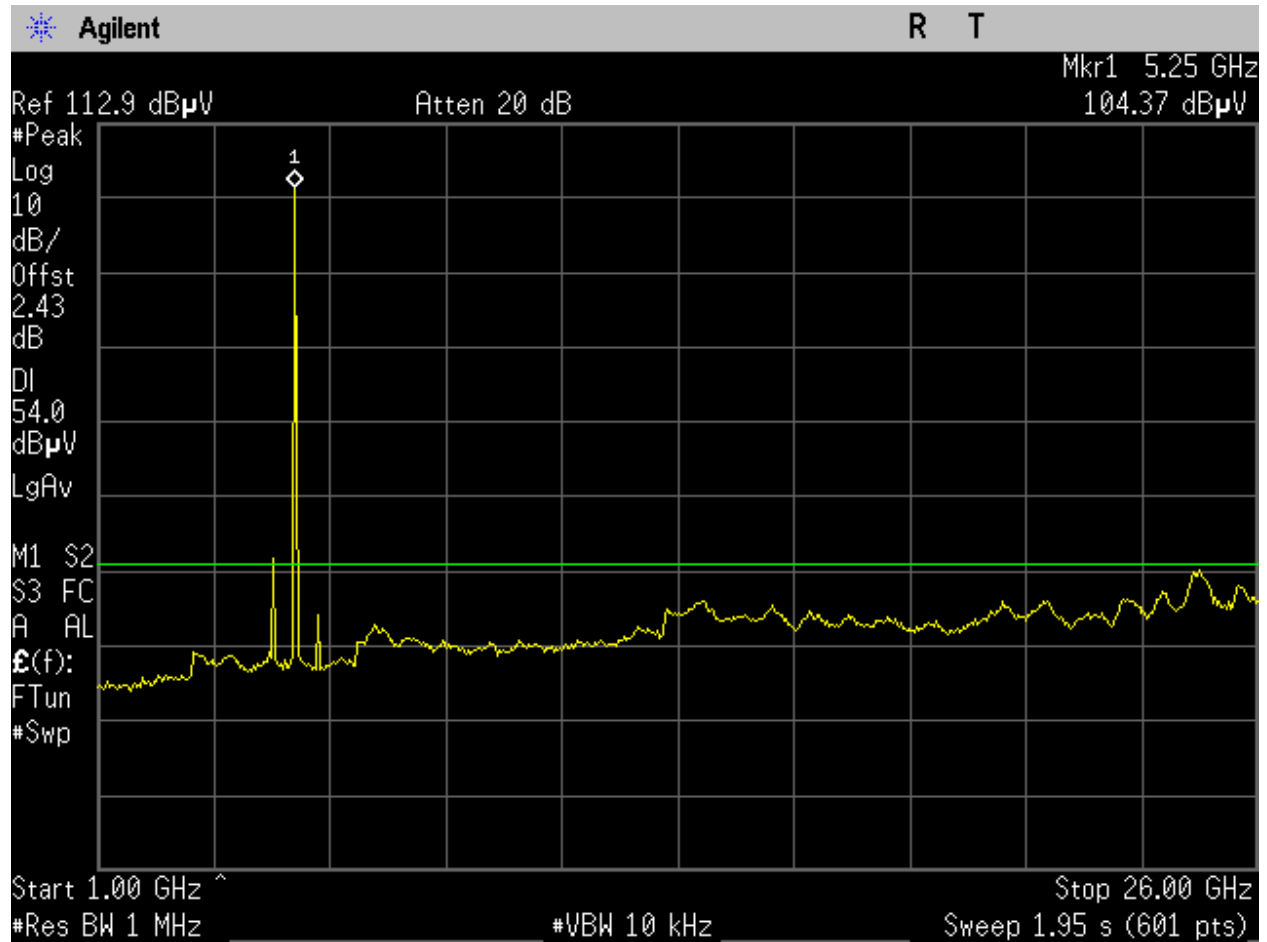


Figure 496: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_15.209_1-26GHz avg_Port 2.

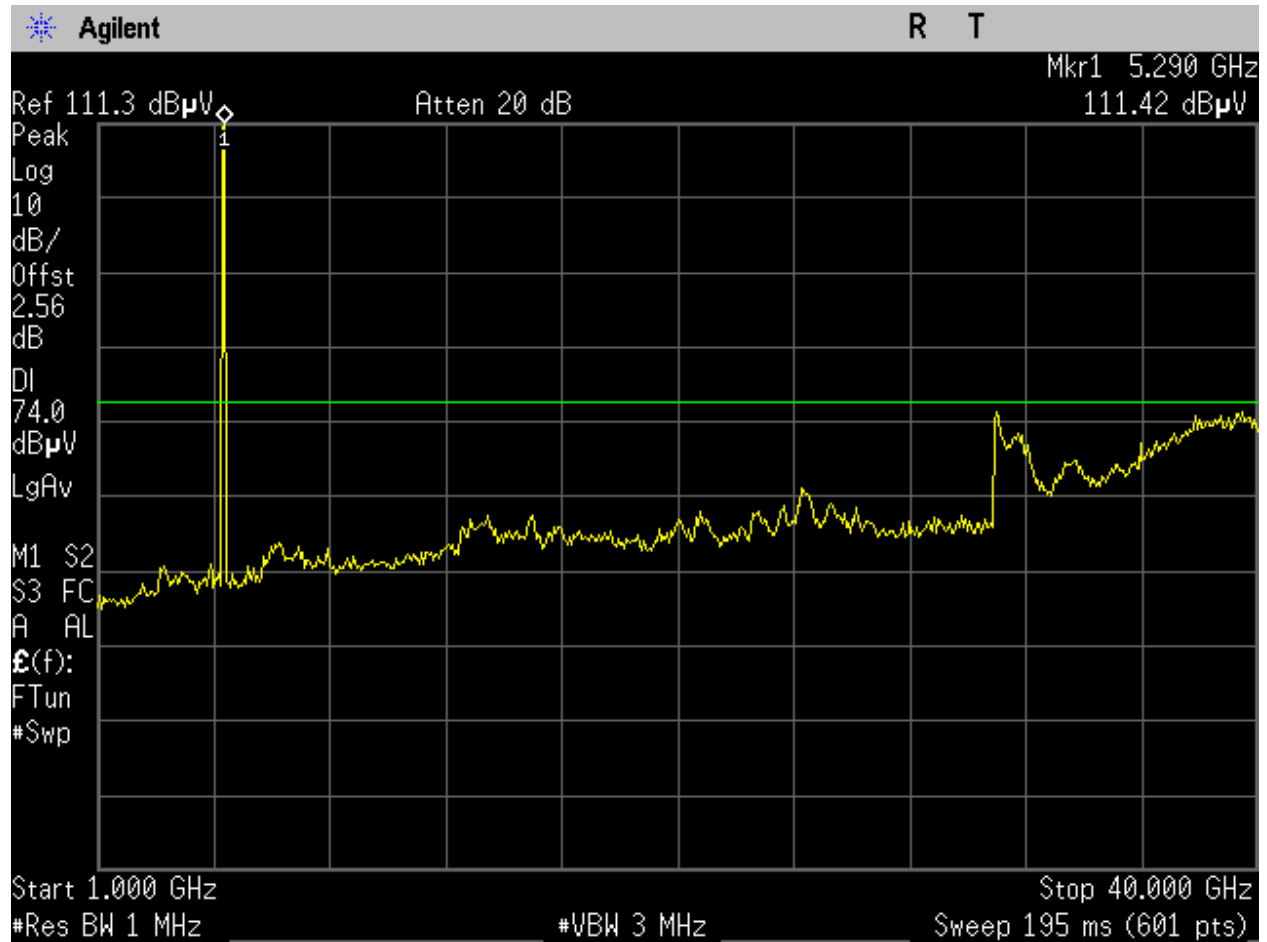


Figure 497: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_15.209_1-40GHz_Peak_Port 1.

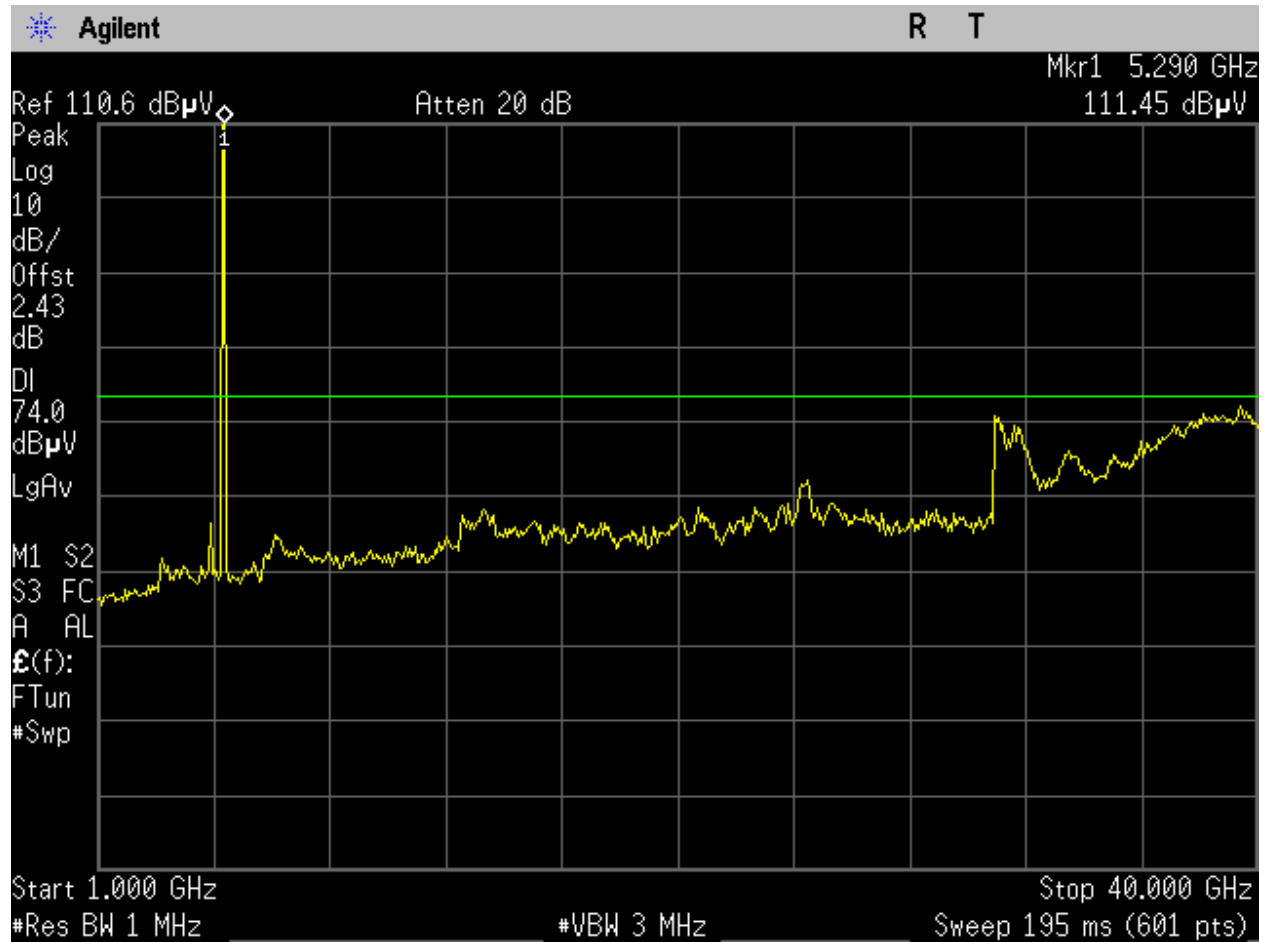


Figure 498: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_15.209_1-40GHz_Peak_Port 2.

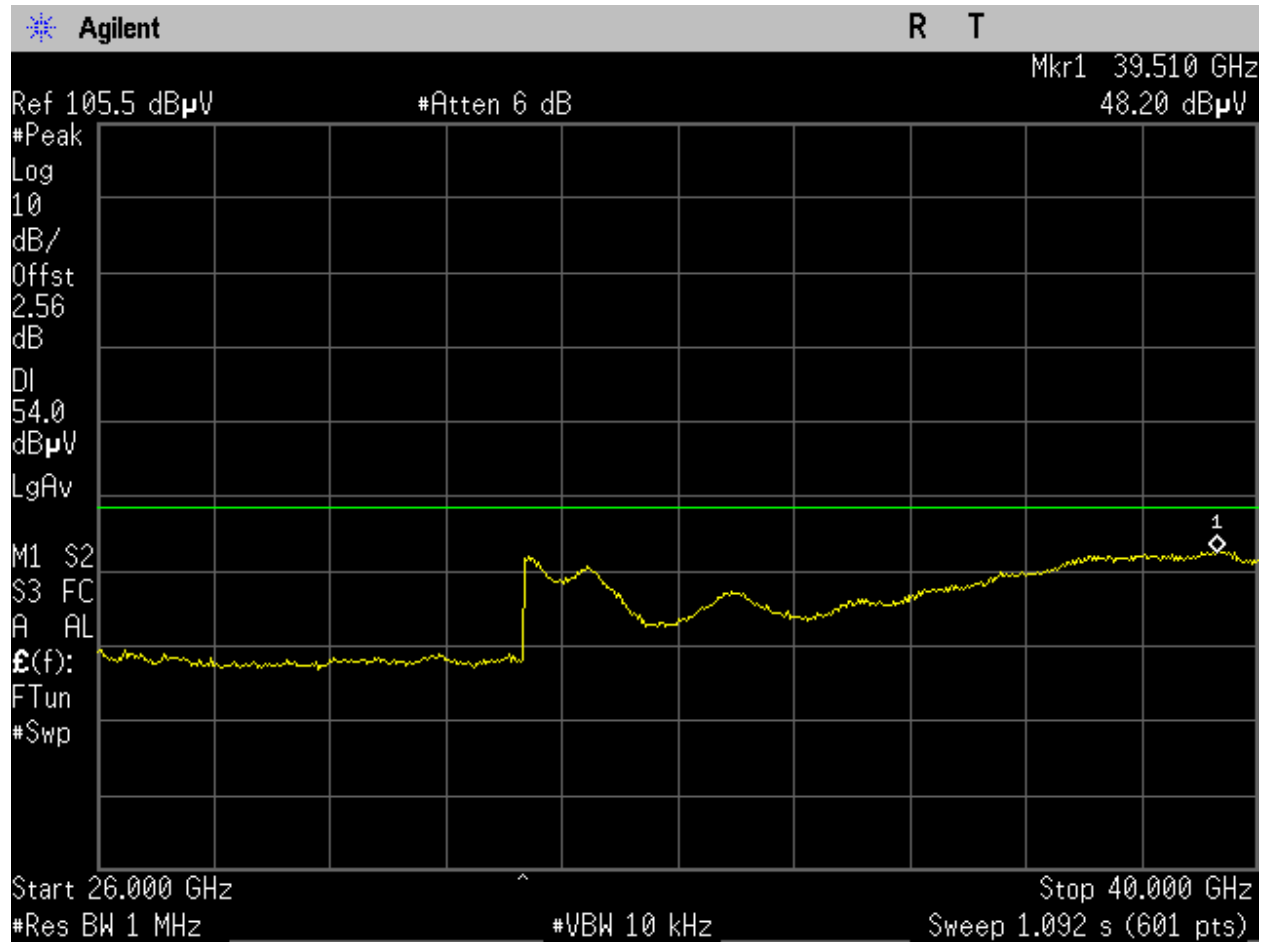


Figure 499: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 1.

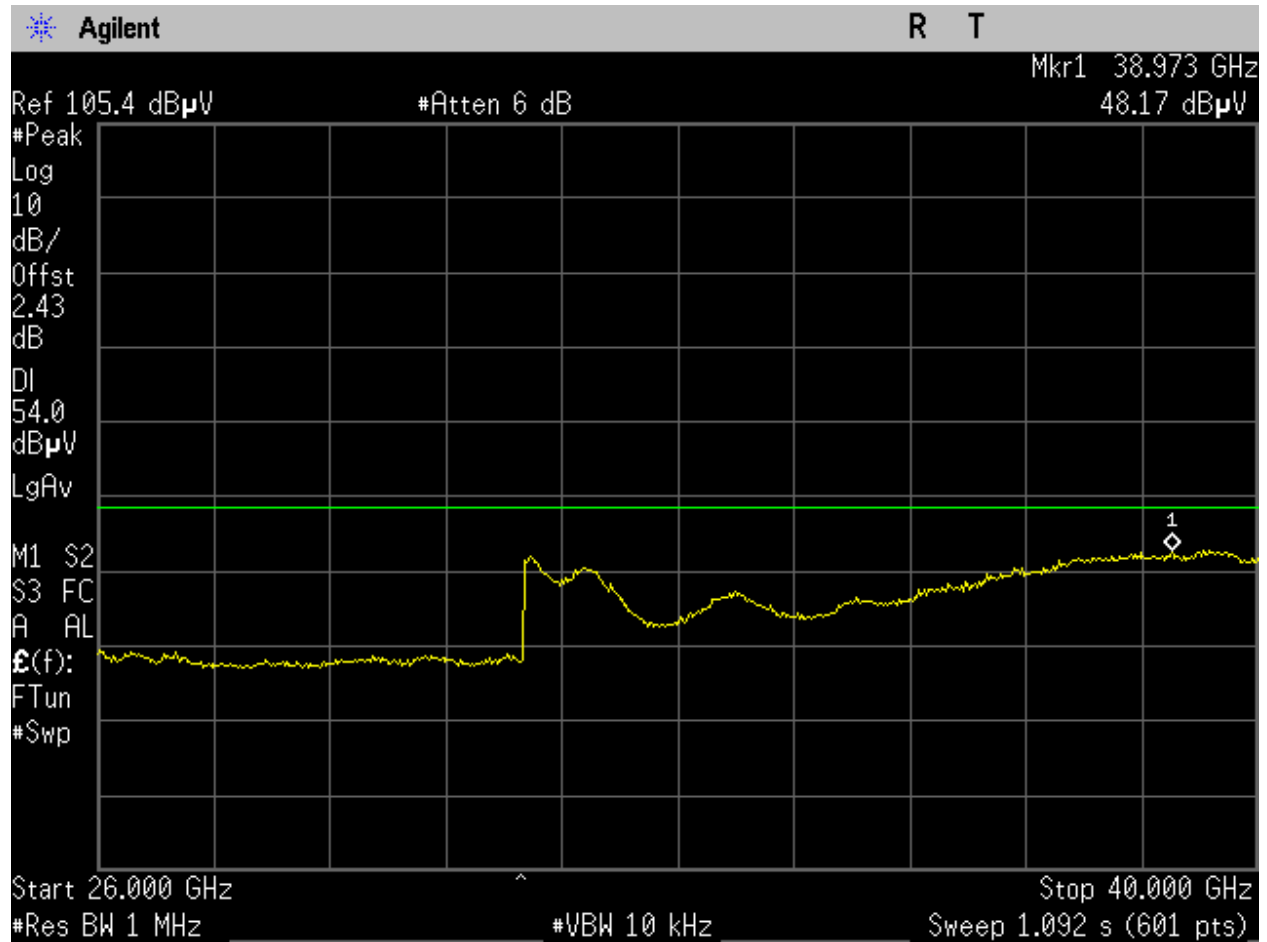


Figure 500: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 2.

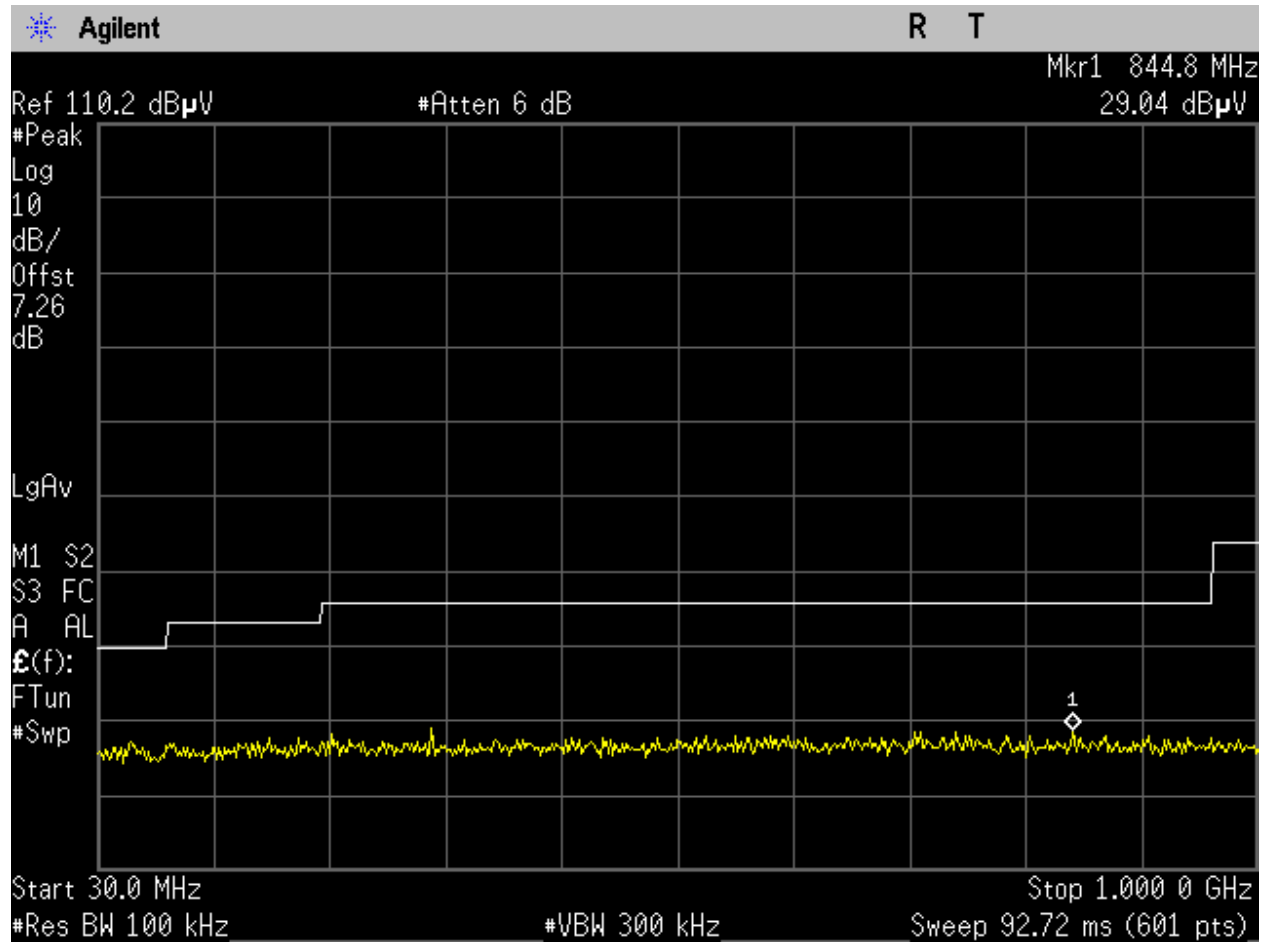


Figure 501: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 1.

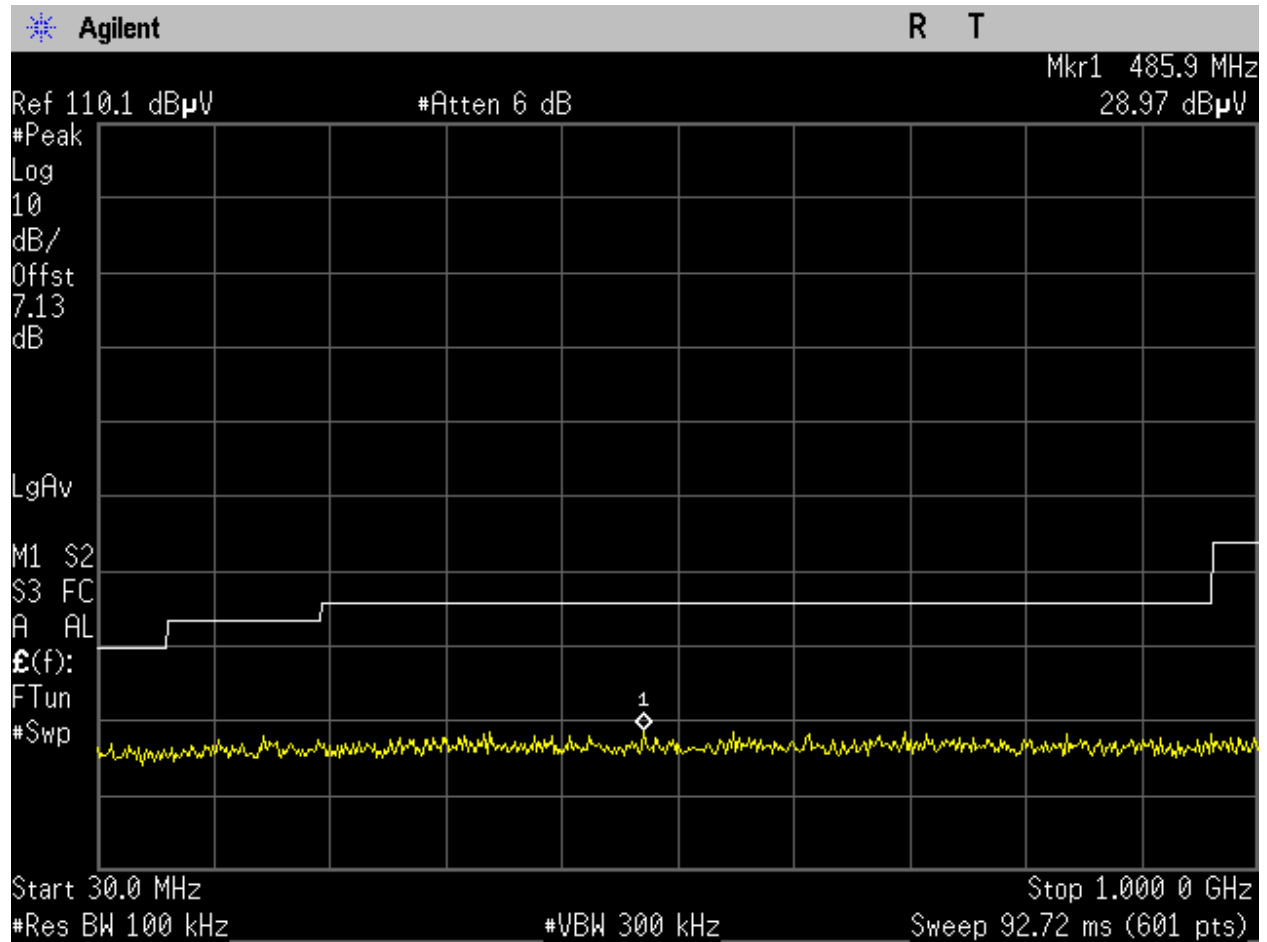


Figure 502: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 2.

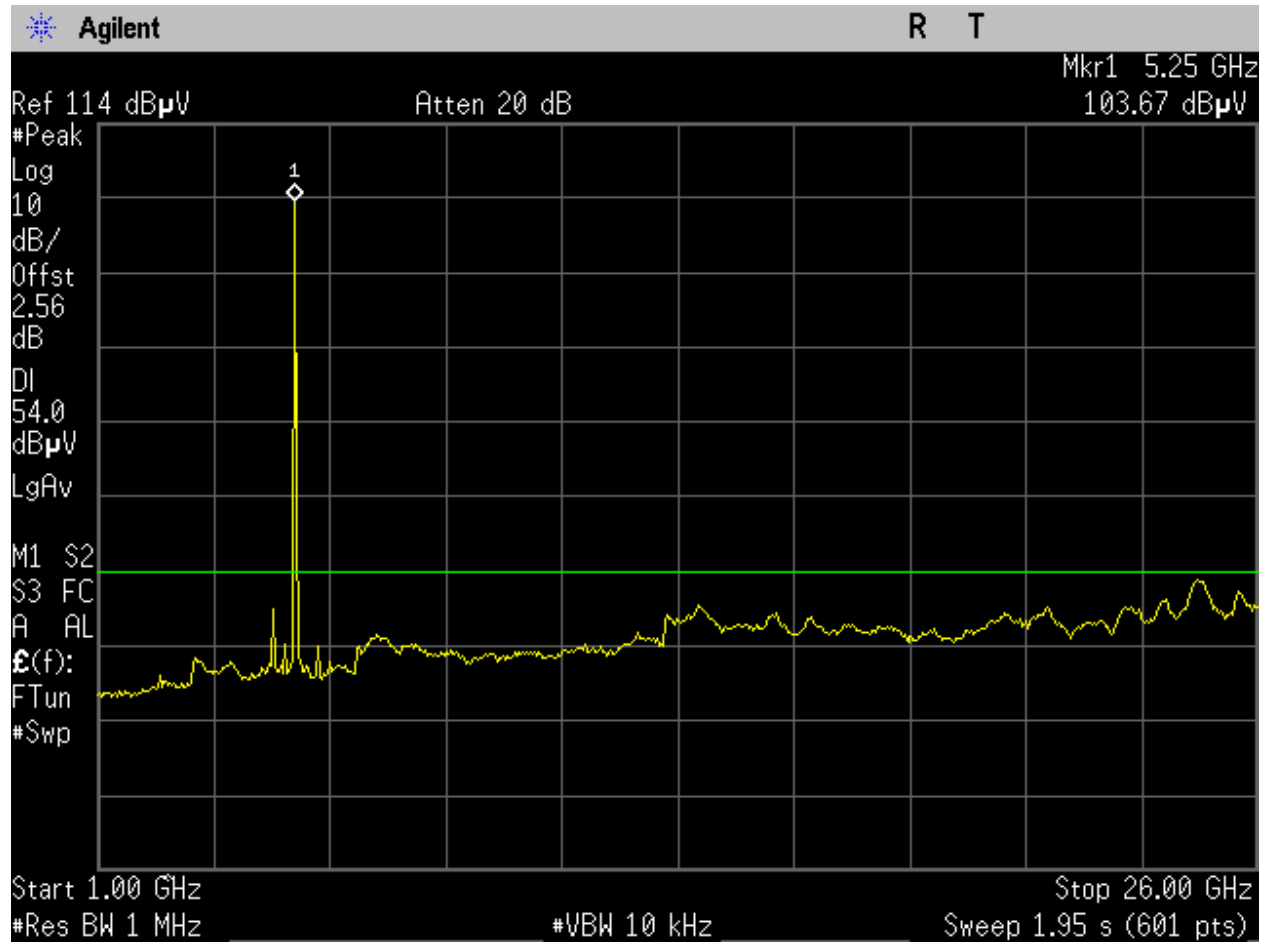


Figure 503: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

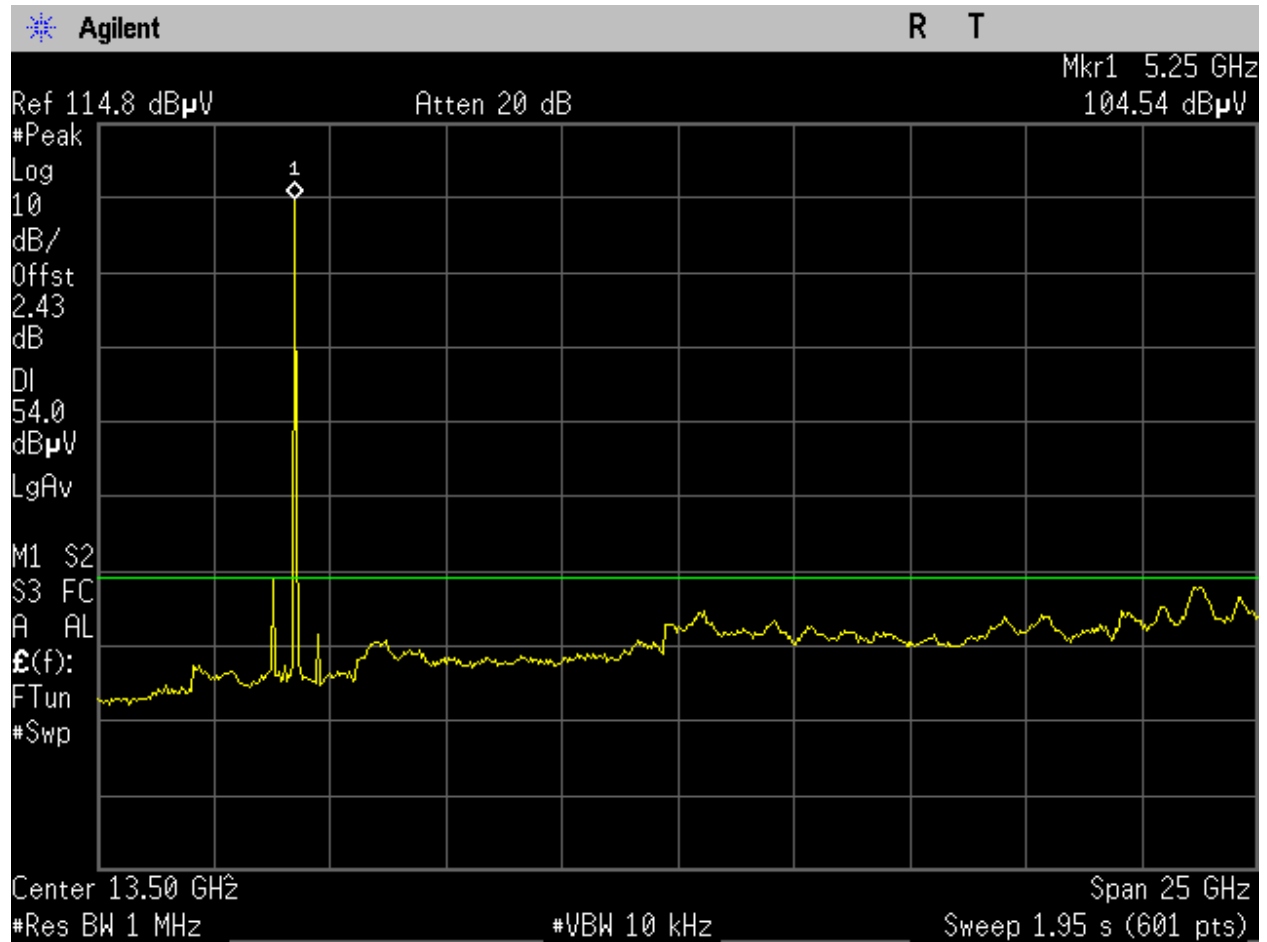


Figure 504: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

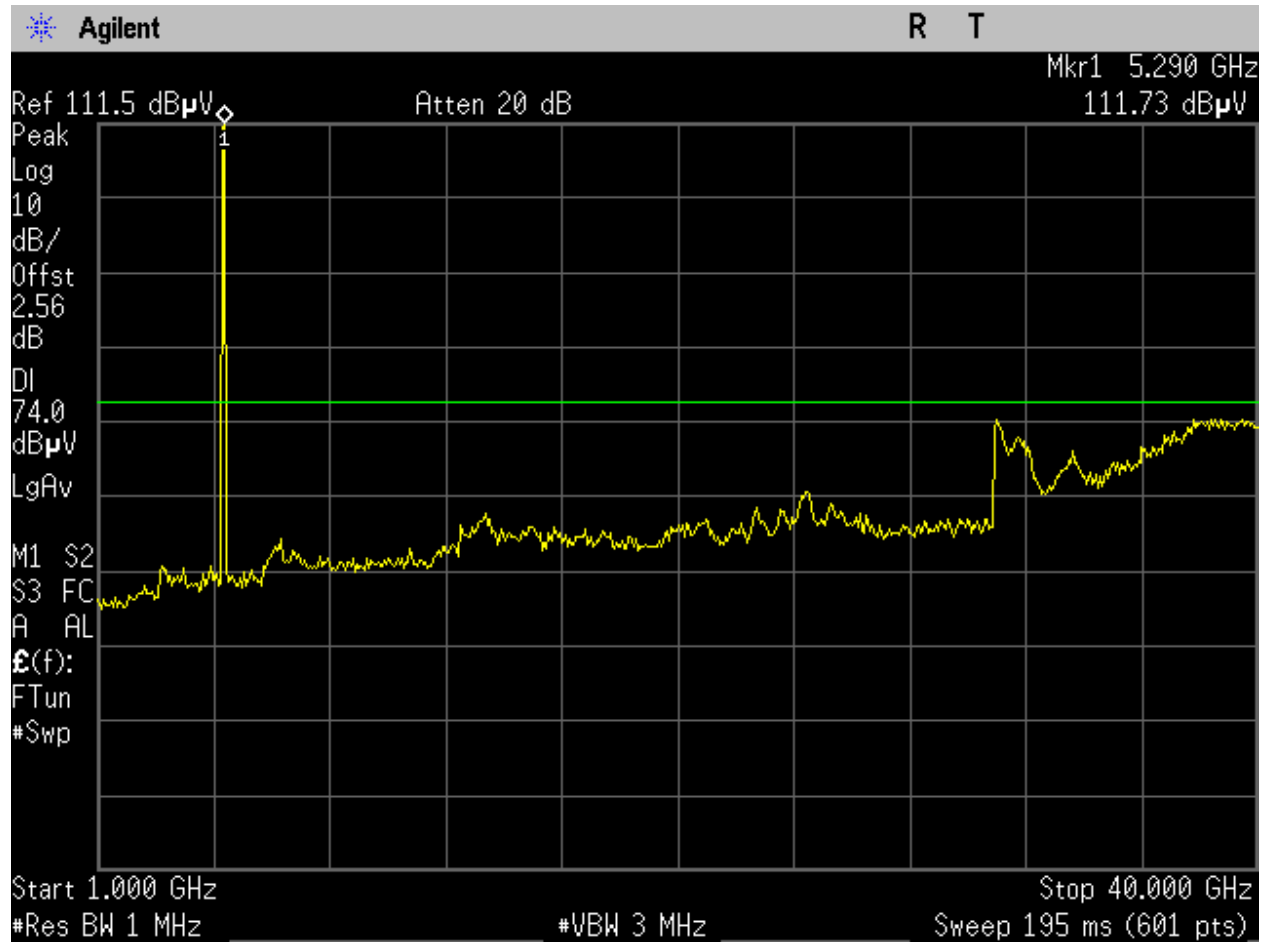


Figure 505: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 1.

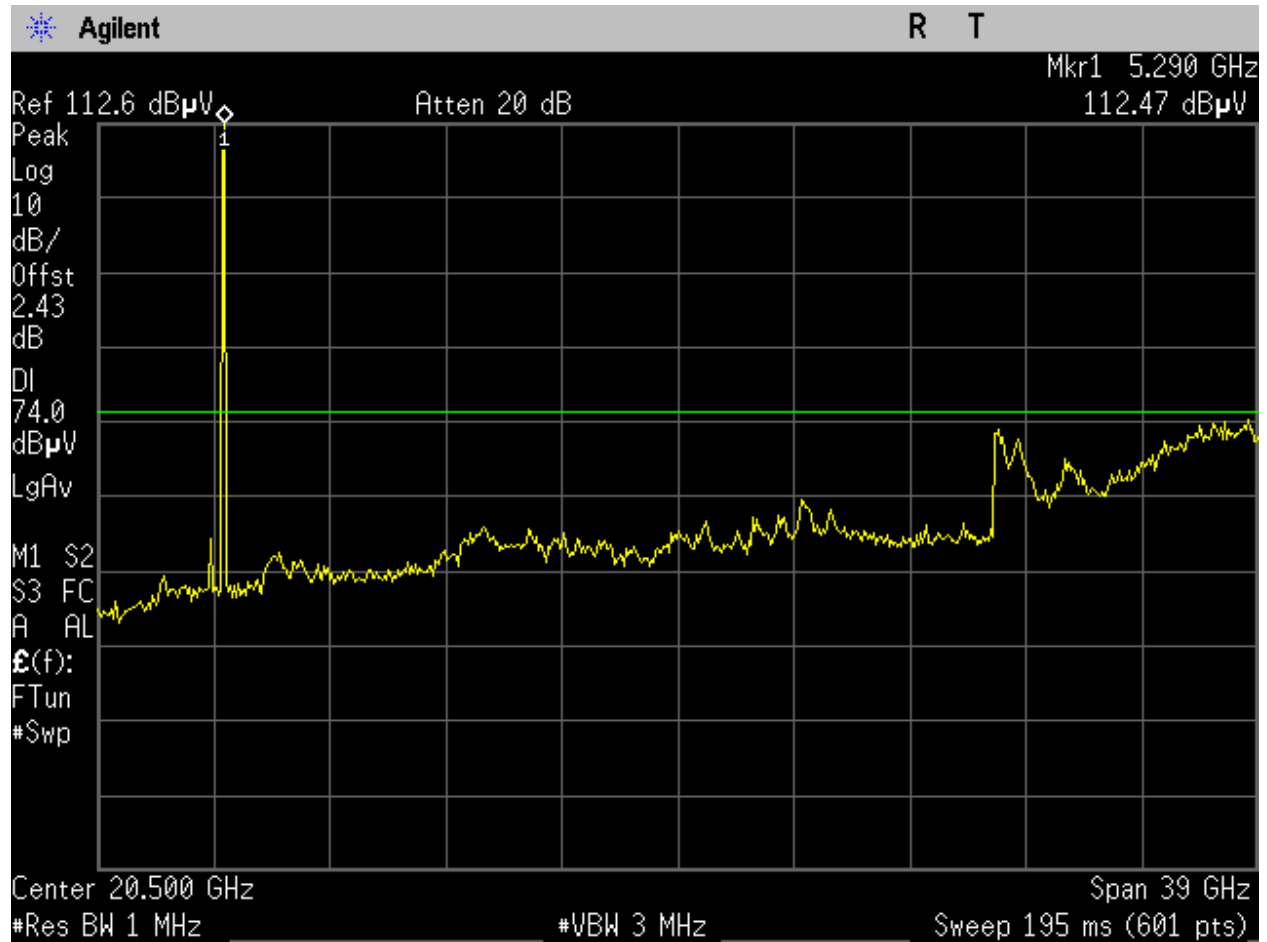


Figure 506: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 2.

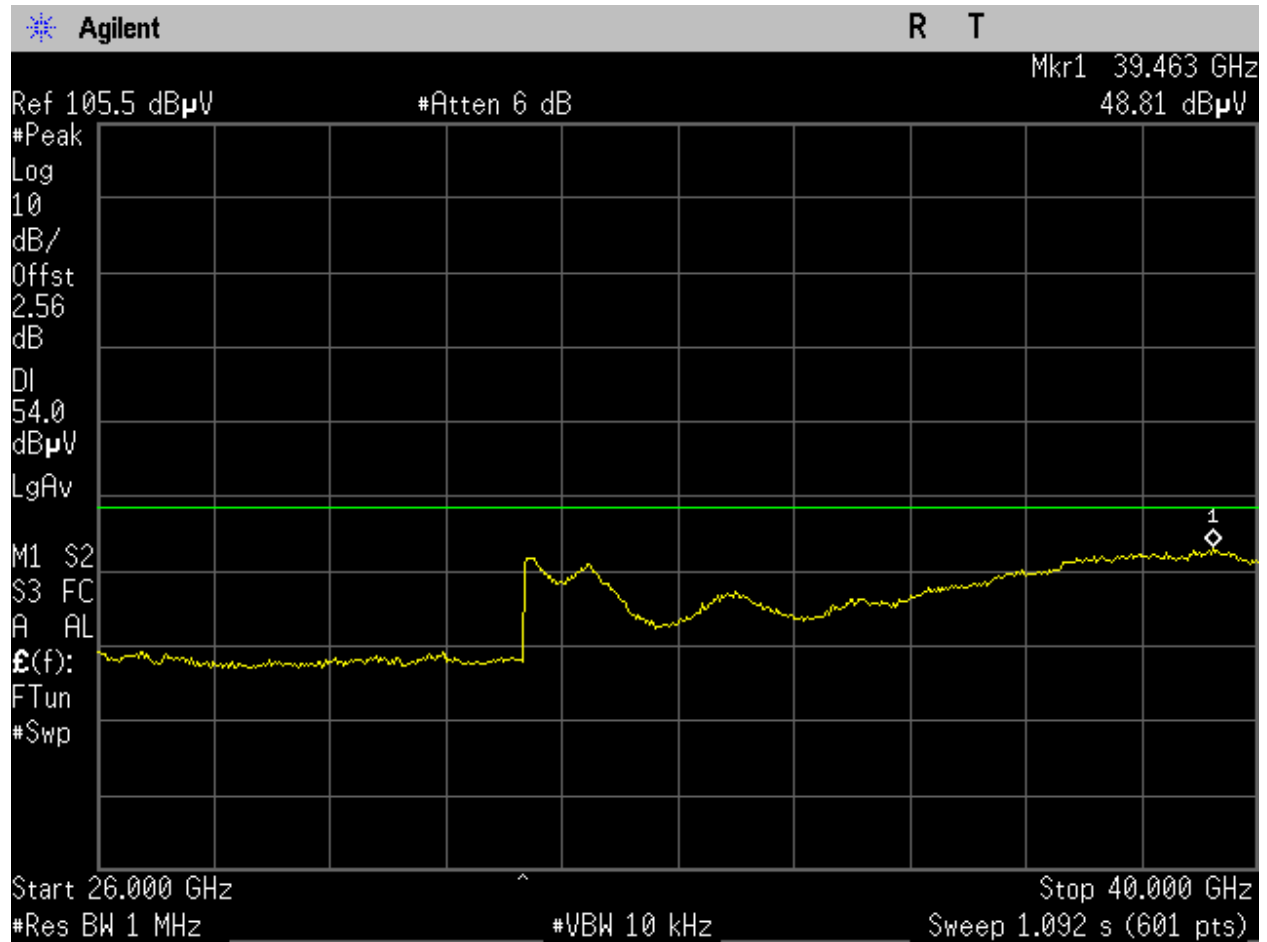


Figure 507: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

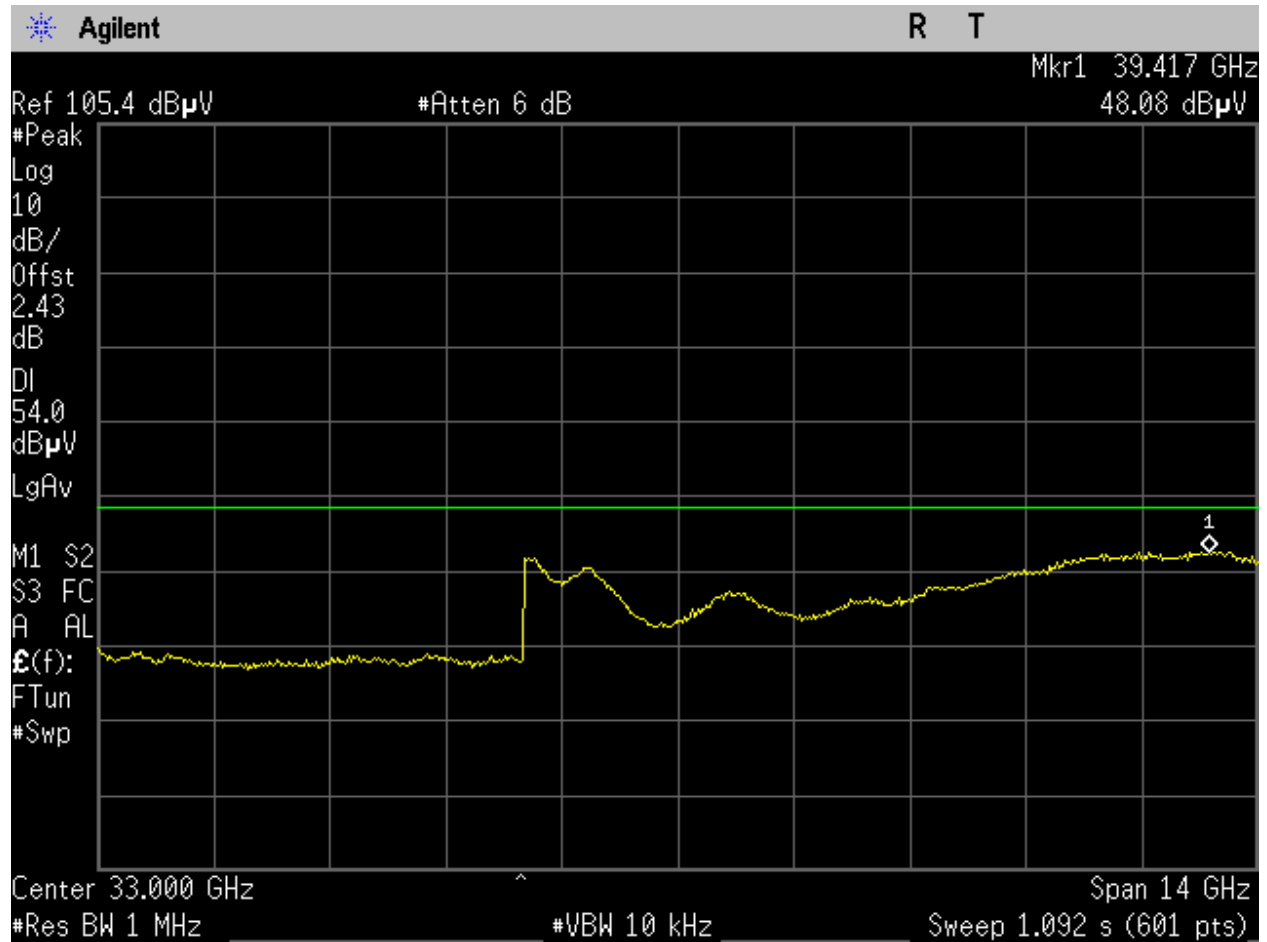


Figure 508: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

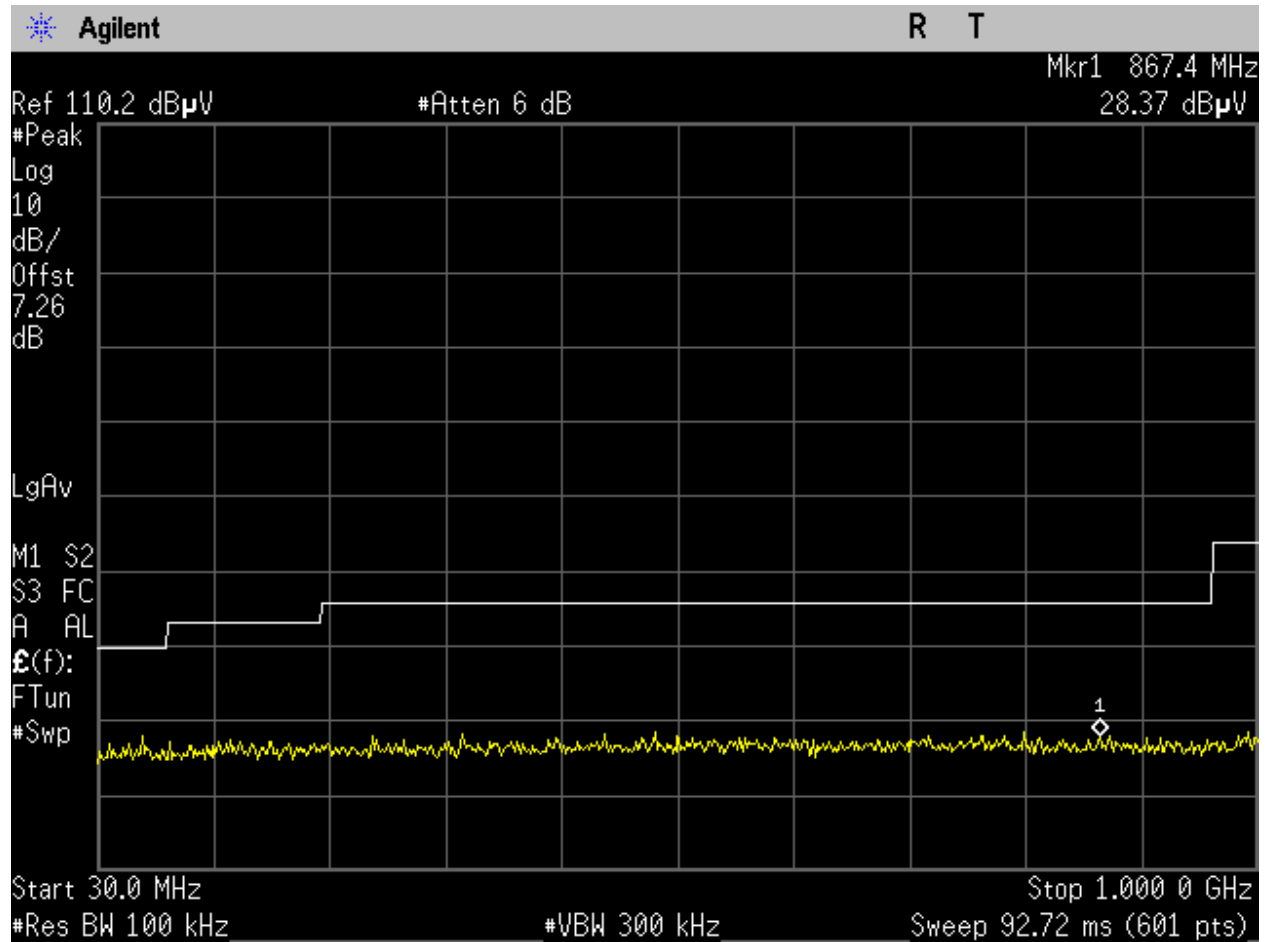


Figure 509: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

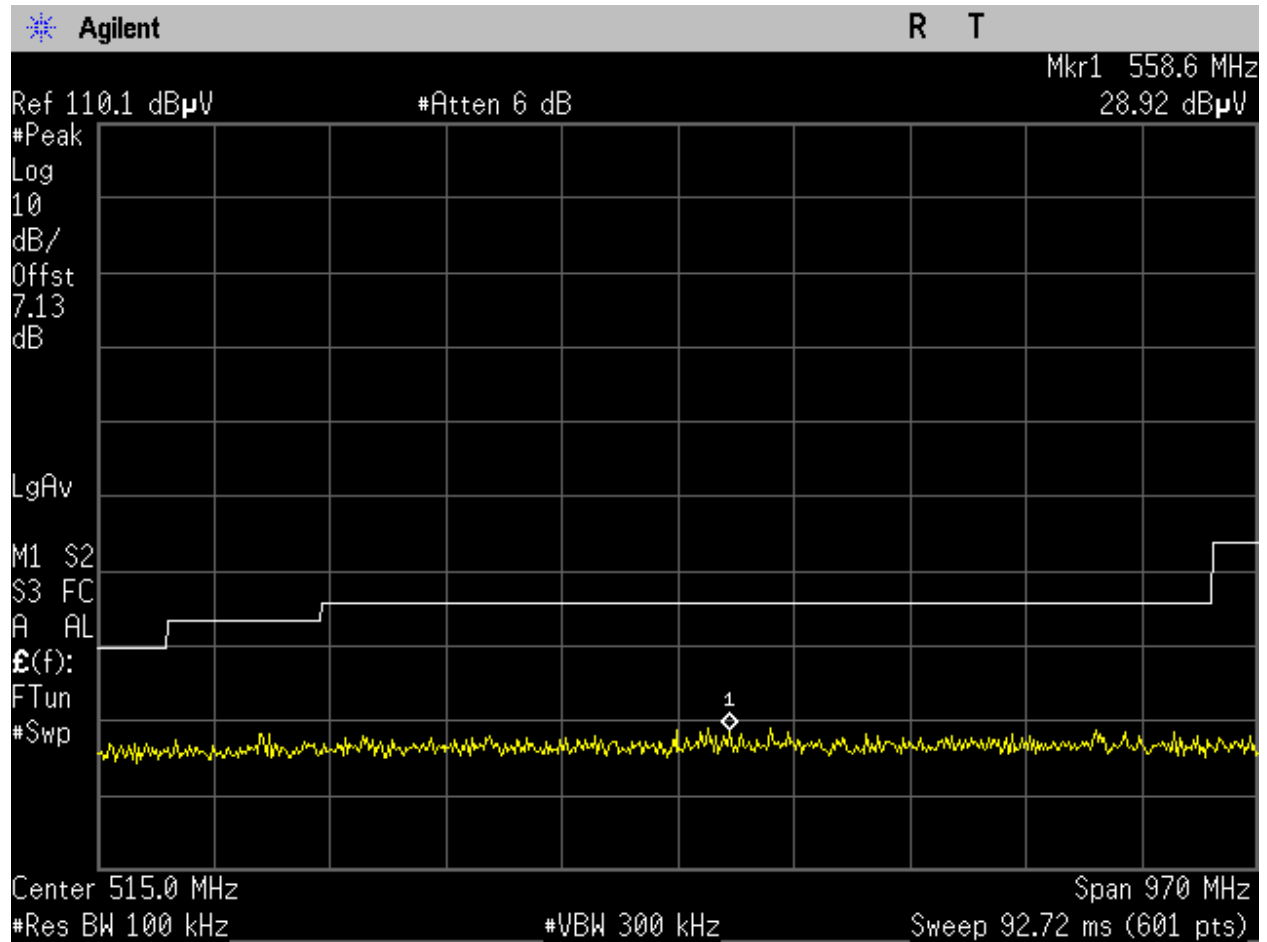


Figure 510: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

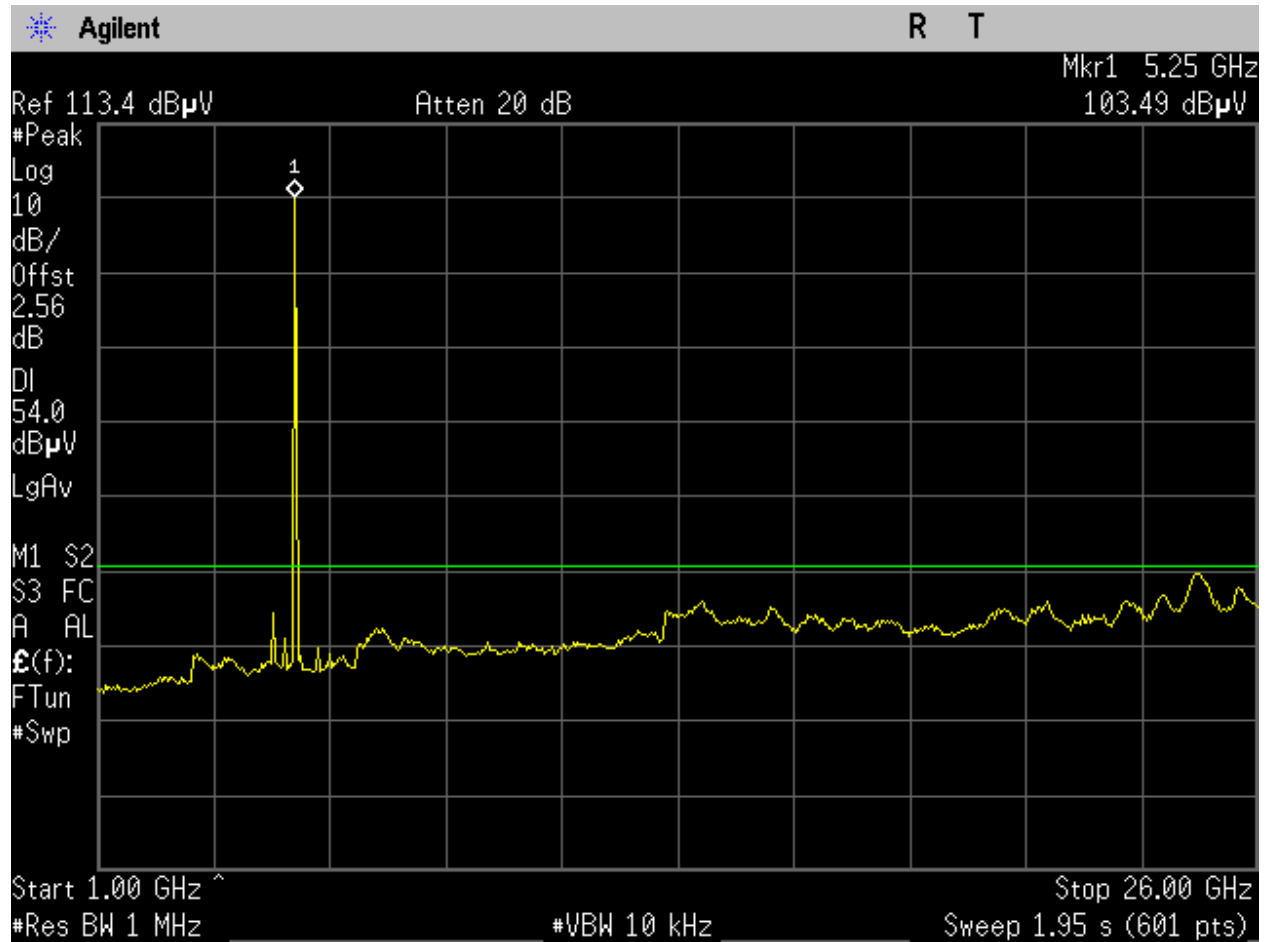


Figure 511: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_15.209_1-26GHz avg_Port 1.

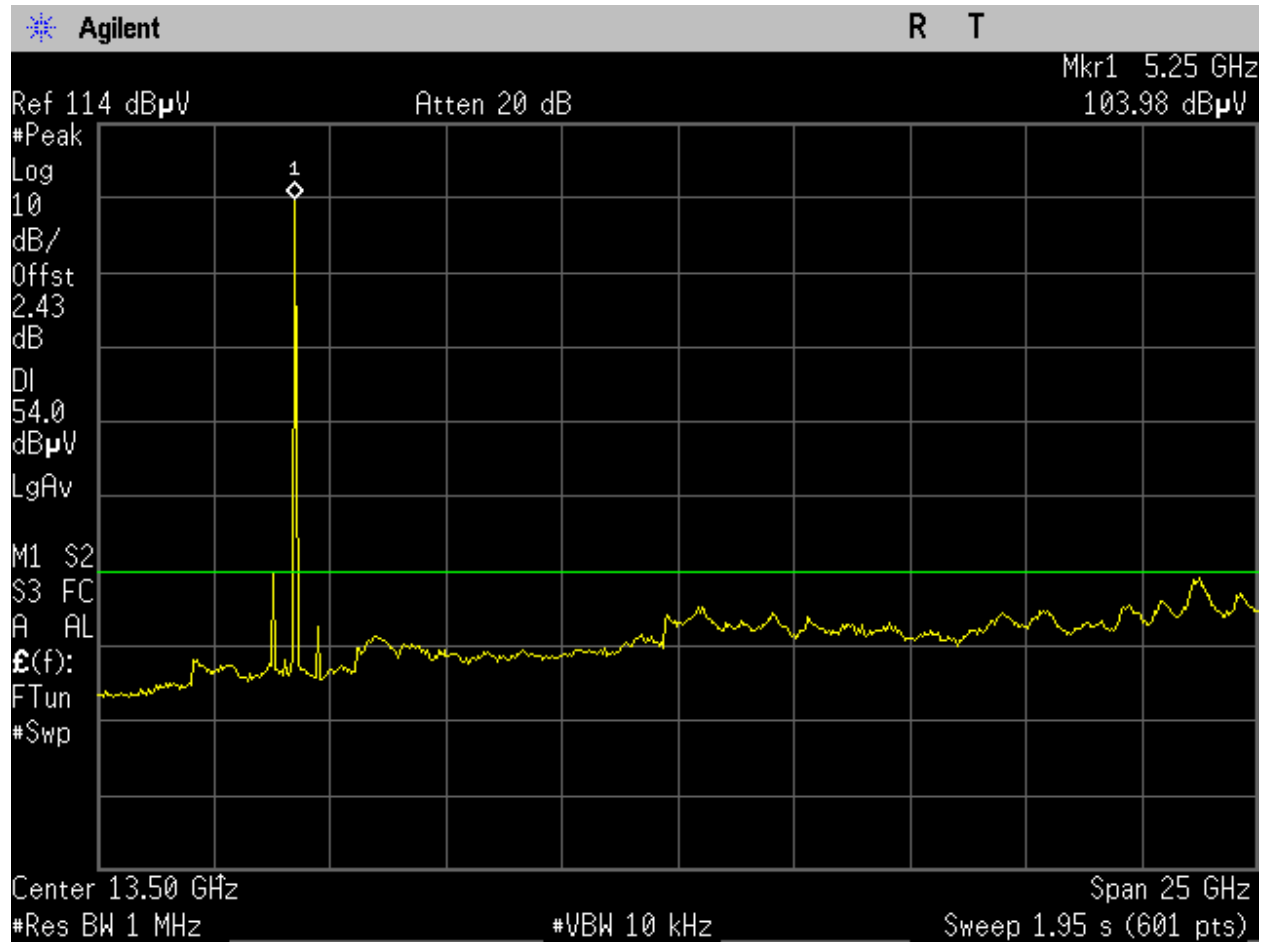


Figure 512: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_15.209_1-26GHz avg_Port 2.

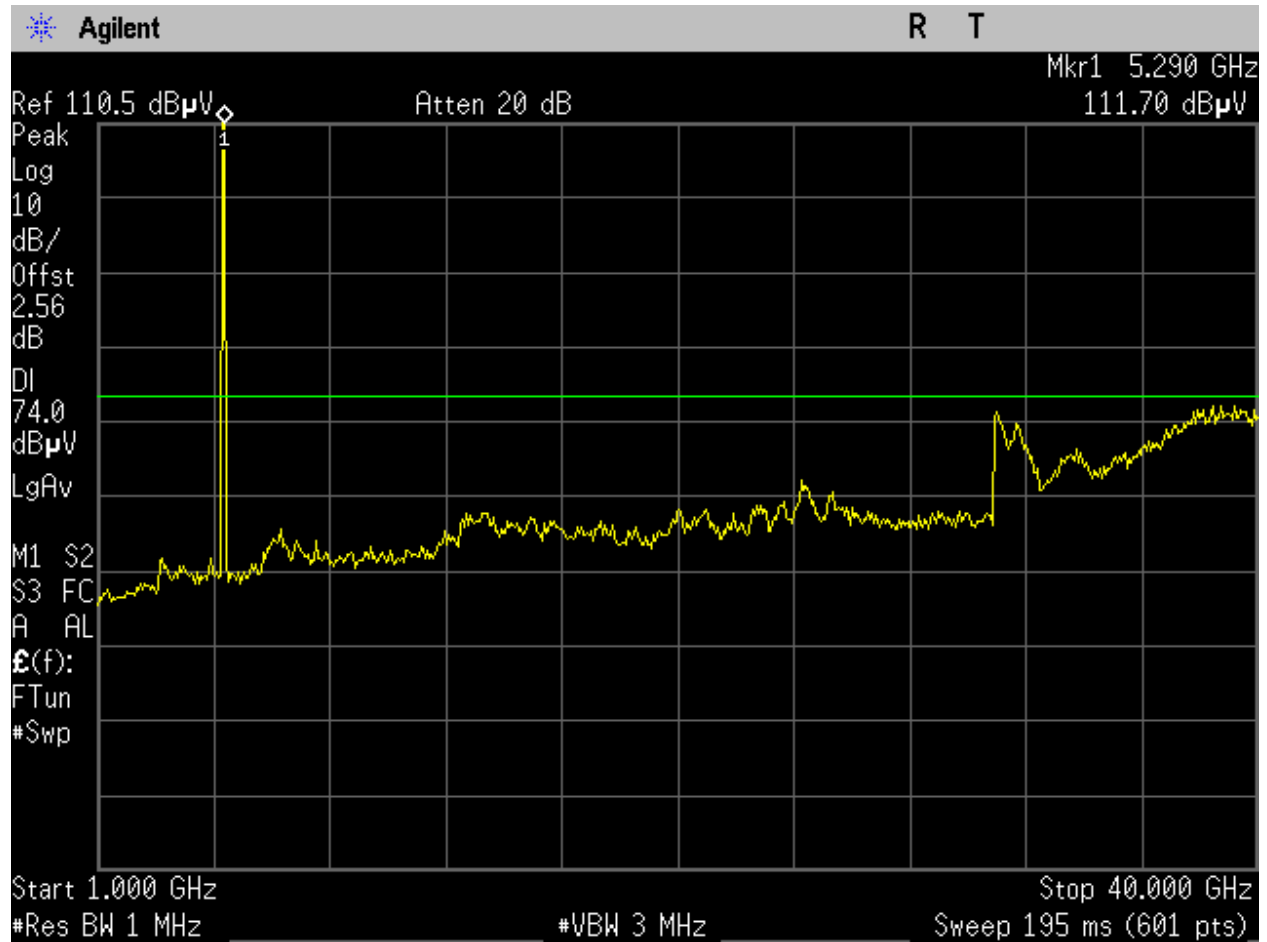


Figure 513: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 1.

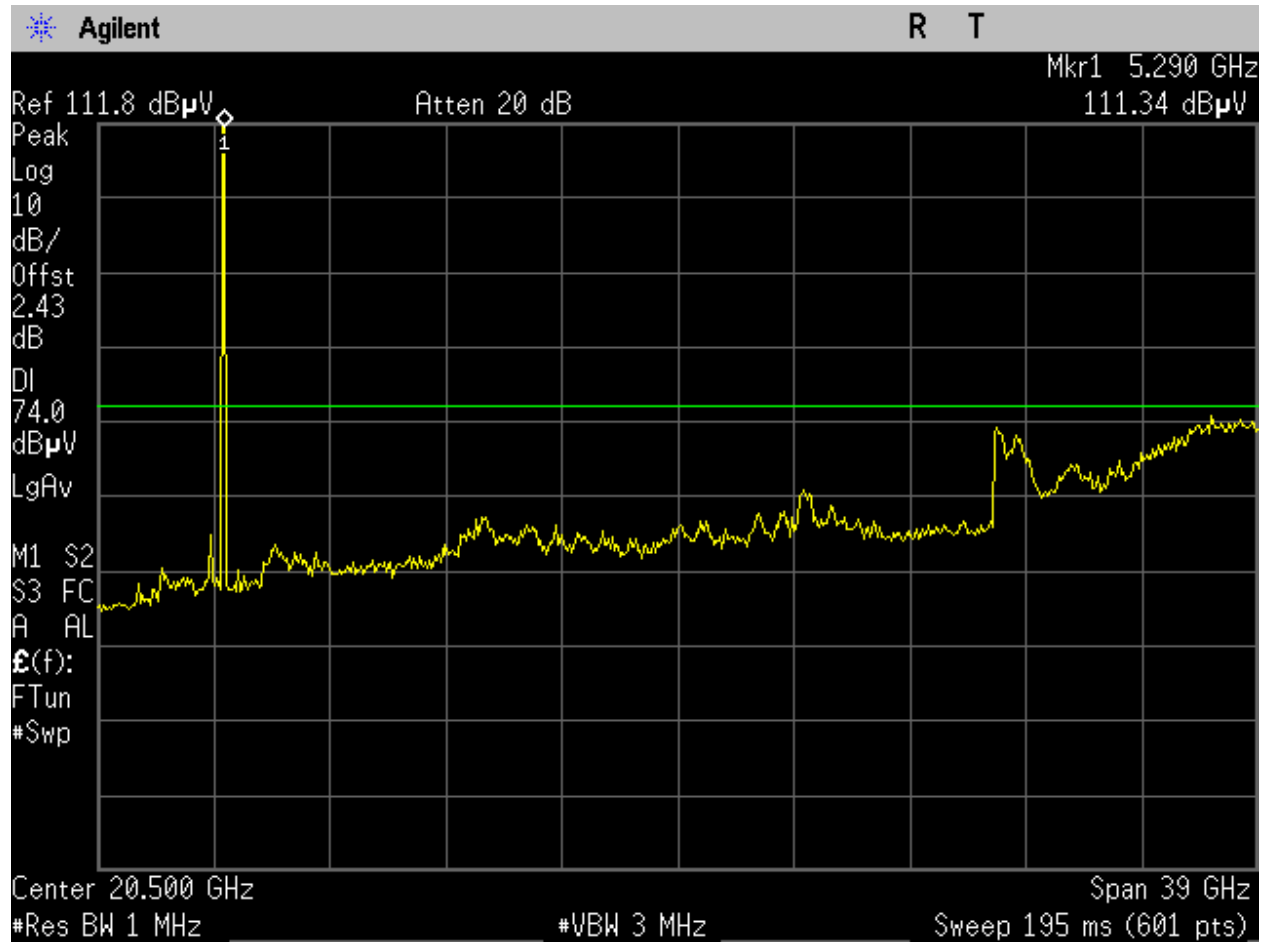


Figure 514: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 2.

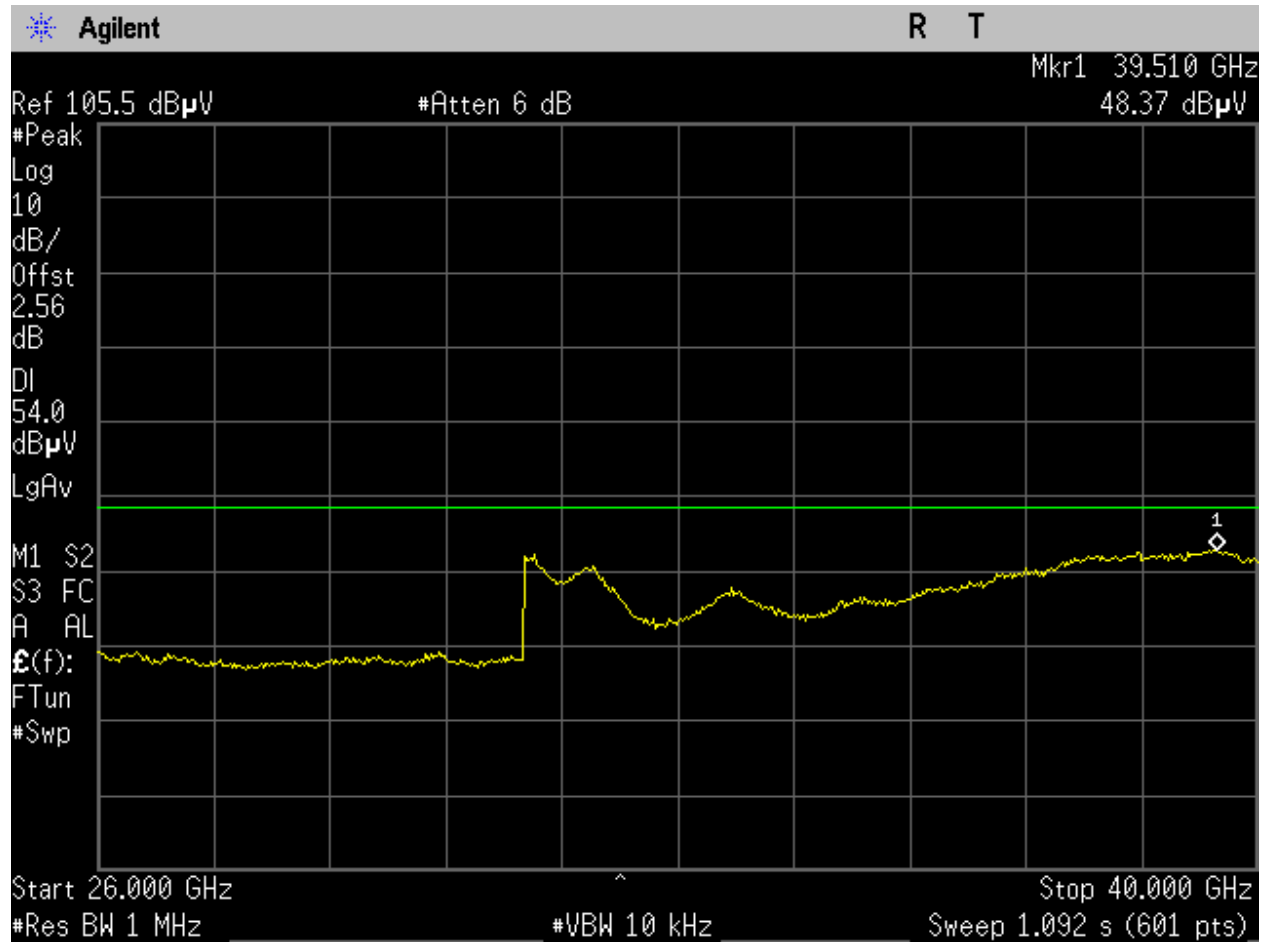


Figure 515: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 1.

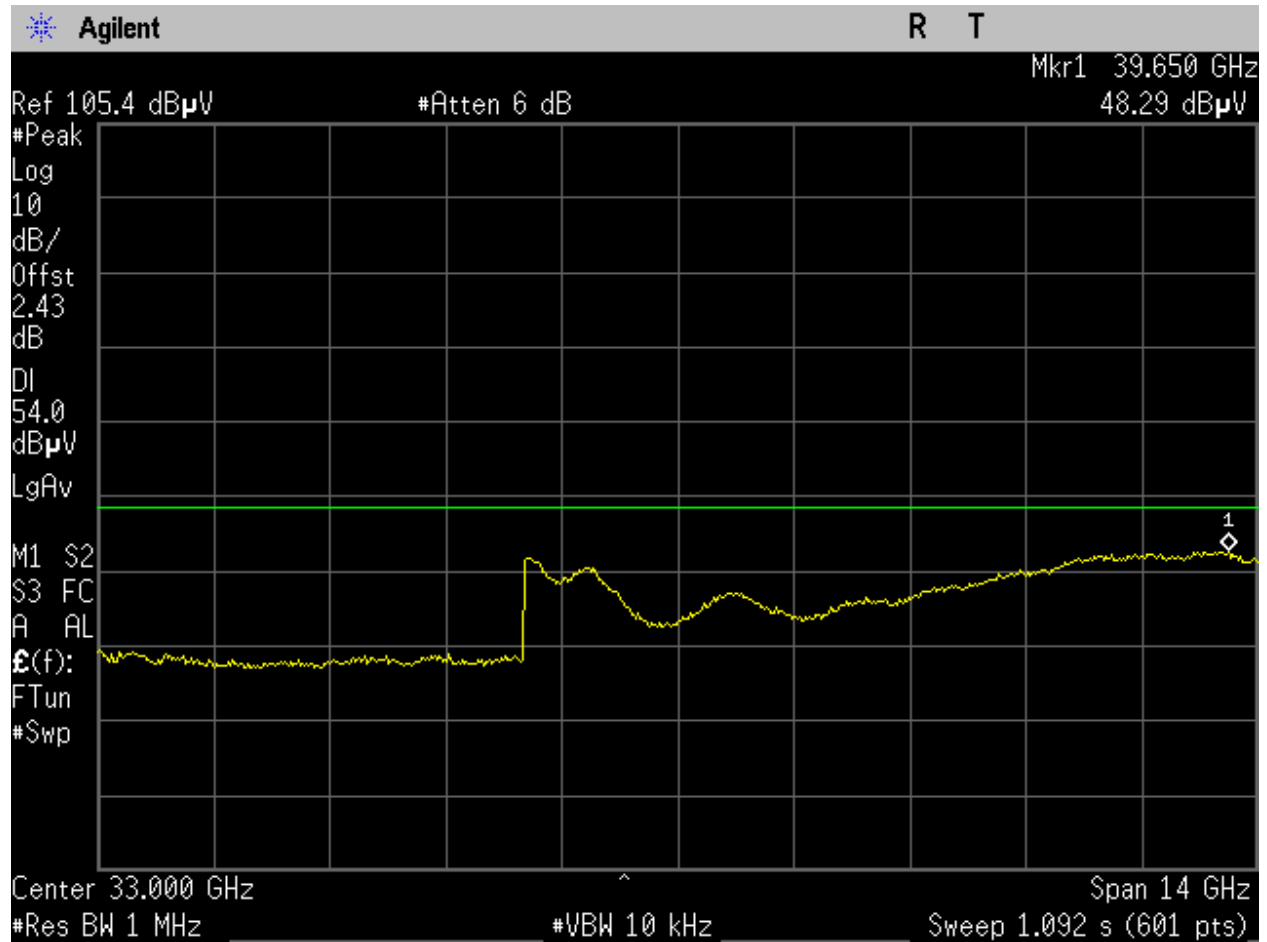


Figure 516: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 2.

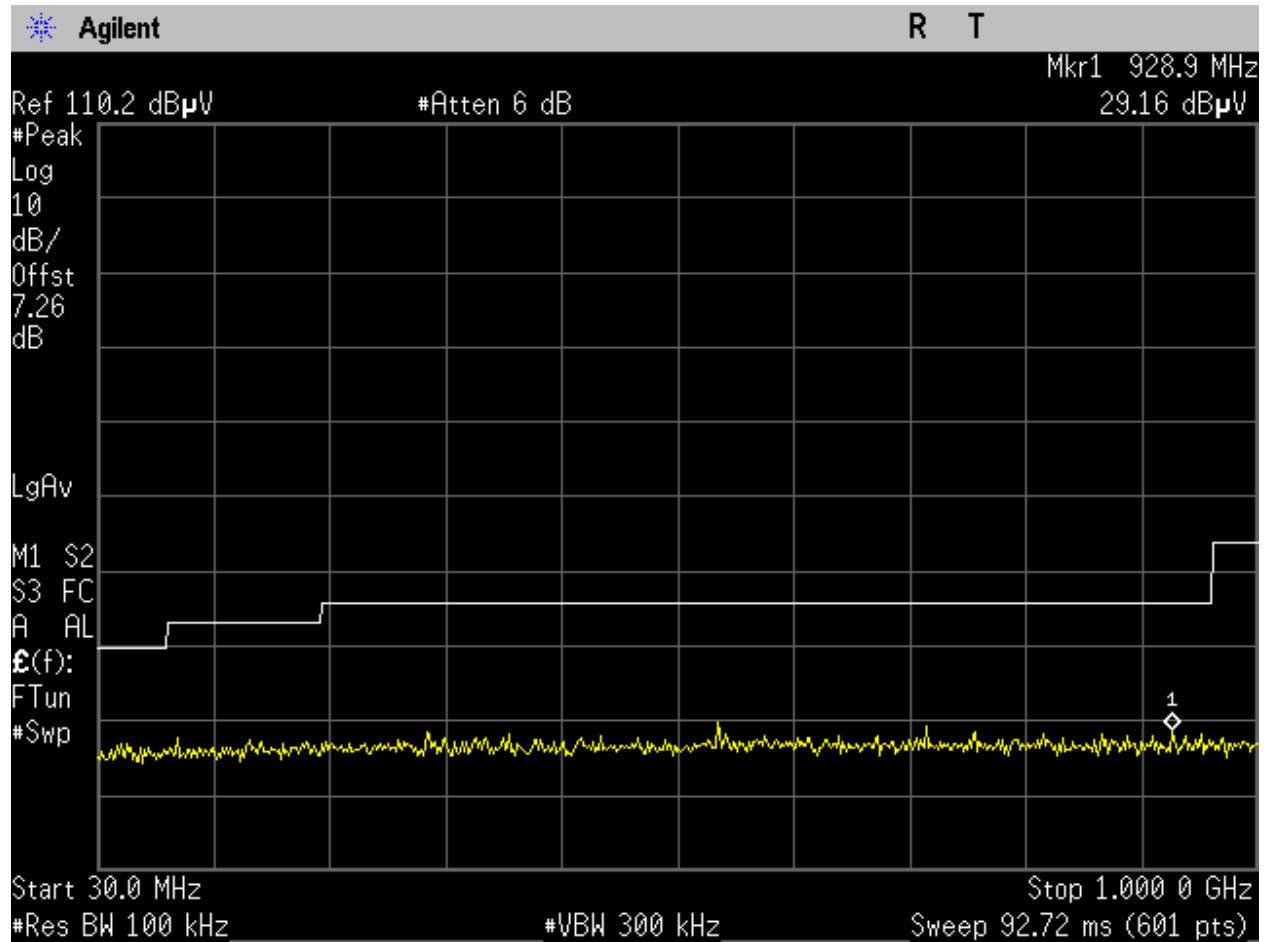


Figure 517: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 1.

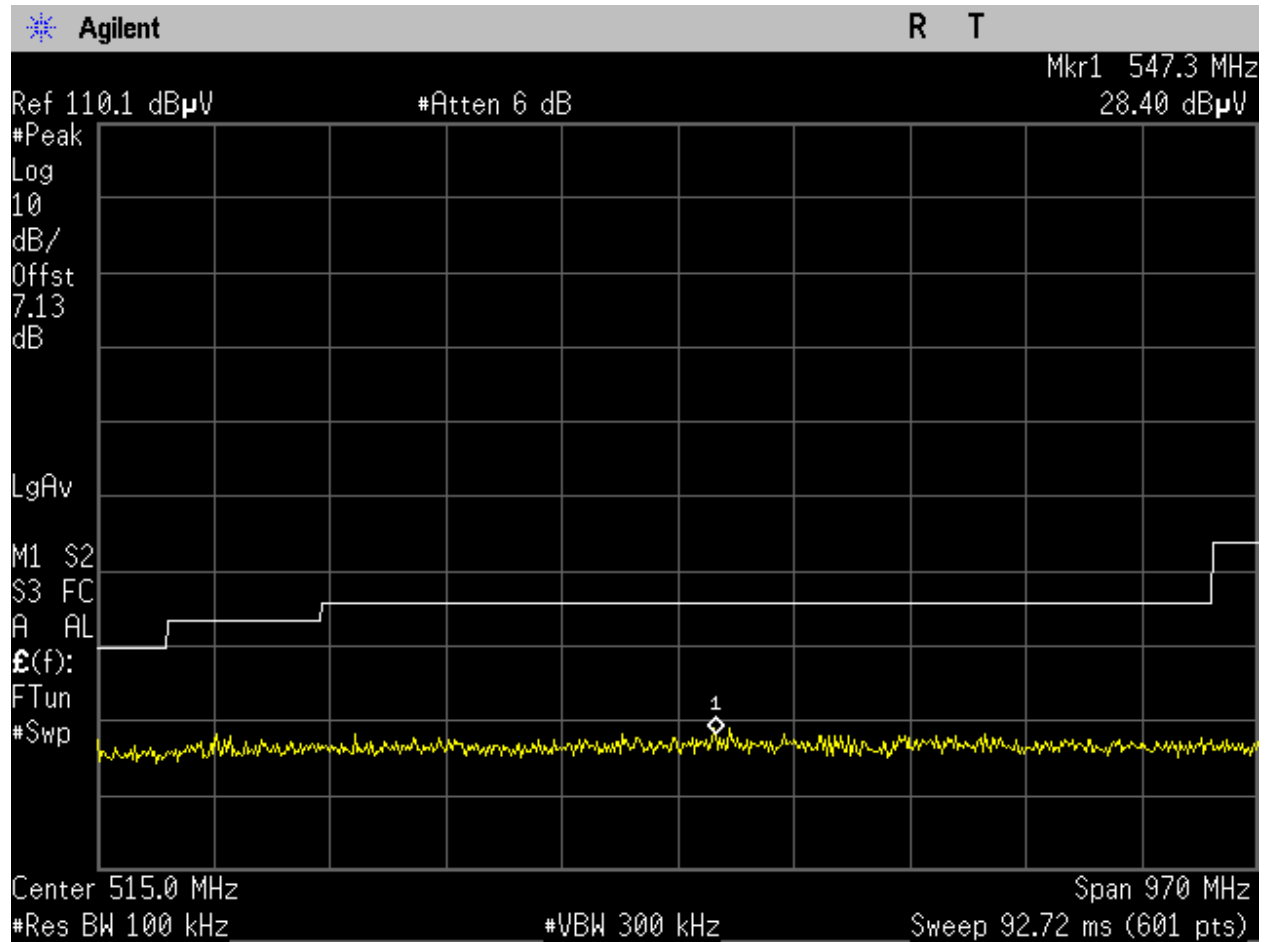


Figure 518: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 2.

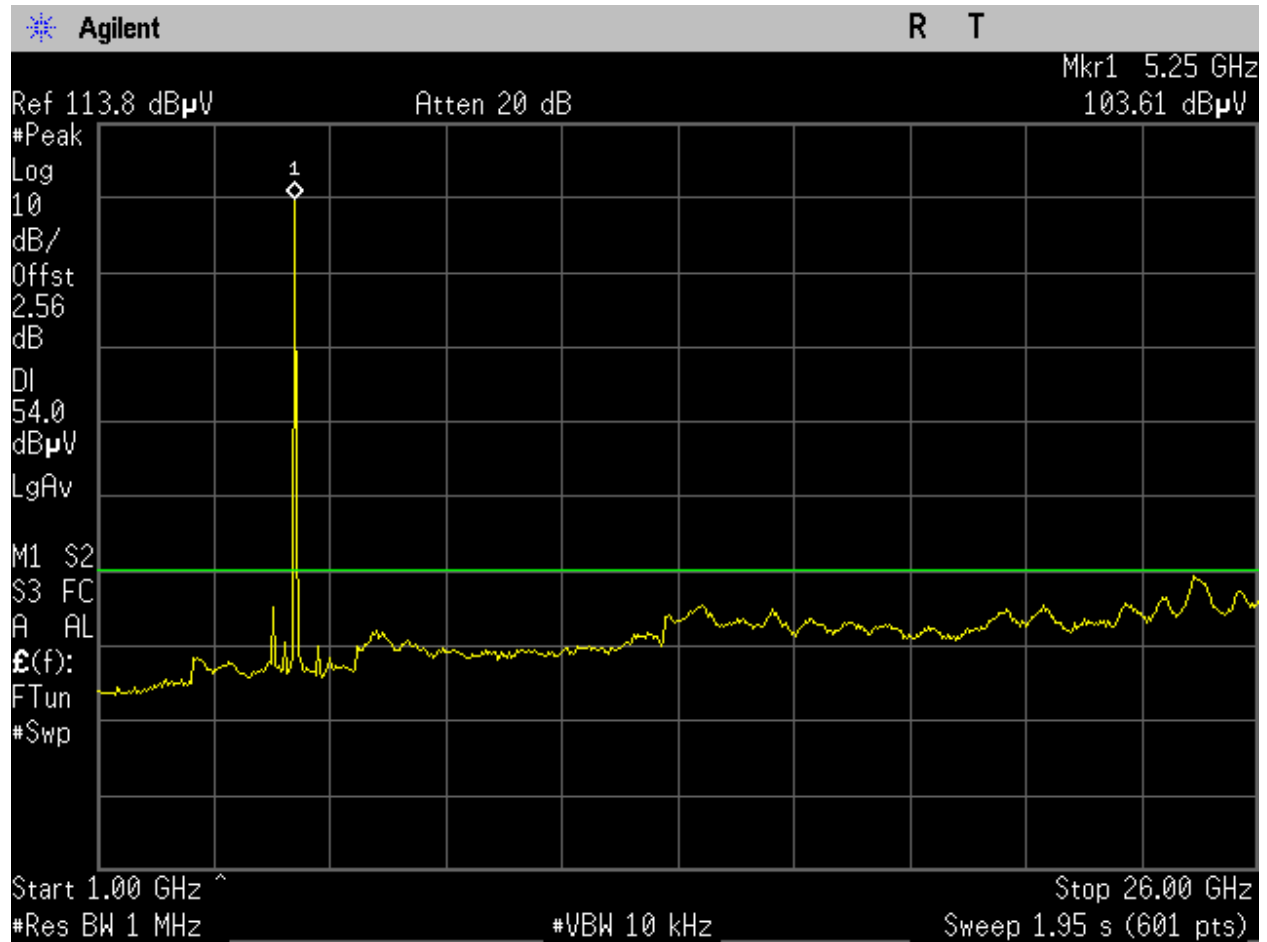


Figure 519: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_15.209_1-26GHz avg_Port 1.

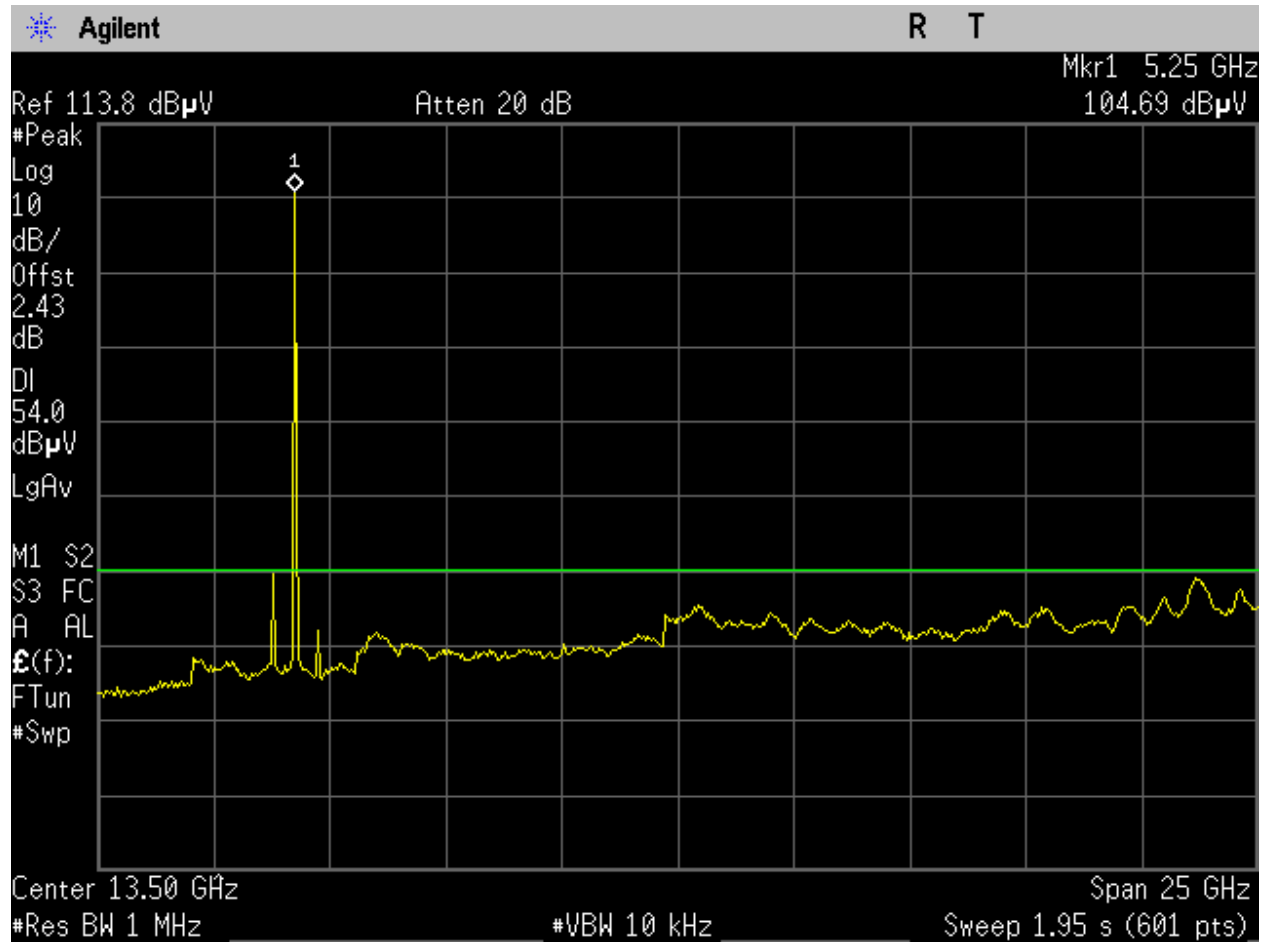


Figure 520: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_15.209_1-26GHz avg_Port 2.

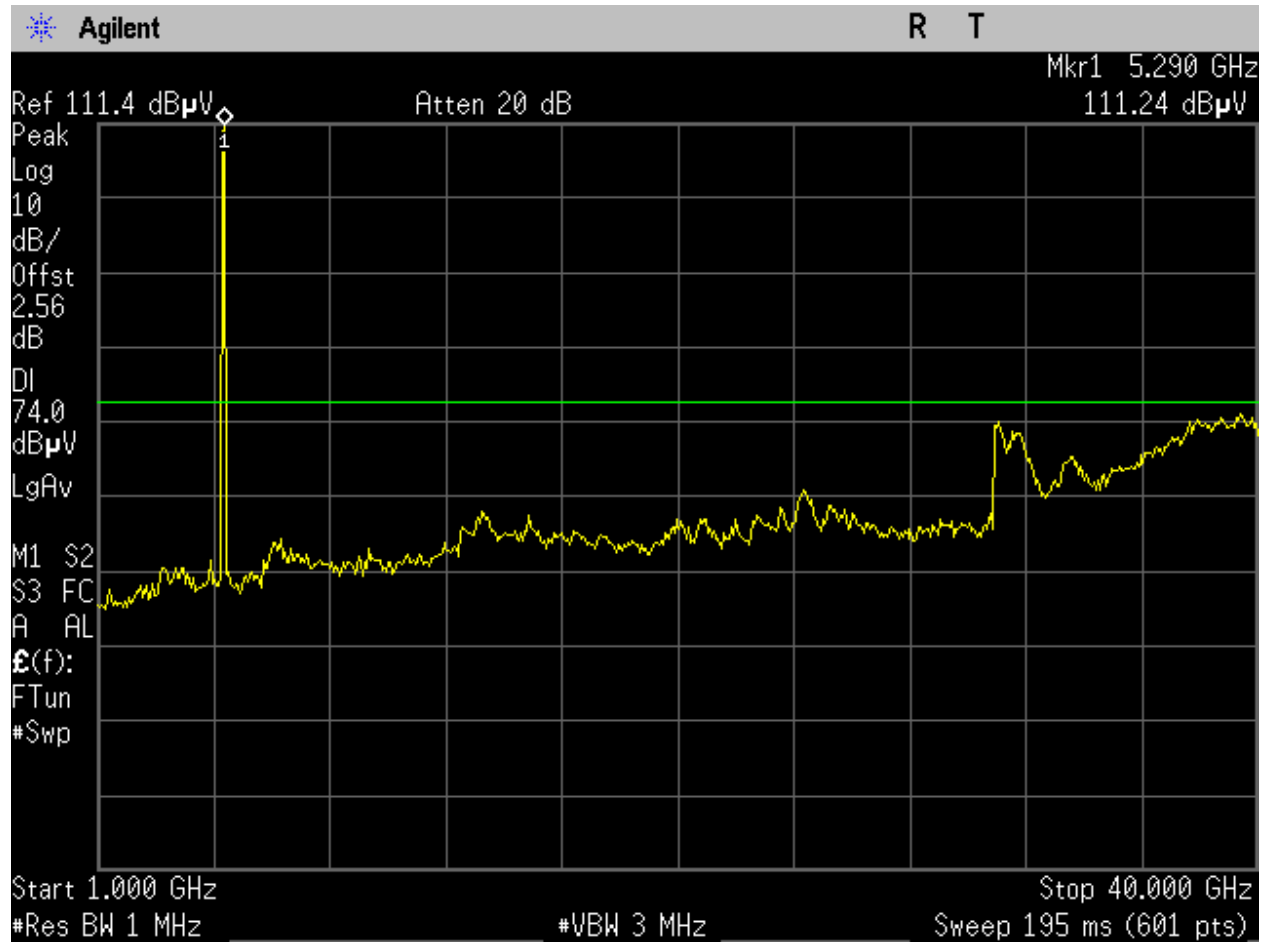


Figure 521: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_15.209_1-40GHz_Peak_Port 1.

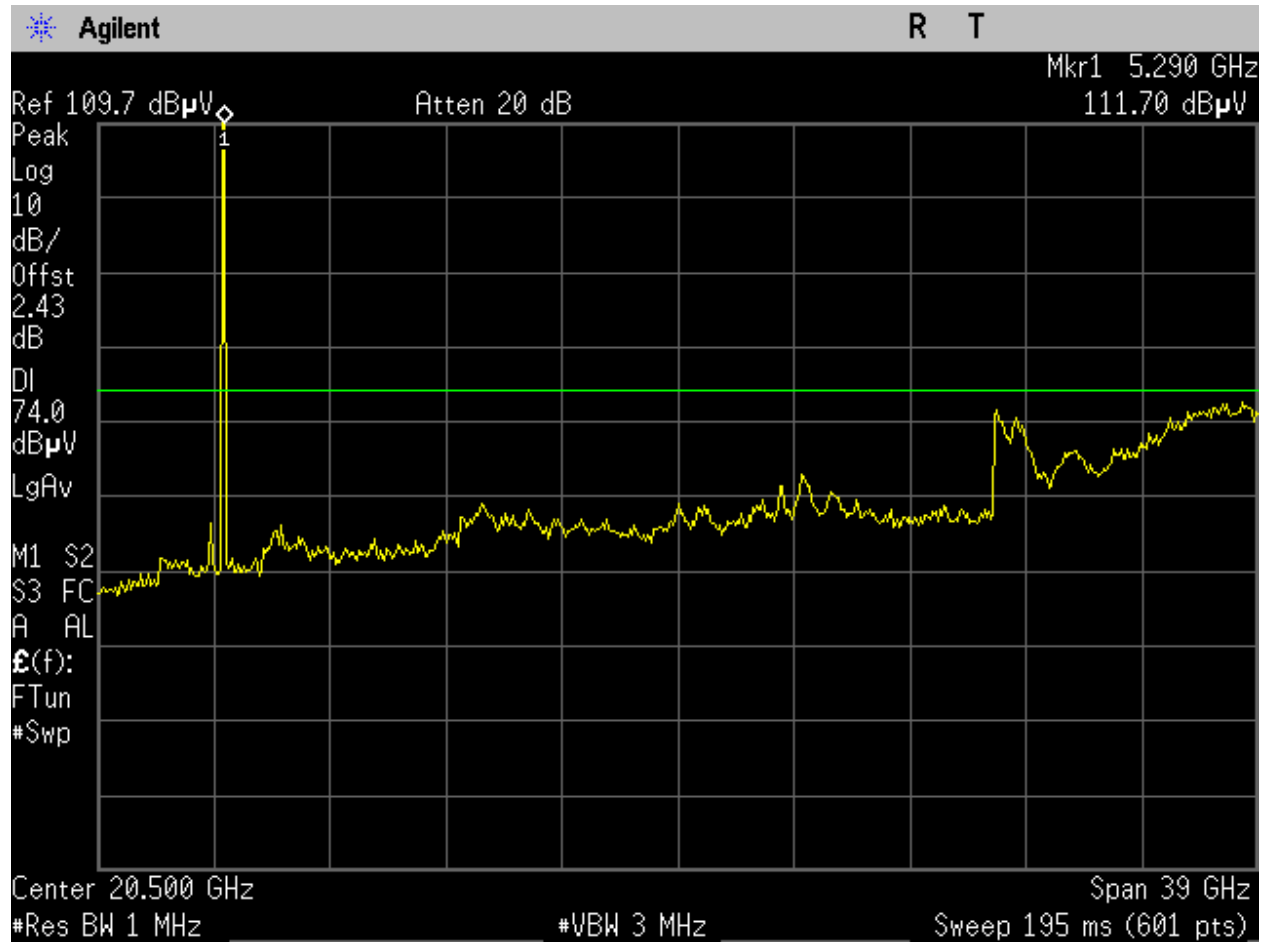


Figure 522: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_15.209_1-40GHz_Peak_Port 2.

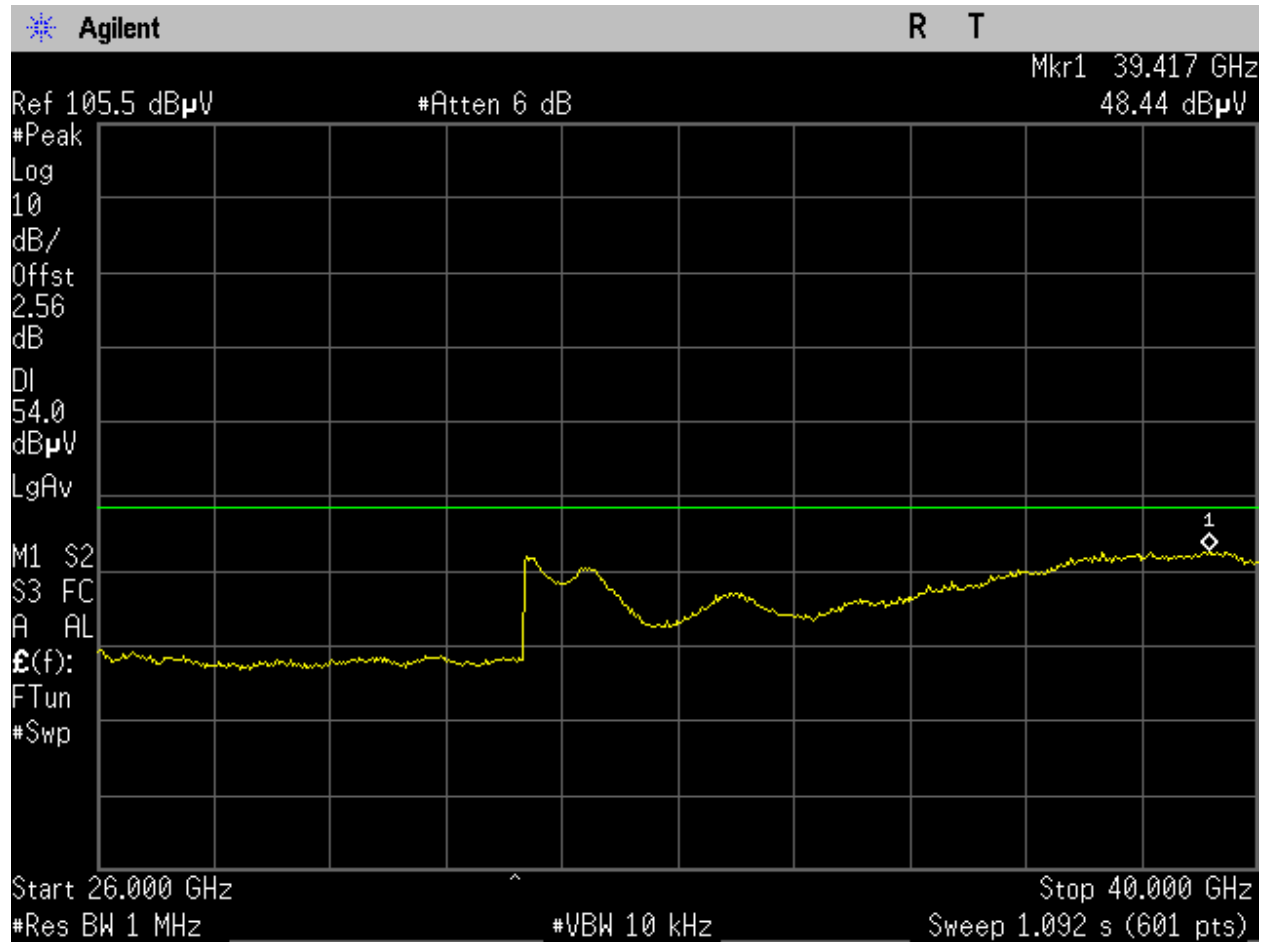


Figure 523: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_15.209_26-40GHz_Avg_Port 1.

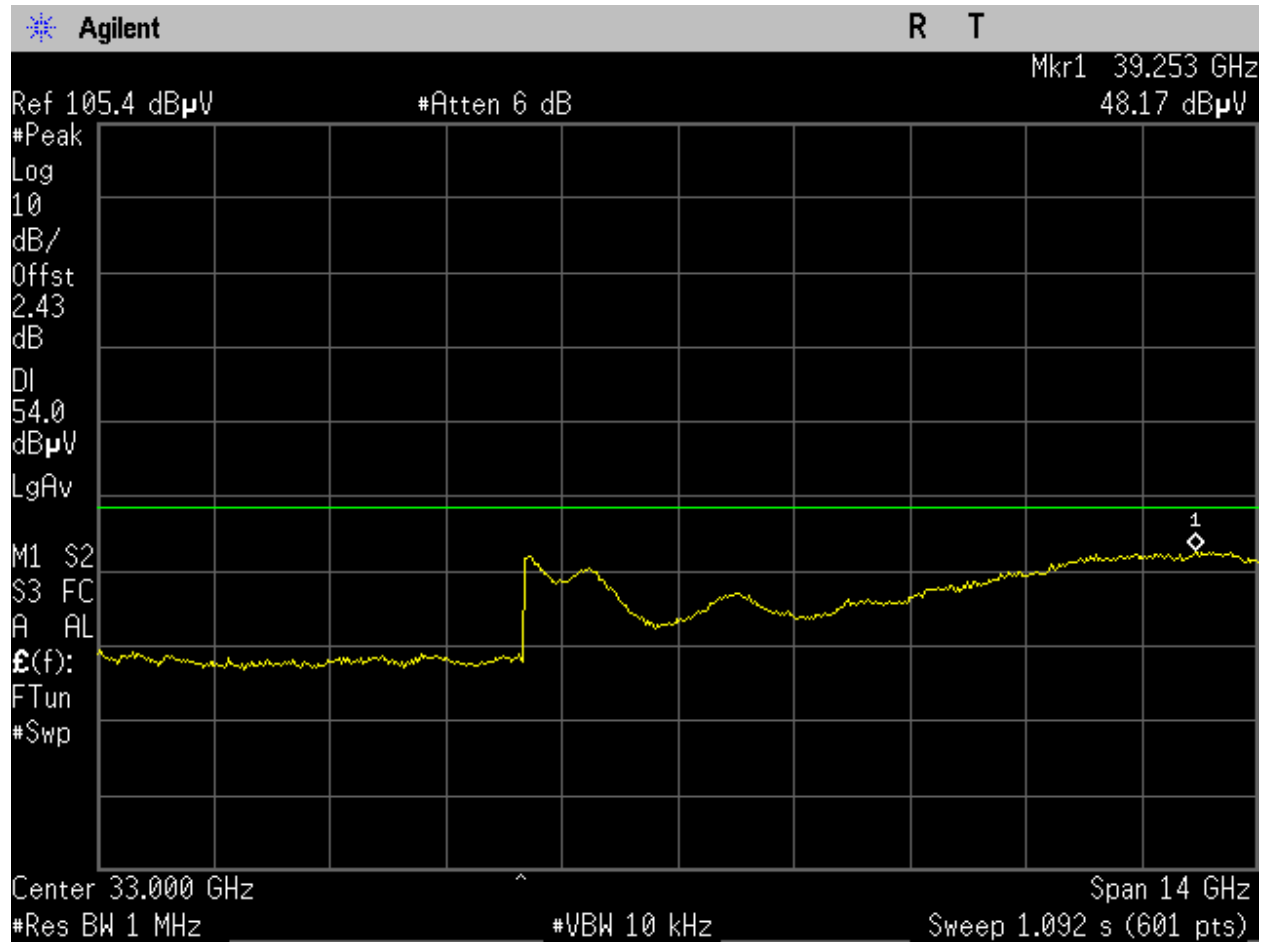


Figure 524: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_15.209_26-40GHz_Avg_Port 2.

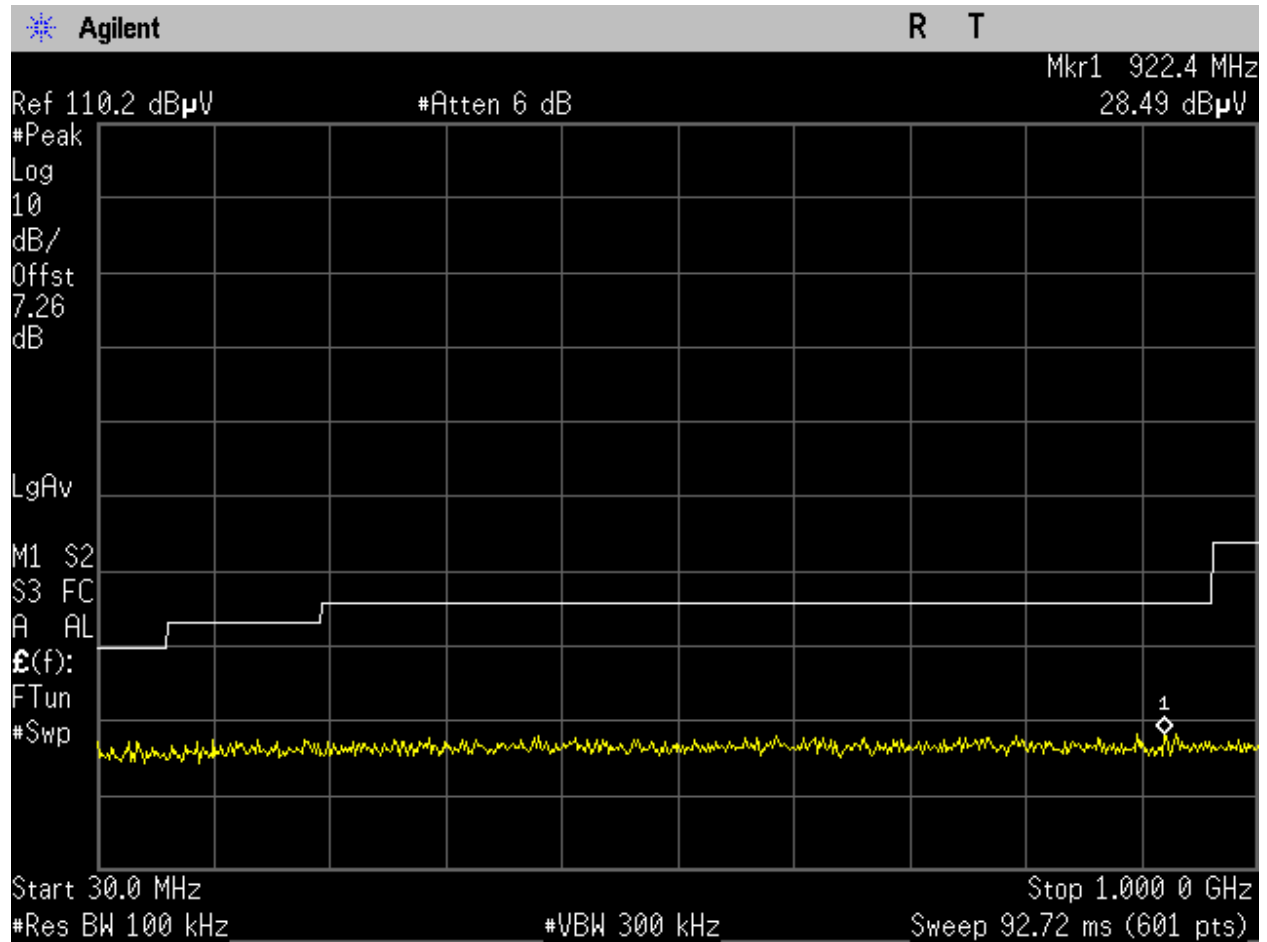


Figure 525: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 1.

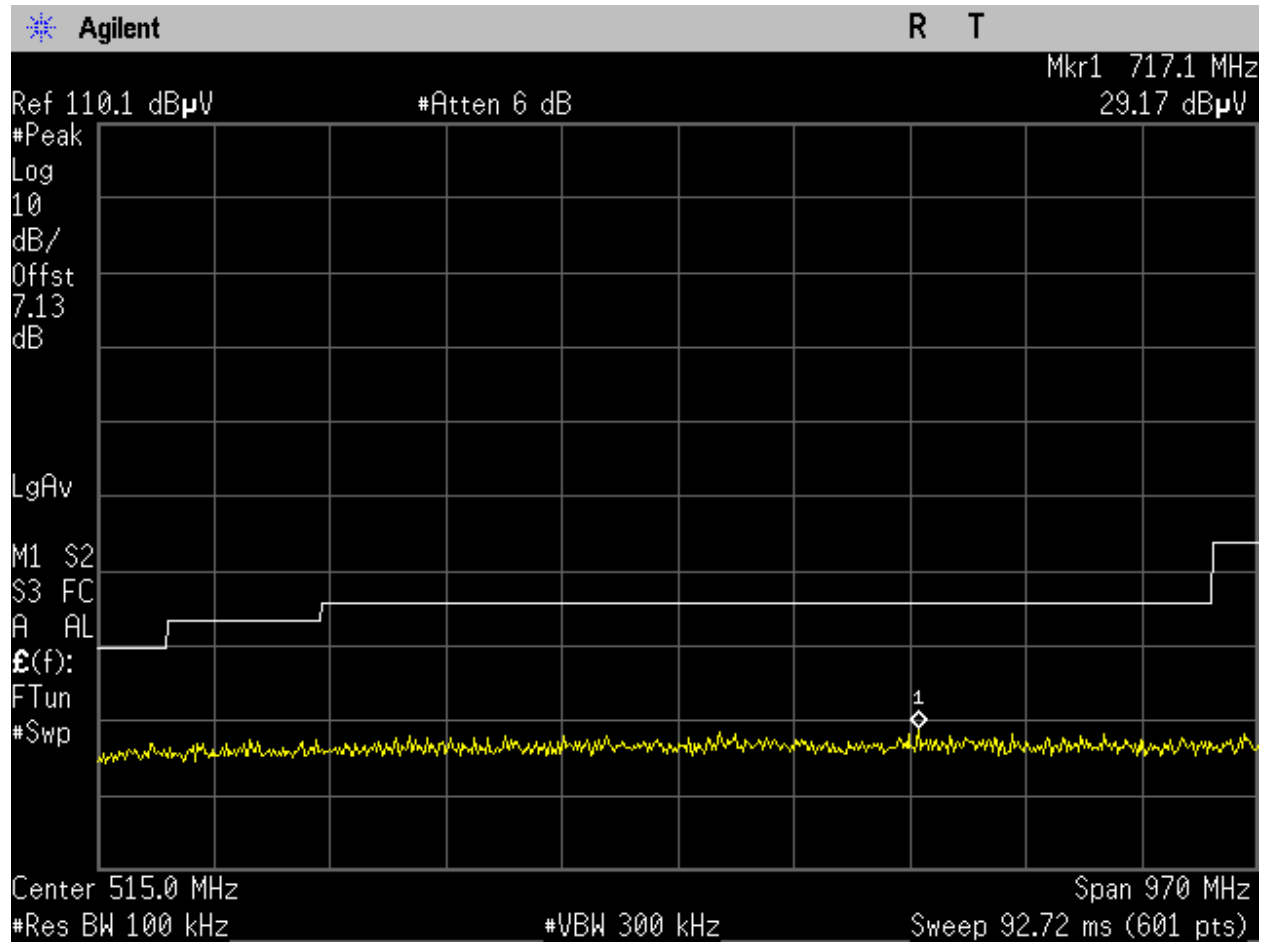


Figure 526: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 2.

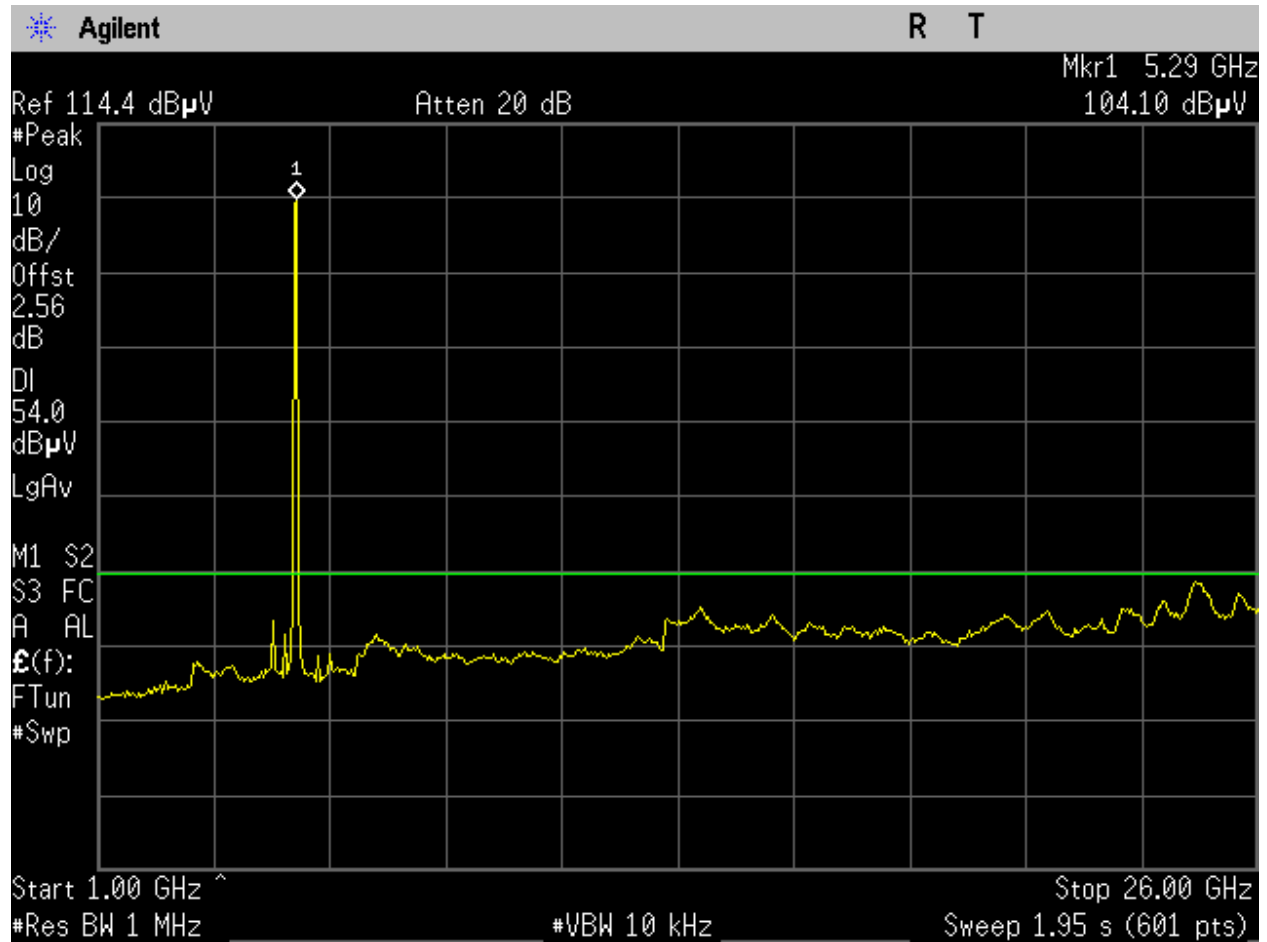


Figure 527: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

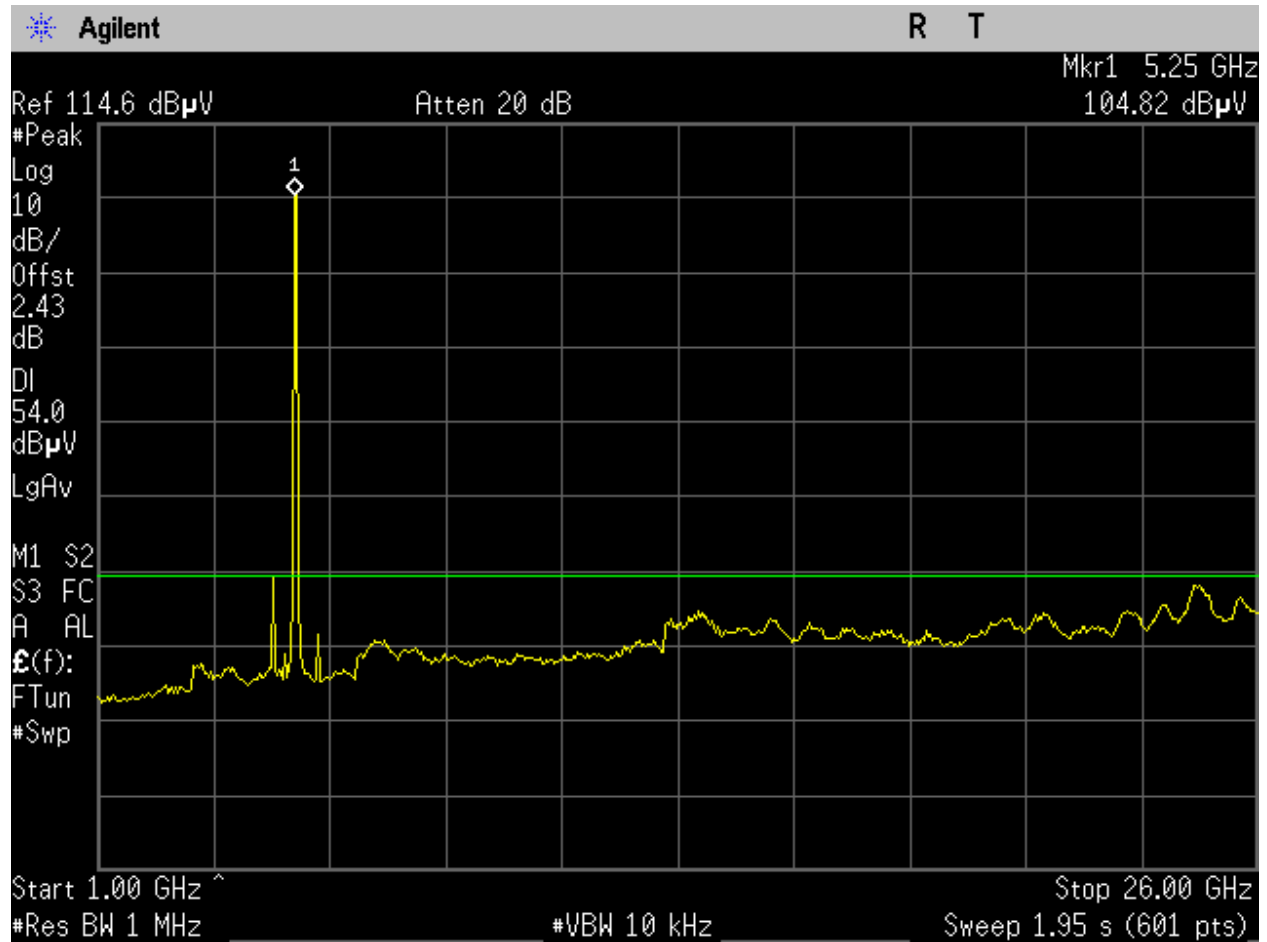


Figure 528: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

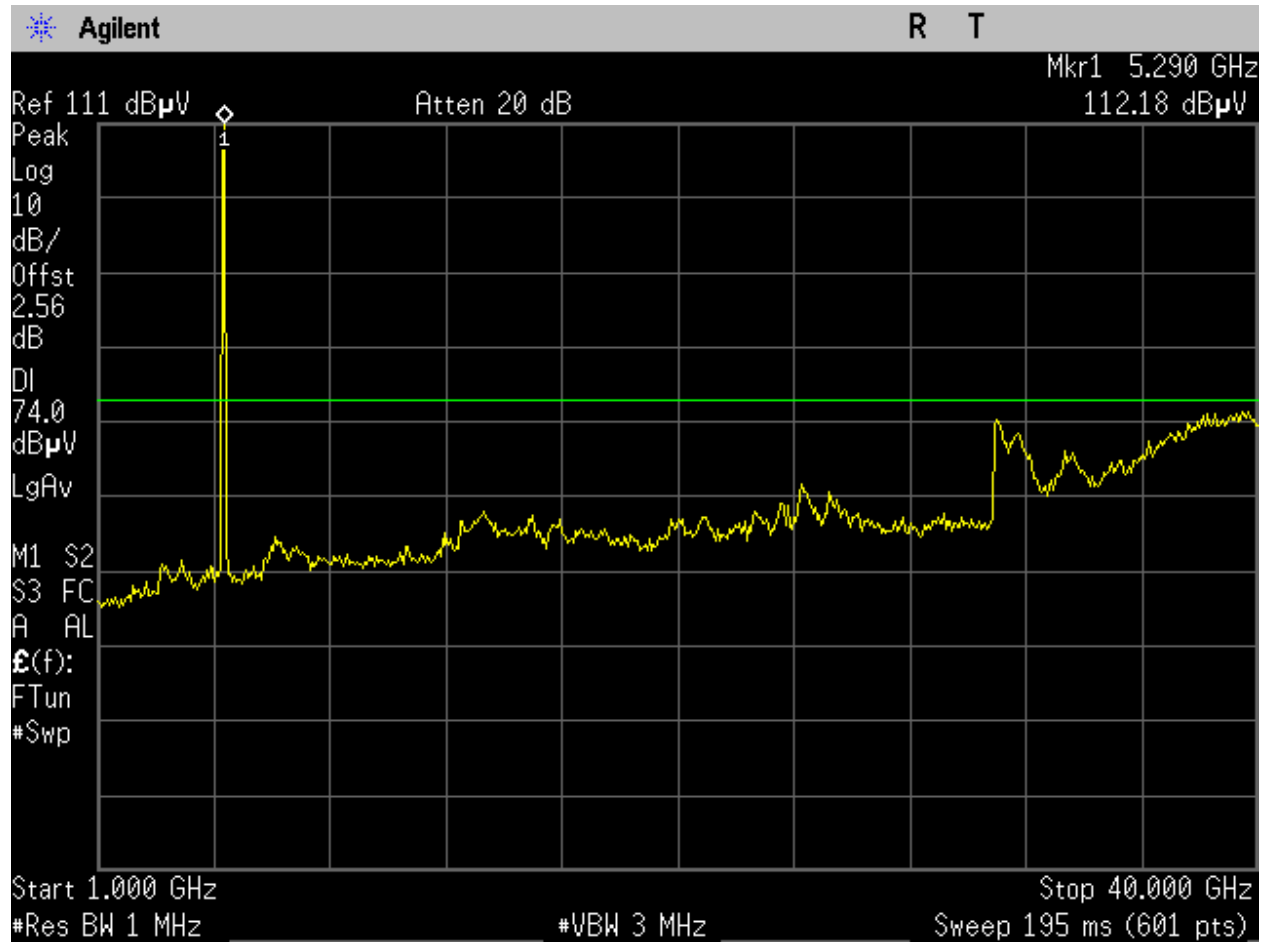


Figure 529: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 1.

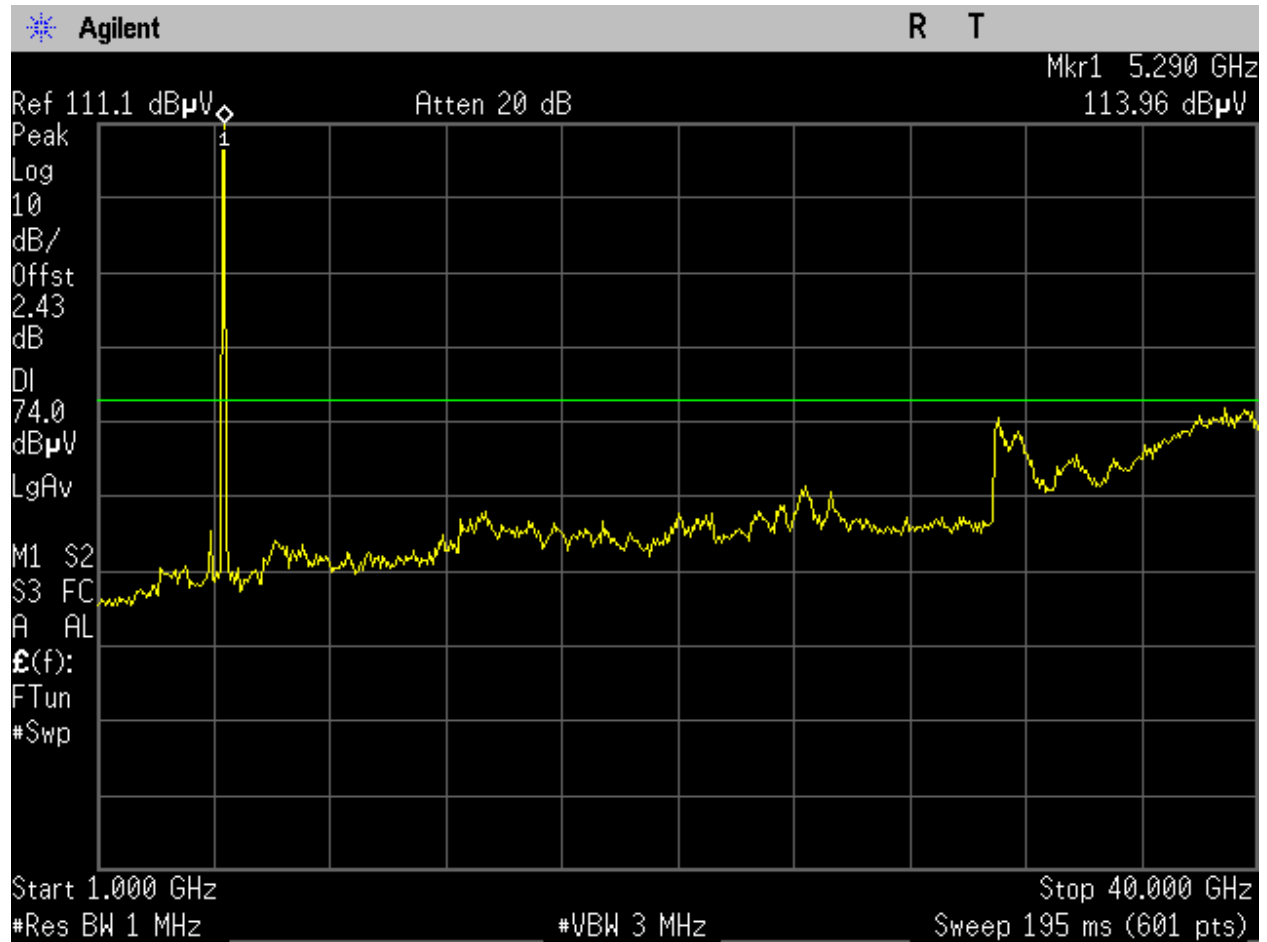


Figure 530: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 2.

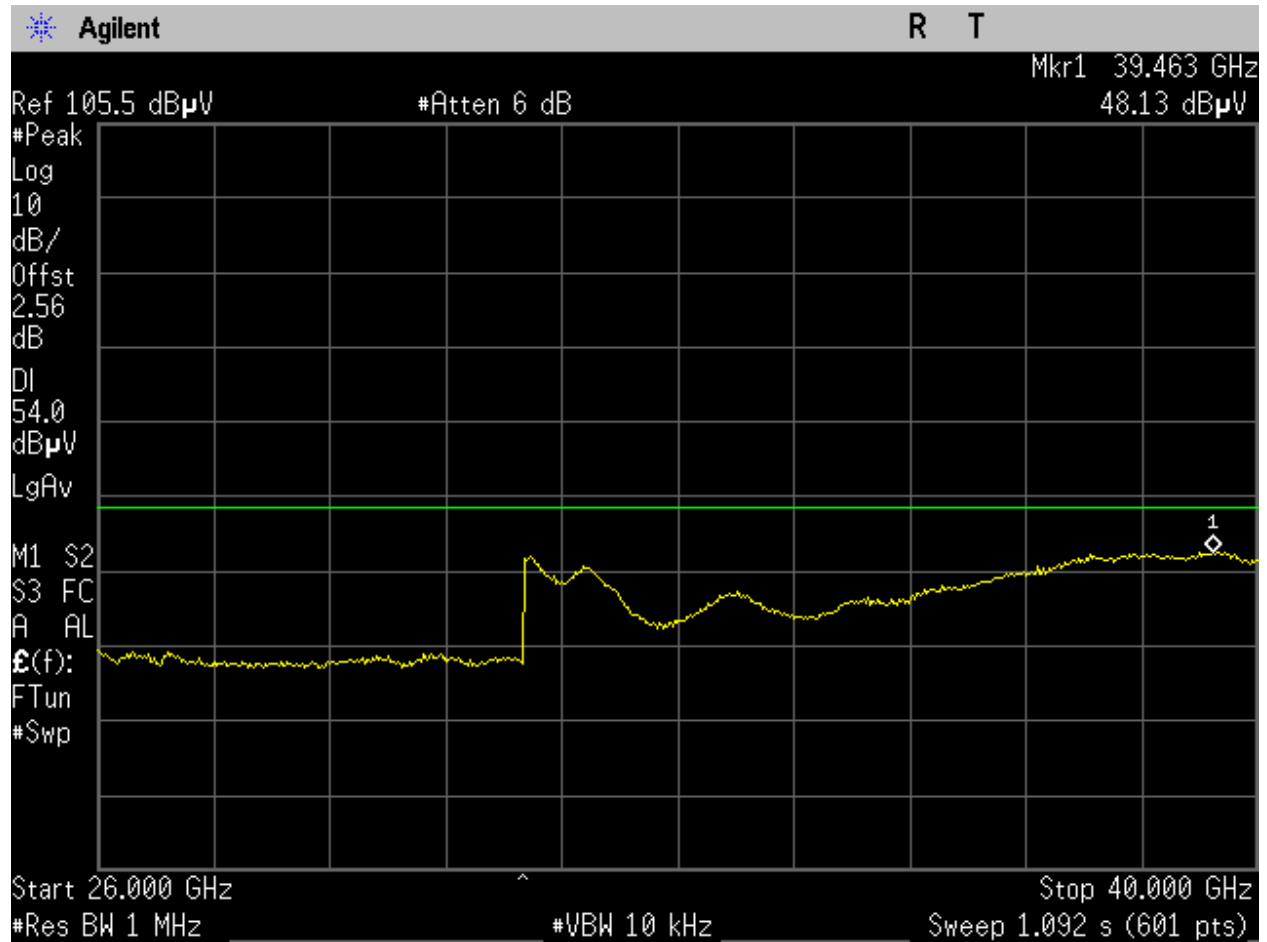


Figure 531: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

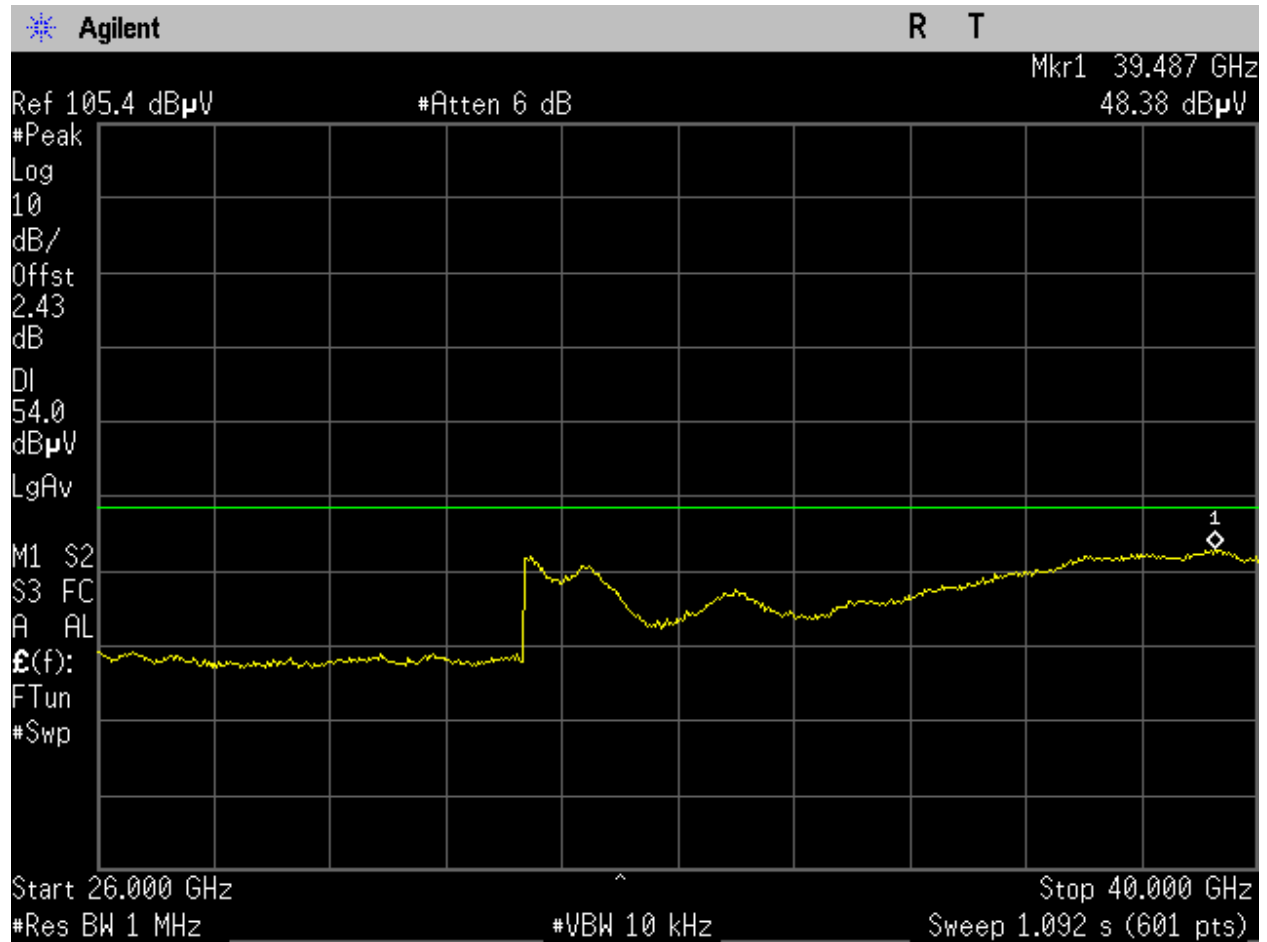


Figure 532: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

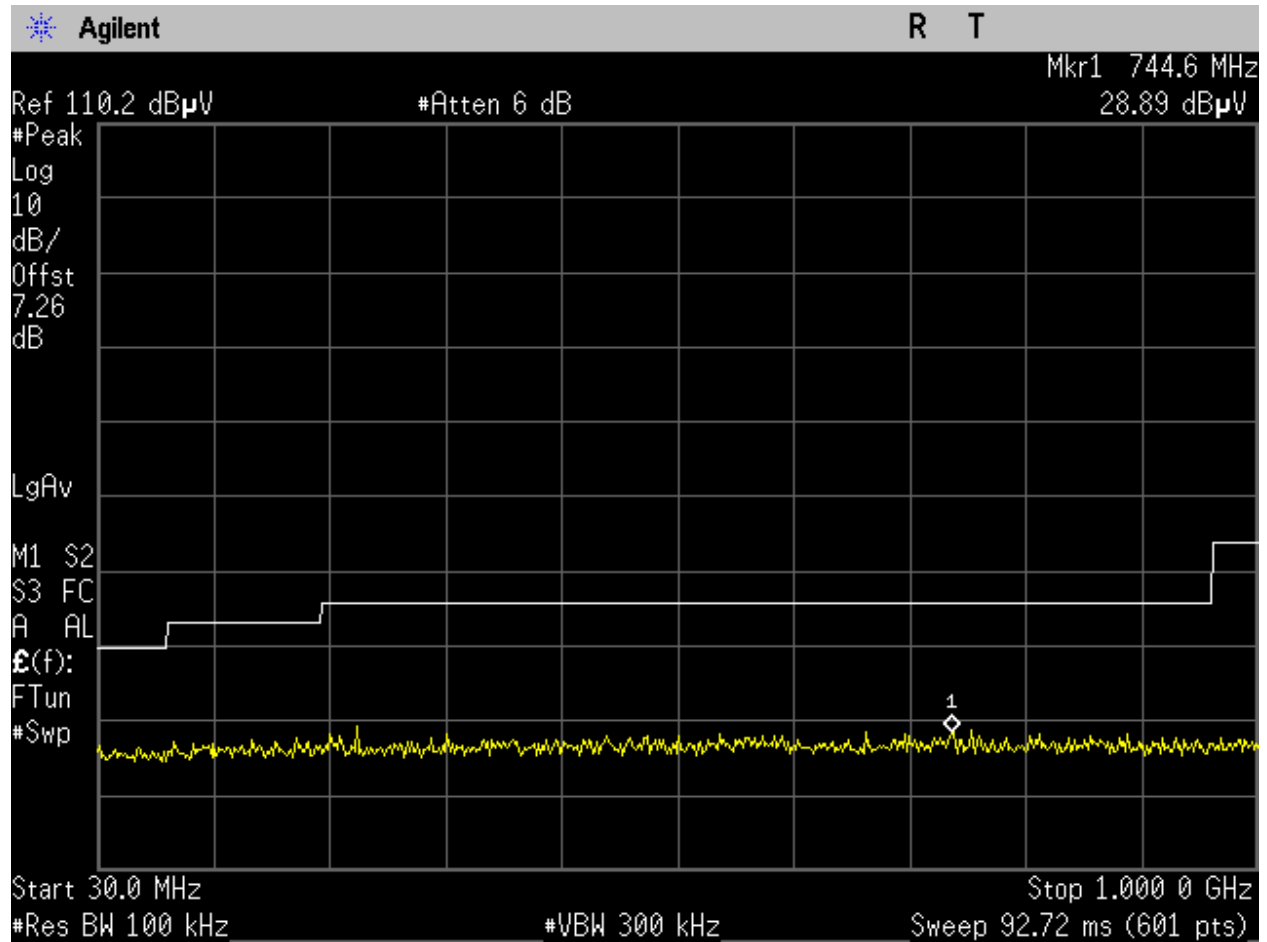


Figure 533: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

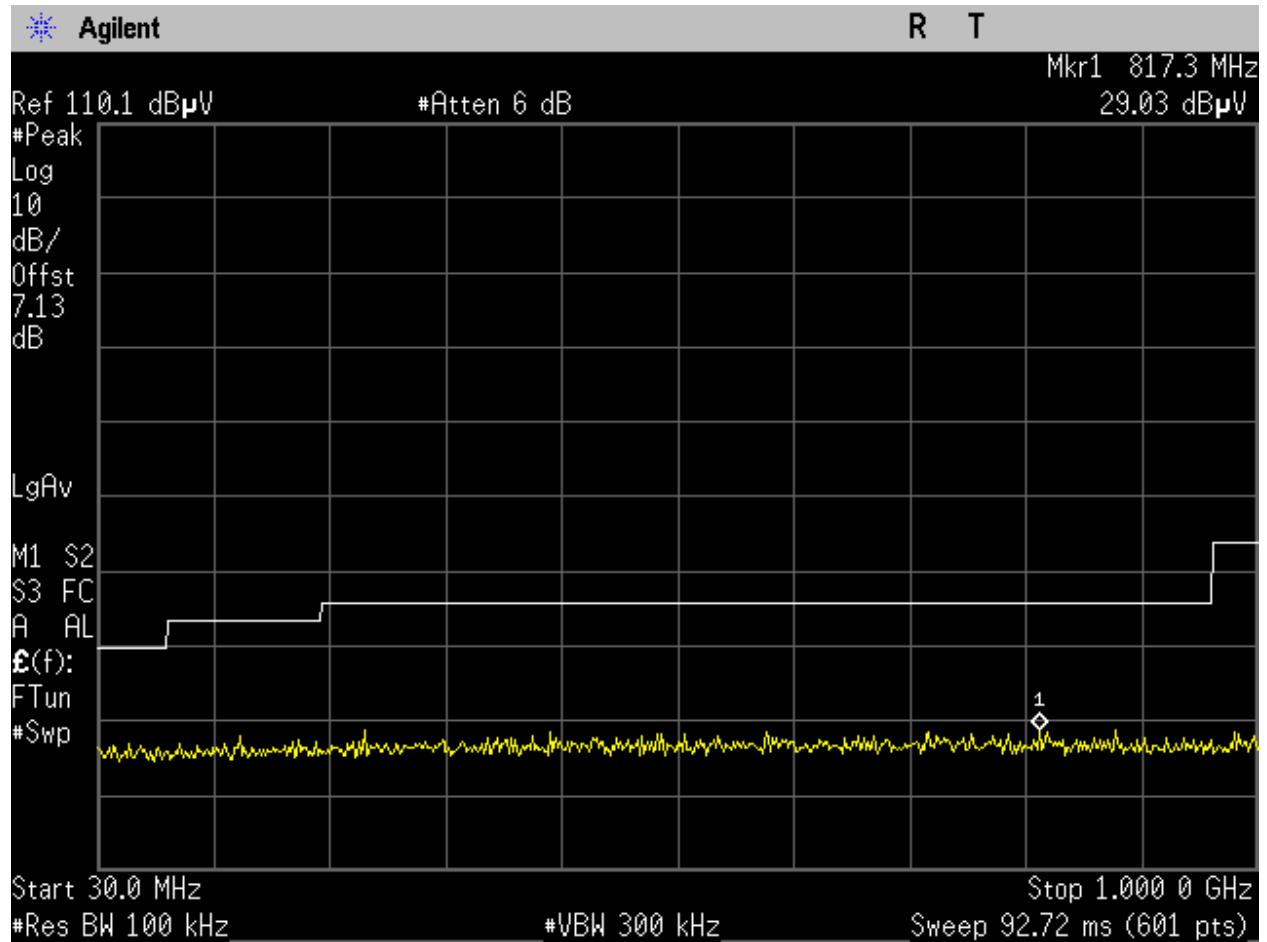


Figure 534: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

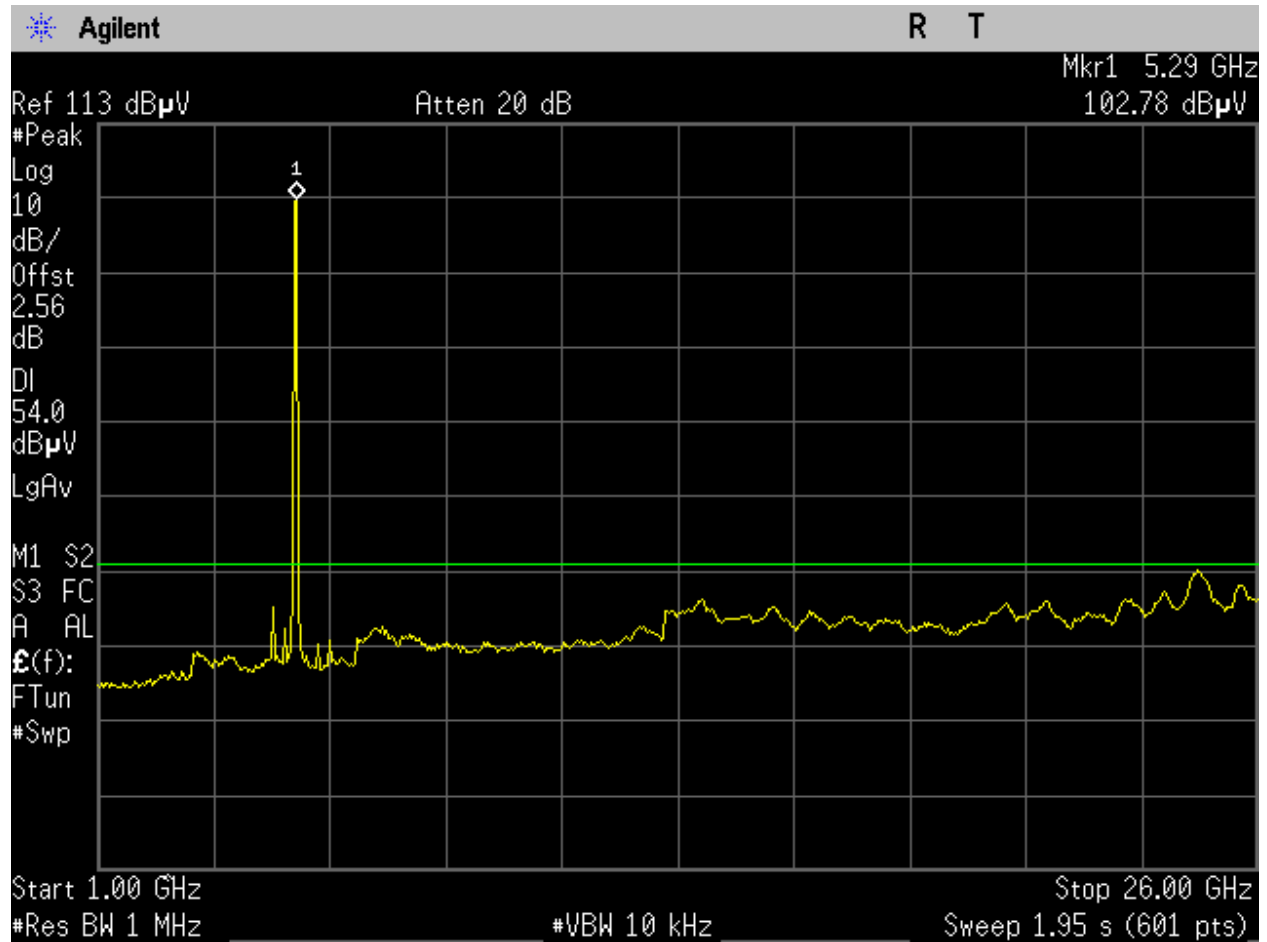


Figure 535: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_15.209_1-26GHz avg_Port 1.

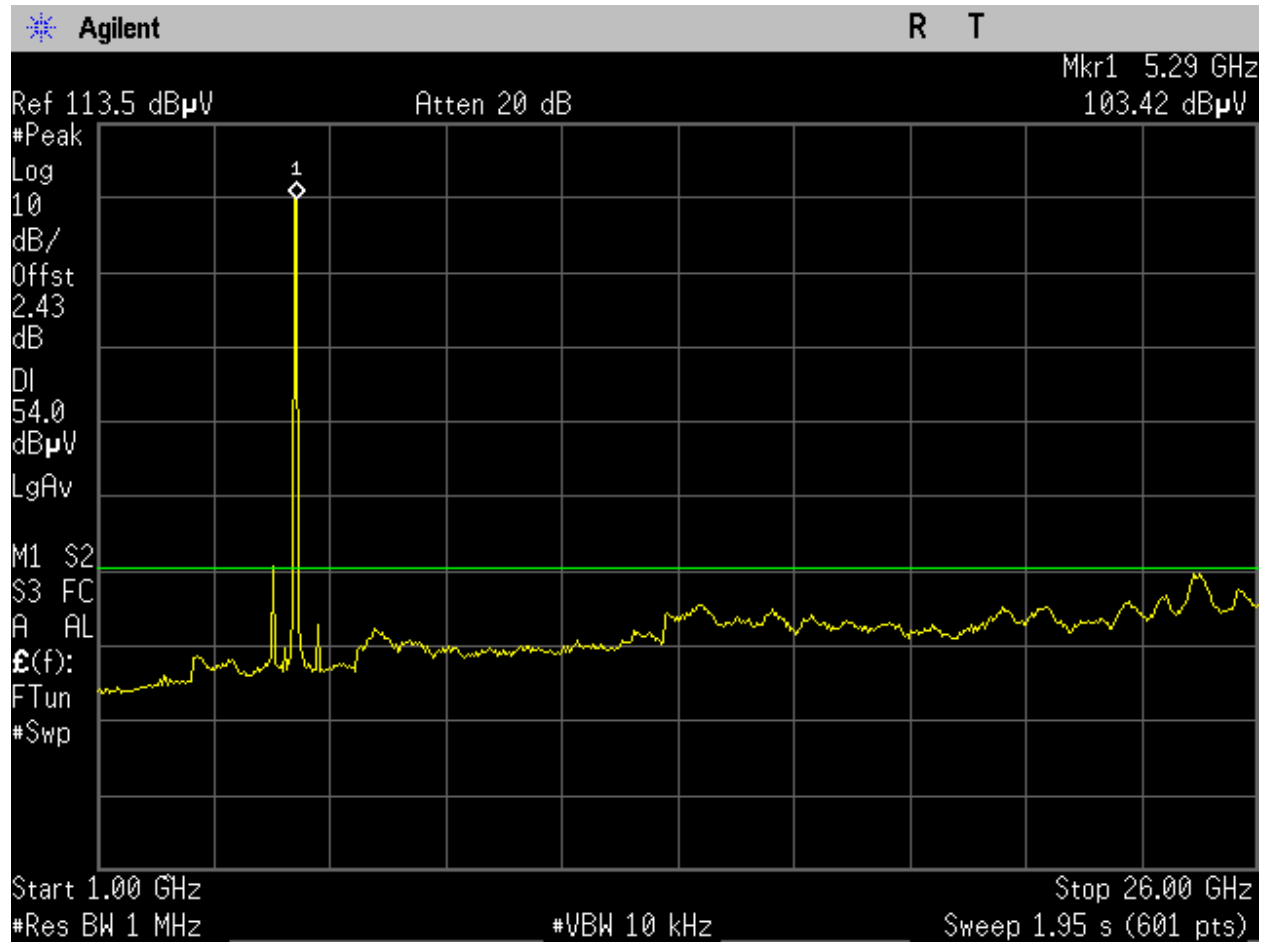


Figure 536: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_15.209_1-26GHz avg_Port 2.

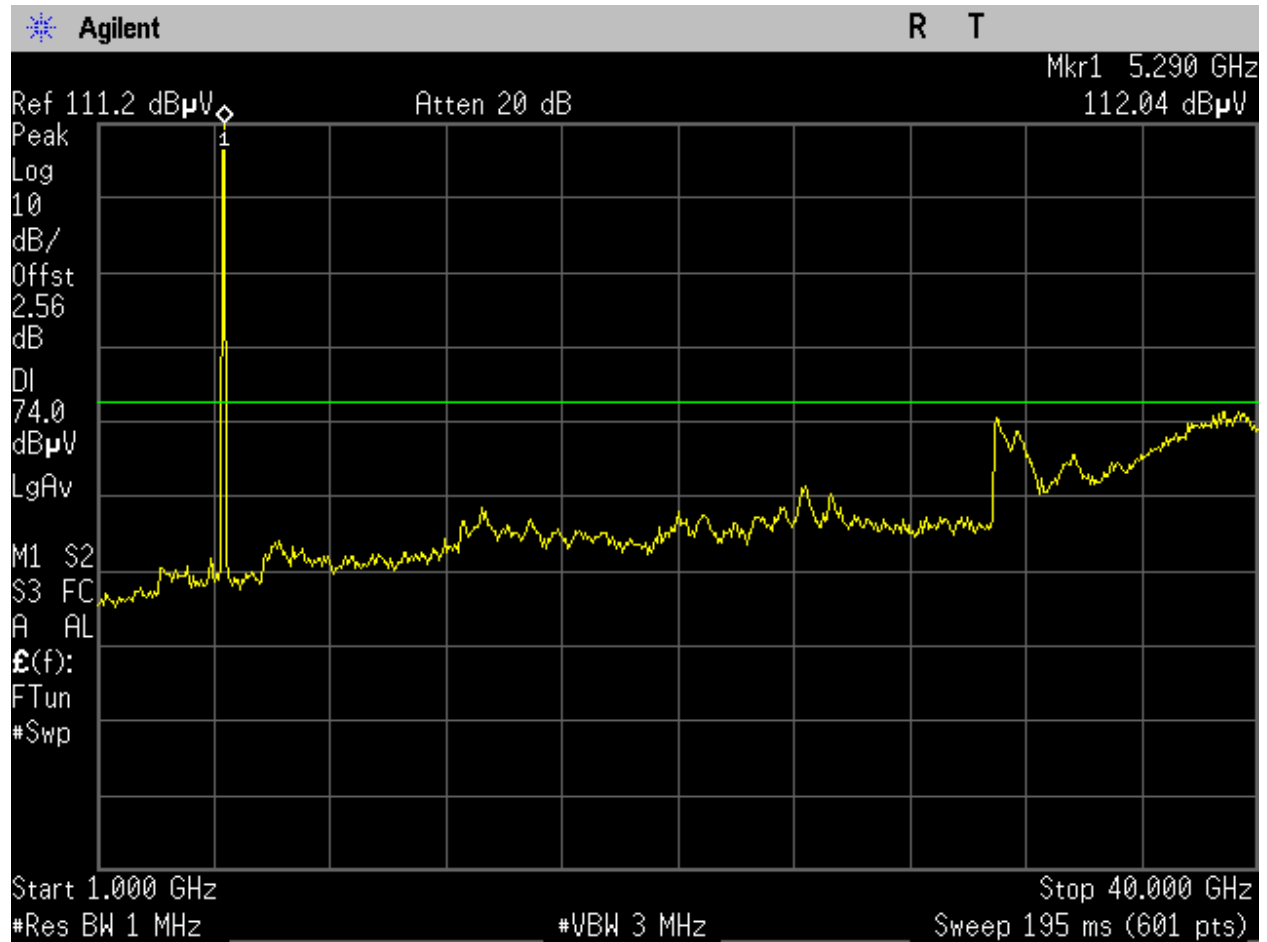


Figure 537: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 1.

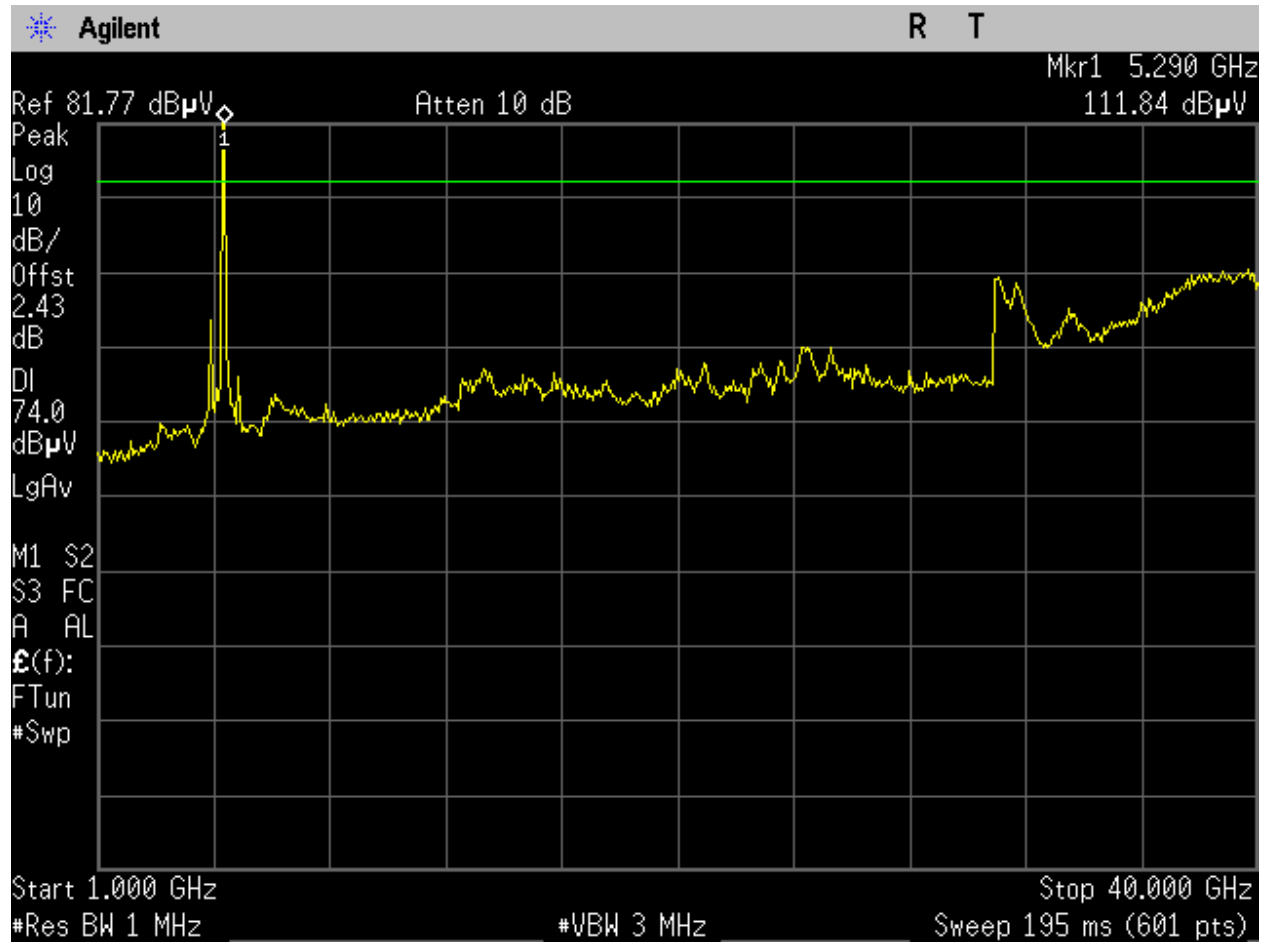


Figure 538: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 2.

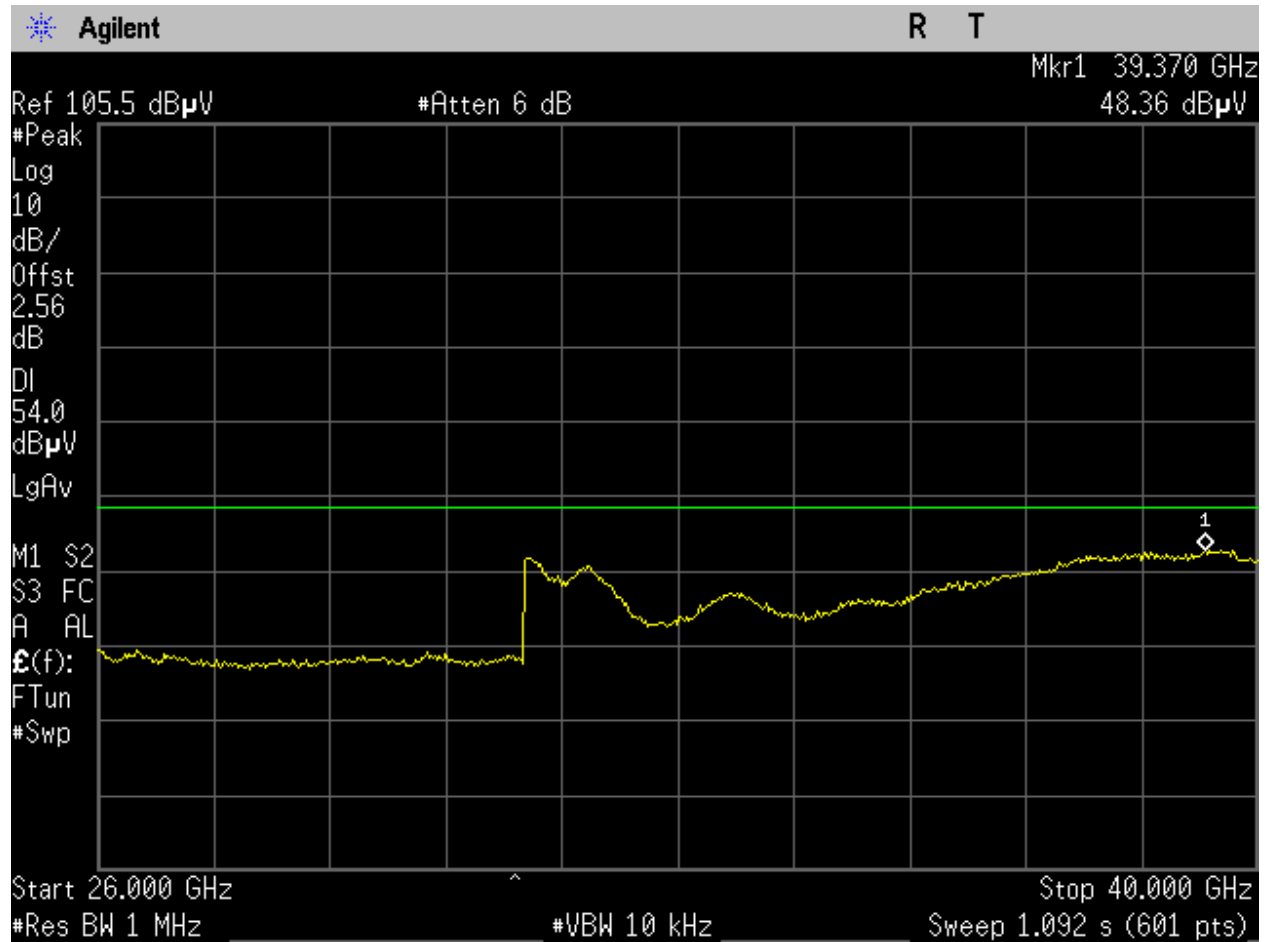


Figure 539: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 1.

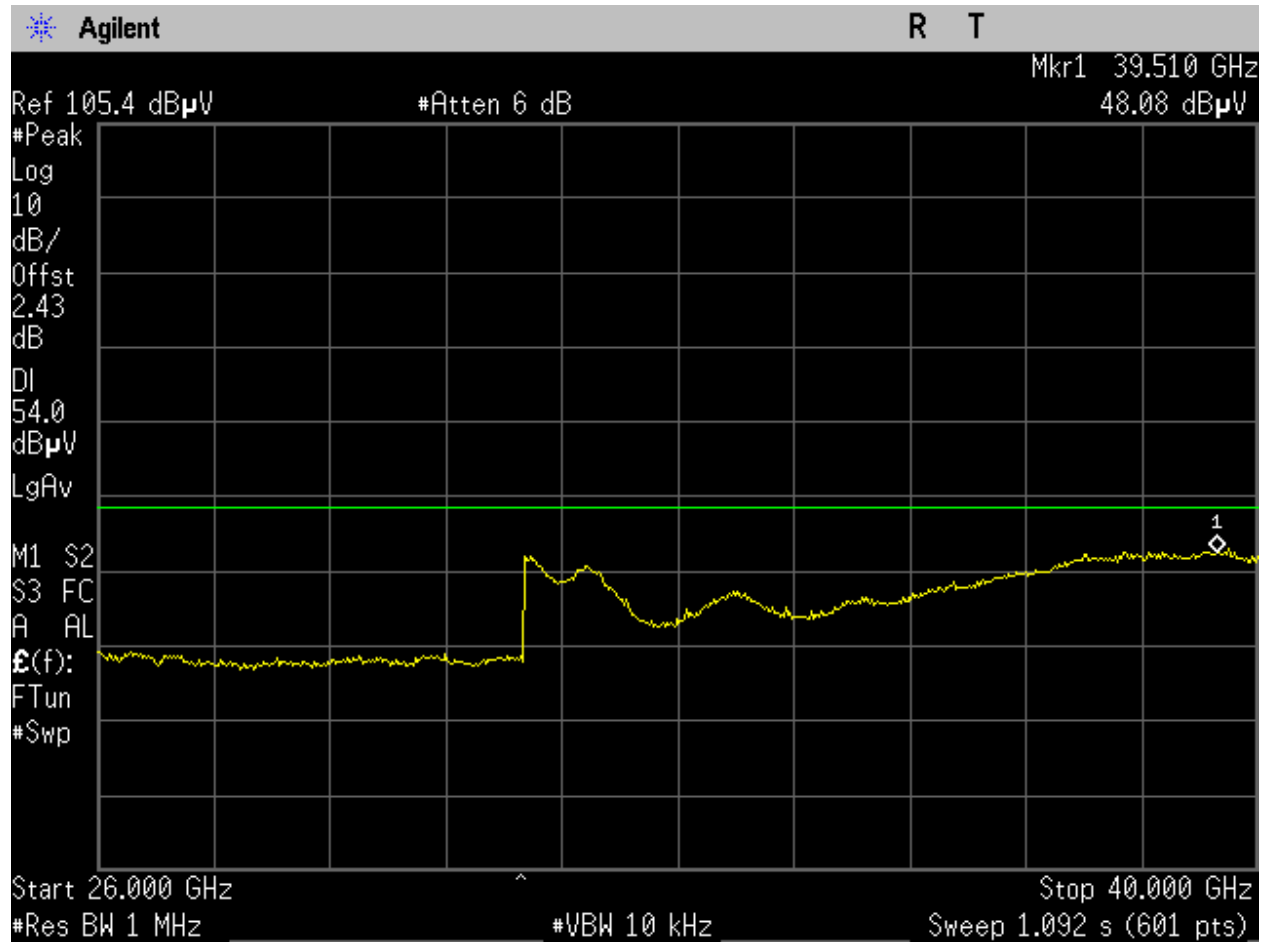


Figure 540: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 2.

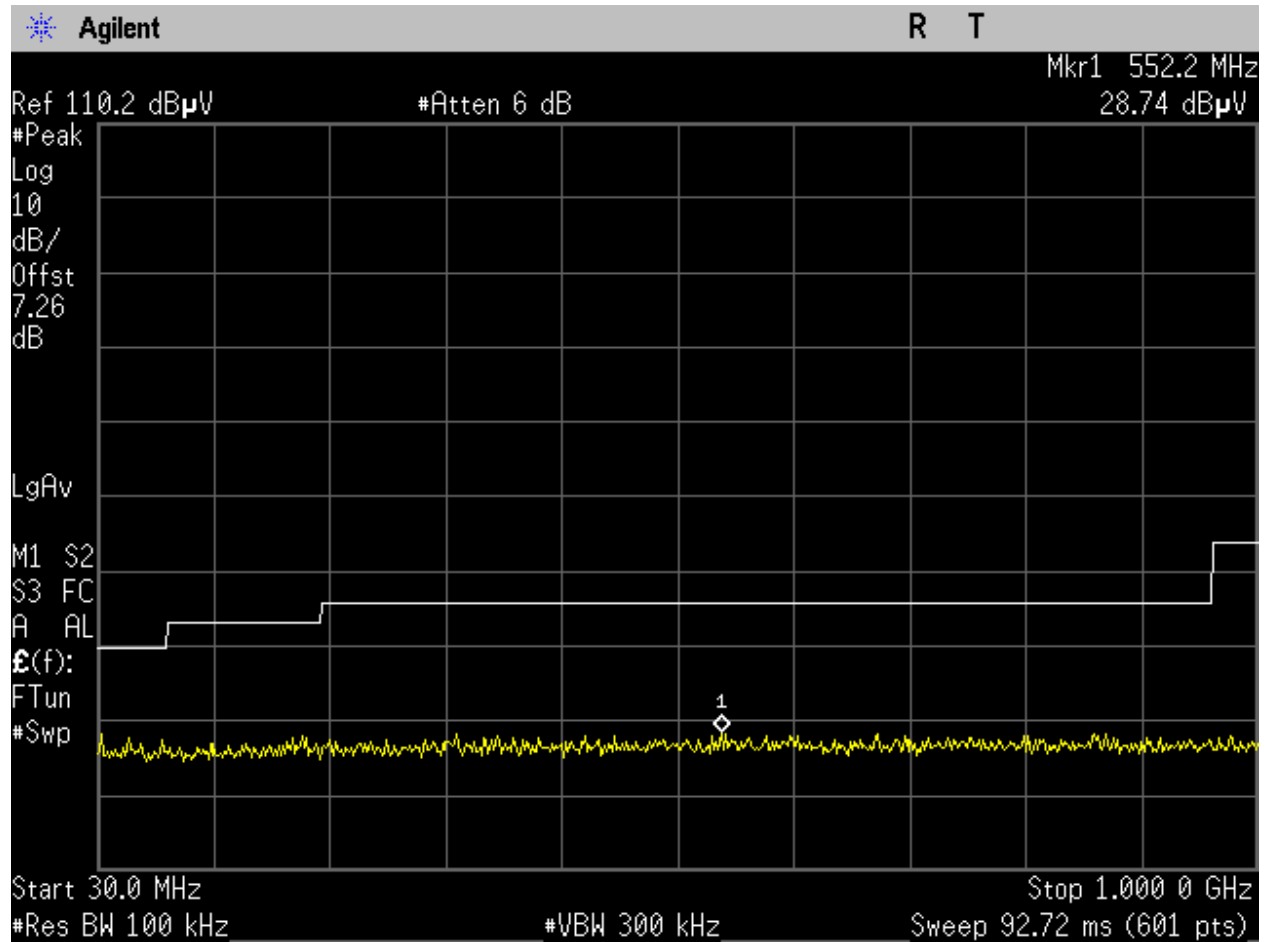


Figure 541: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 1.

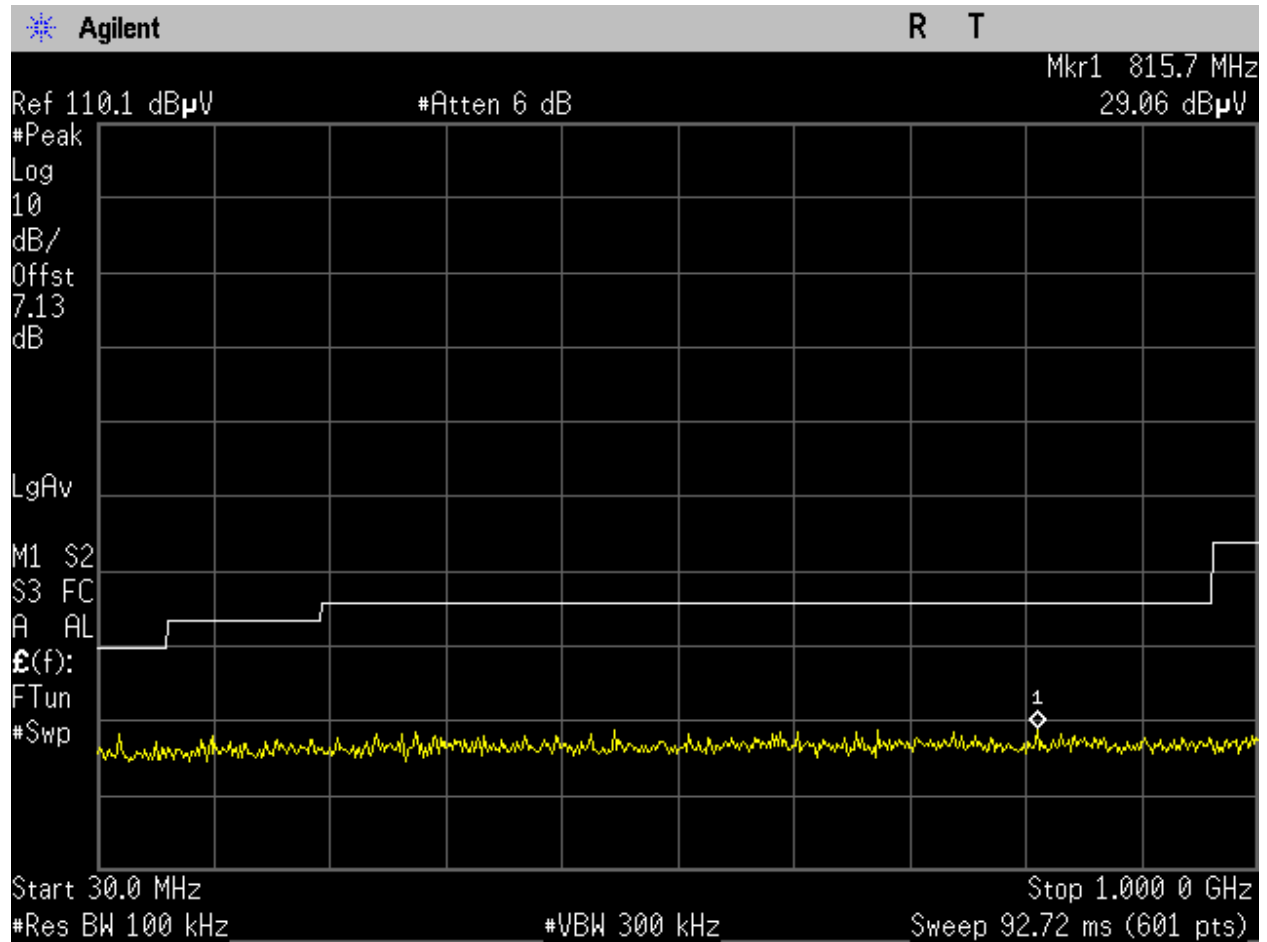


Figure 542: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 2.

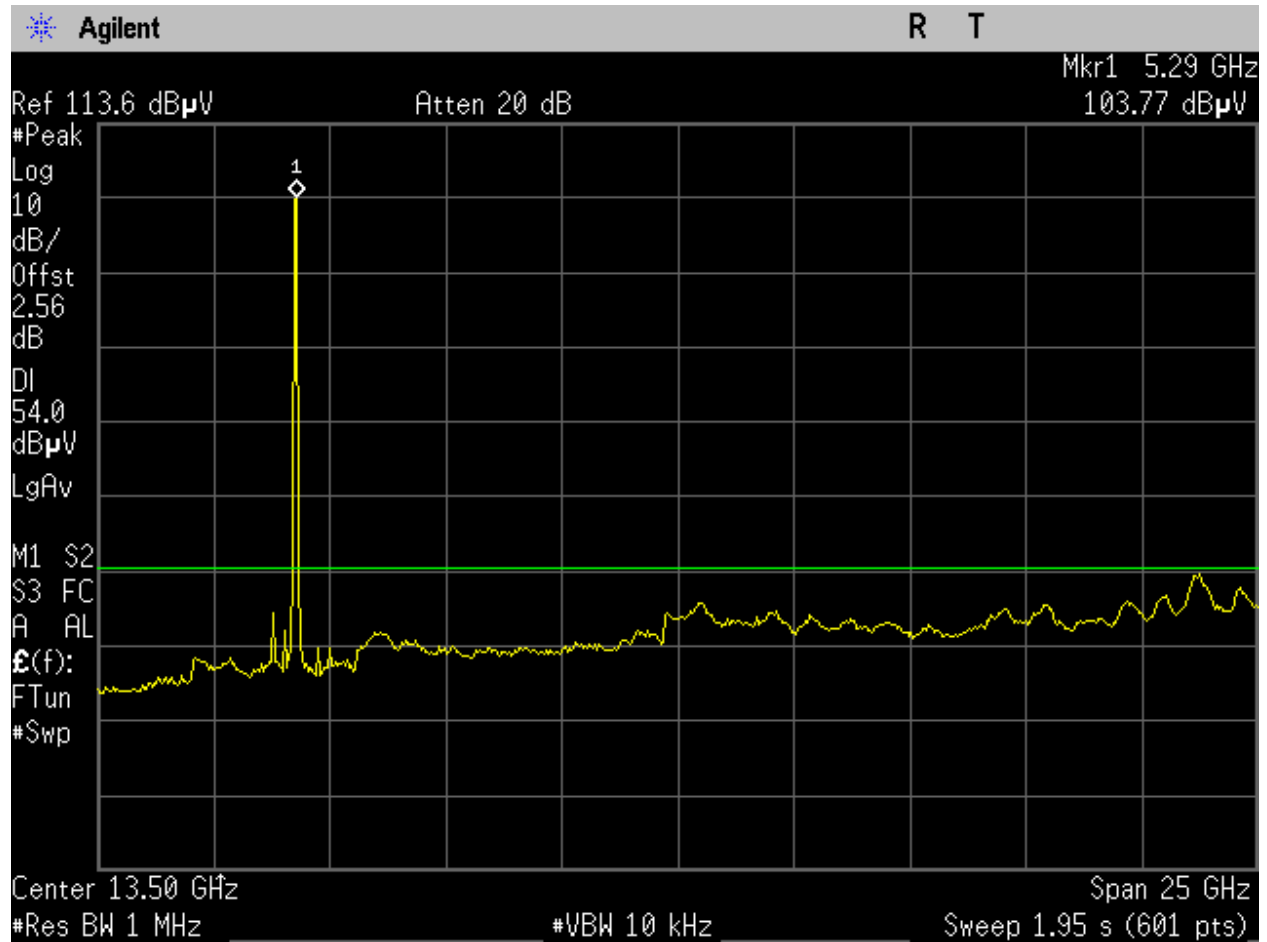


Figure 543: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_15.209_1-26GHz avg_Port 1.

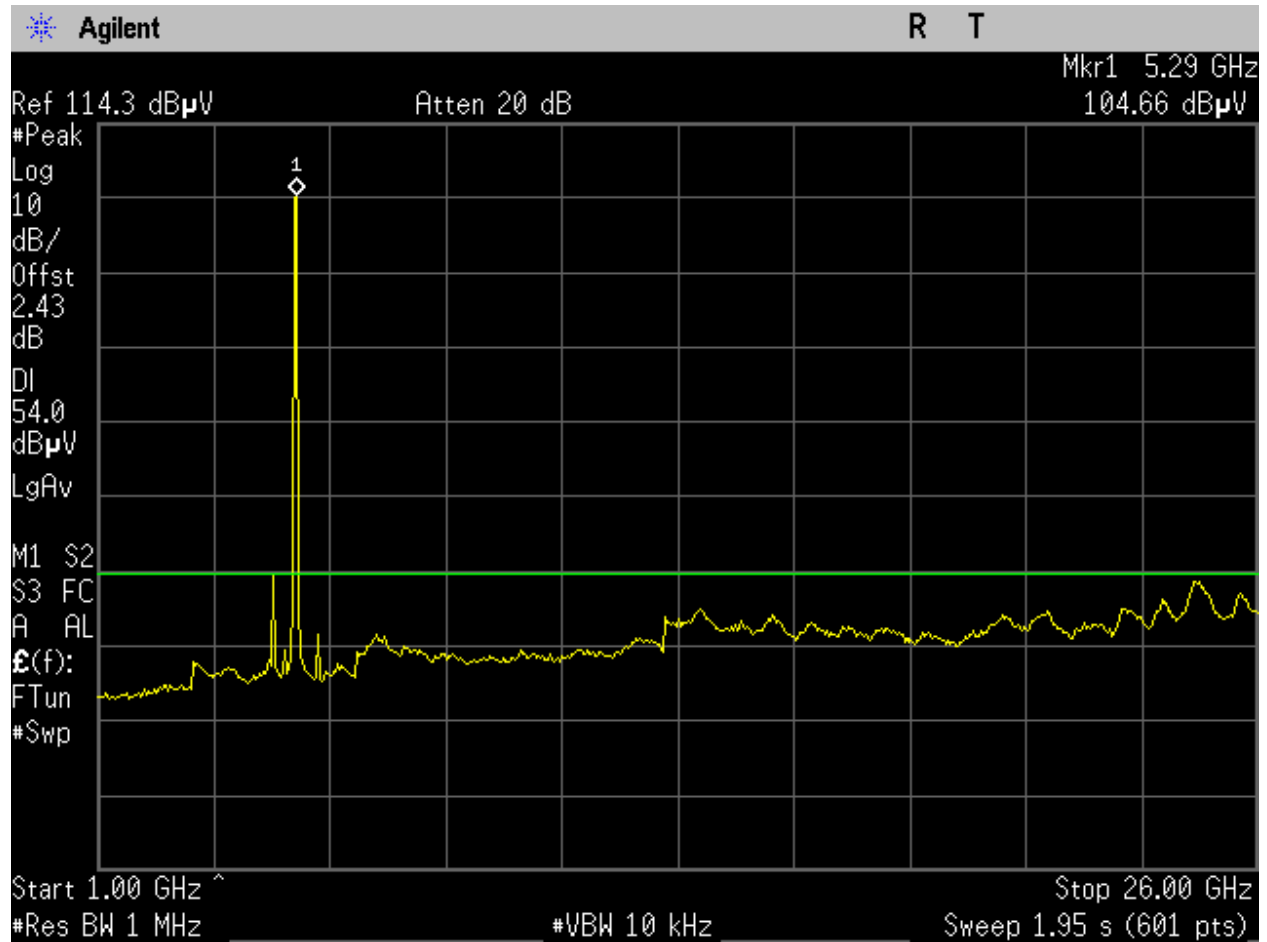


Figure 544: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_15.209_1-26GHz avg_Port 2.

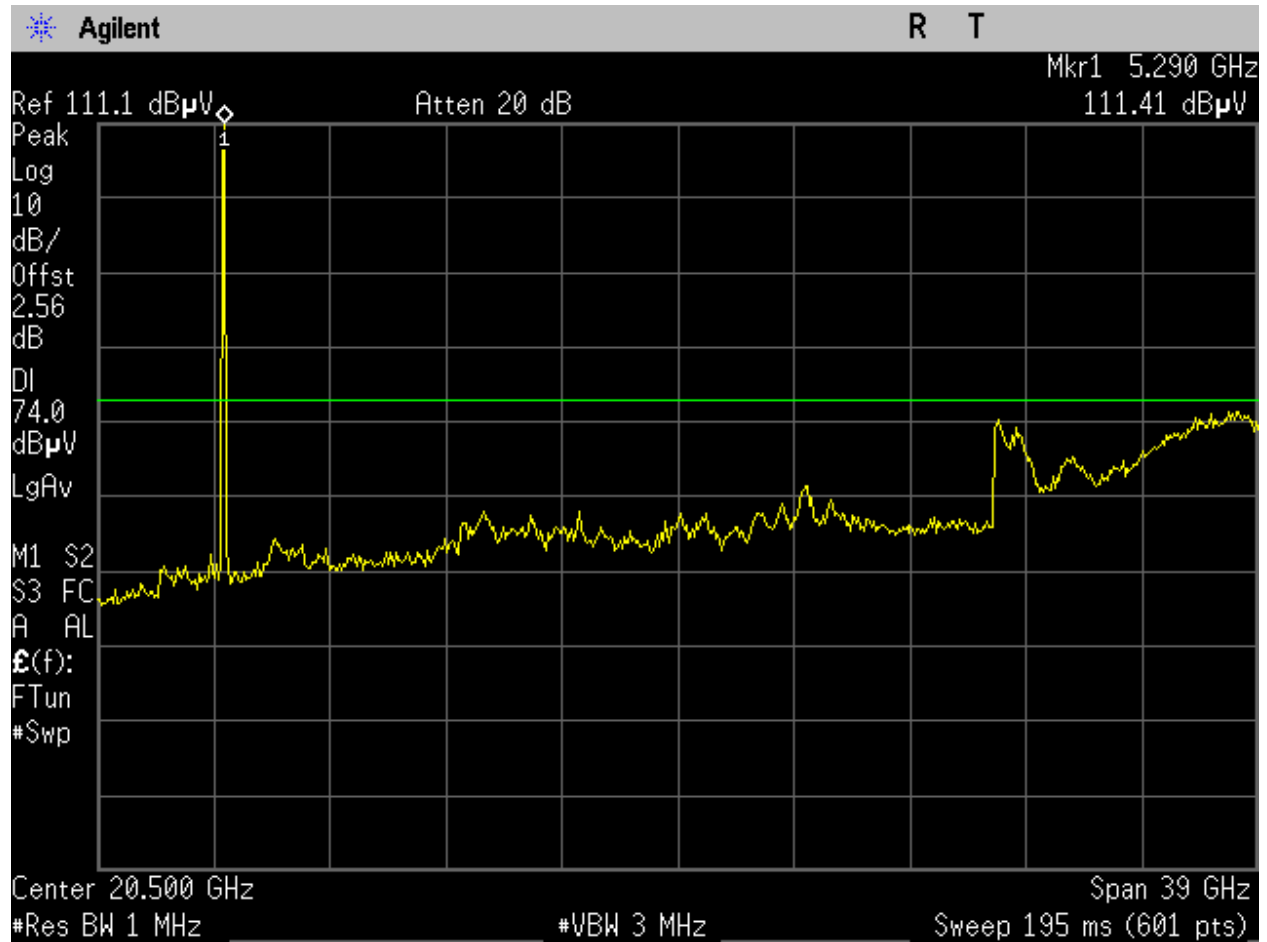


Figure 545: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_15.209_1-40GHz_Peak_Port 1.

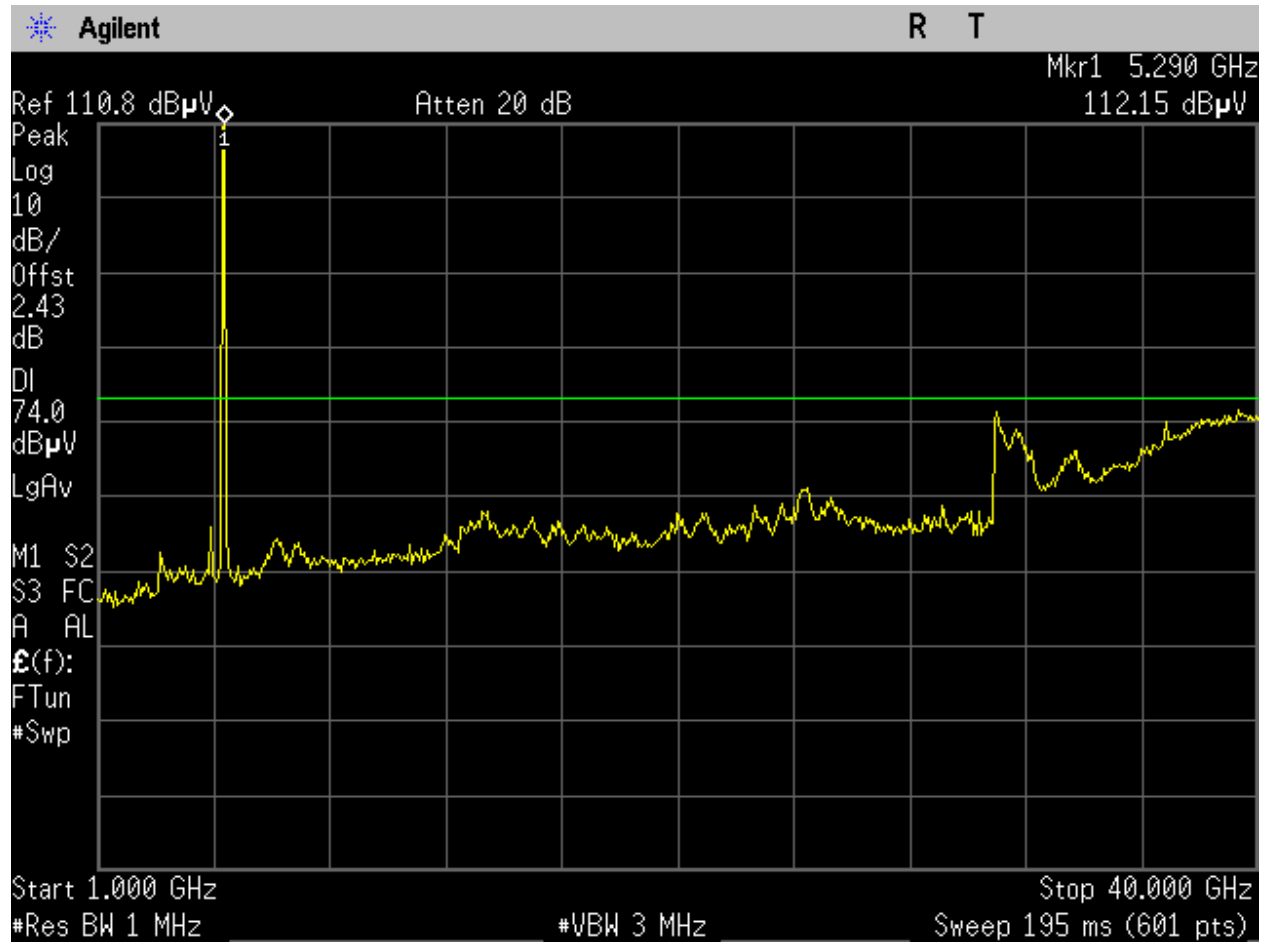


Figure 546: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_15.209_1-40GHz_Peak_Port 2.

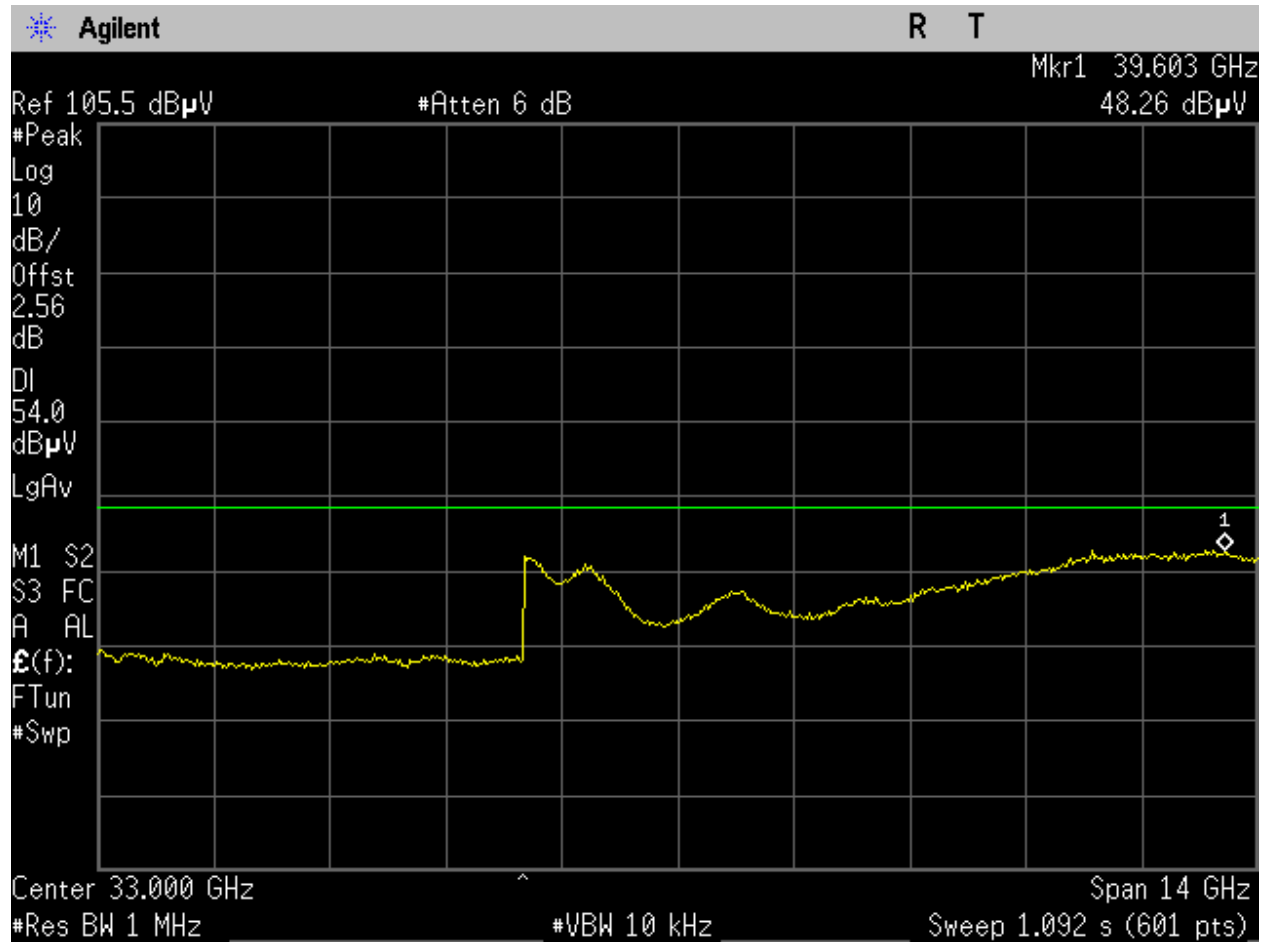


Figure 547: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_15.209_26-40GHz_Avg_Port 1.

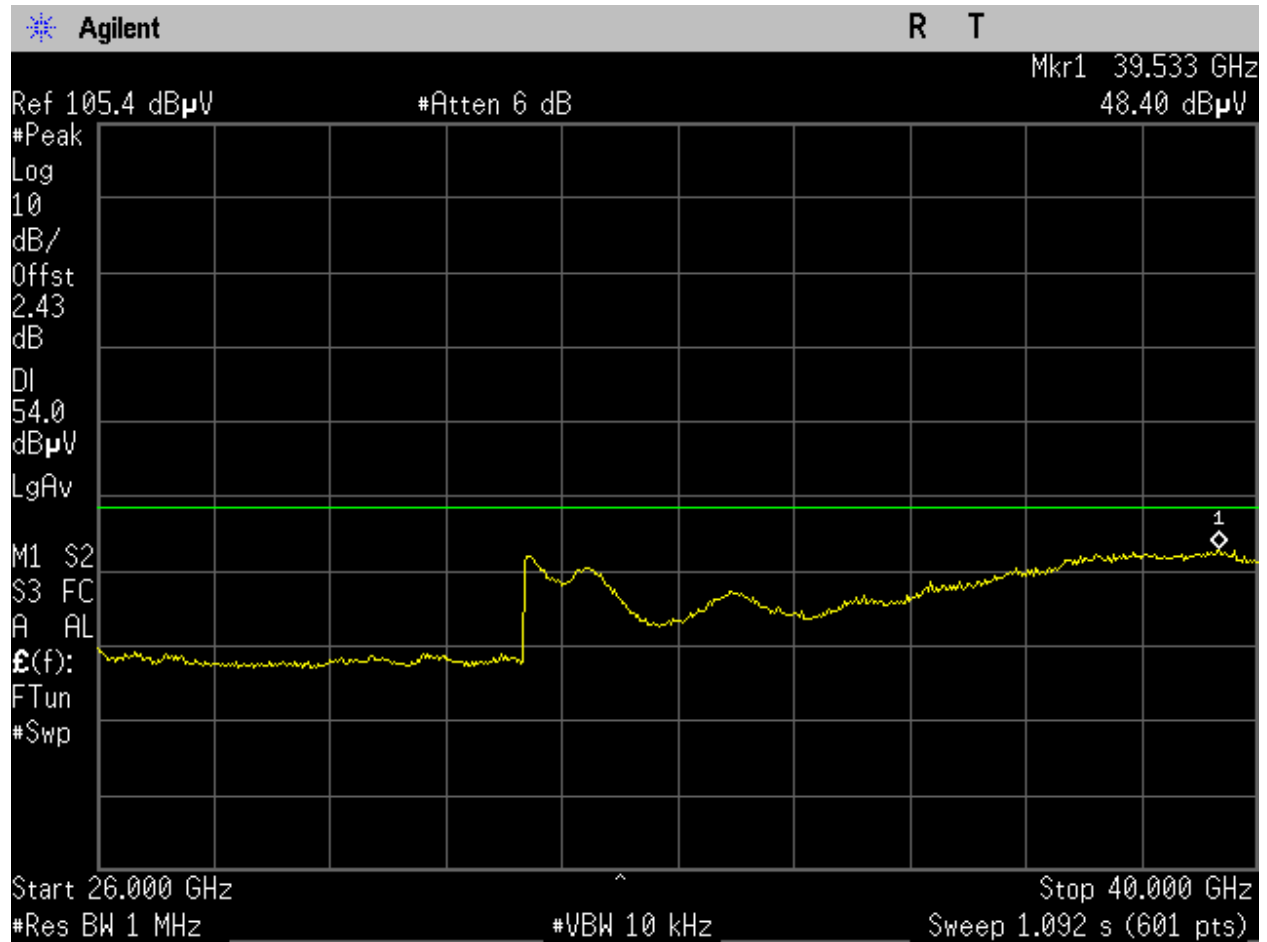


Figure 548: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_15.209_26-40GHz_Avg_Port 2.

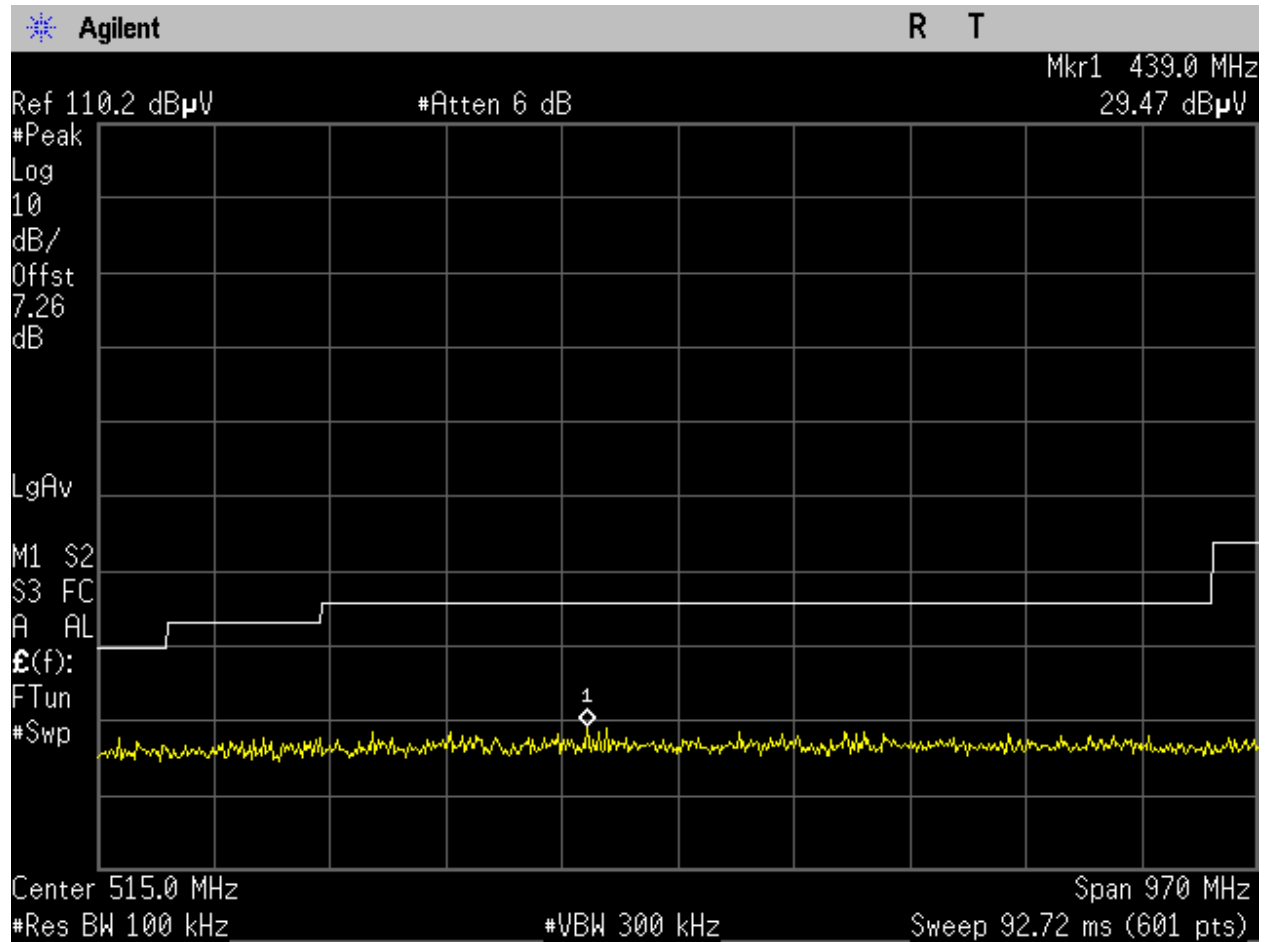


Figure 549: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 1.

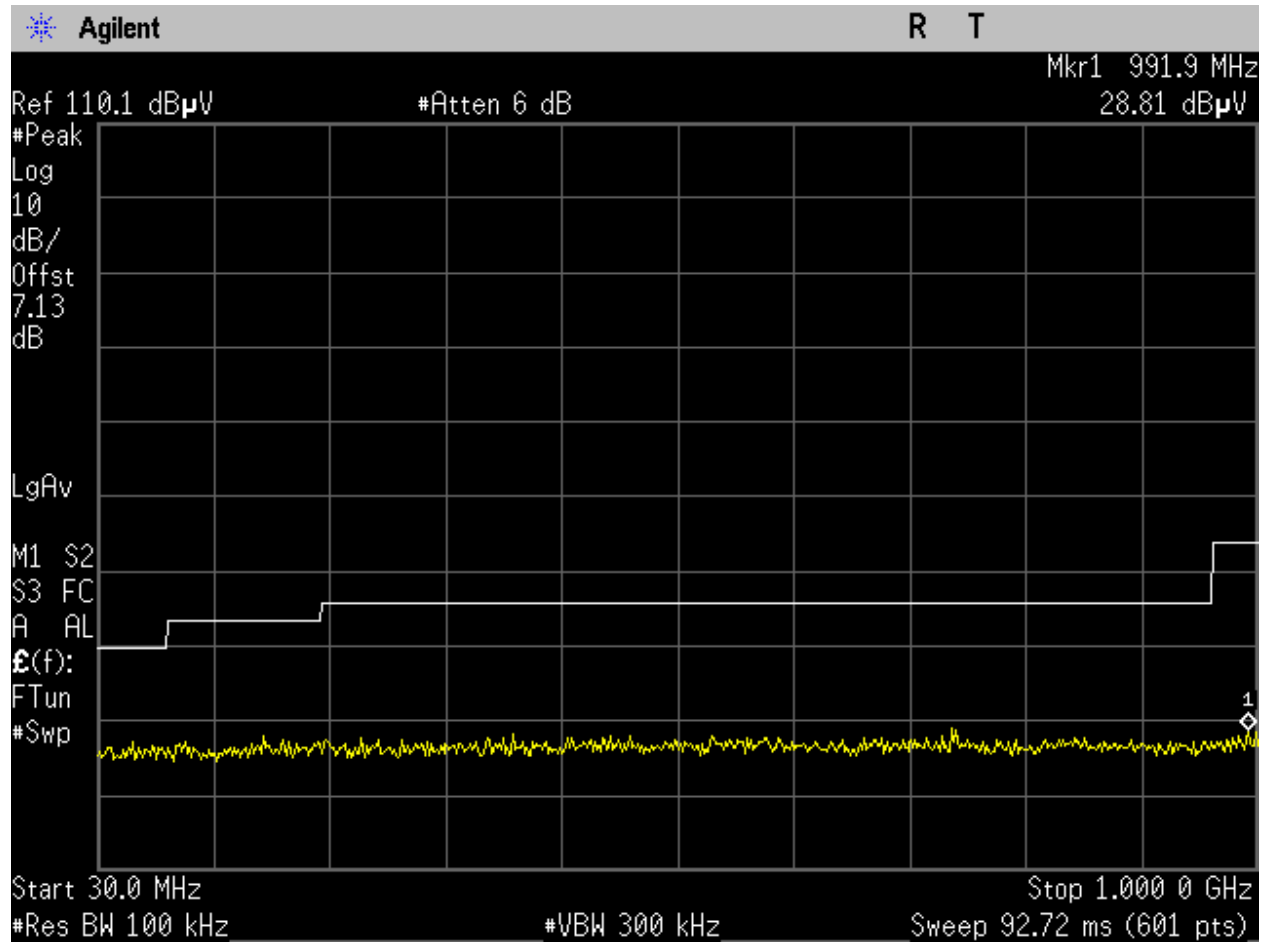


Figure 550: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 2.

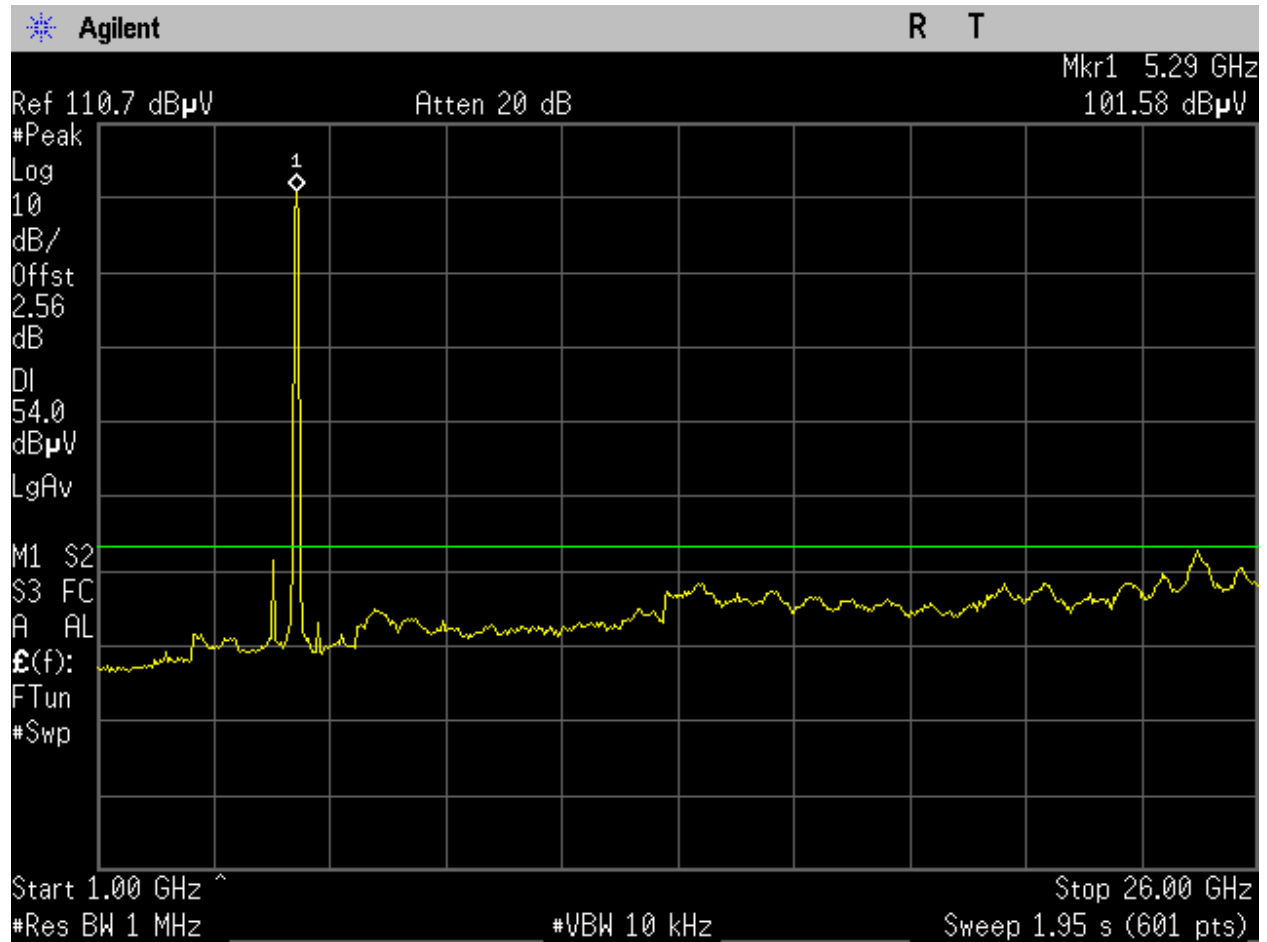


Figure 551: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

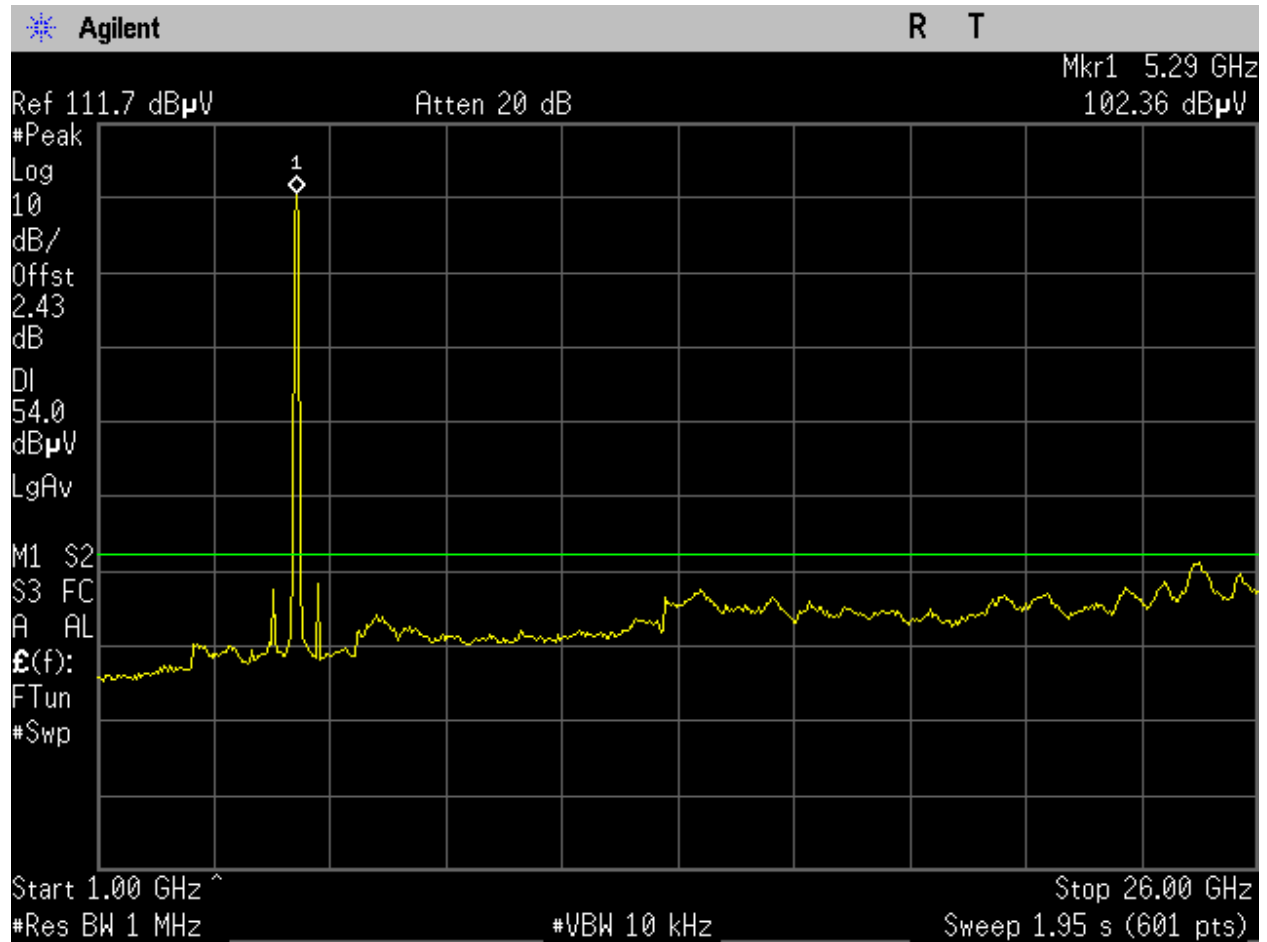


Figure 552: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

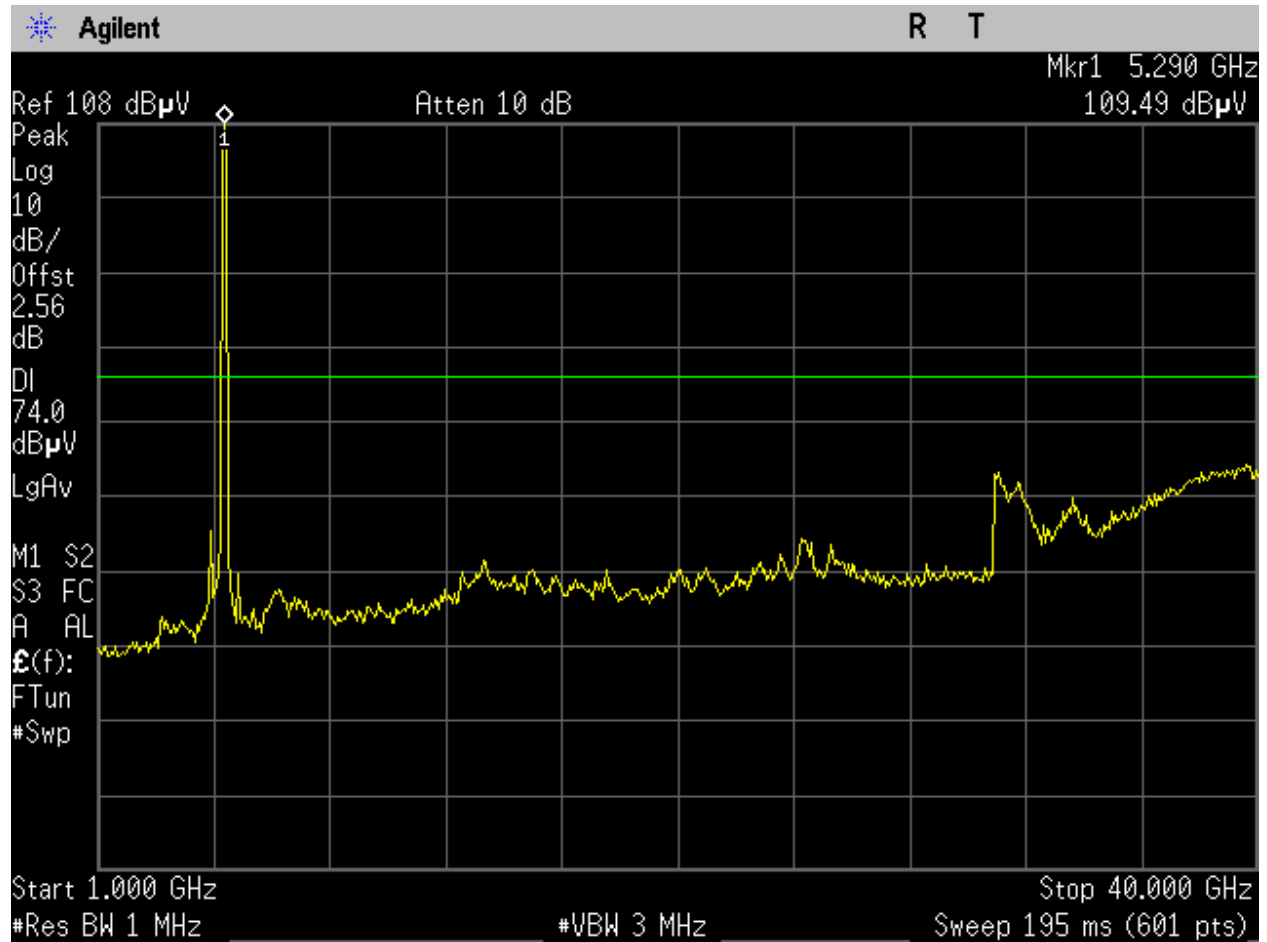


Figure 553: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 1.

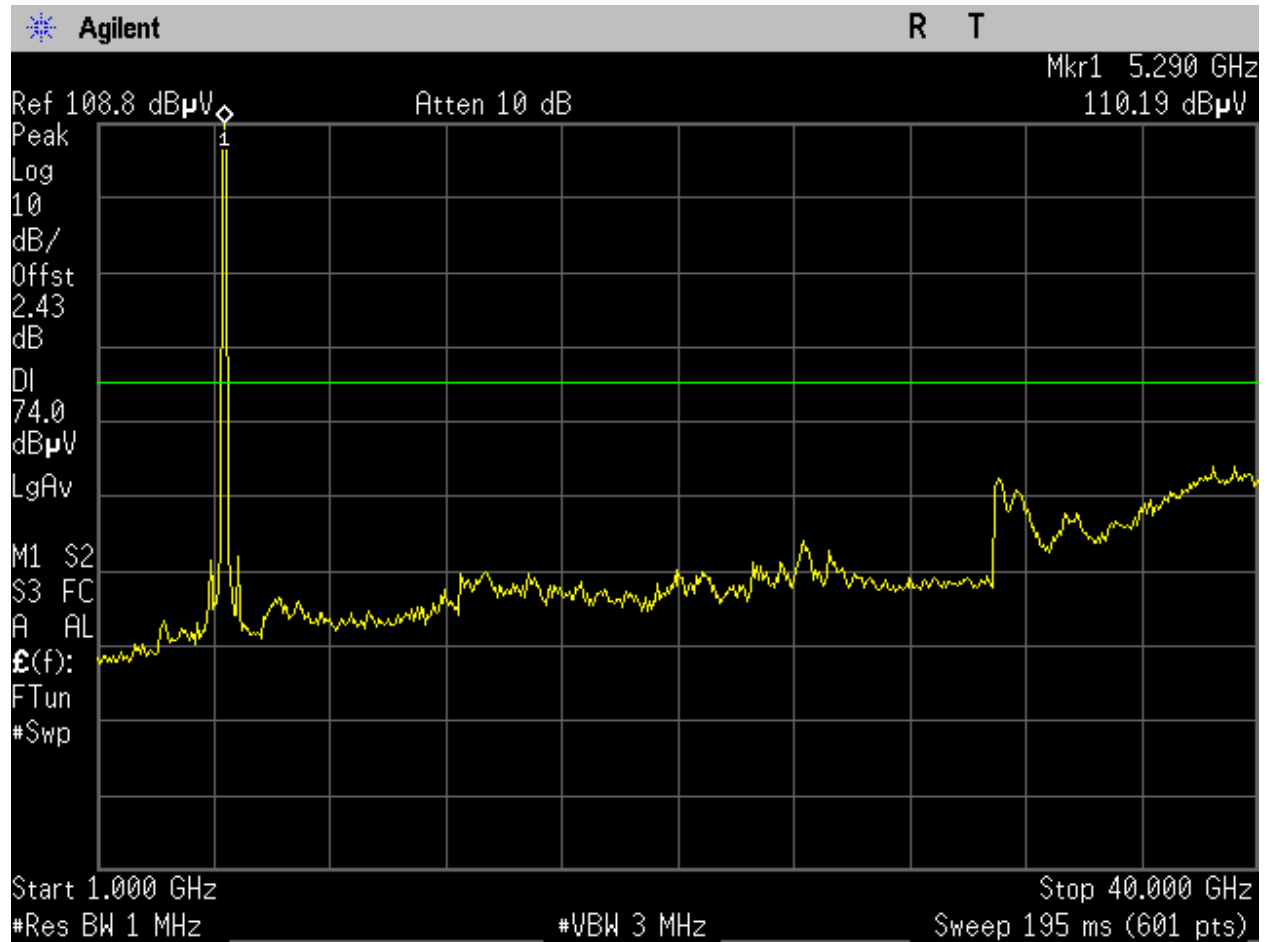


Figure 554: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 2.

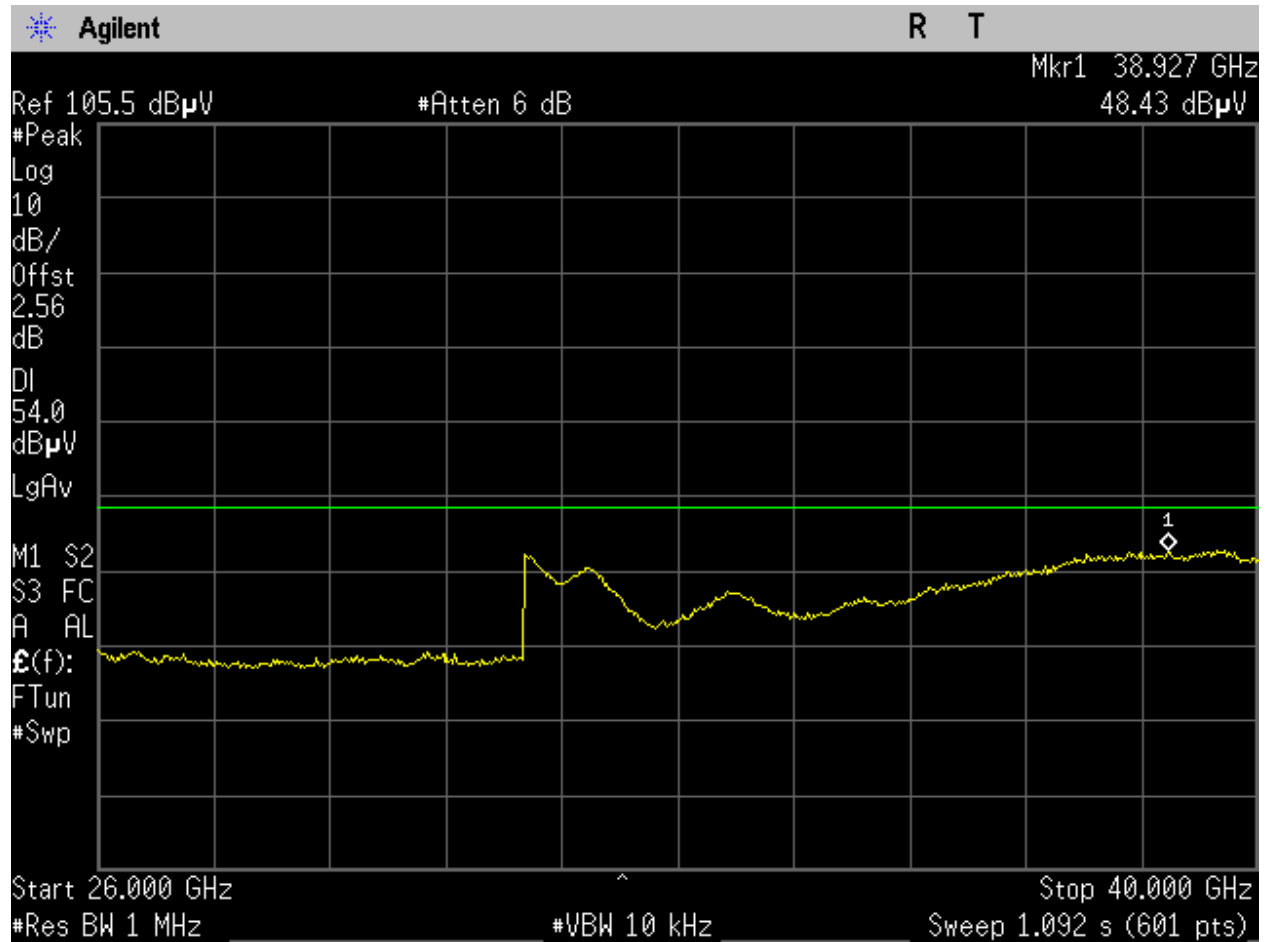


Figure 555: U-NII-2A_5290MHz_High_Mid_Ch_58_80MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

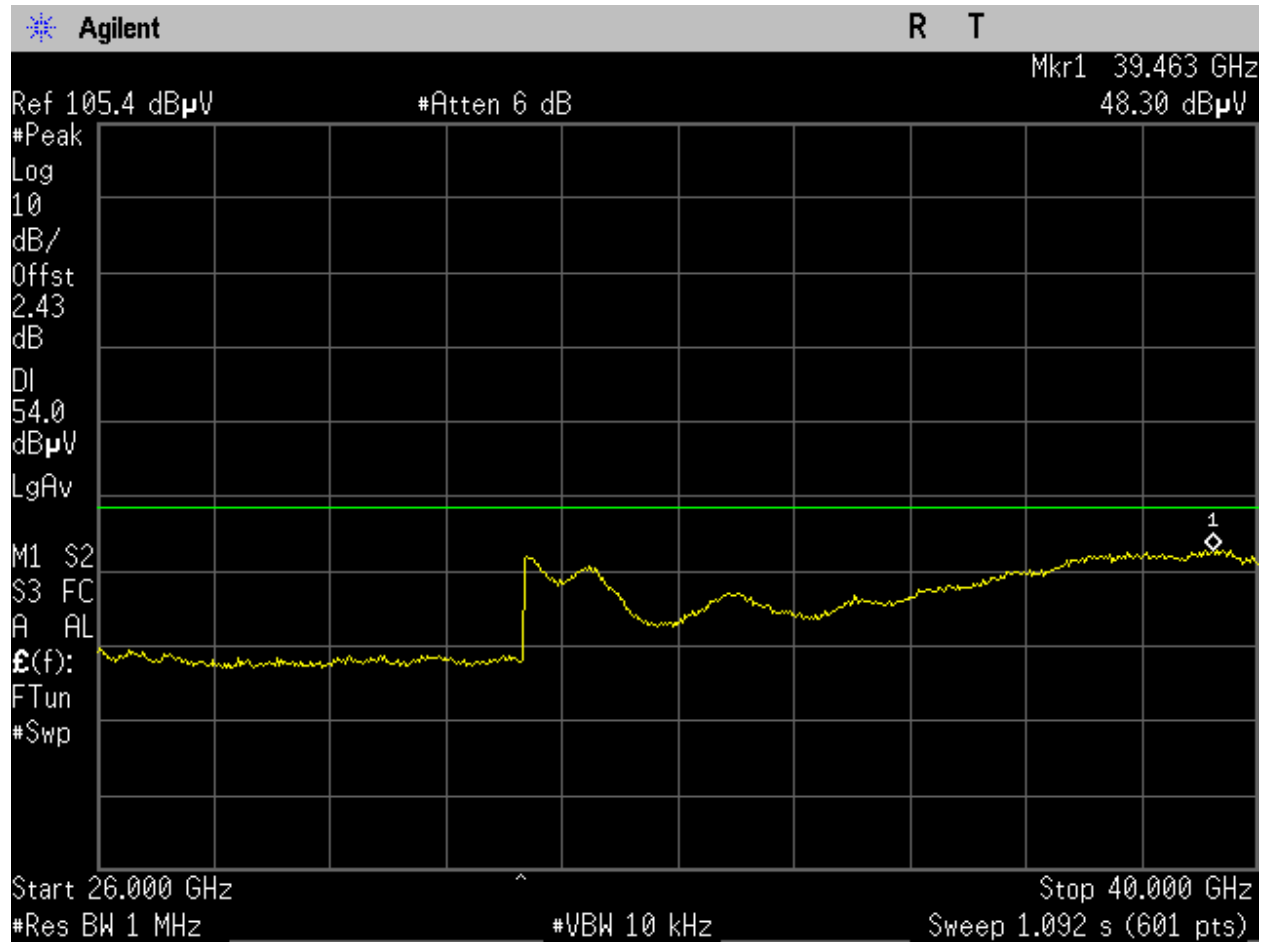


Figure 556: U-NII-2A_5290MHz_High_Mid_Ch_58_80MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

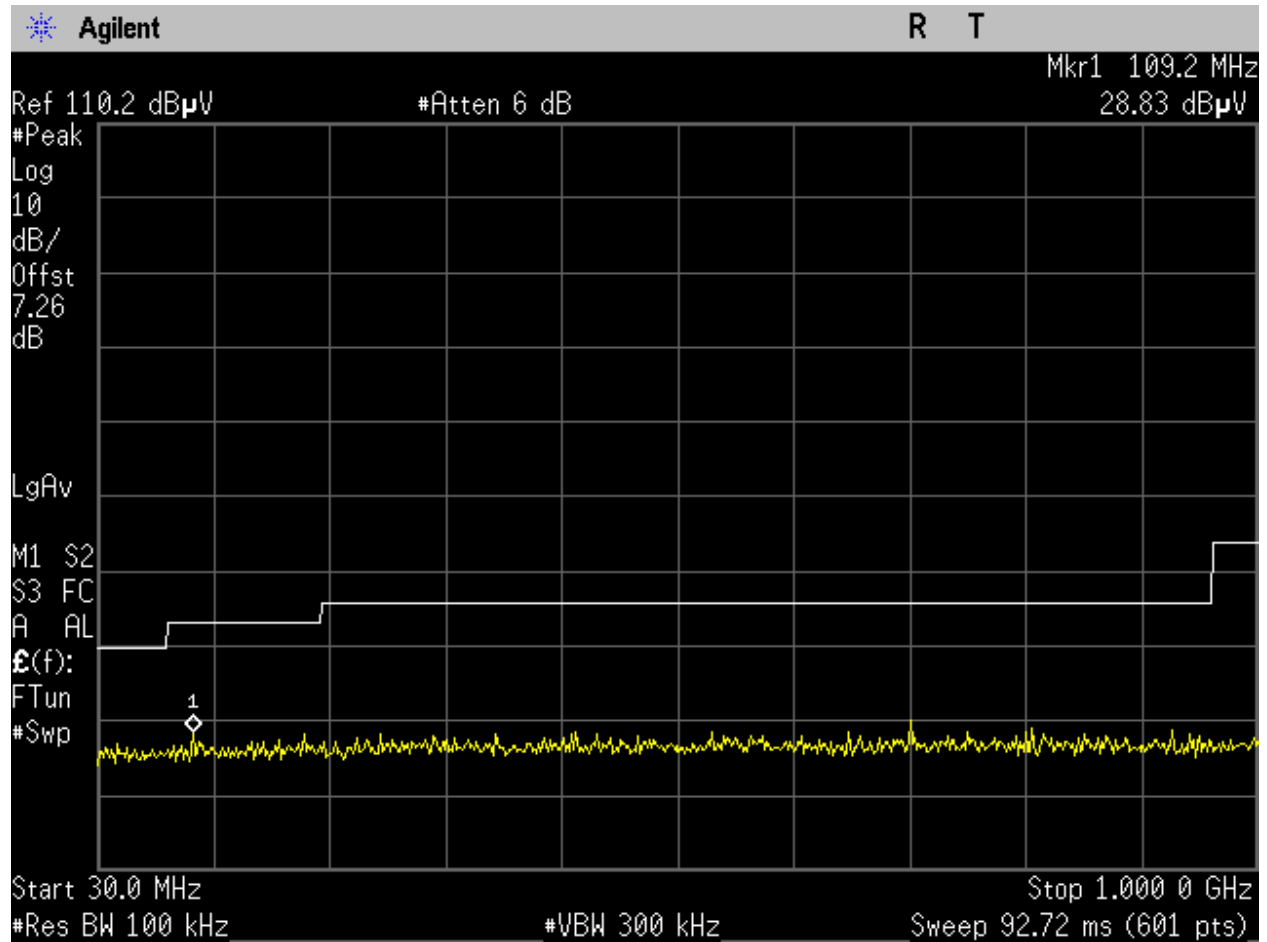


Figure 557: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

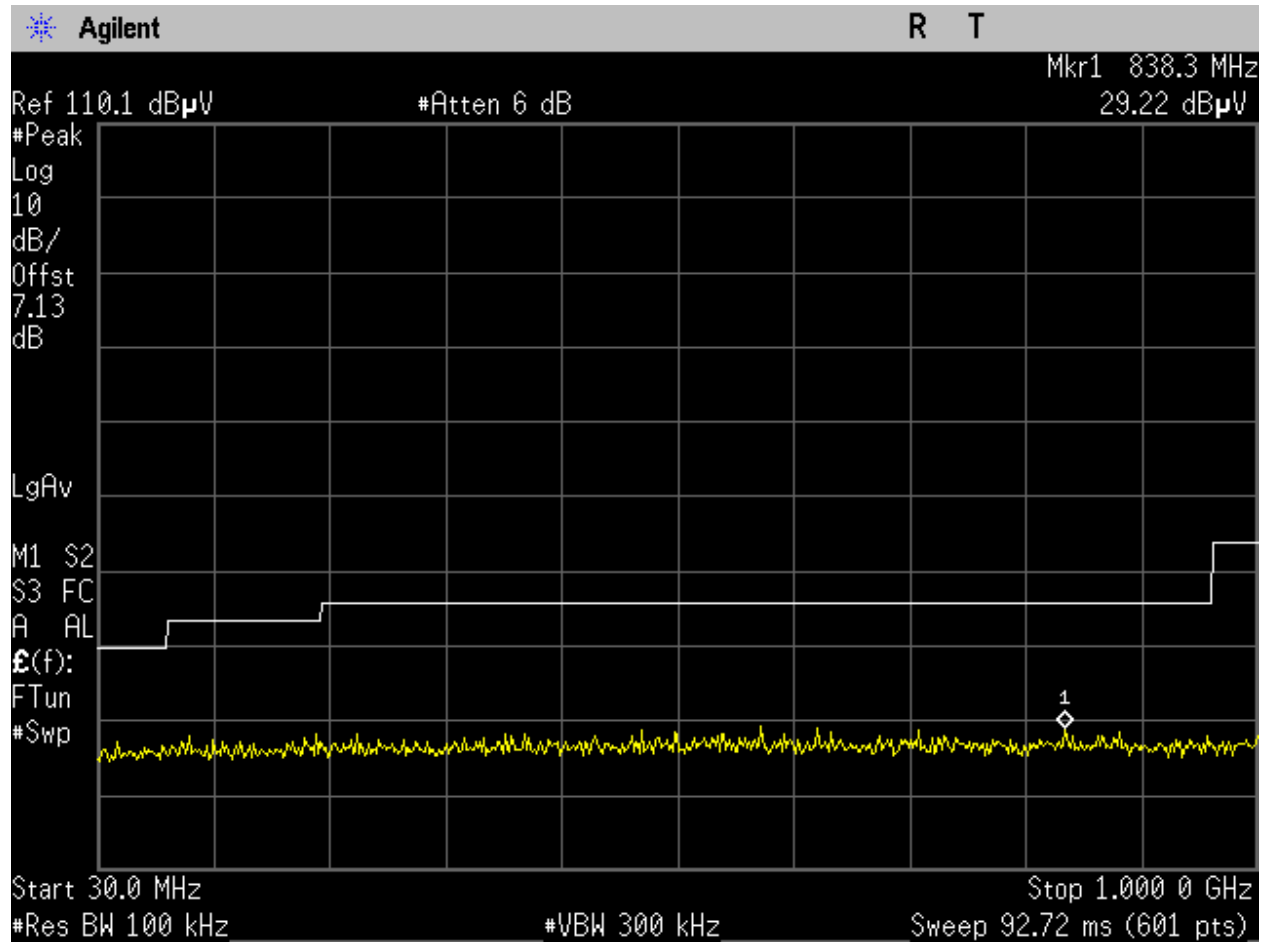


Figure 558: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

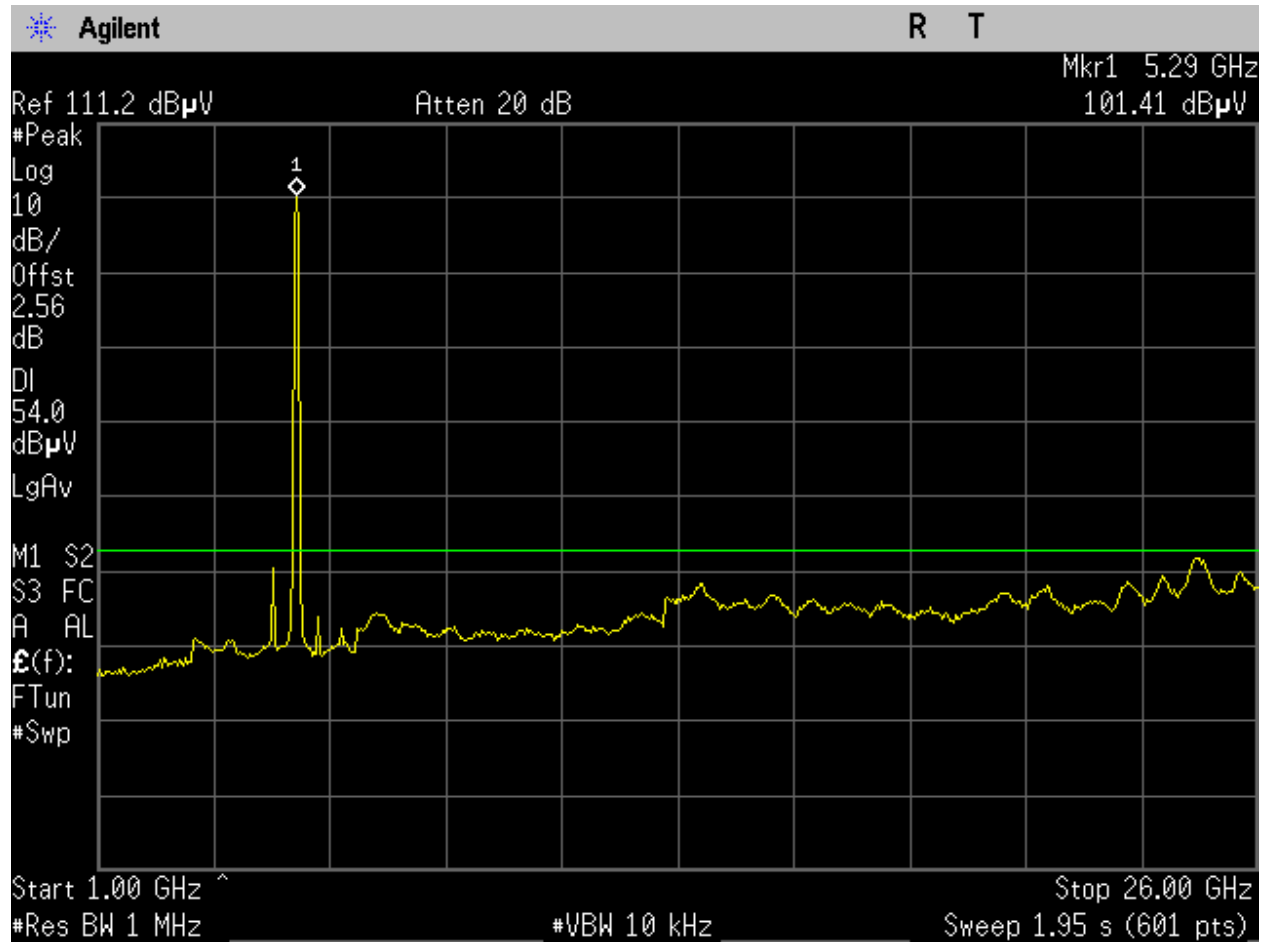


Figure 559: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_1-26GHz avg_Port 1.

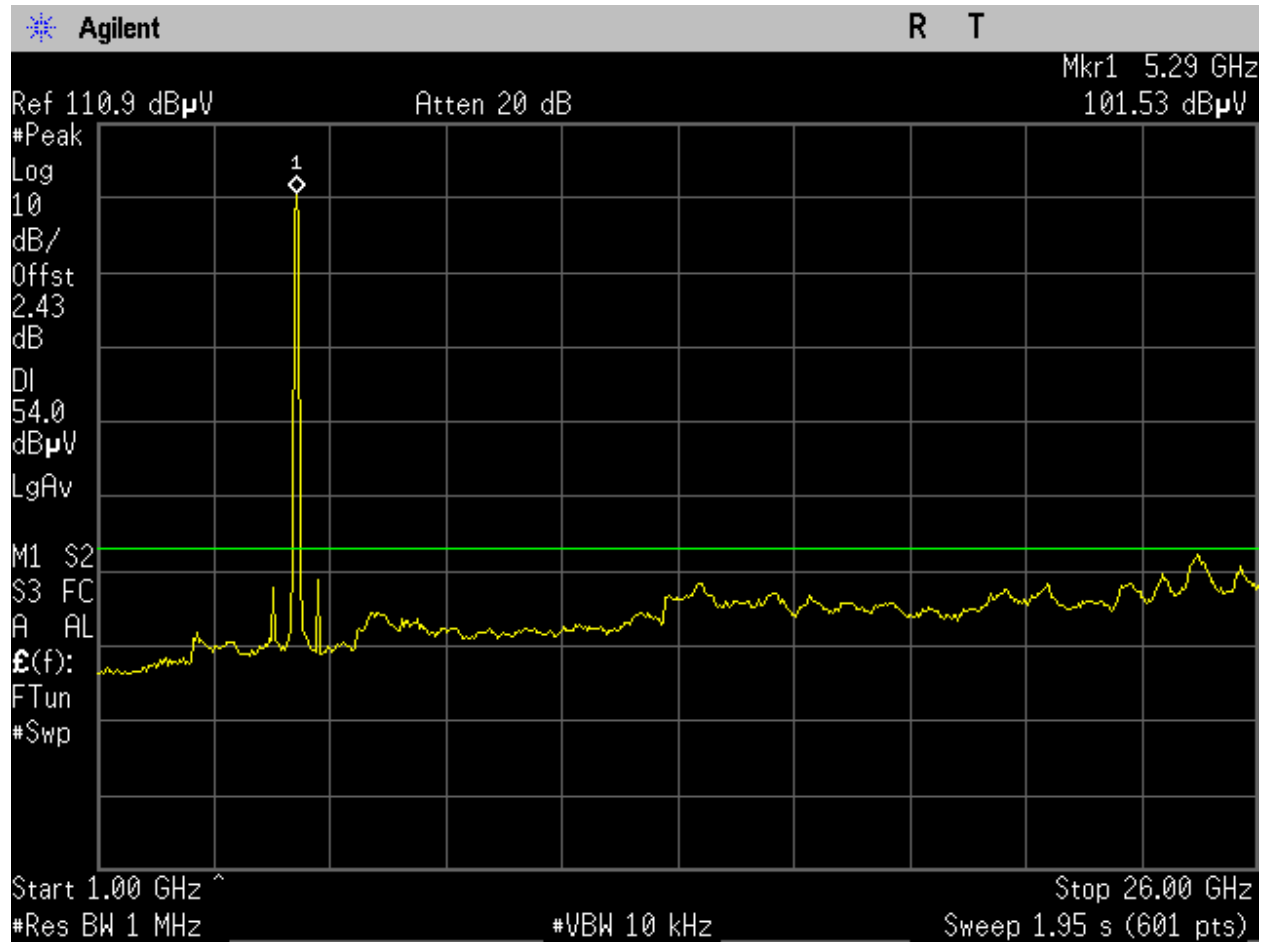


Figure 560: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_1-26GHz avg_Port 2.

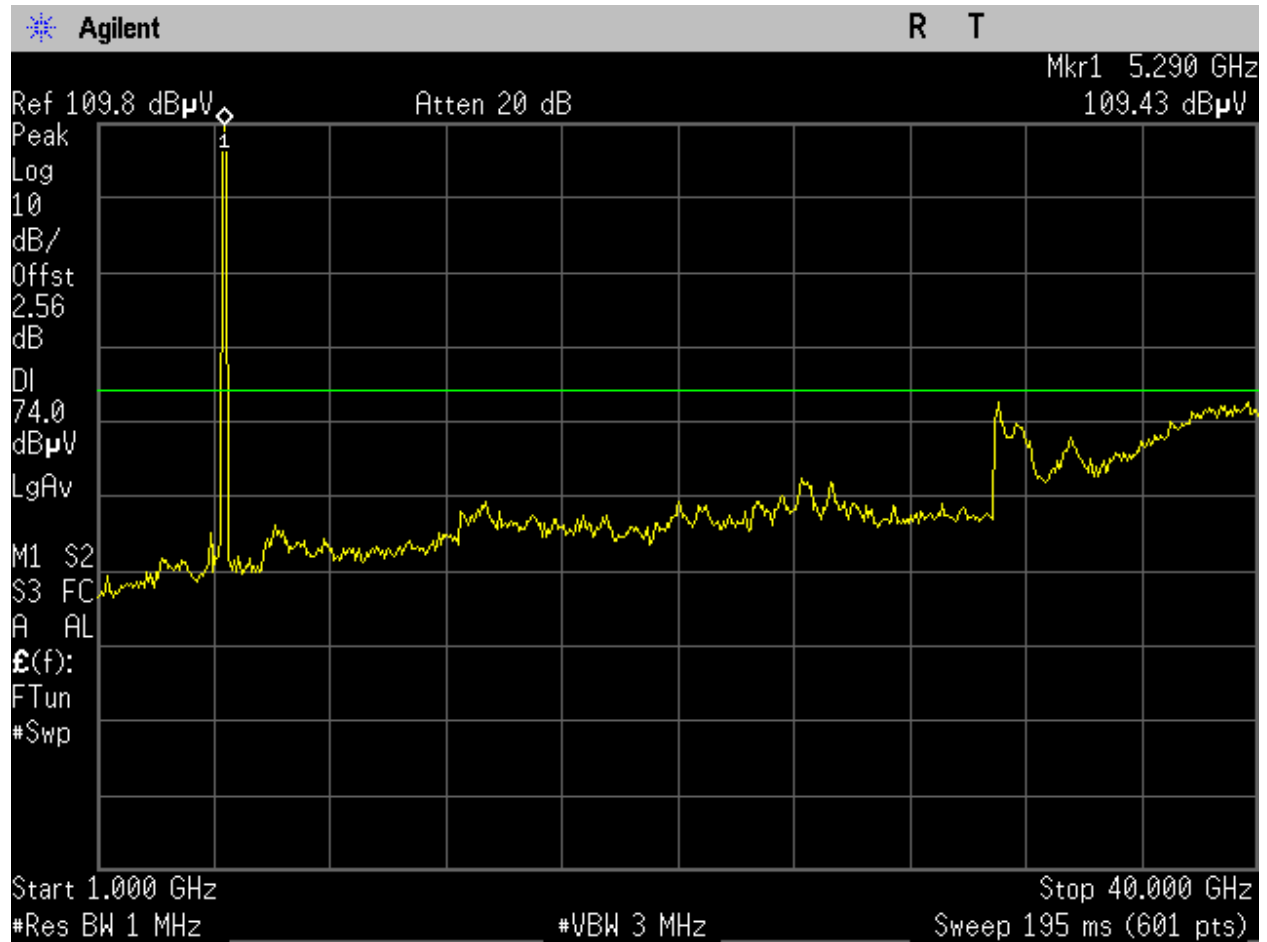


Figure 561: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 1.

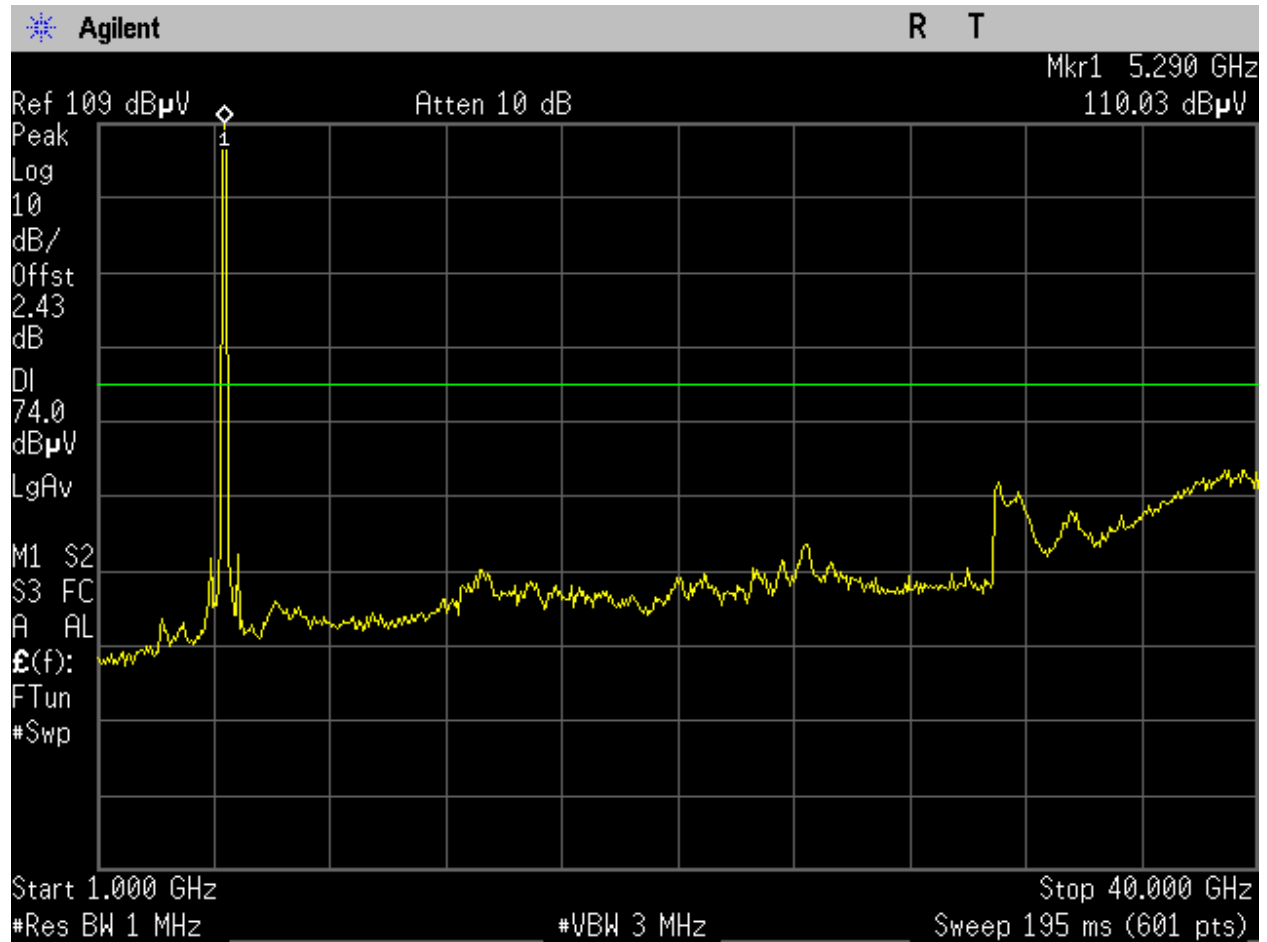


Figure 562: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 2.

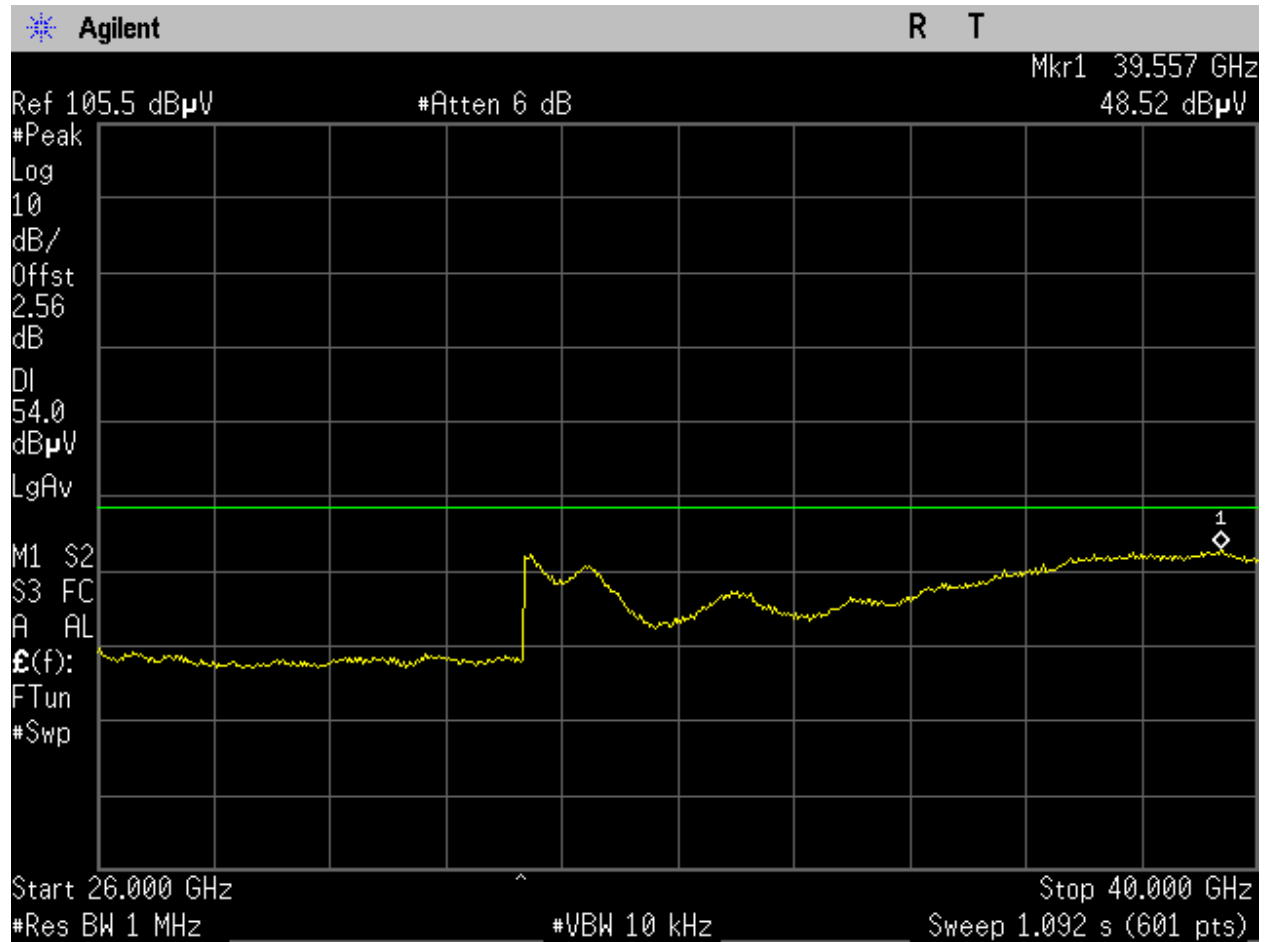


Figure 563: U-NII-2A_5290MHz_High_Mid_Ch_58_80MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 1.

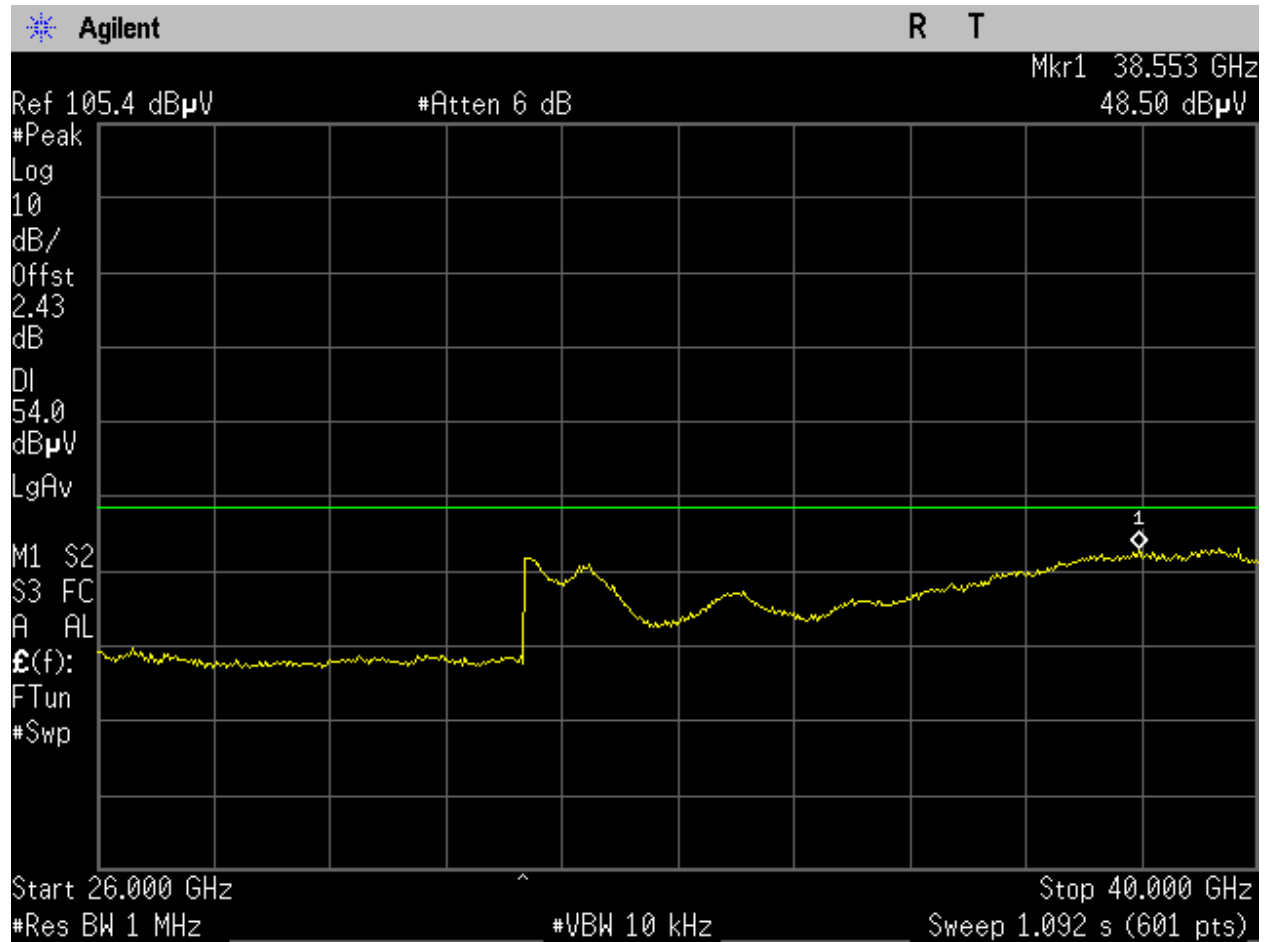


Figure 564: U-NII-2A_5290MHz_High_Mid_Ch_58_80MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 2.

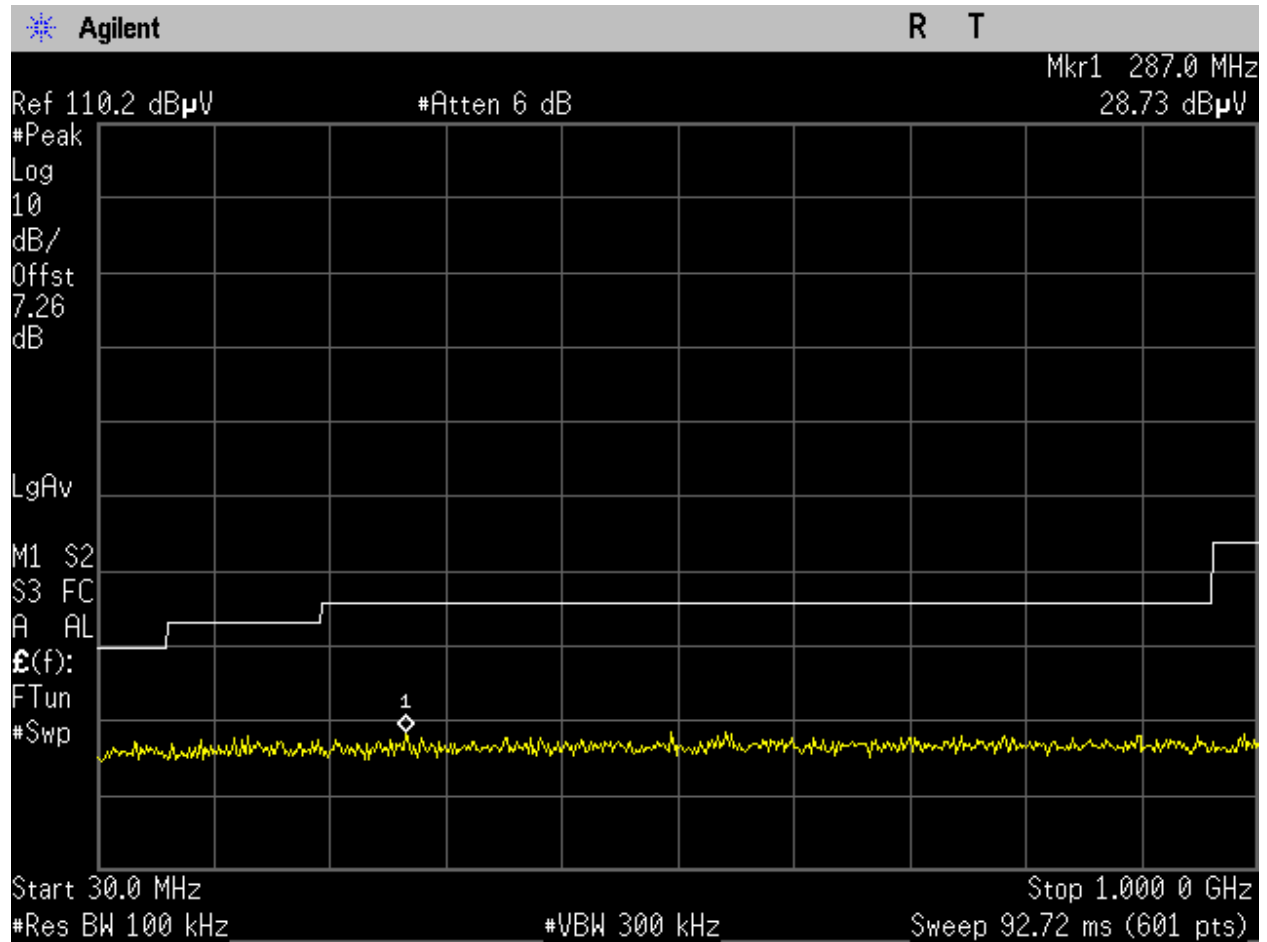


Figure 565: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 1.

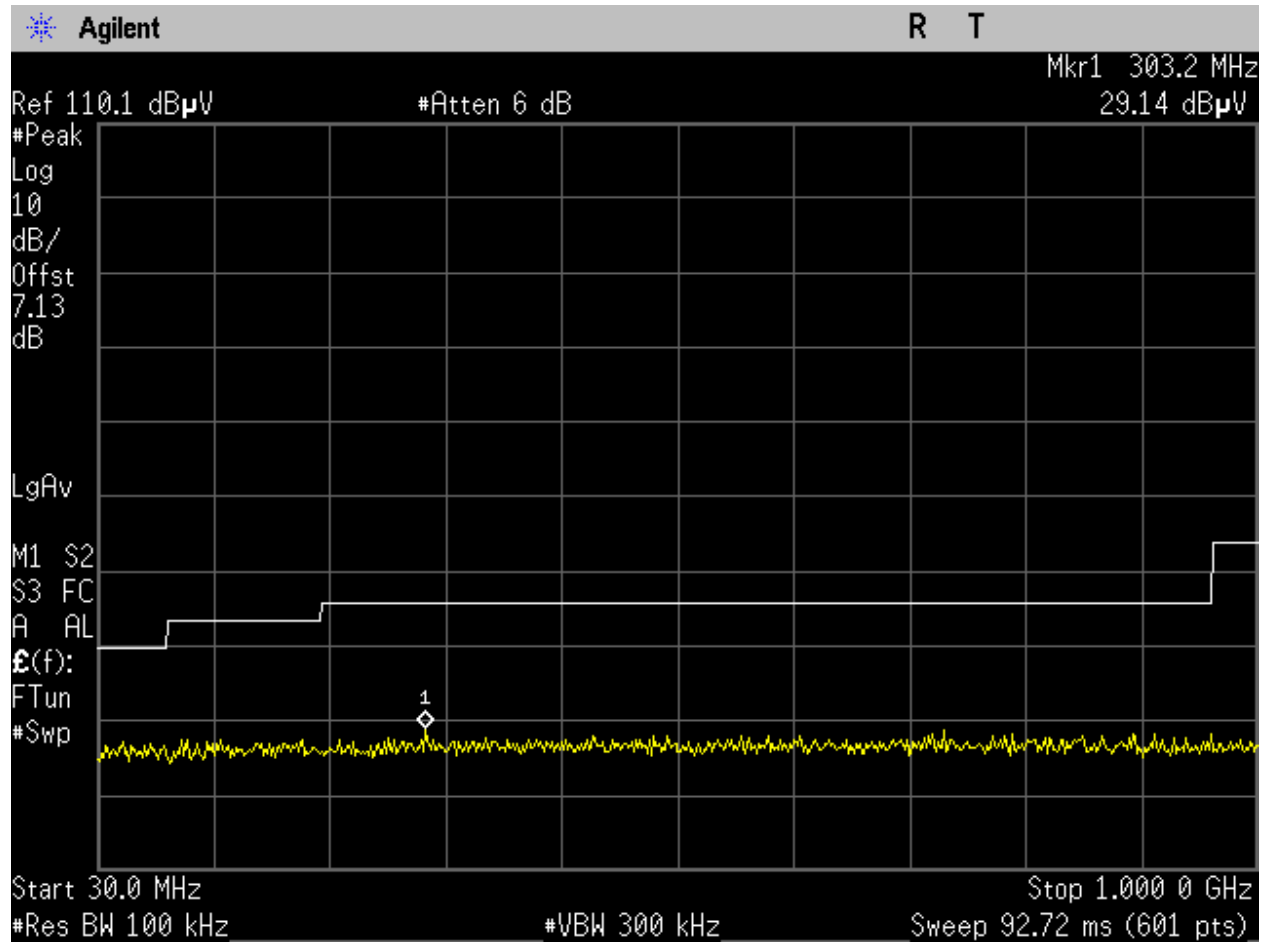


Figure 566: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 2.

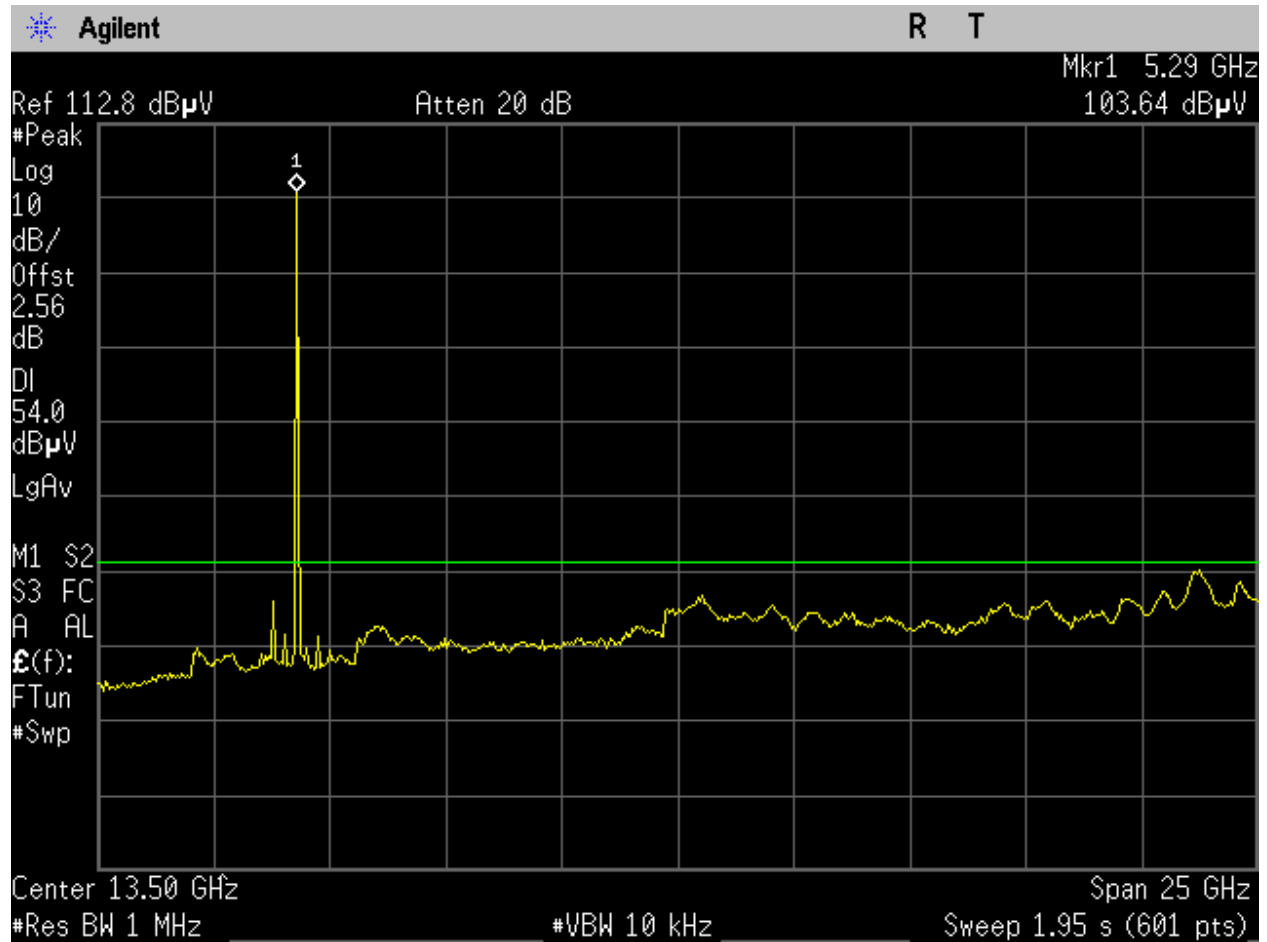


Figure 567: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_15.209_1-26GHz avg_Port 1.

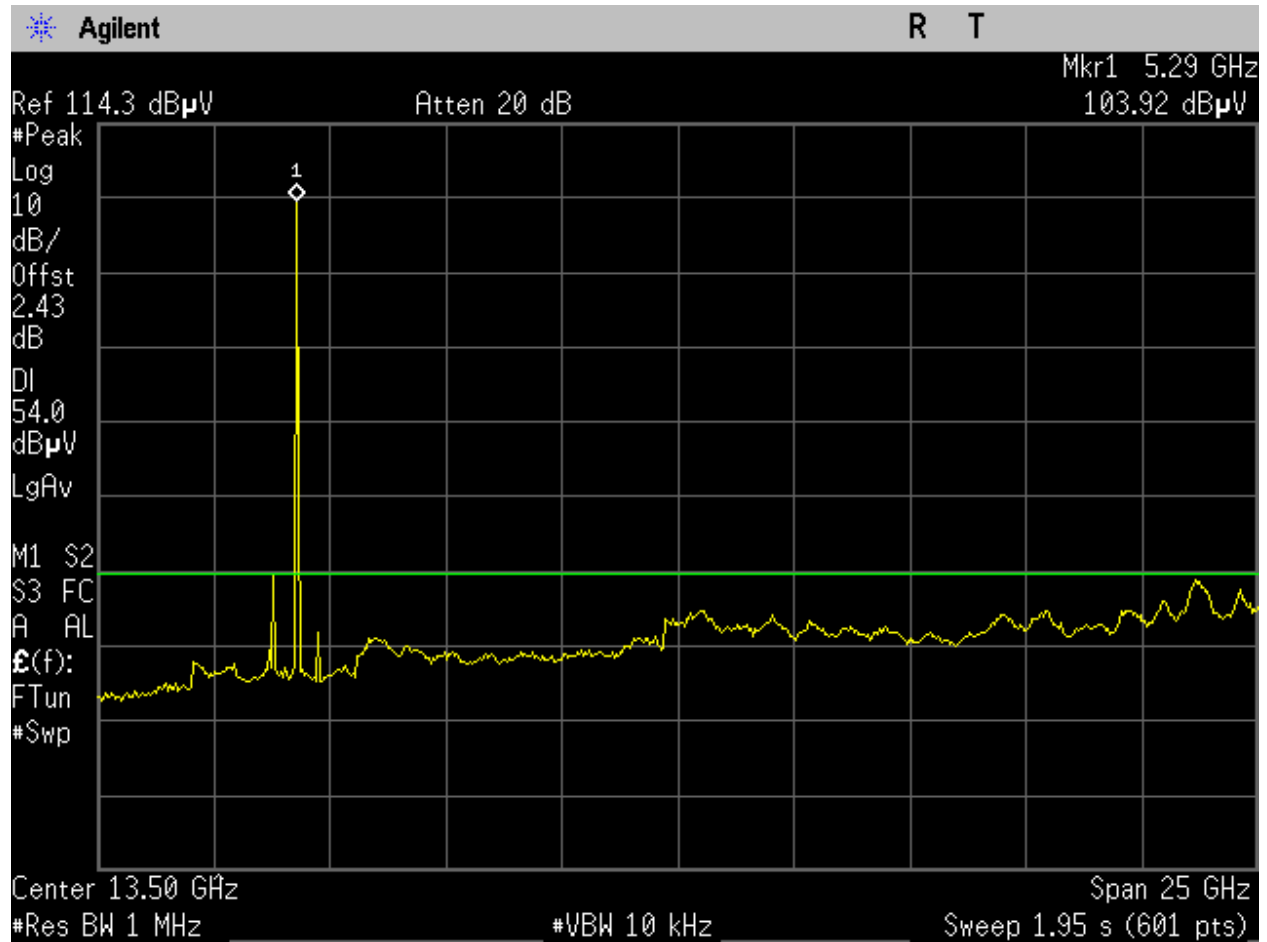


Figure 568: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_15.209_1-26GHz avg_Port 2.

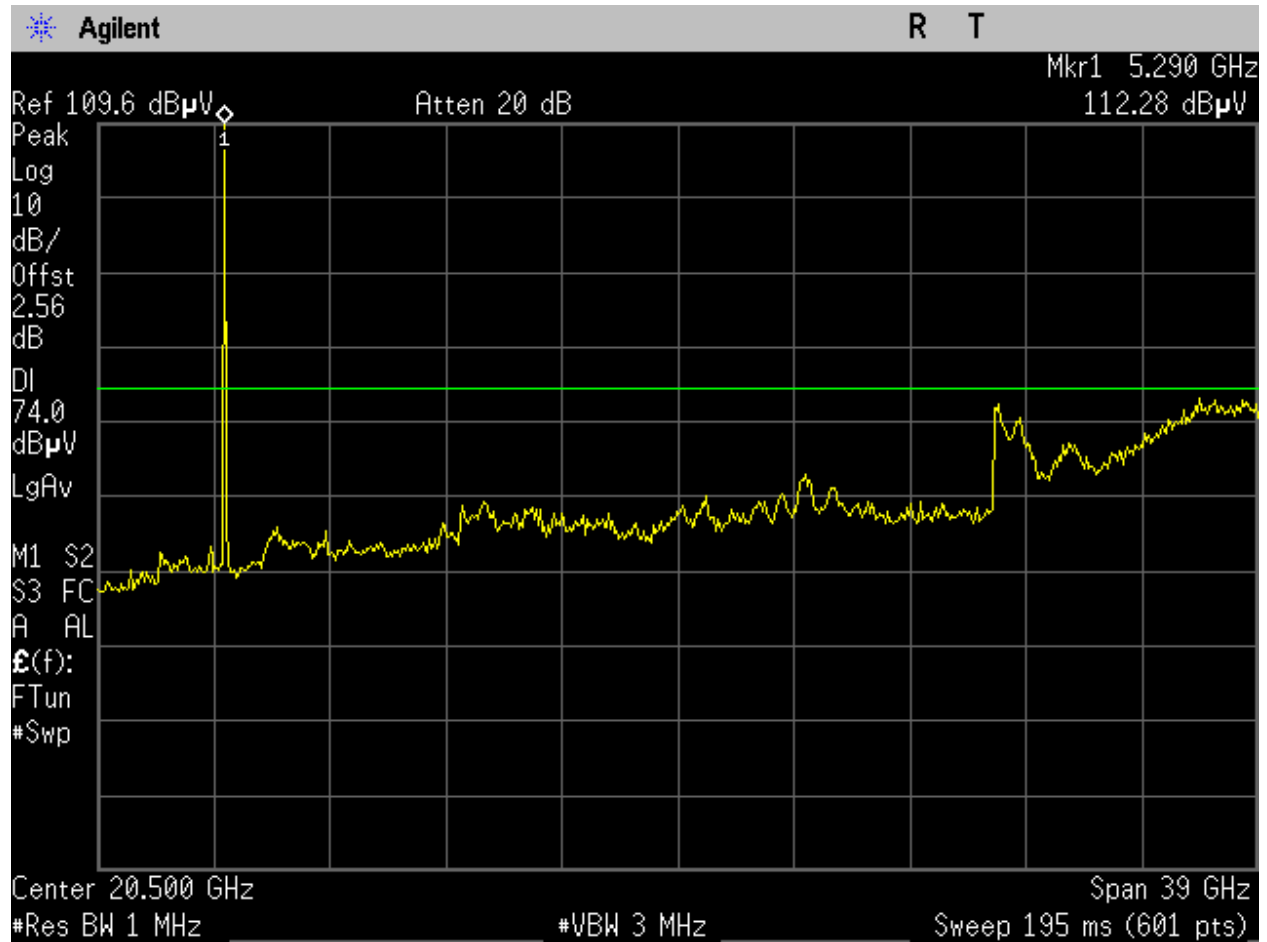


Figure 569: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_15.209_1-40GHz_Peak_Port 1.

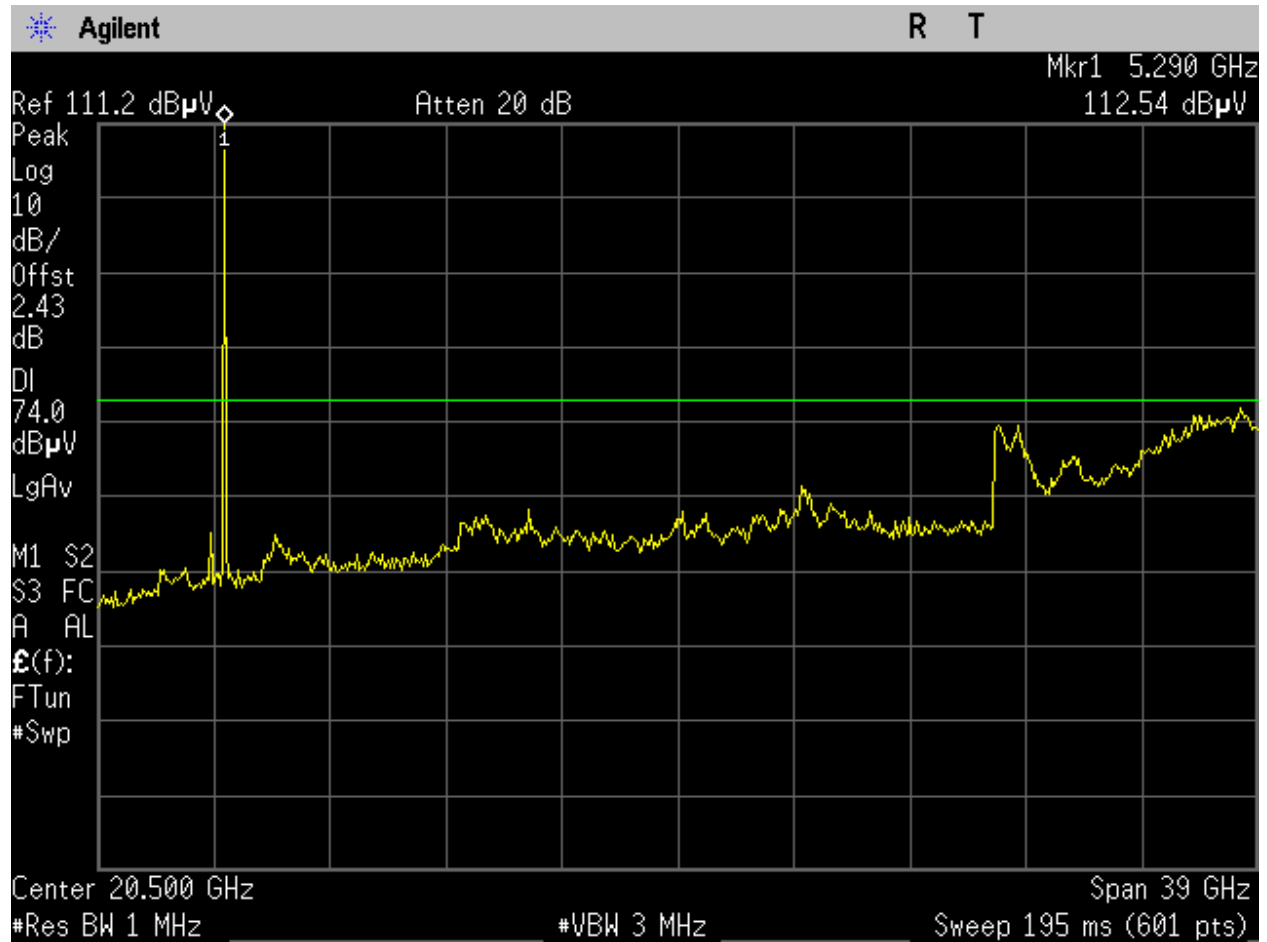


Figure 570: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_15.209_1-40GHz_Peak_Port 2.

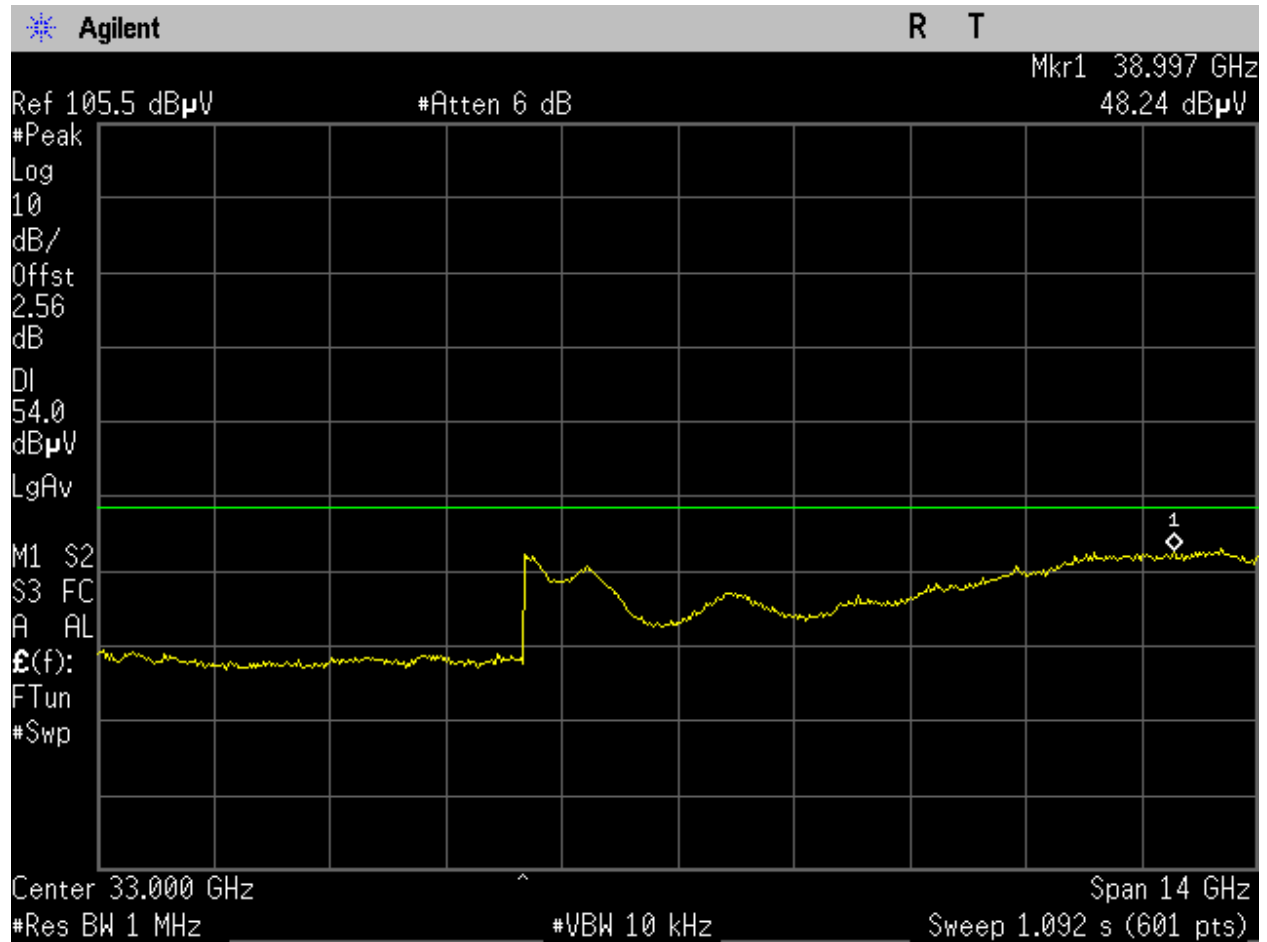


Figure 571: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 1.

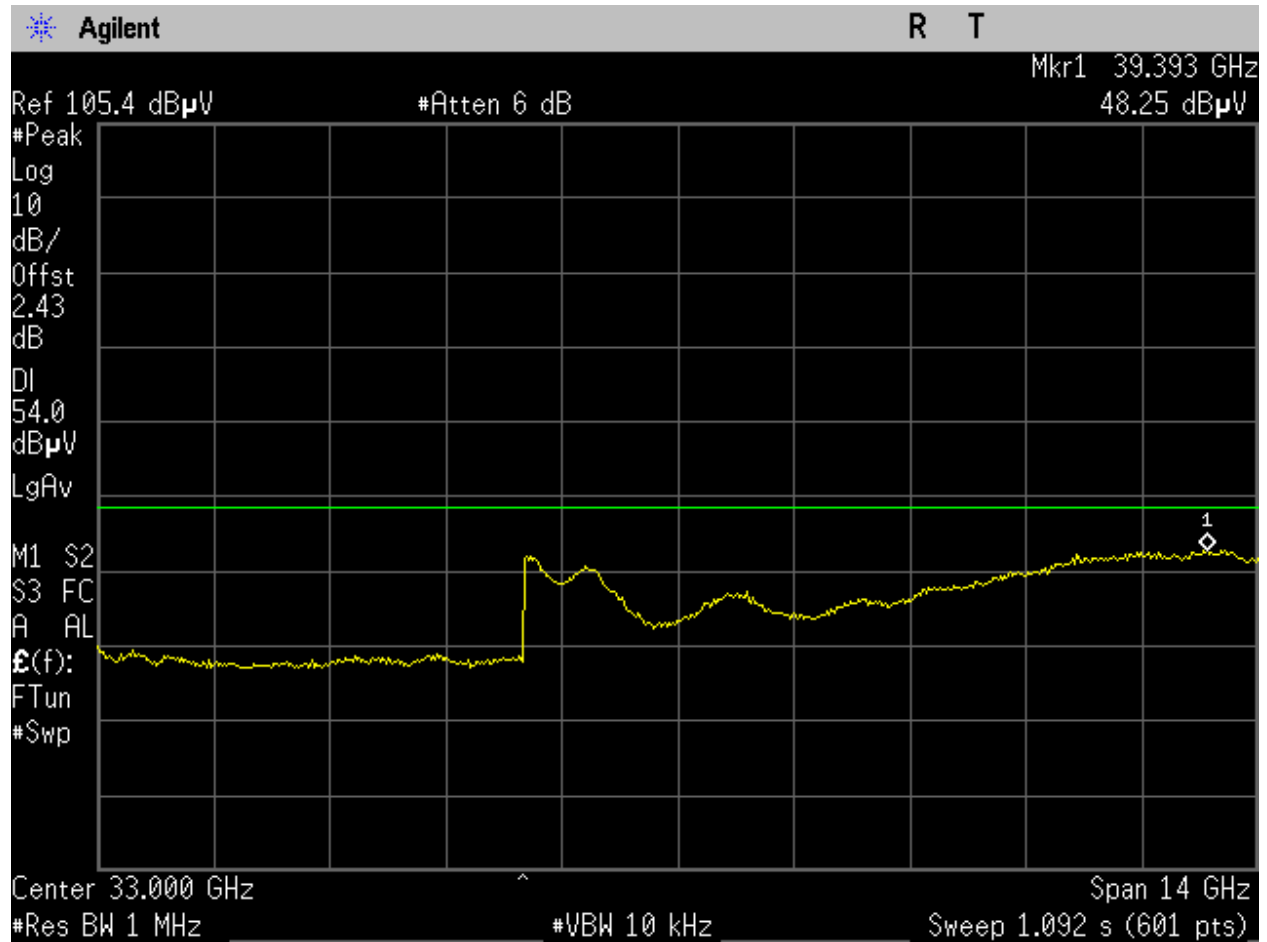


Figure 572: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 2.

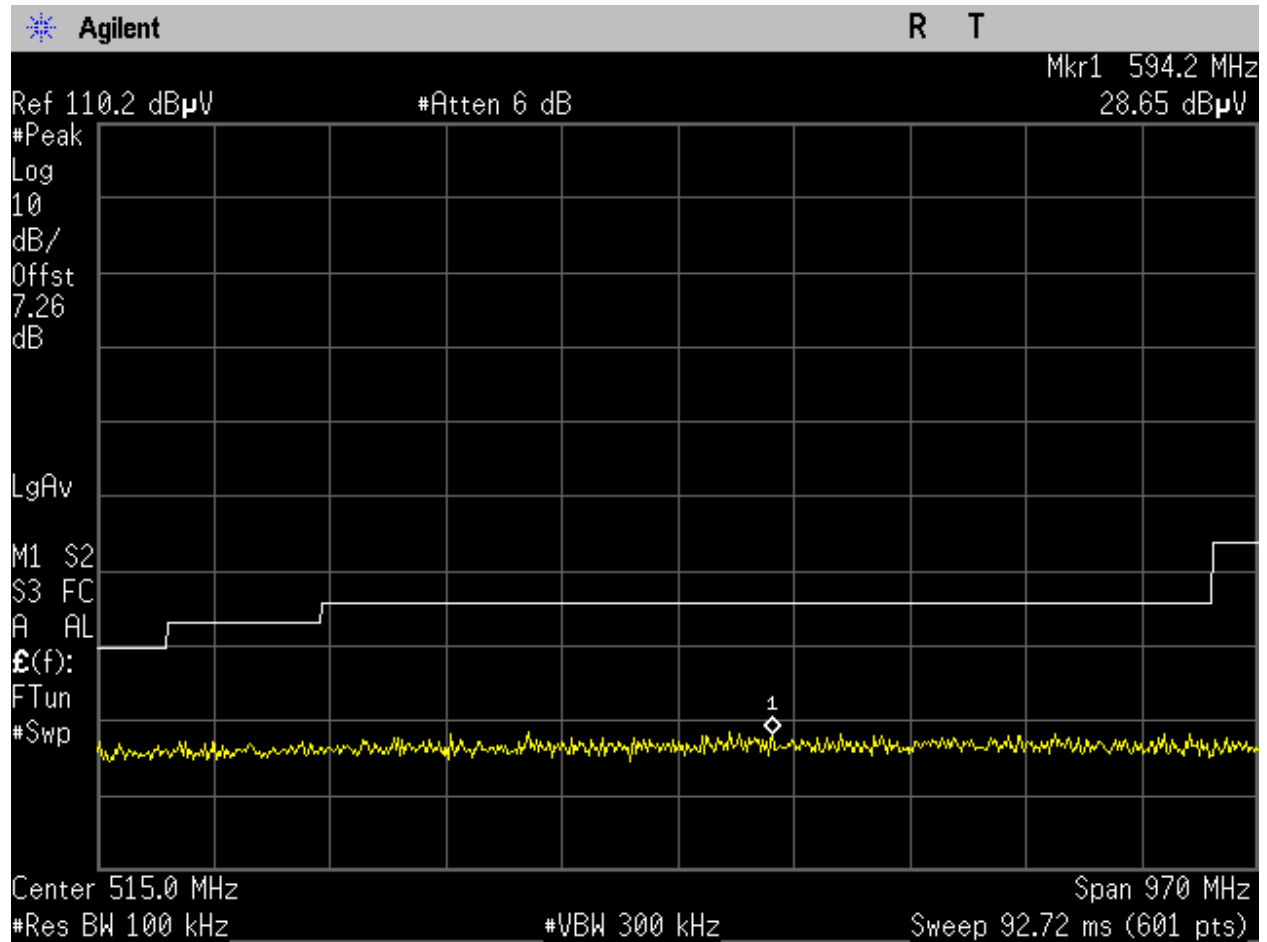


Figure 573: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 1.

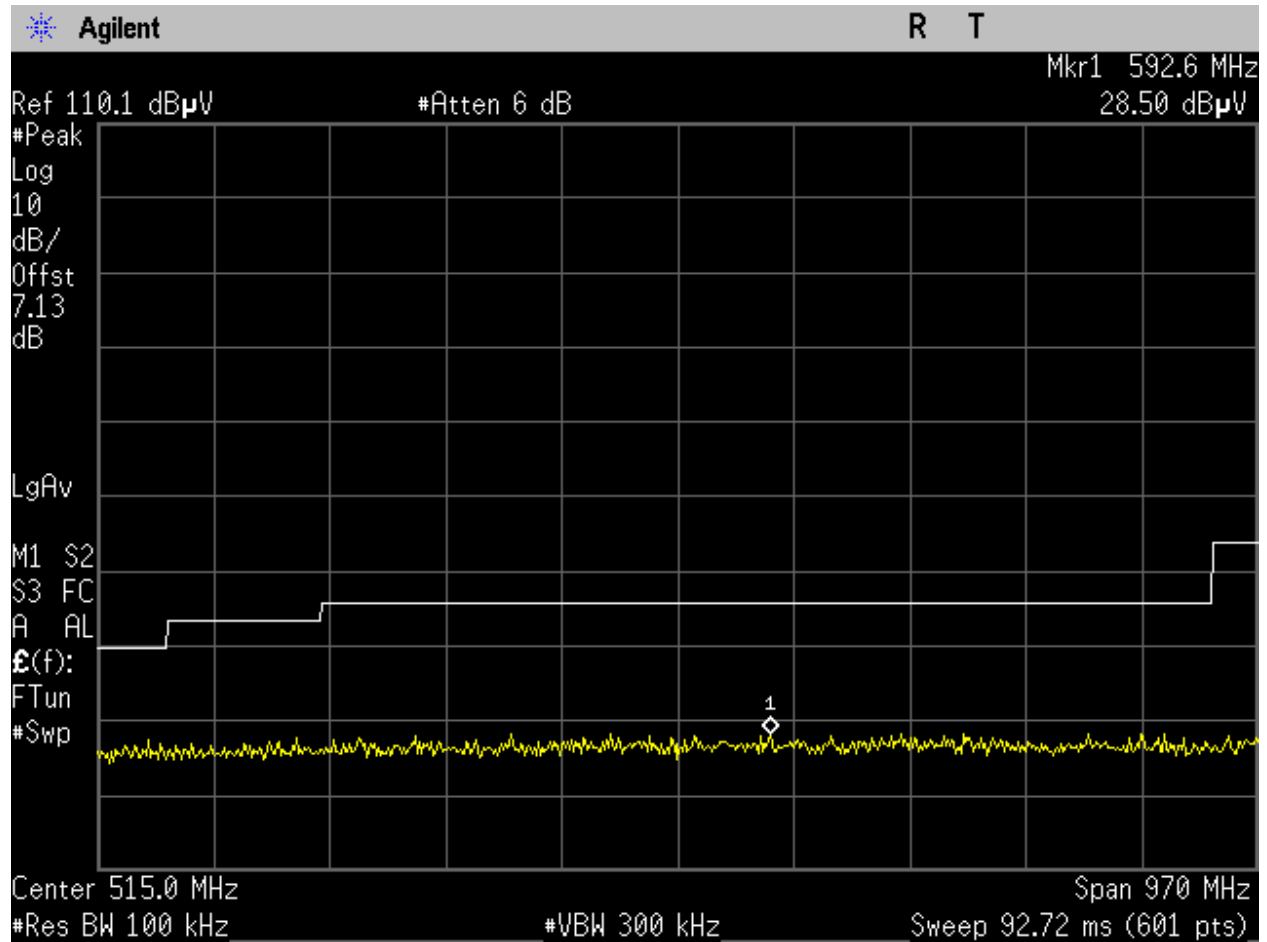


Figure 574: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 2.

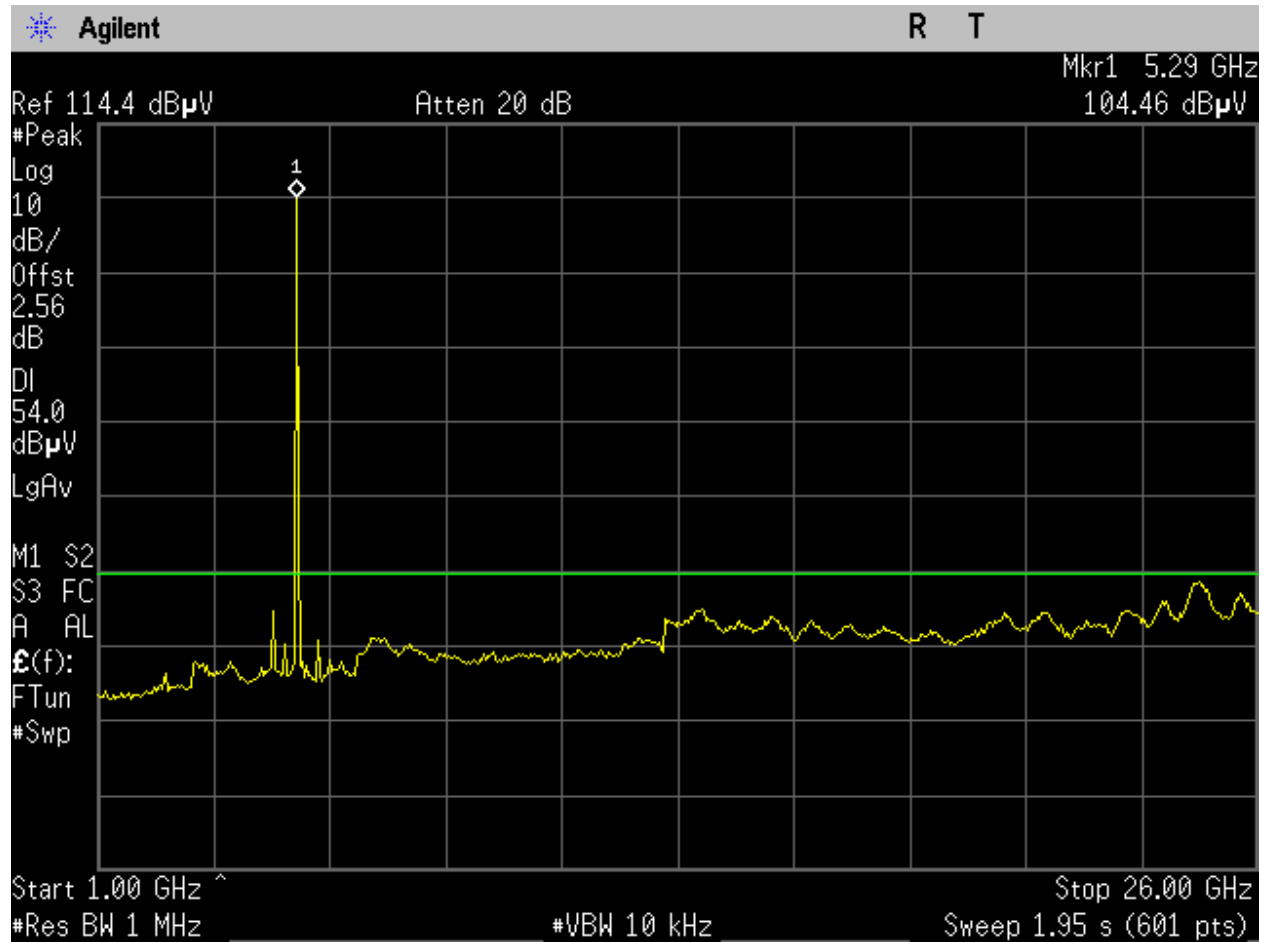


Figure 575: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

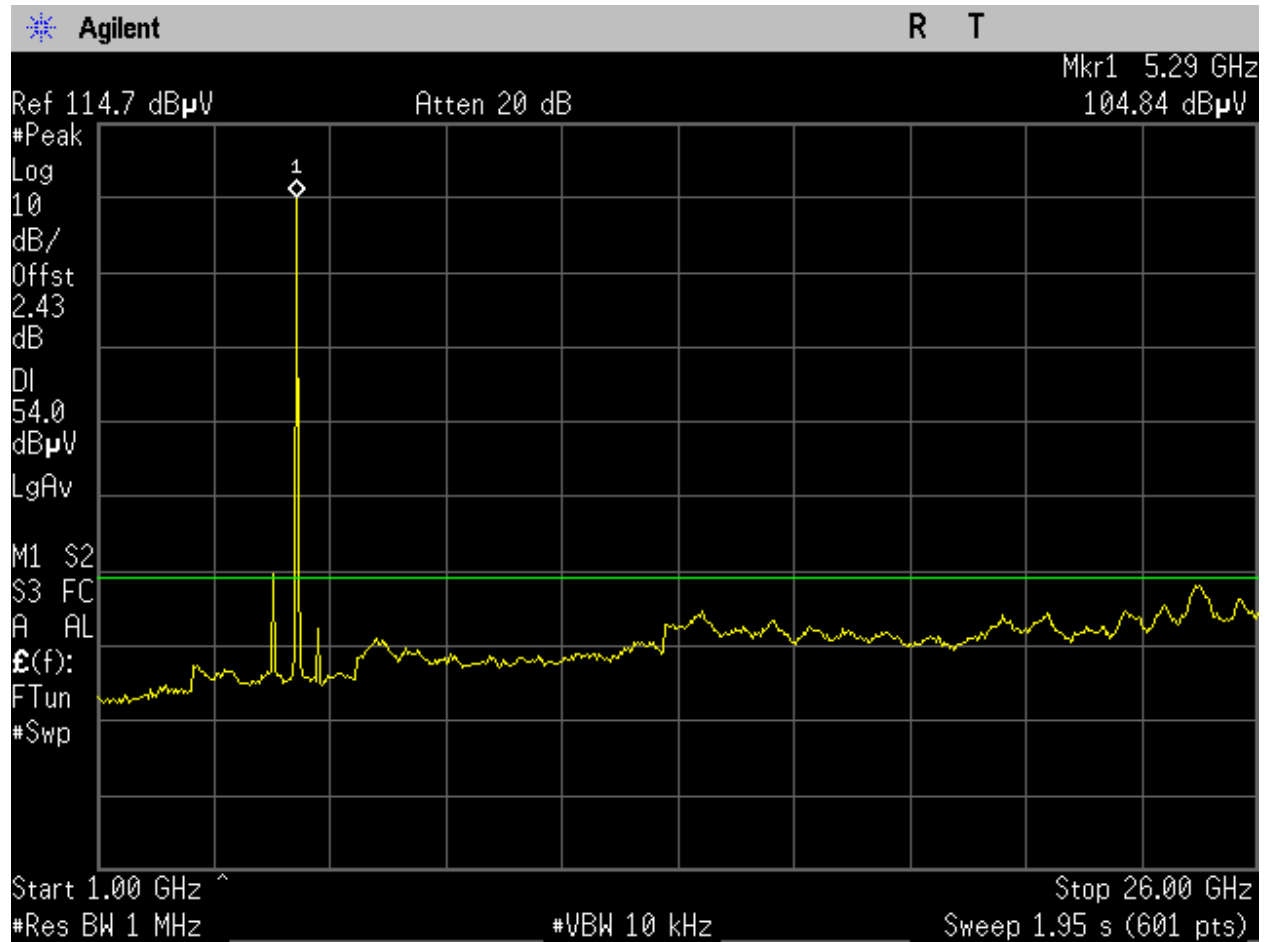


Figure 576: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

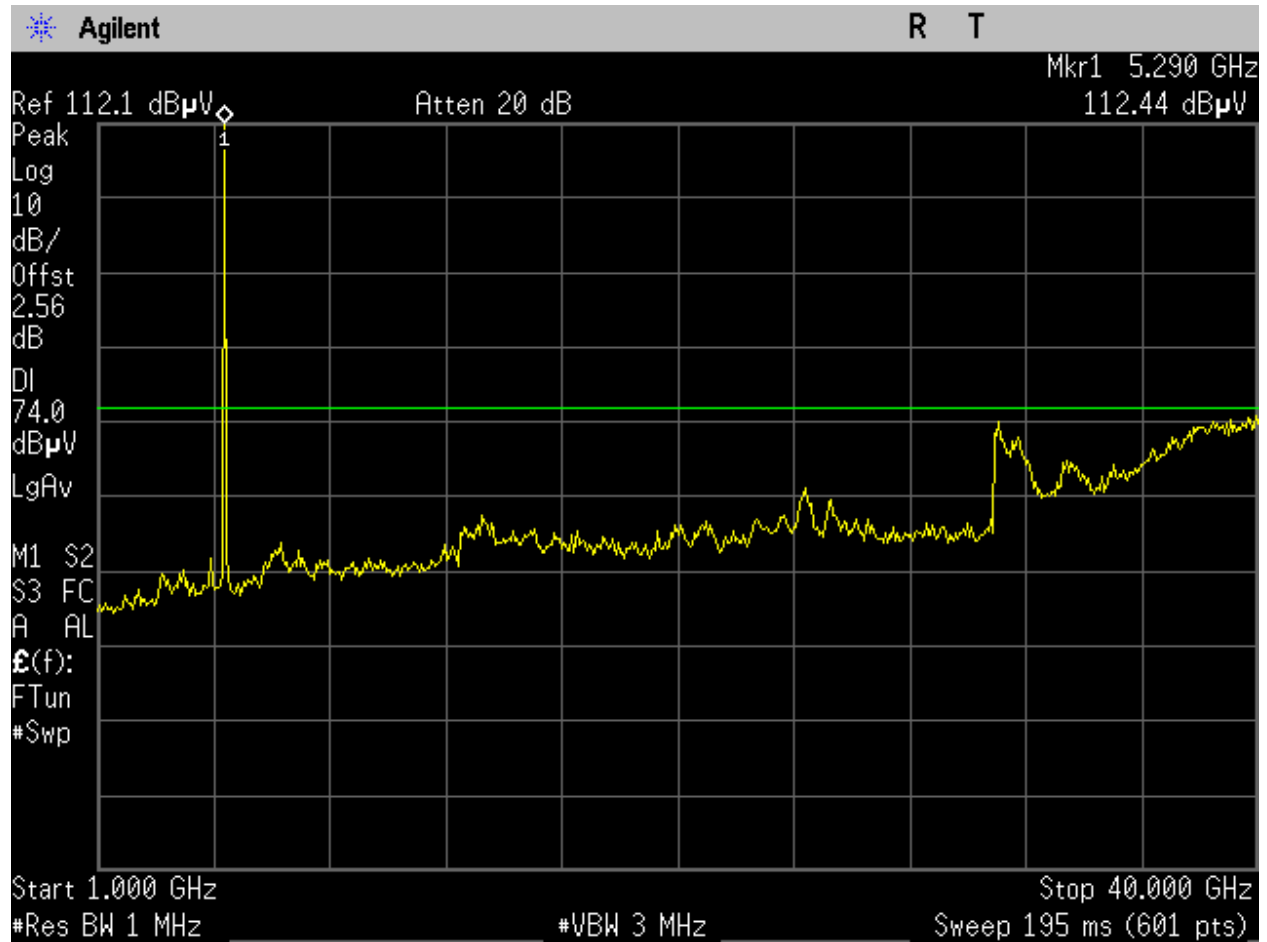


Figure 577: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 1.

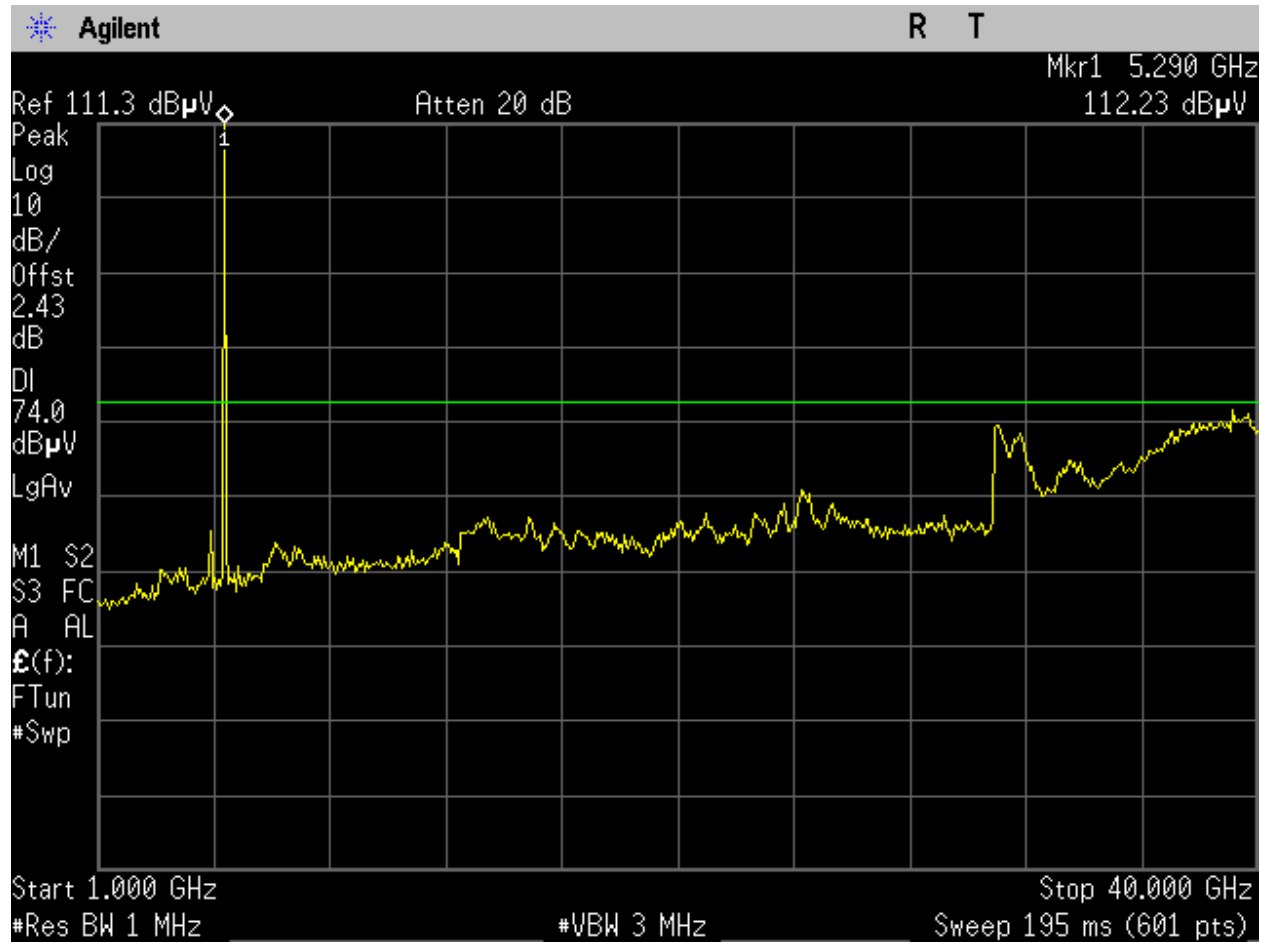


Figure 578: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 2.

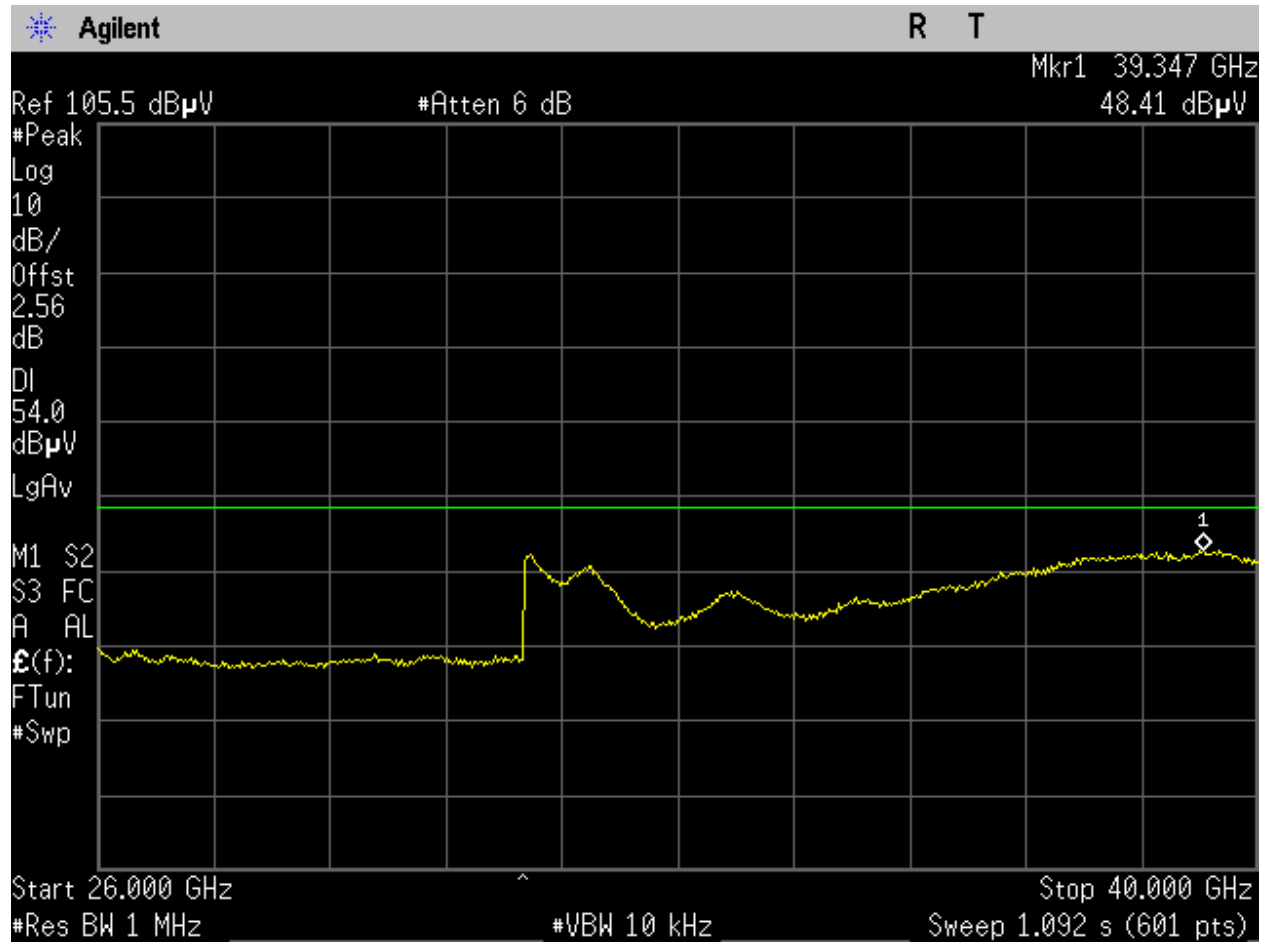


Figure 579: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

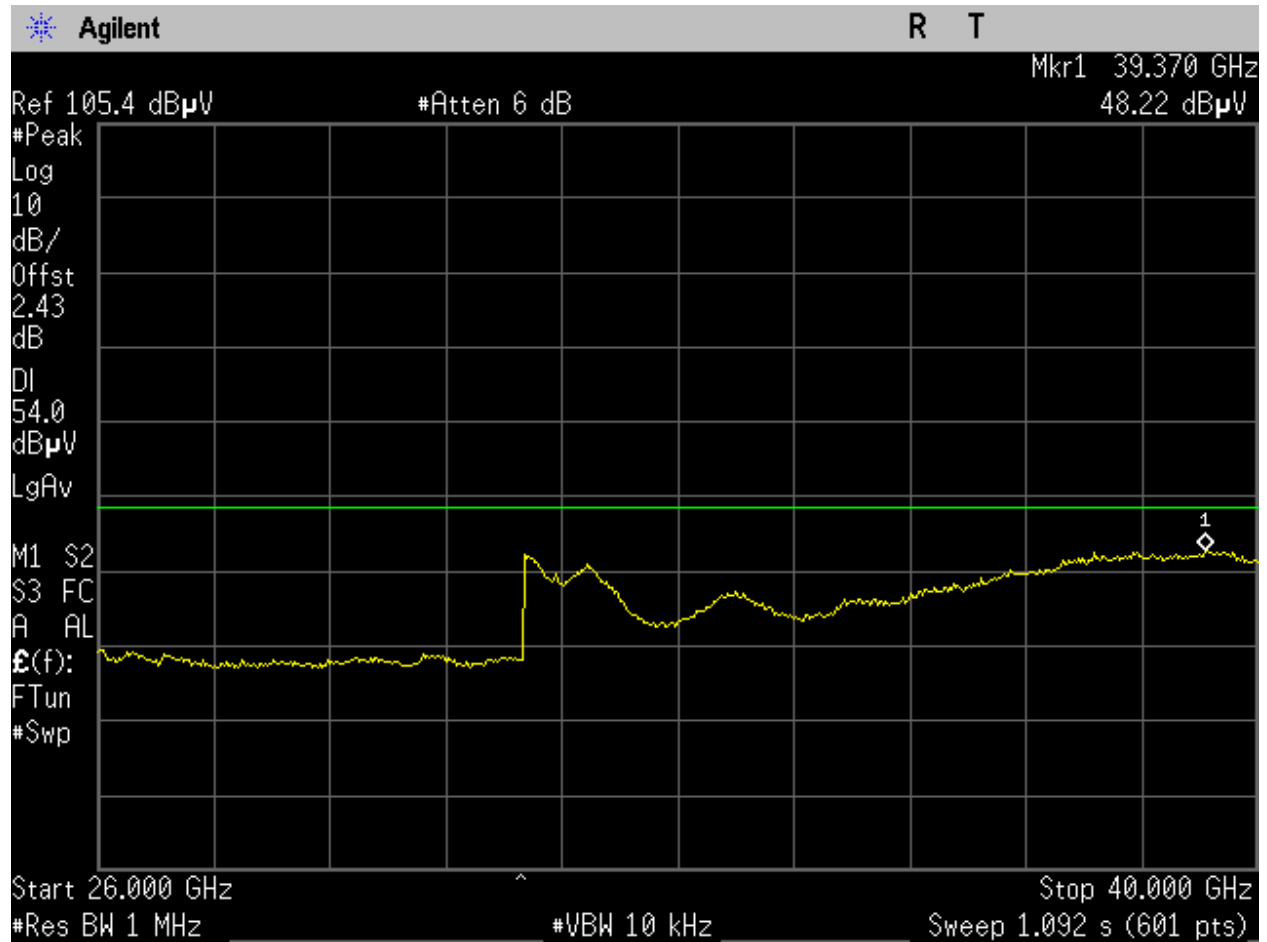


Figure 580: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

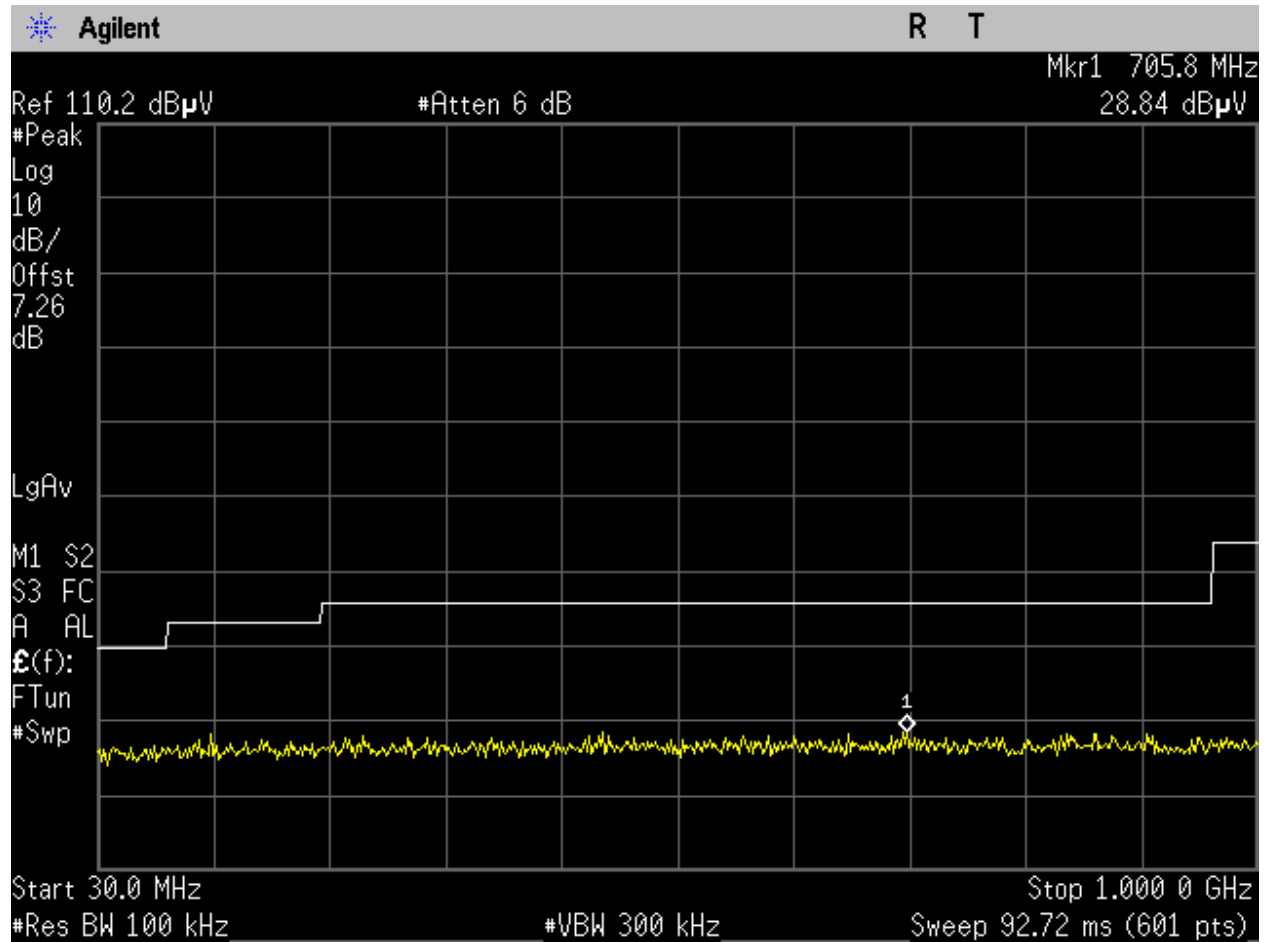


Figure 581: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

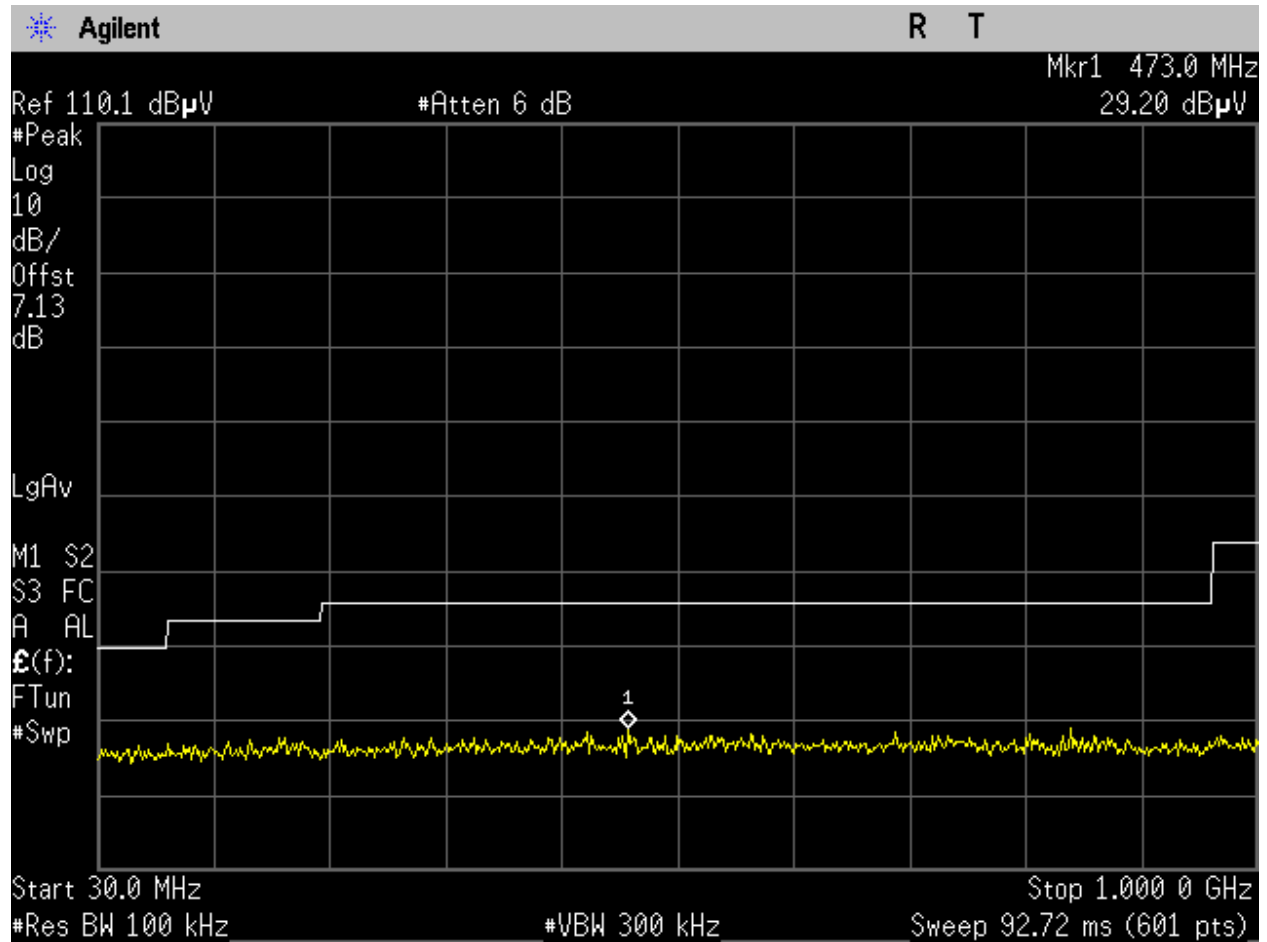


Figure 582: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

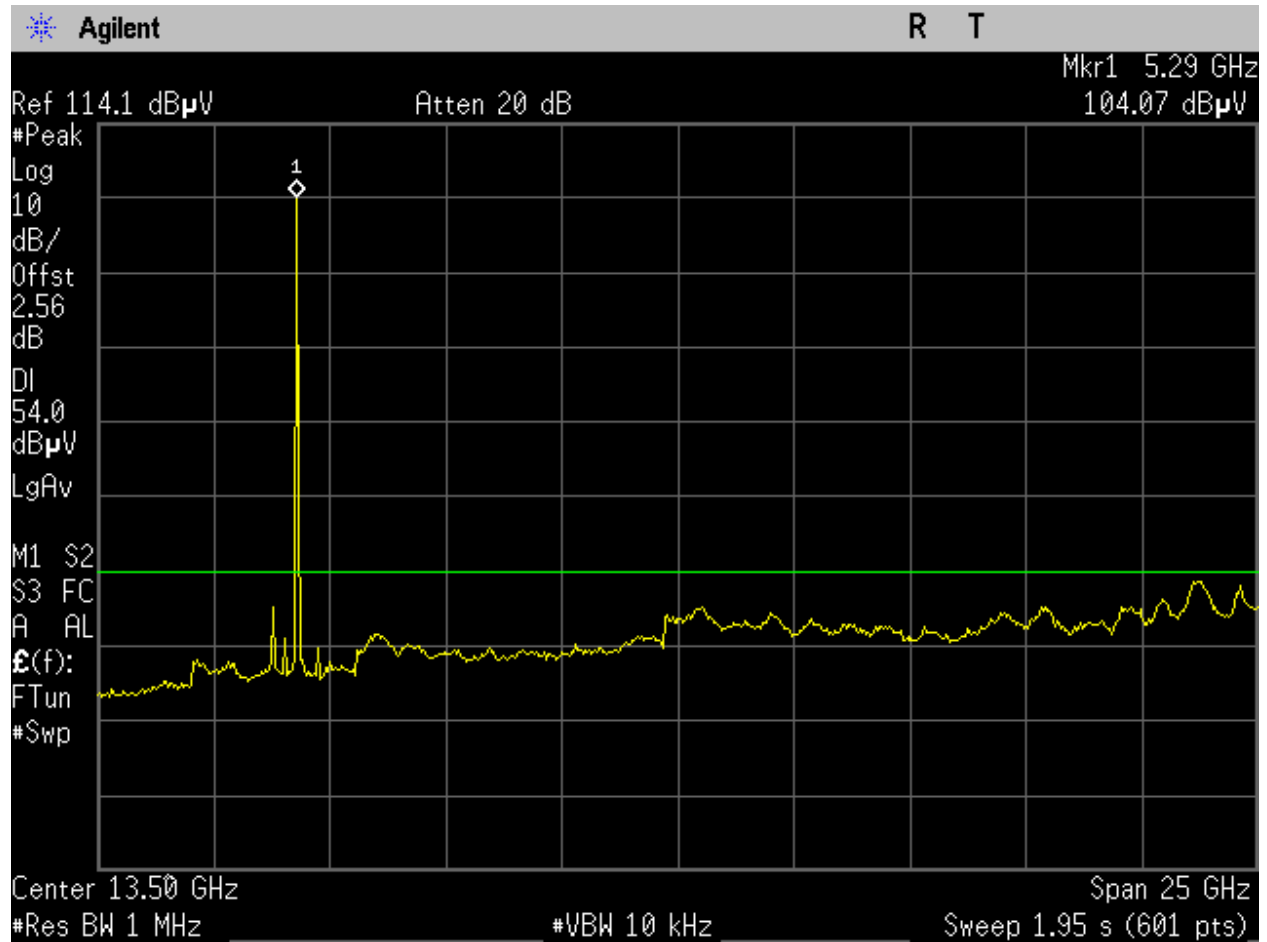


Figure 583: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_15.209_1-26GHz avg_Port 1.

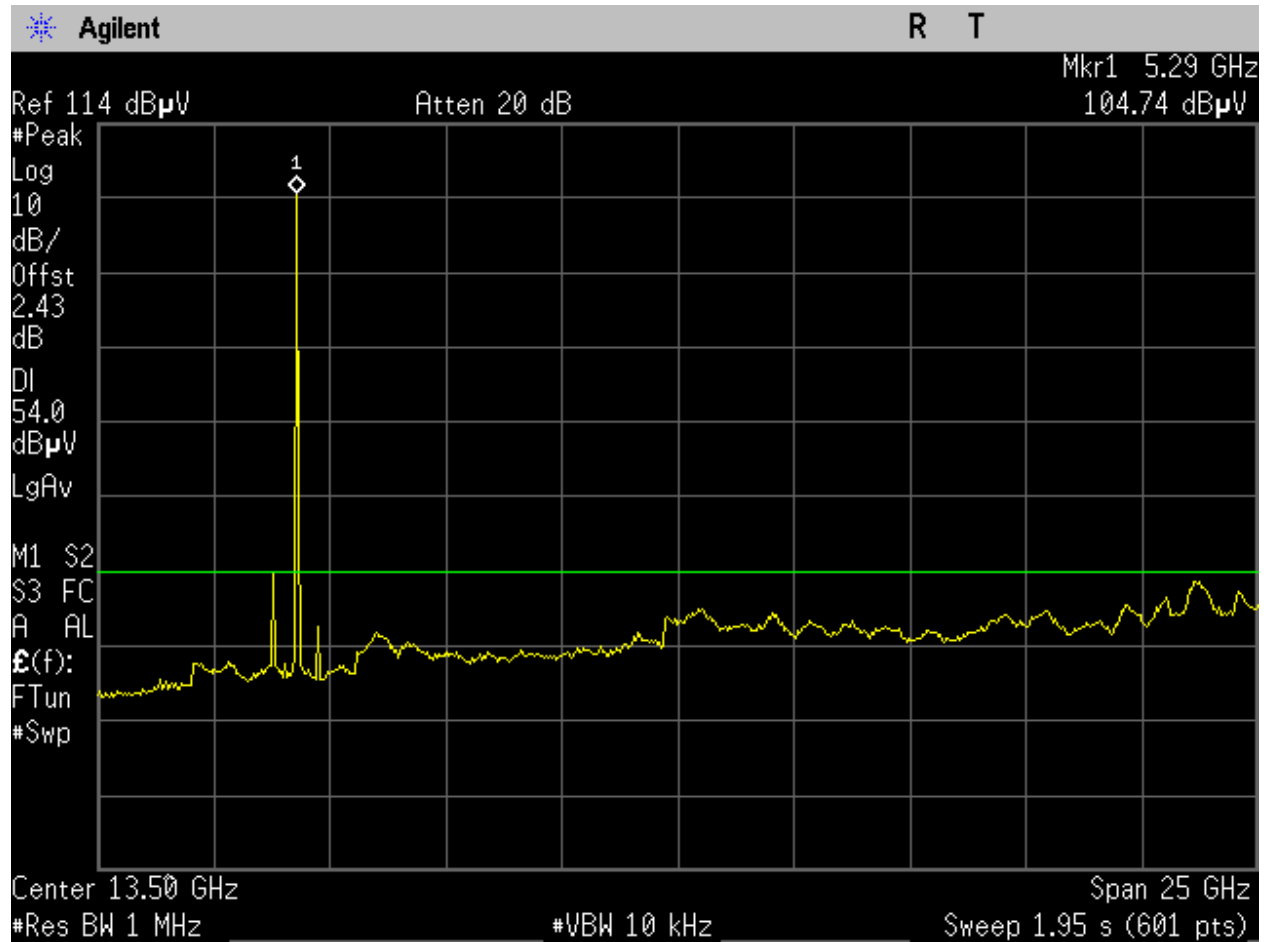


Figure 584: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_15.209_1-26GHz avg_Port 2.

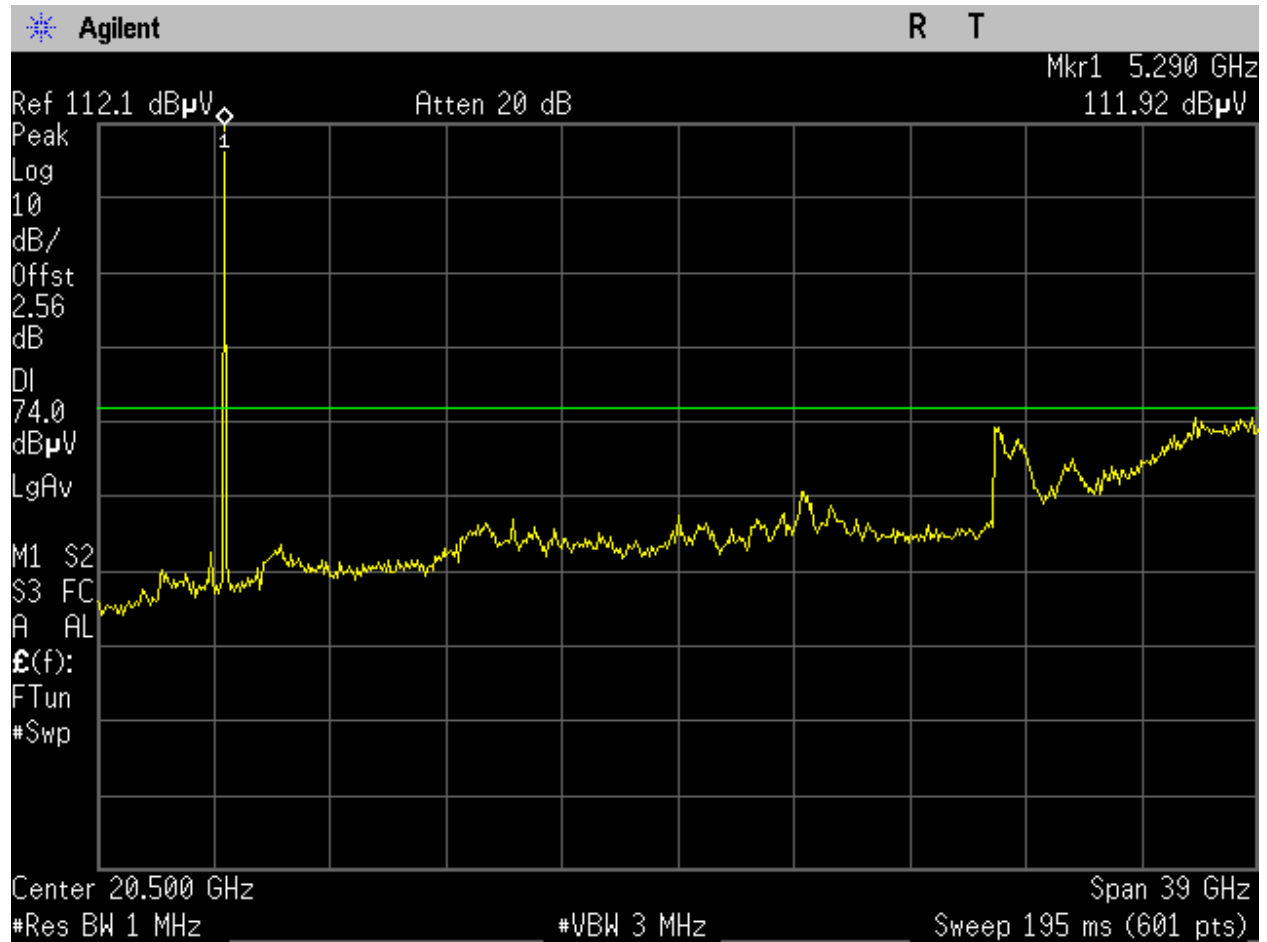


Figure 585: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 1.

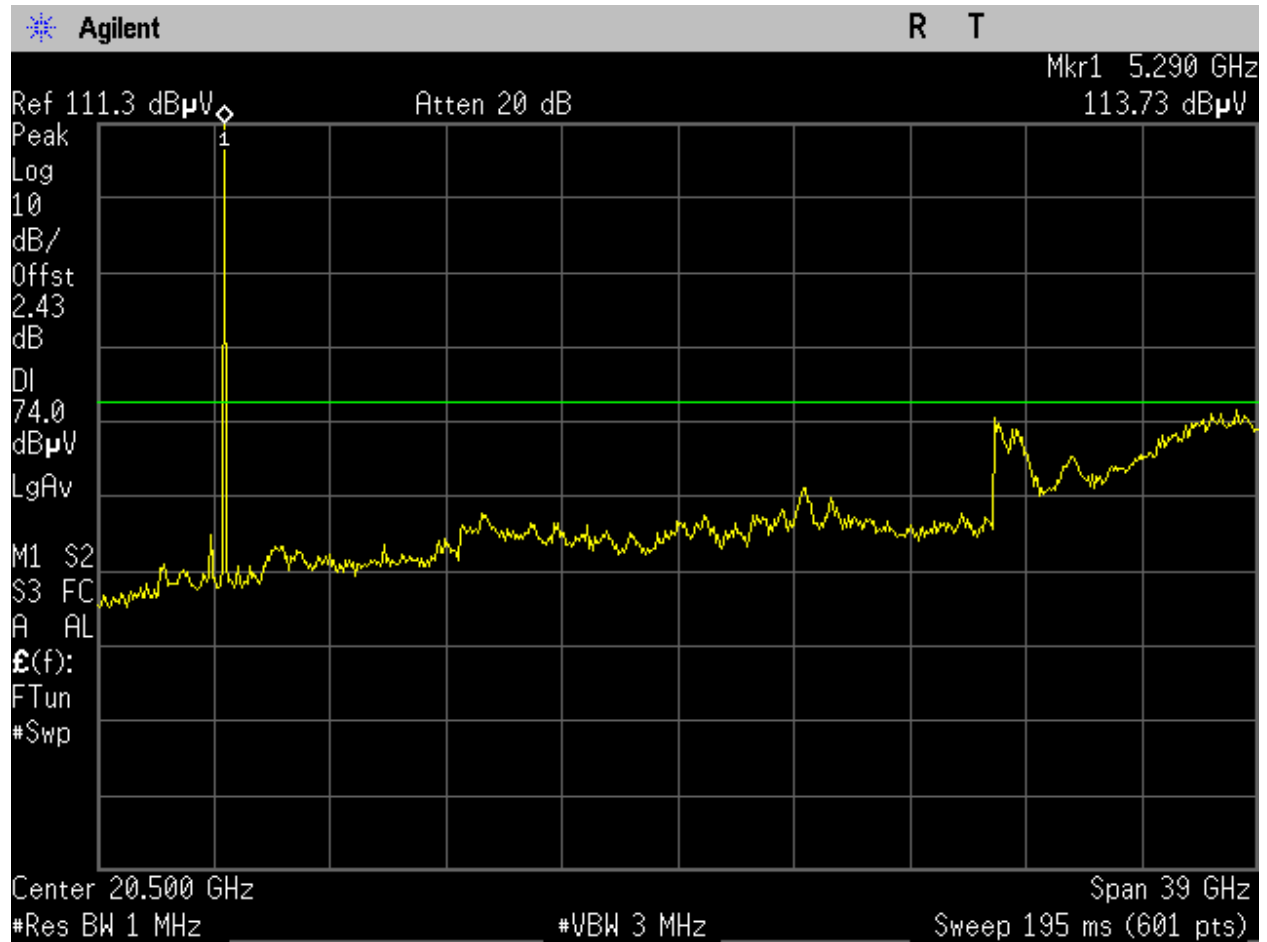


Figure 586: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 2.

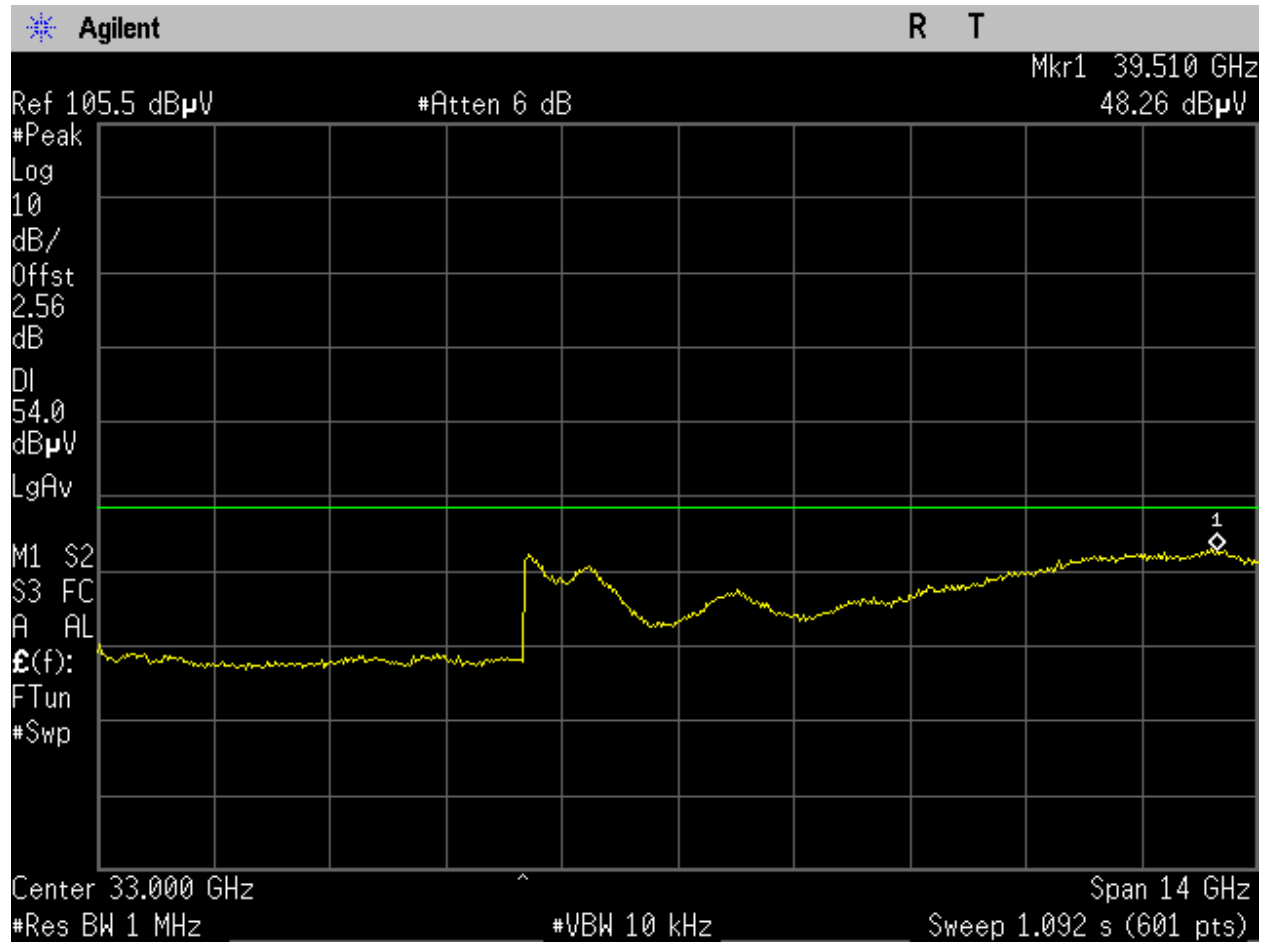


Figure 587: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 1.

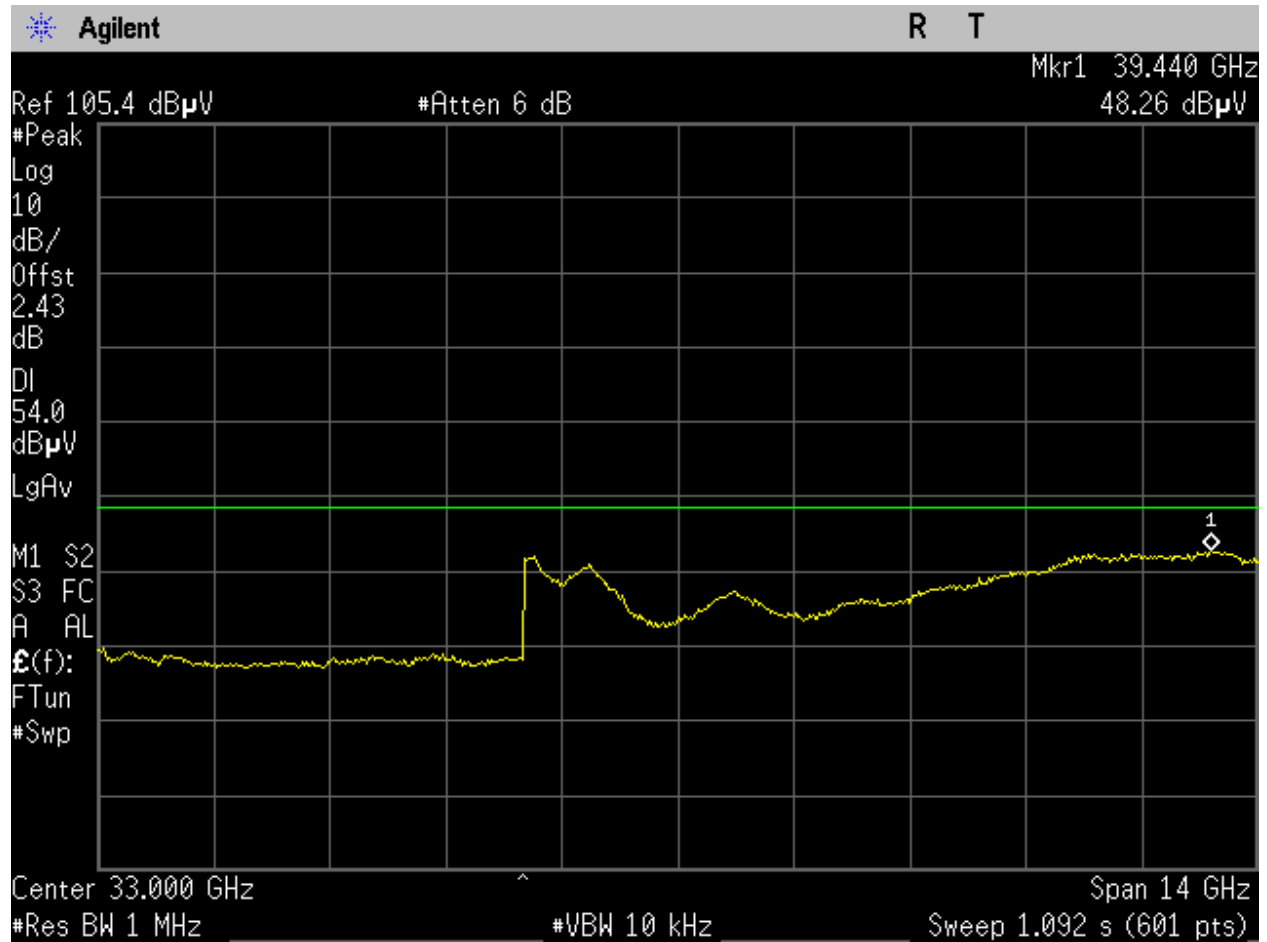


Figure 588: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 2.

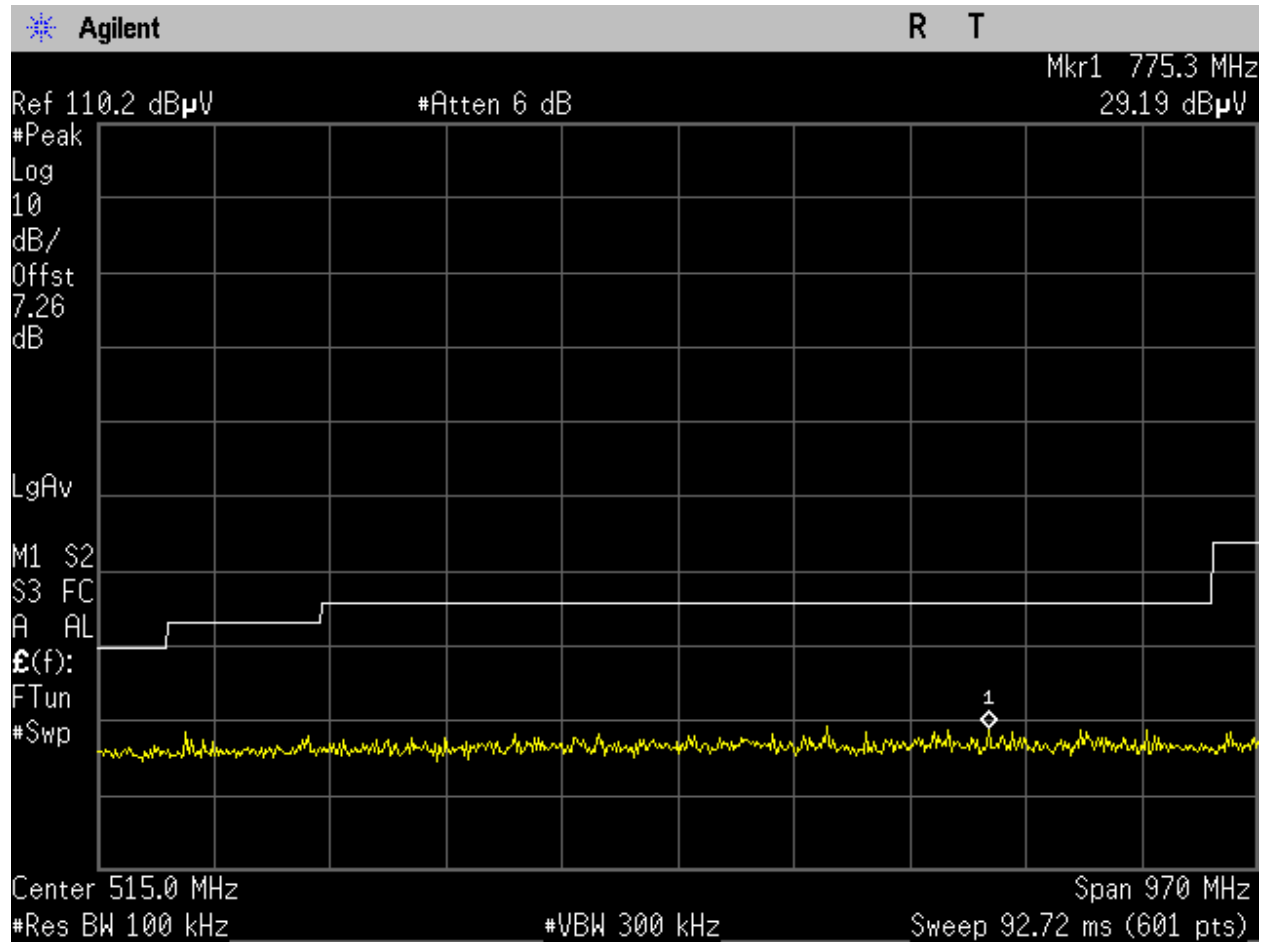


Figure 589: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 1.

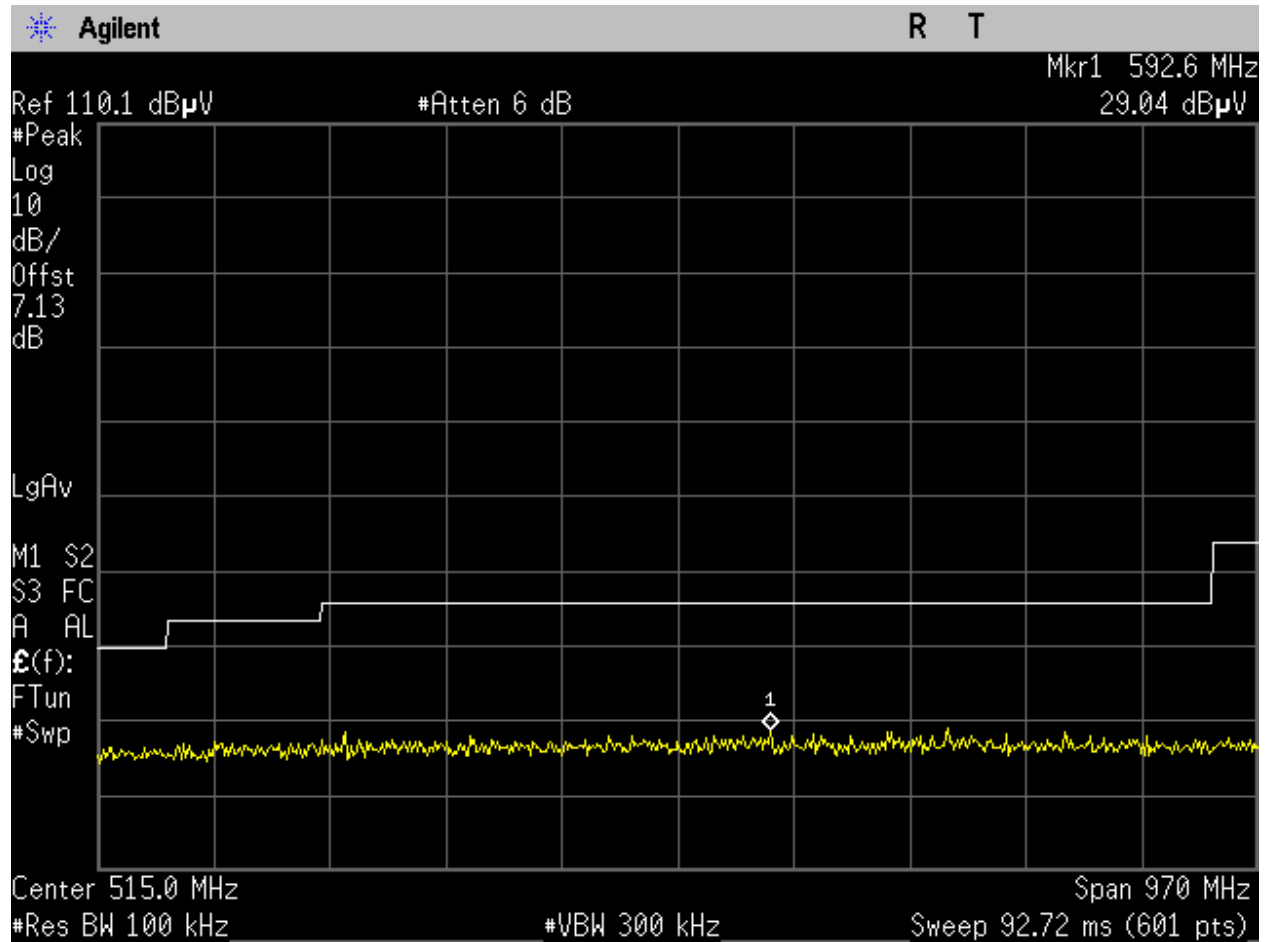


Figure 590: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 2.

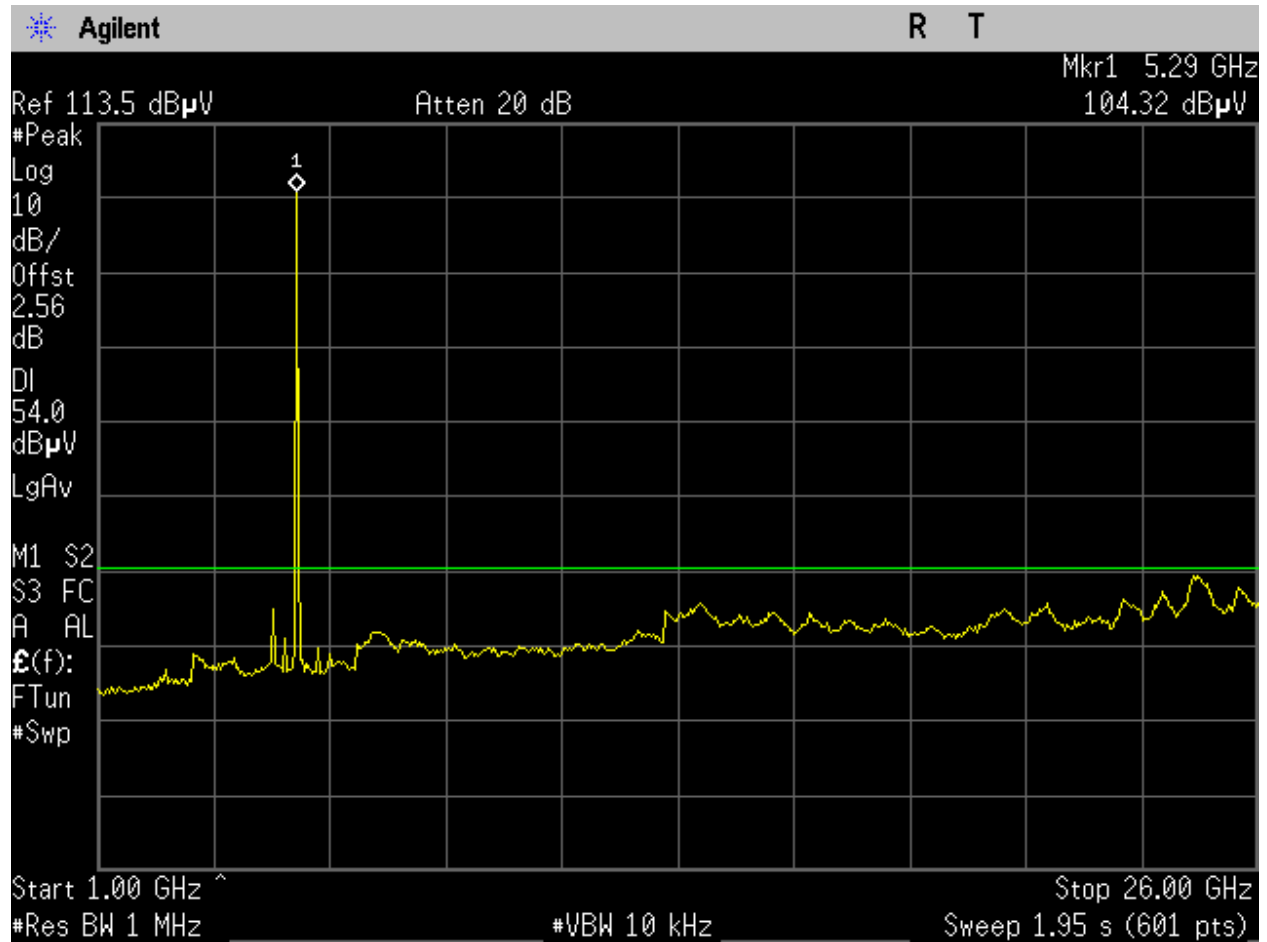


Figure 591: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_15.209_1-26GHz avg_Port 1.

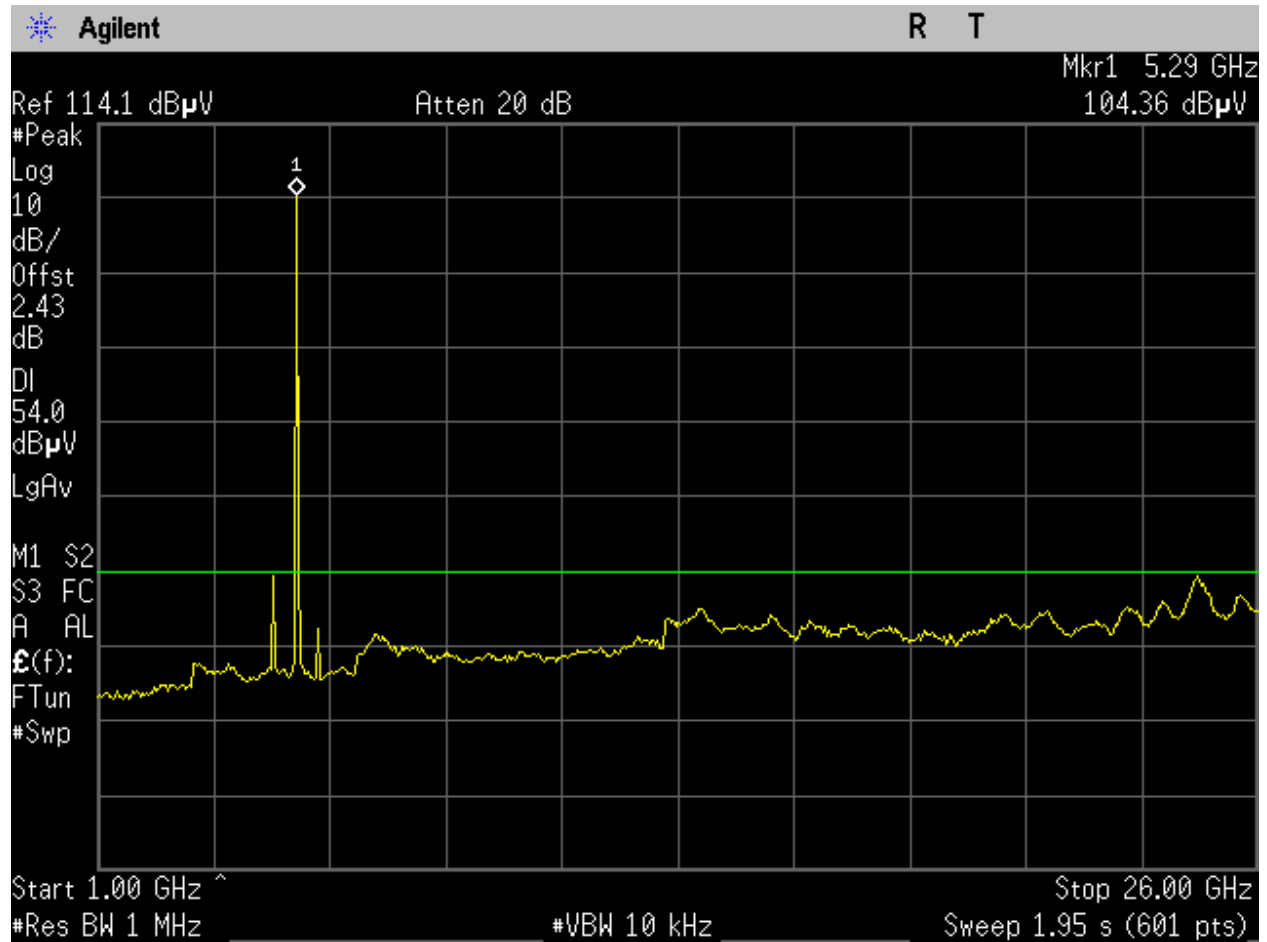


Figure 592: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_15.209_1-26GHz avg_Port 2.

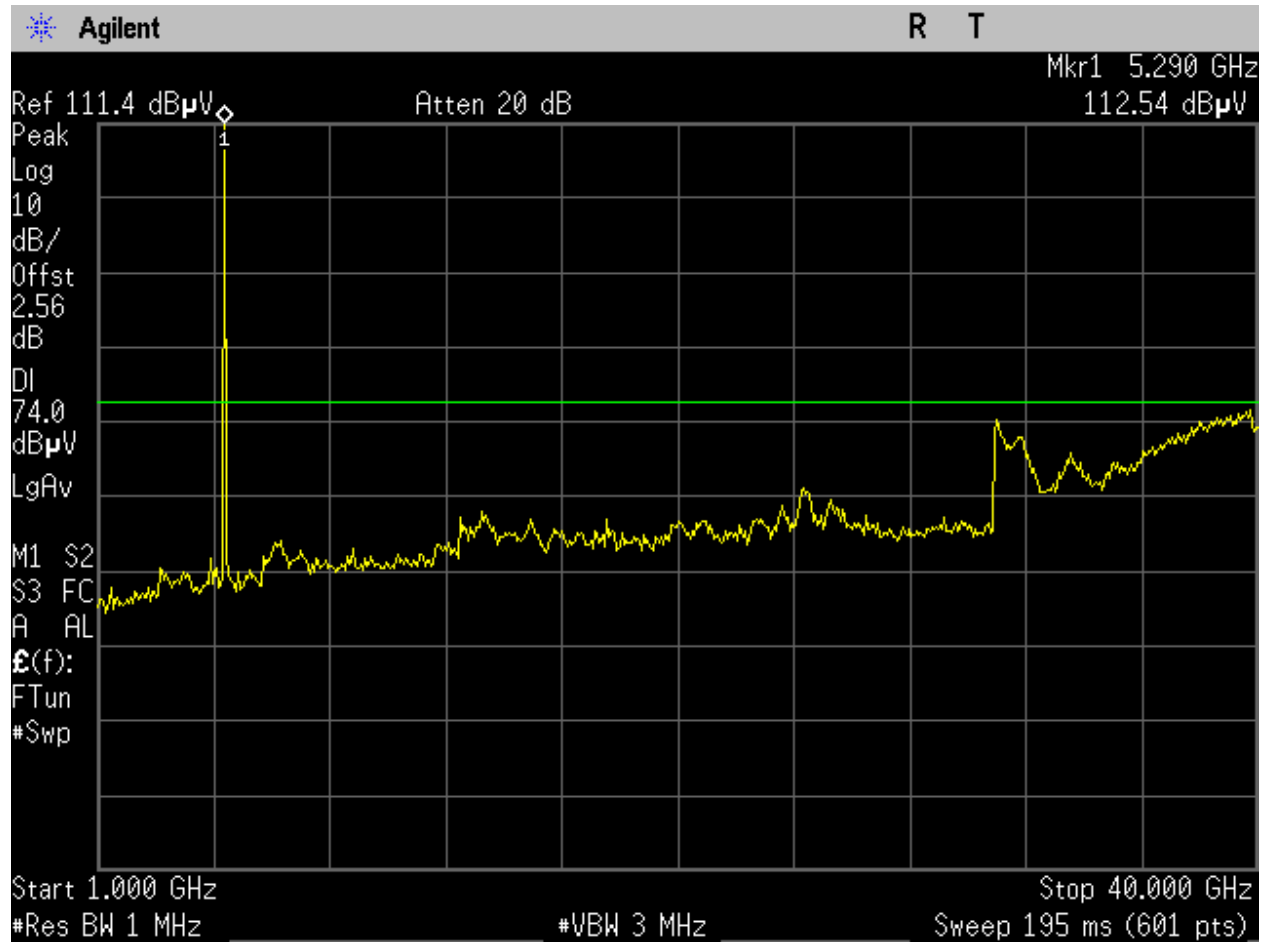


Figure 593: U-NII-2A_5300MHz_high_Mid_Ch_60_20MHz BW_n-mode_15.209_1-40GHz_Peak_Port 1.

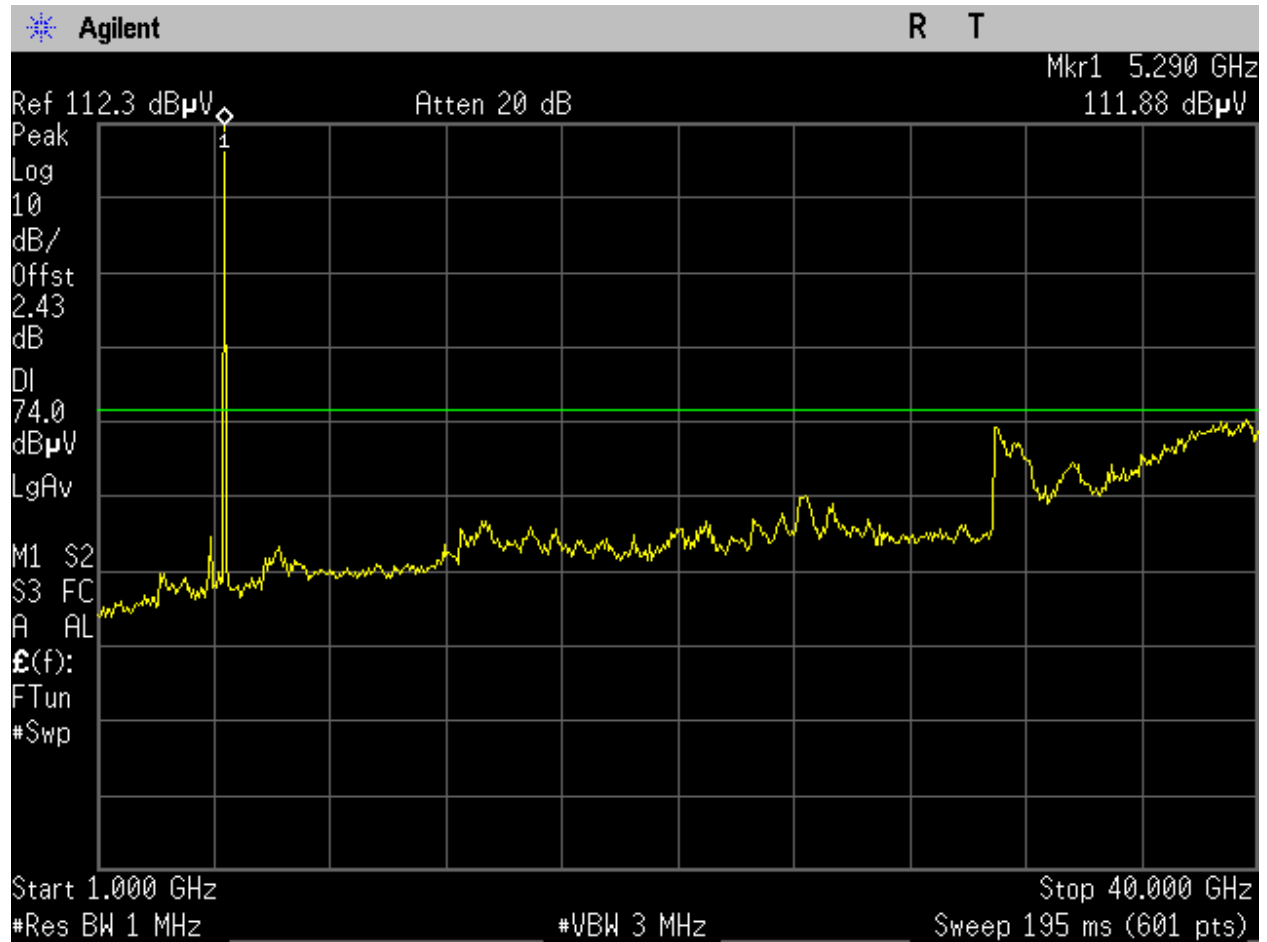


Figure 594: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_15.209_1-40GHz_Peak_Port 2.

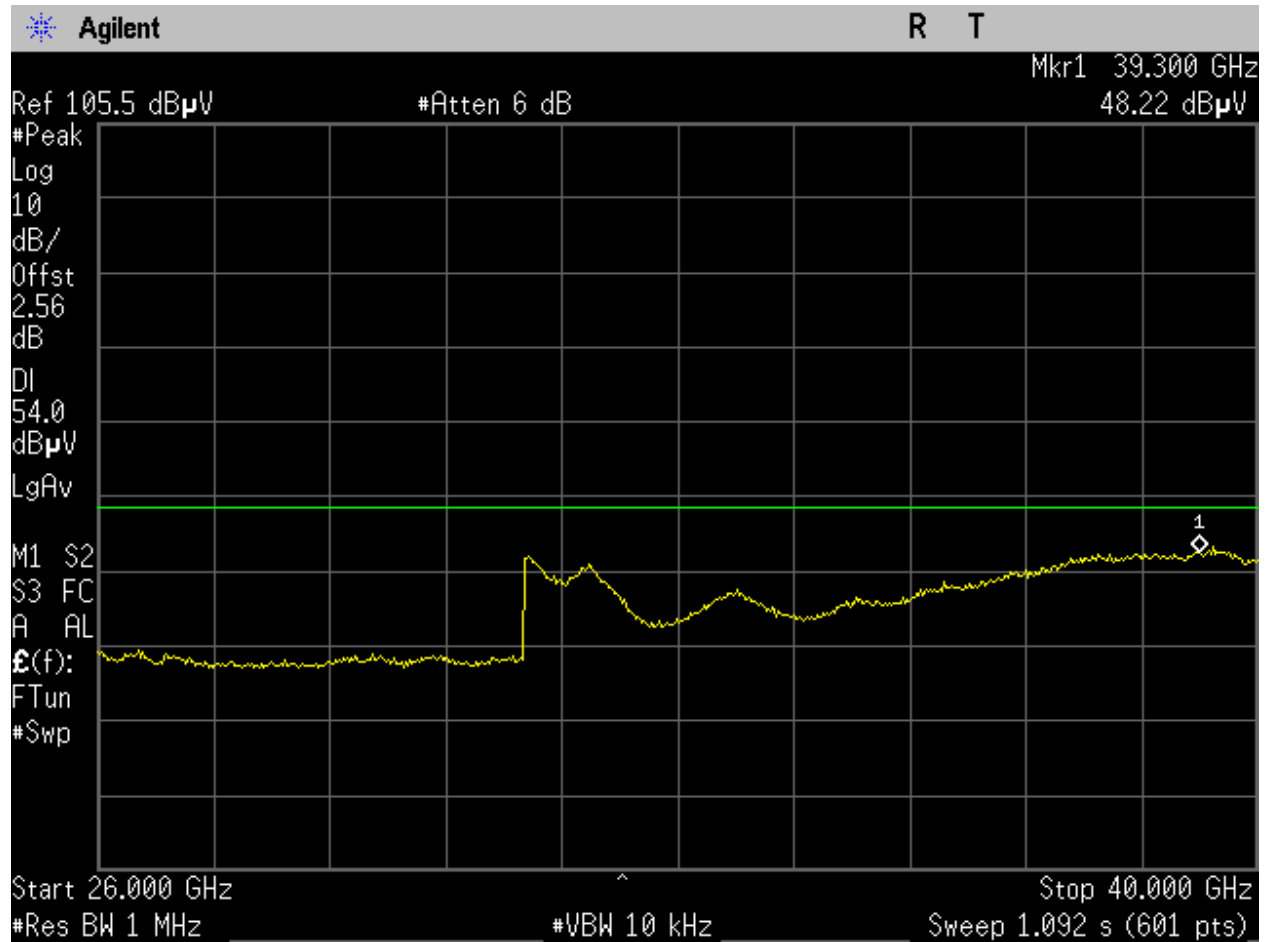


Figure 595: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_15.209_26-40GHz_Avg_Port 1.

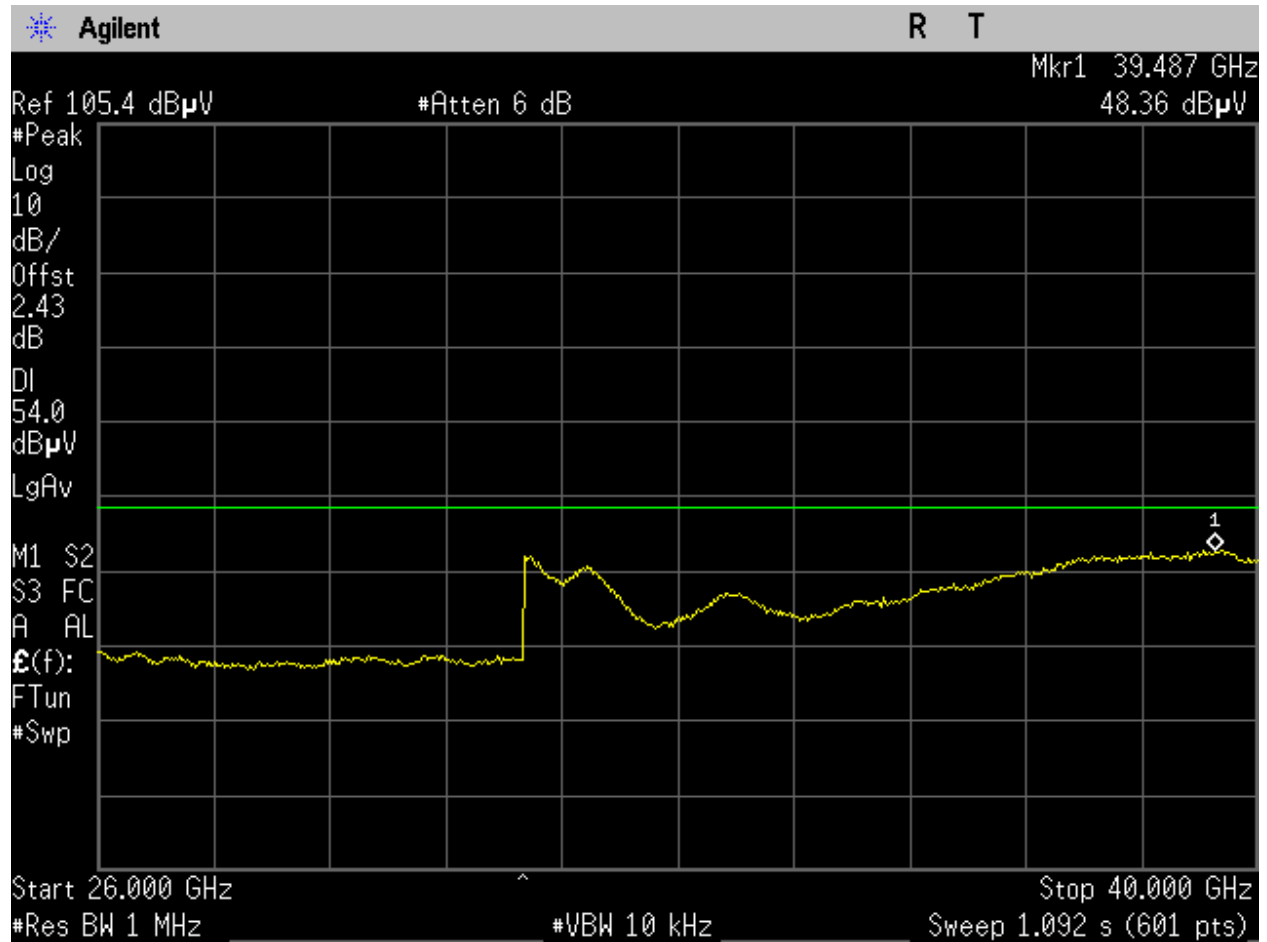


Figure 596: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_15.209_26-40GHz_Avg_Port 2.

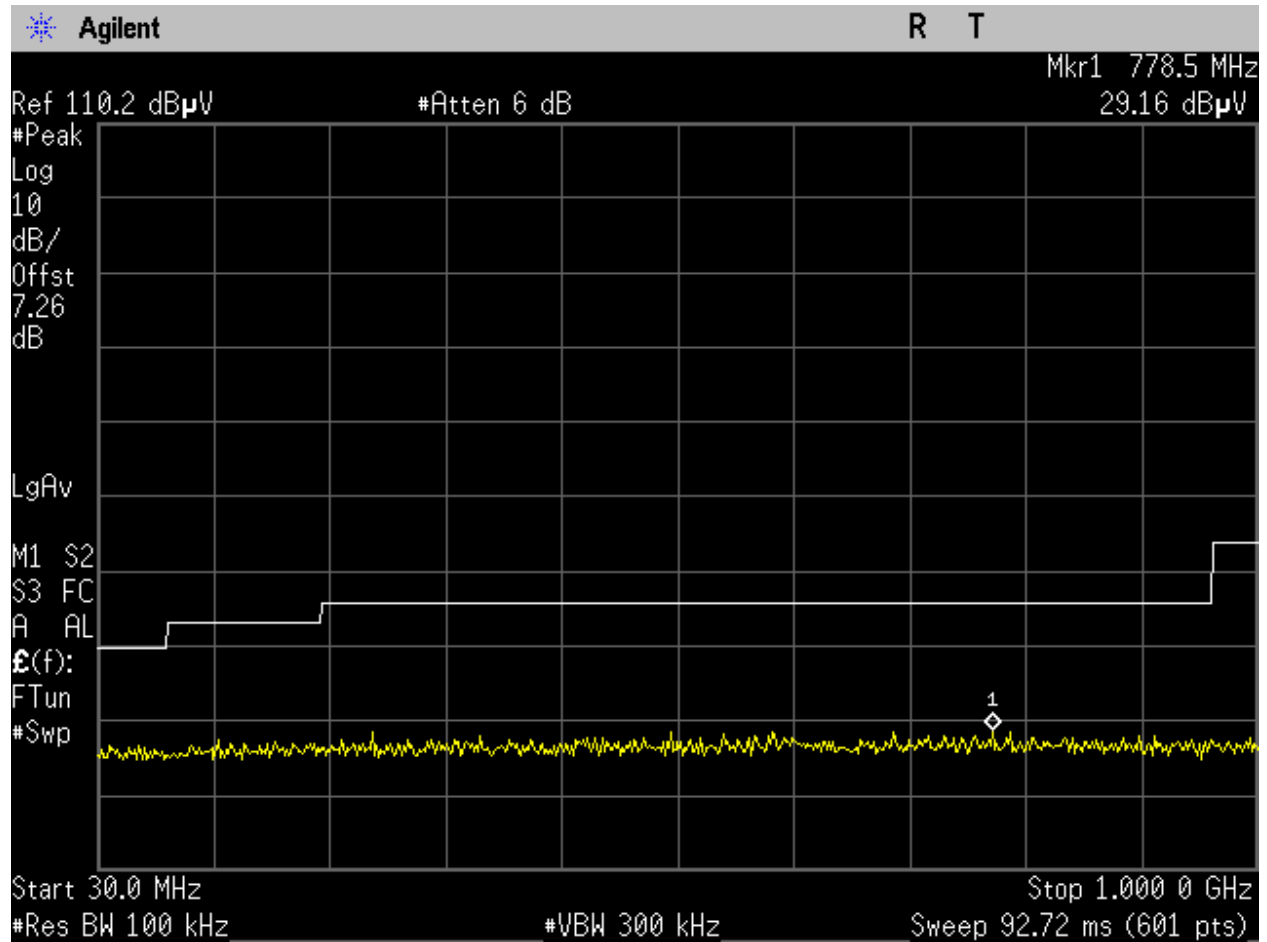


Figure 597: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 1.

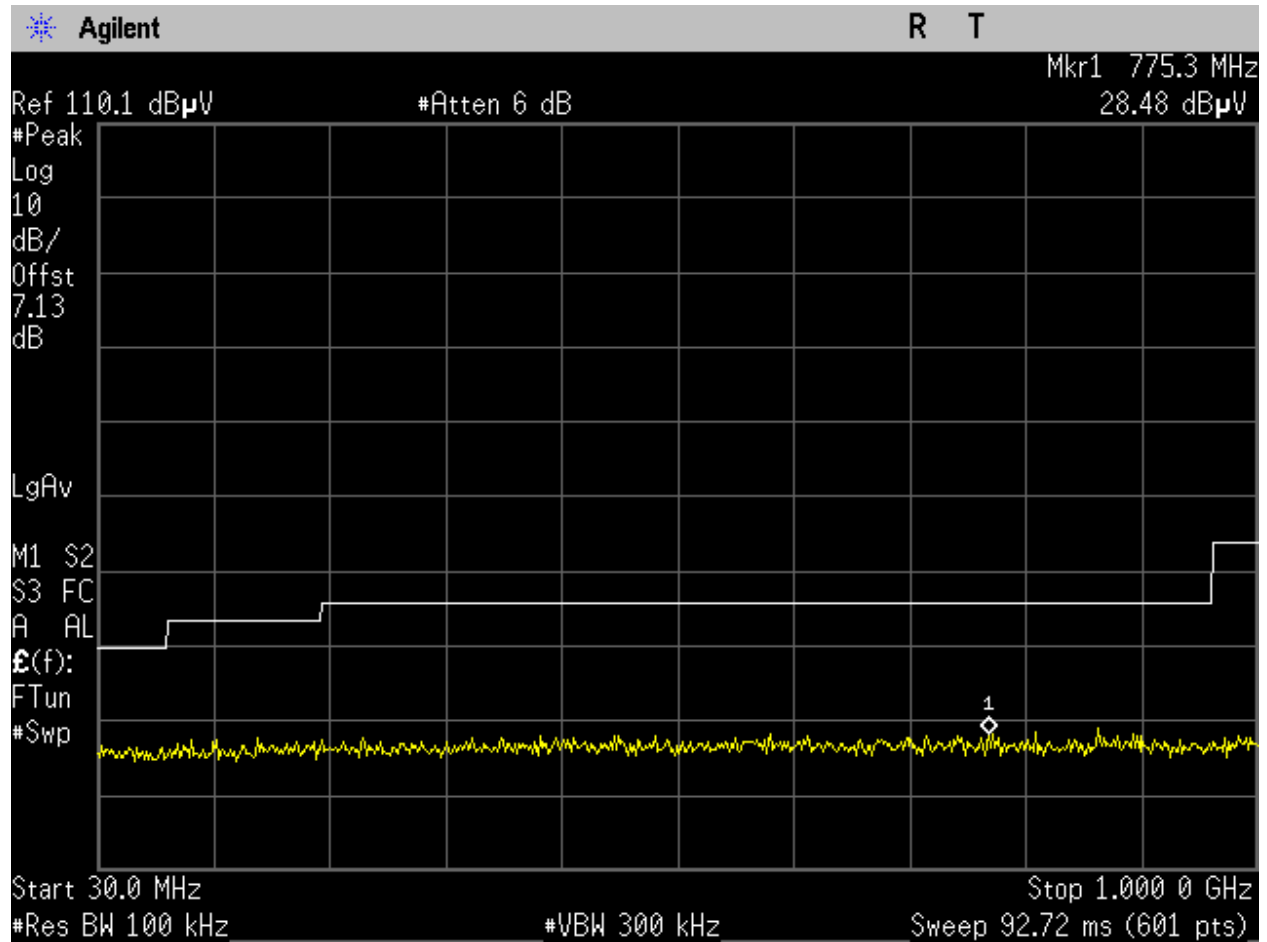


Figure 598: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 2.

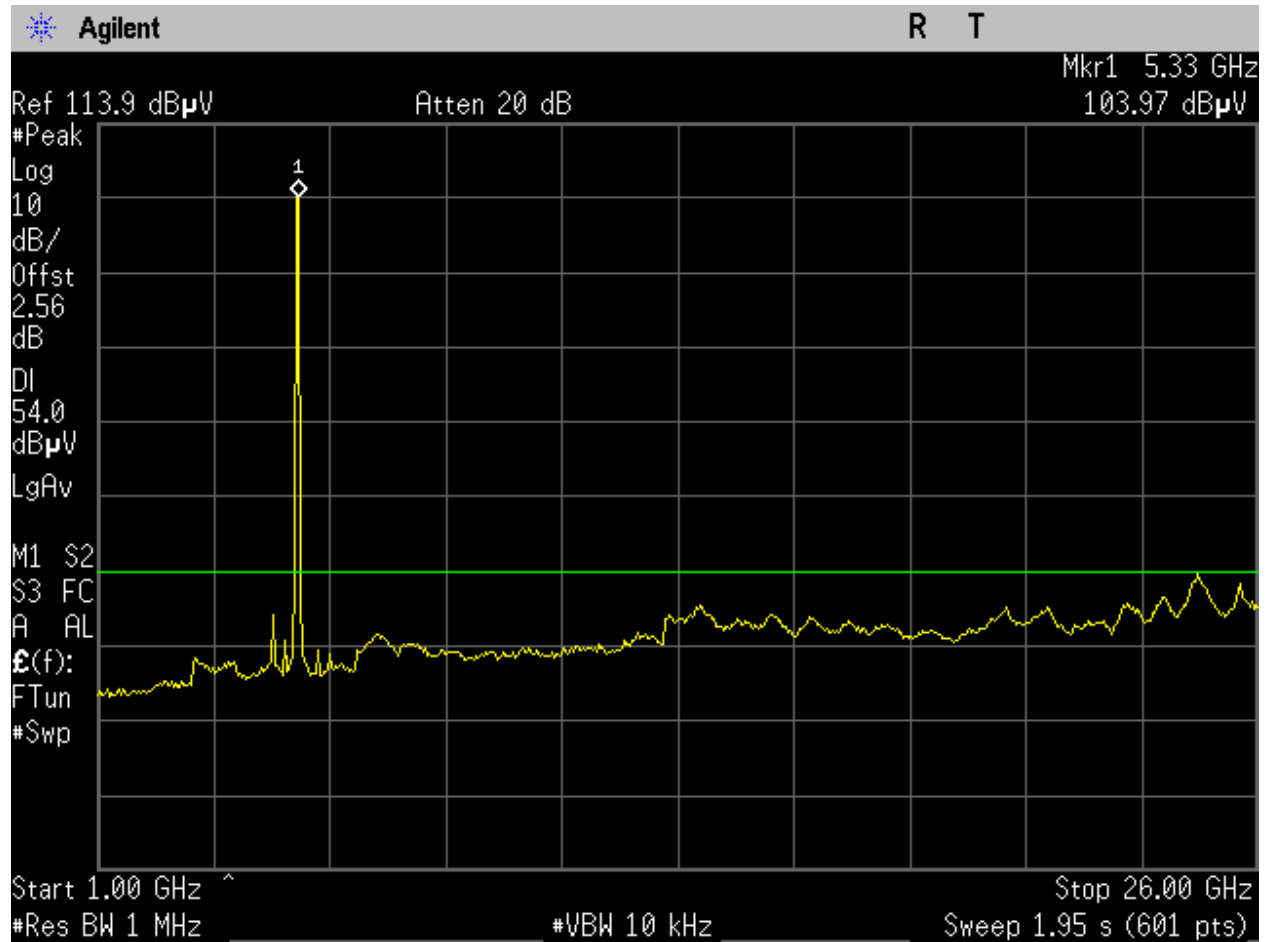


Figure 599: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

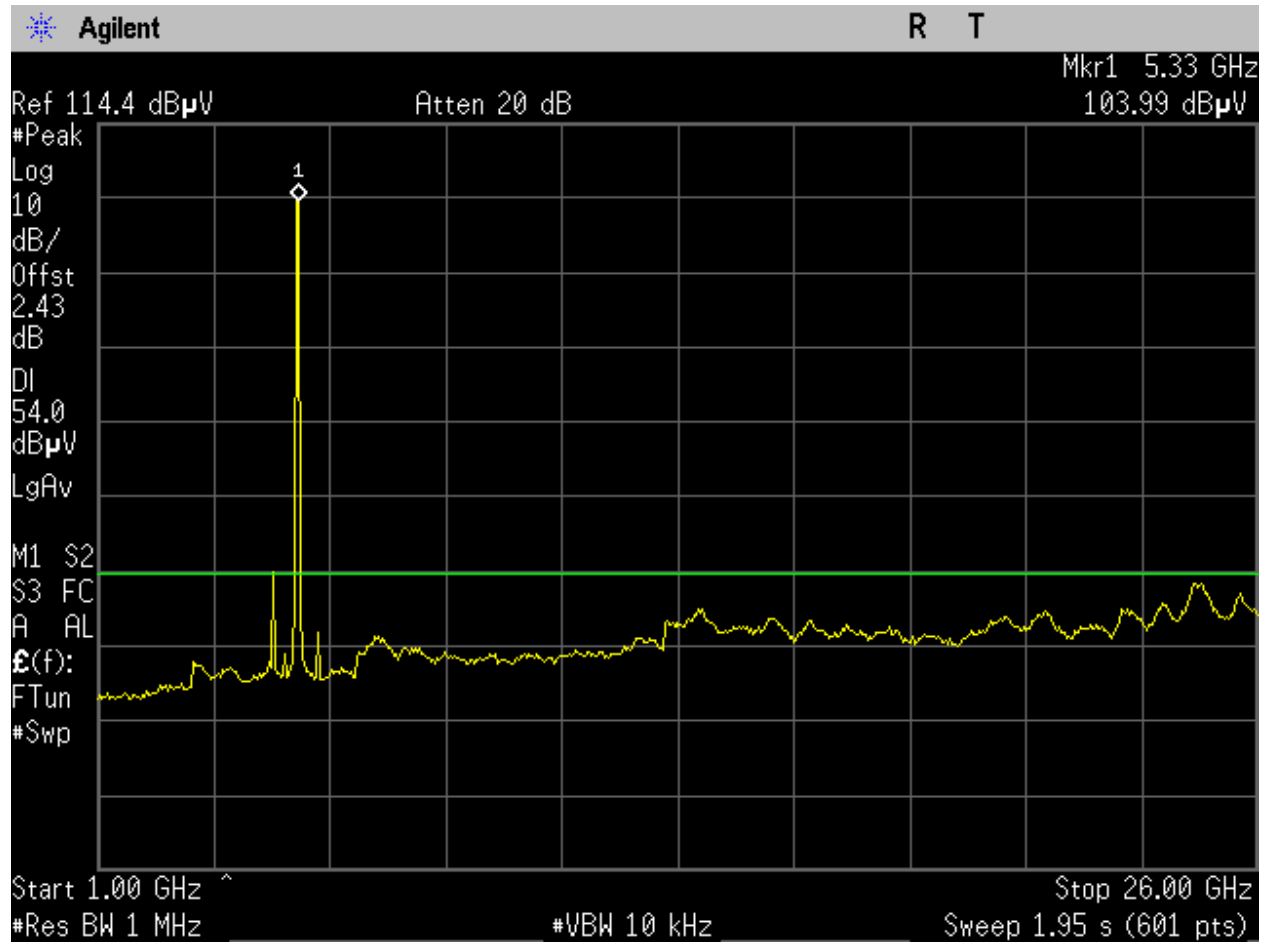


Figure 600: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

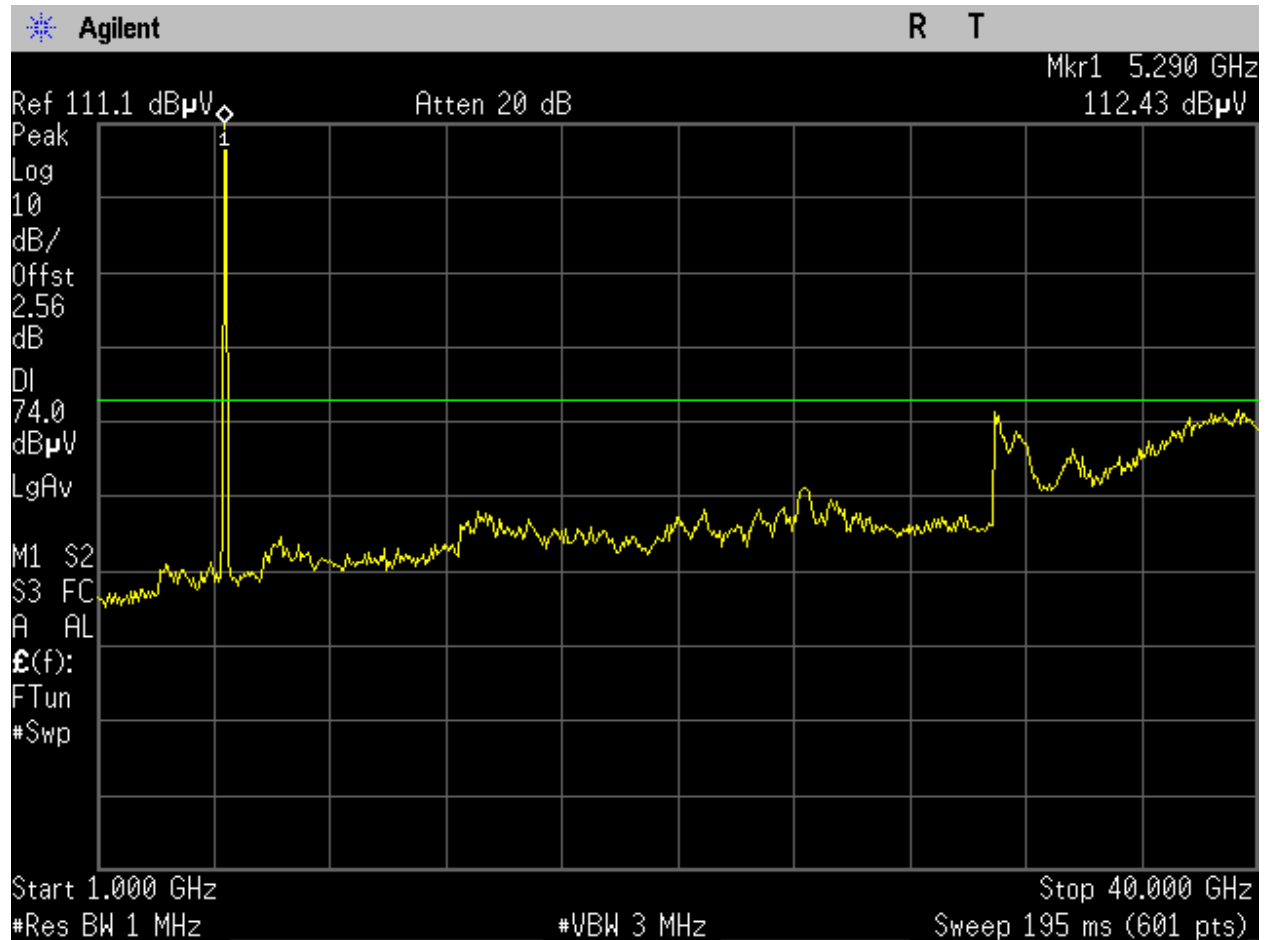


Figure 601: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 1.

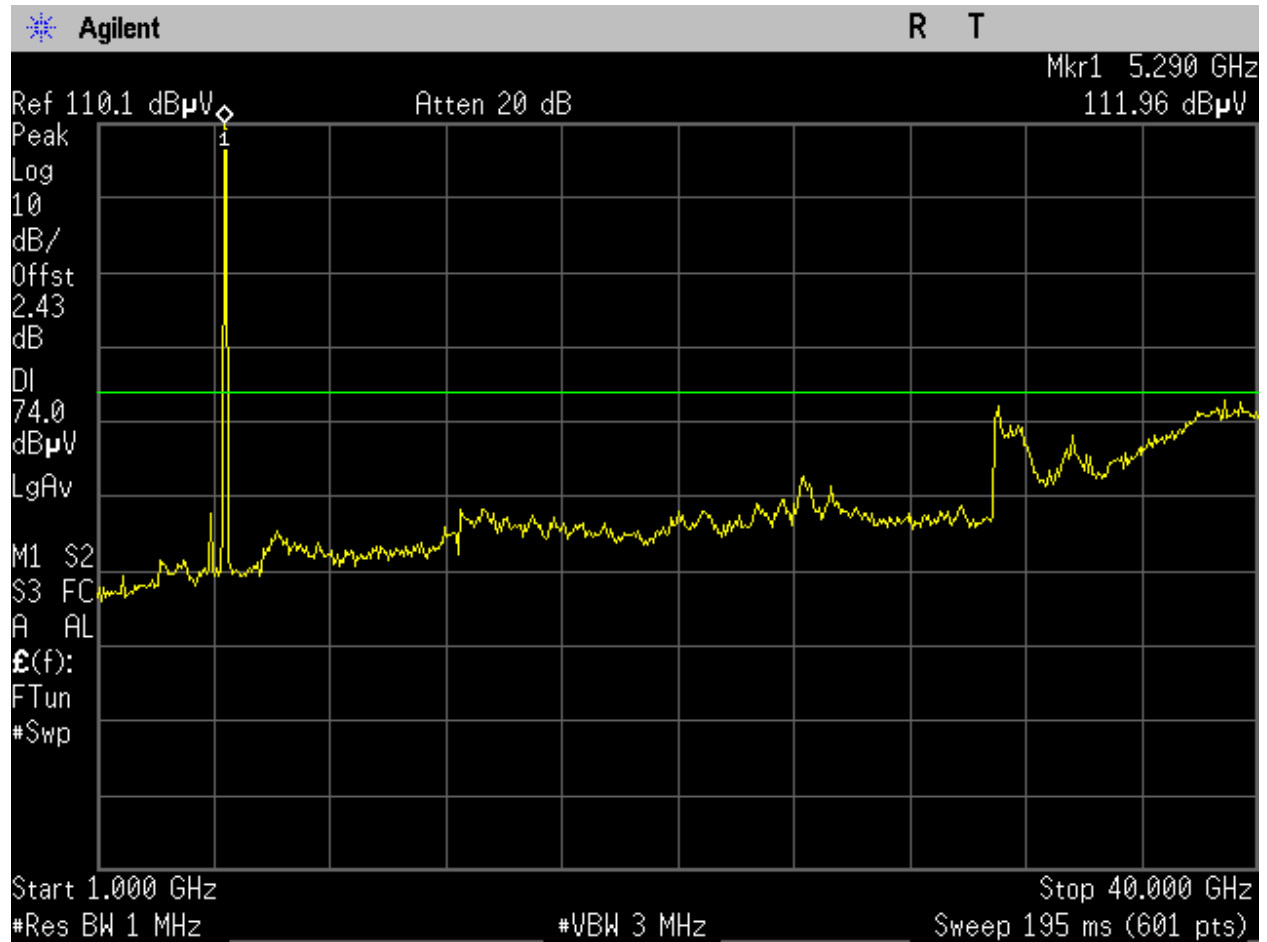


Figure 602: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 2.

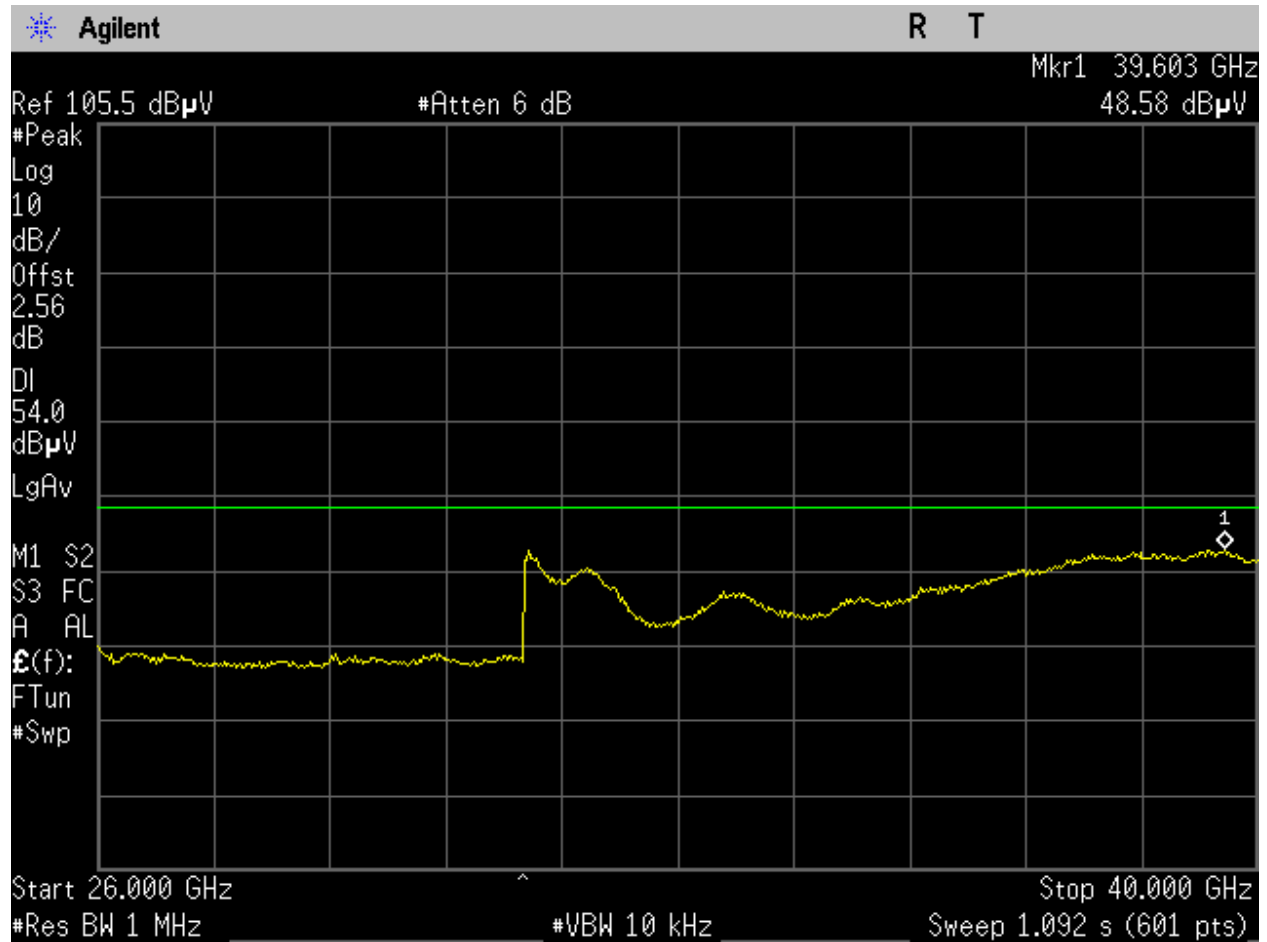


Figure 603: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

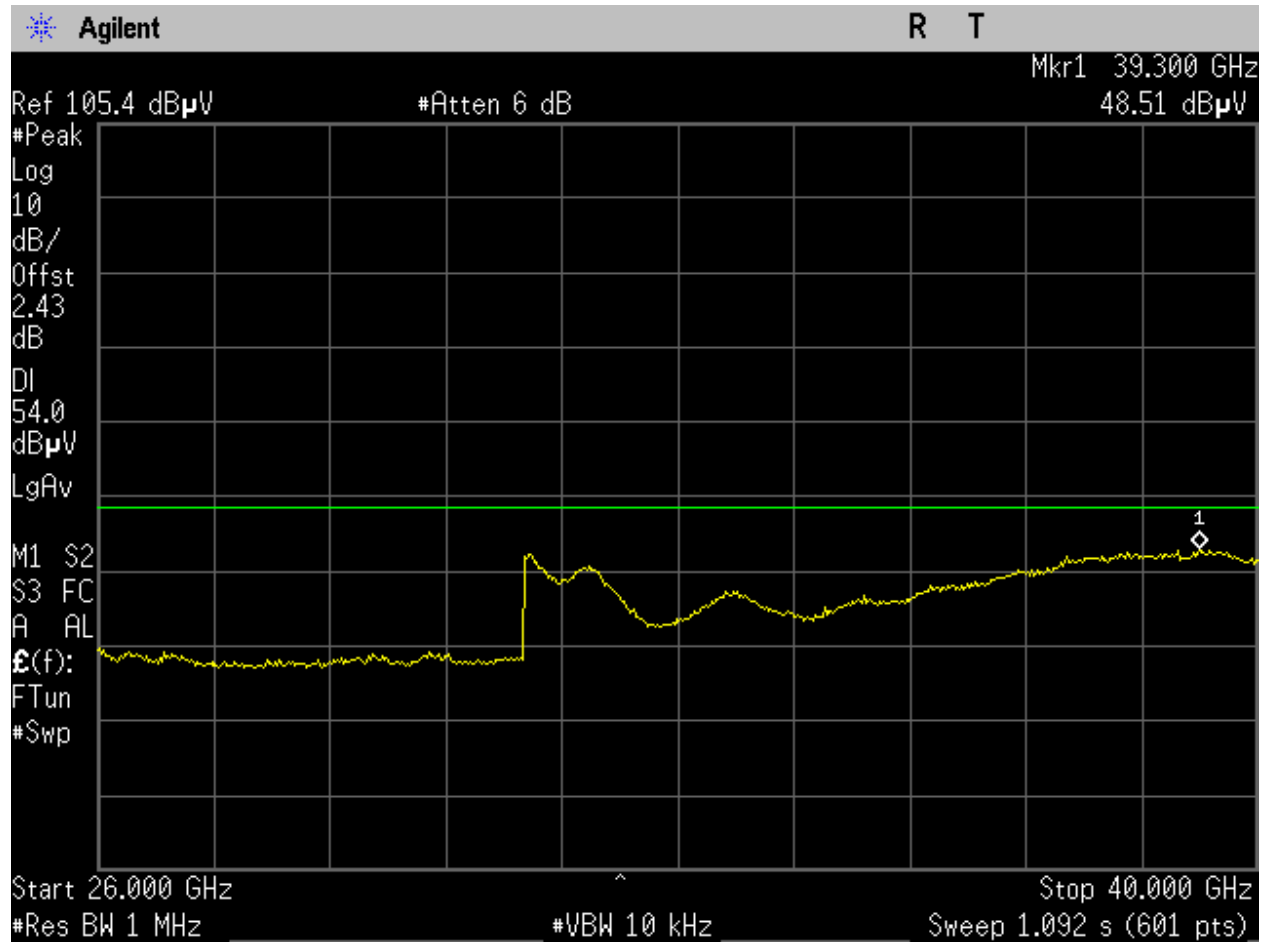


Figure 604: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

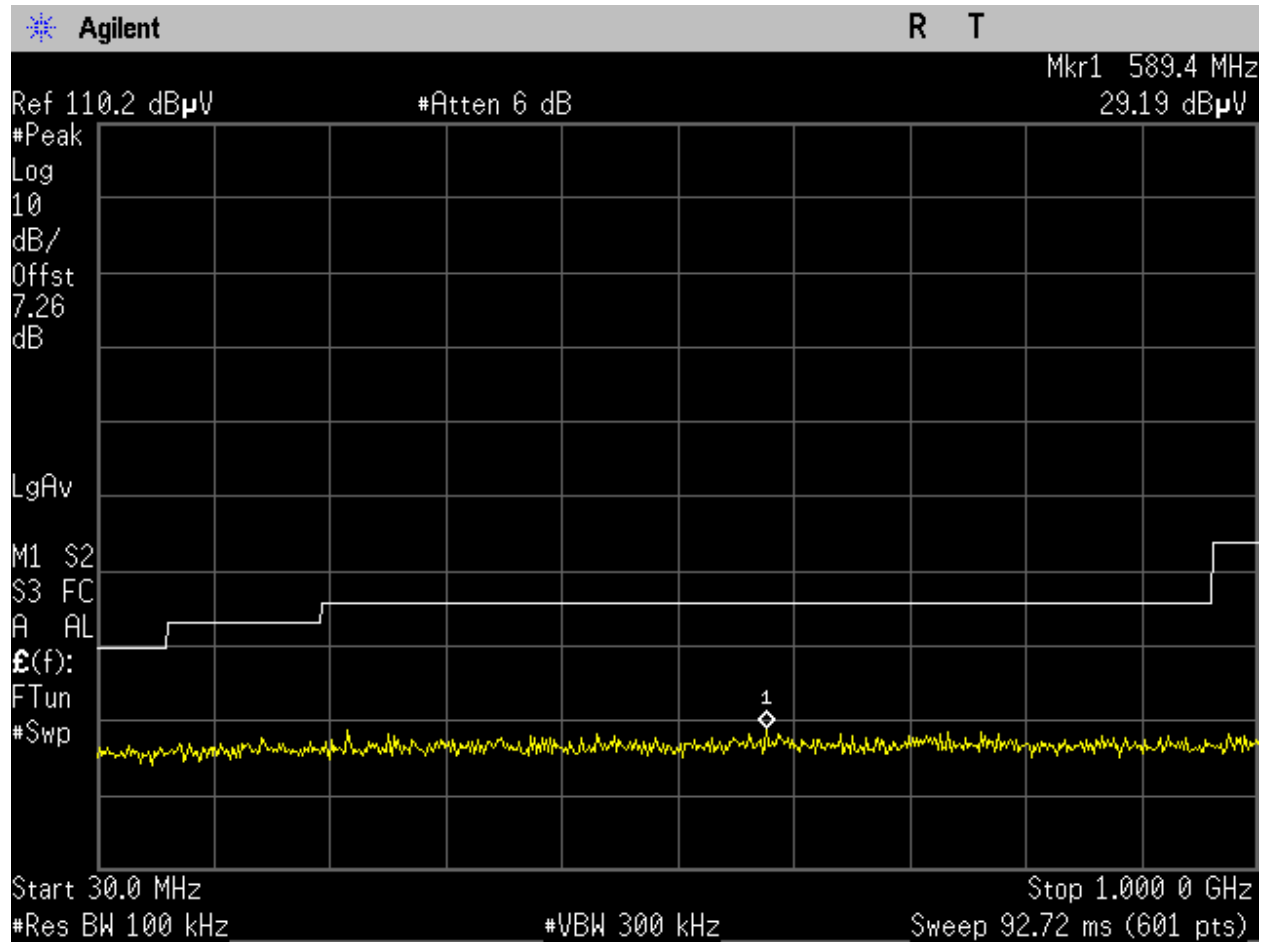


Figure 605: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

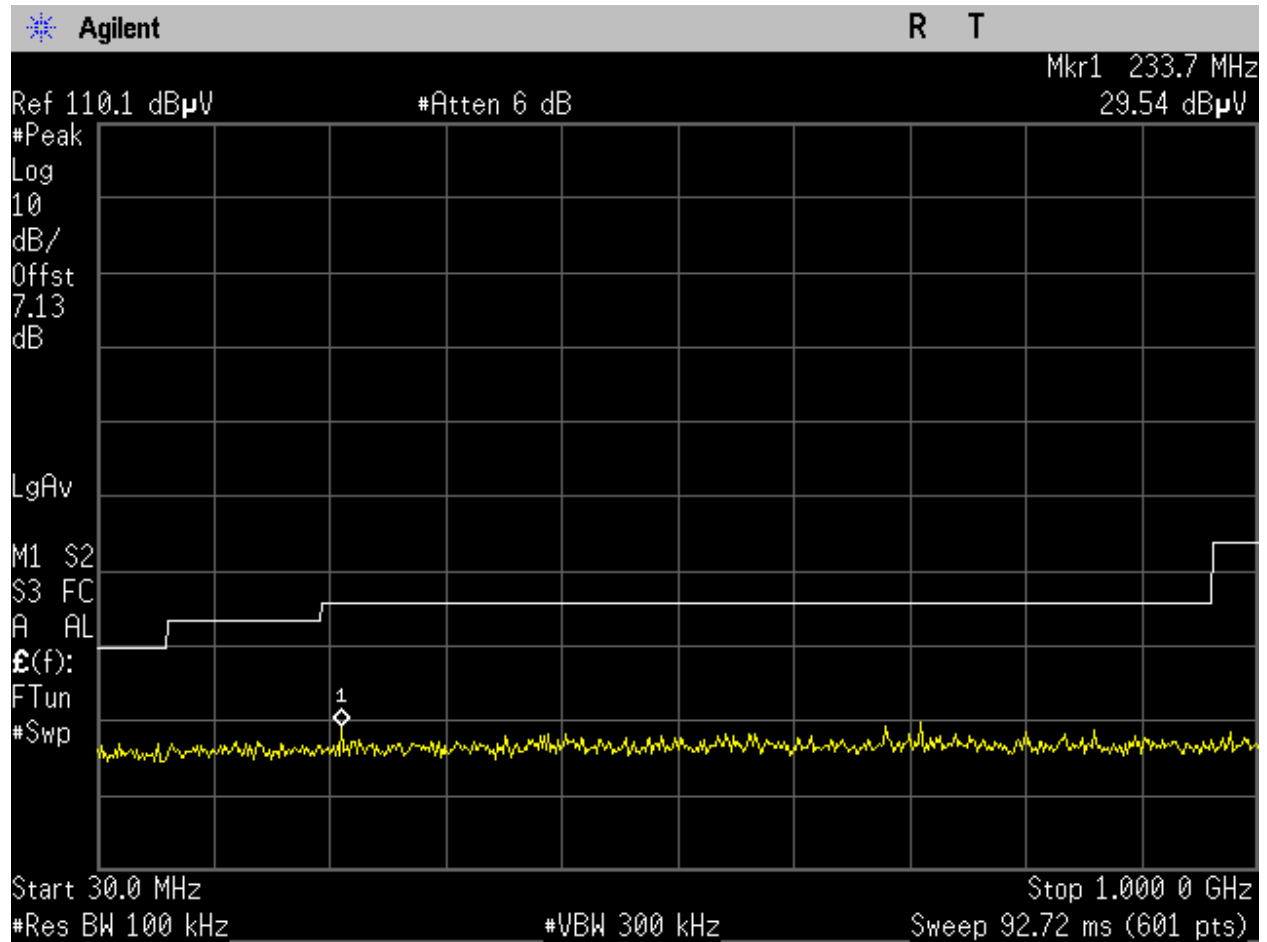


Figure 606: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

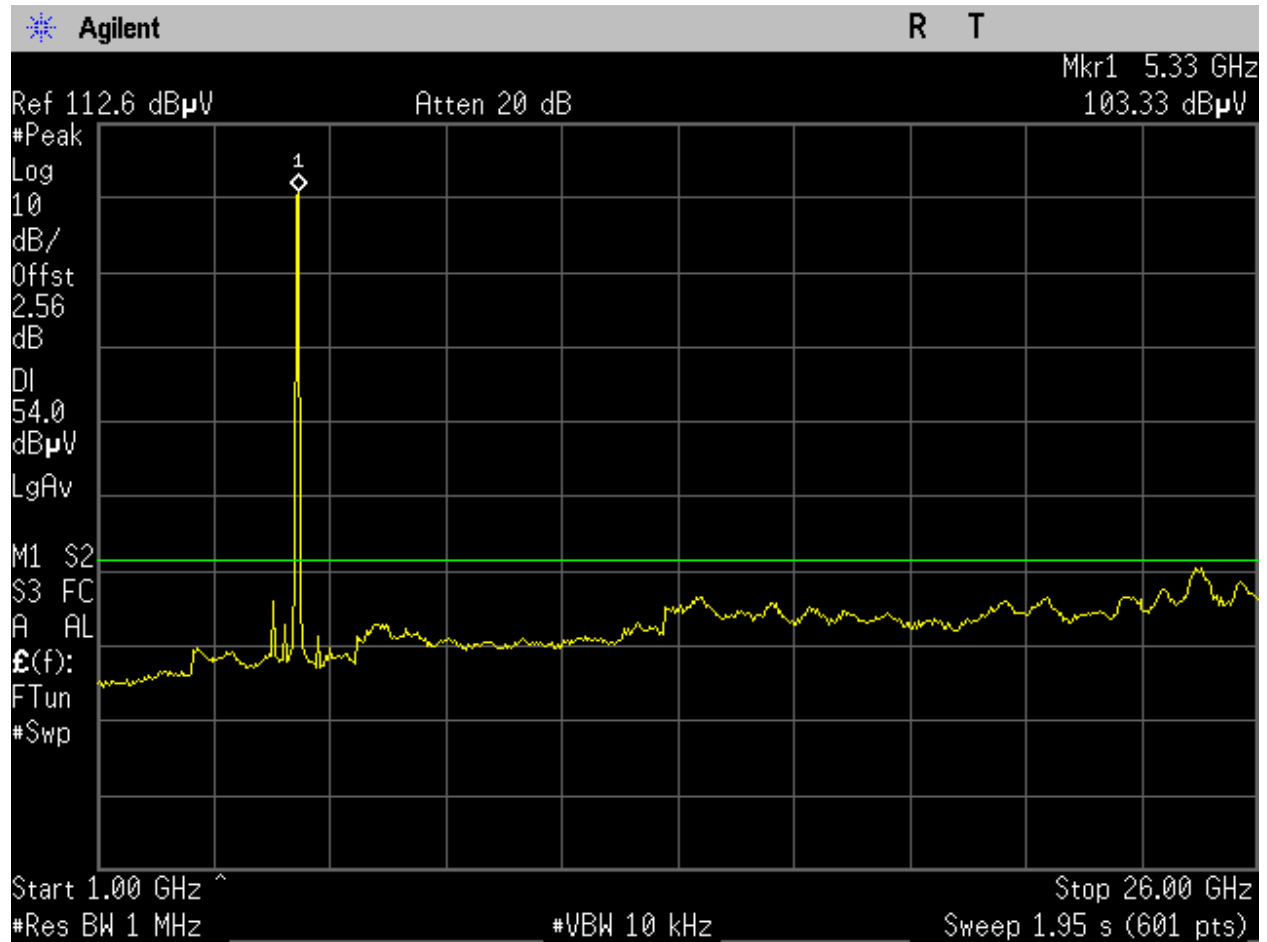


Figure 607: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_1-26GHz avg_Port 1.

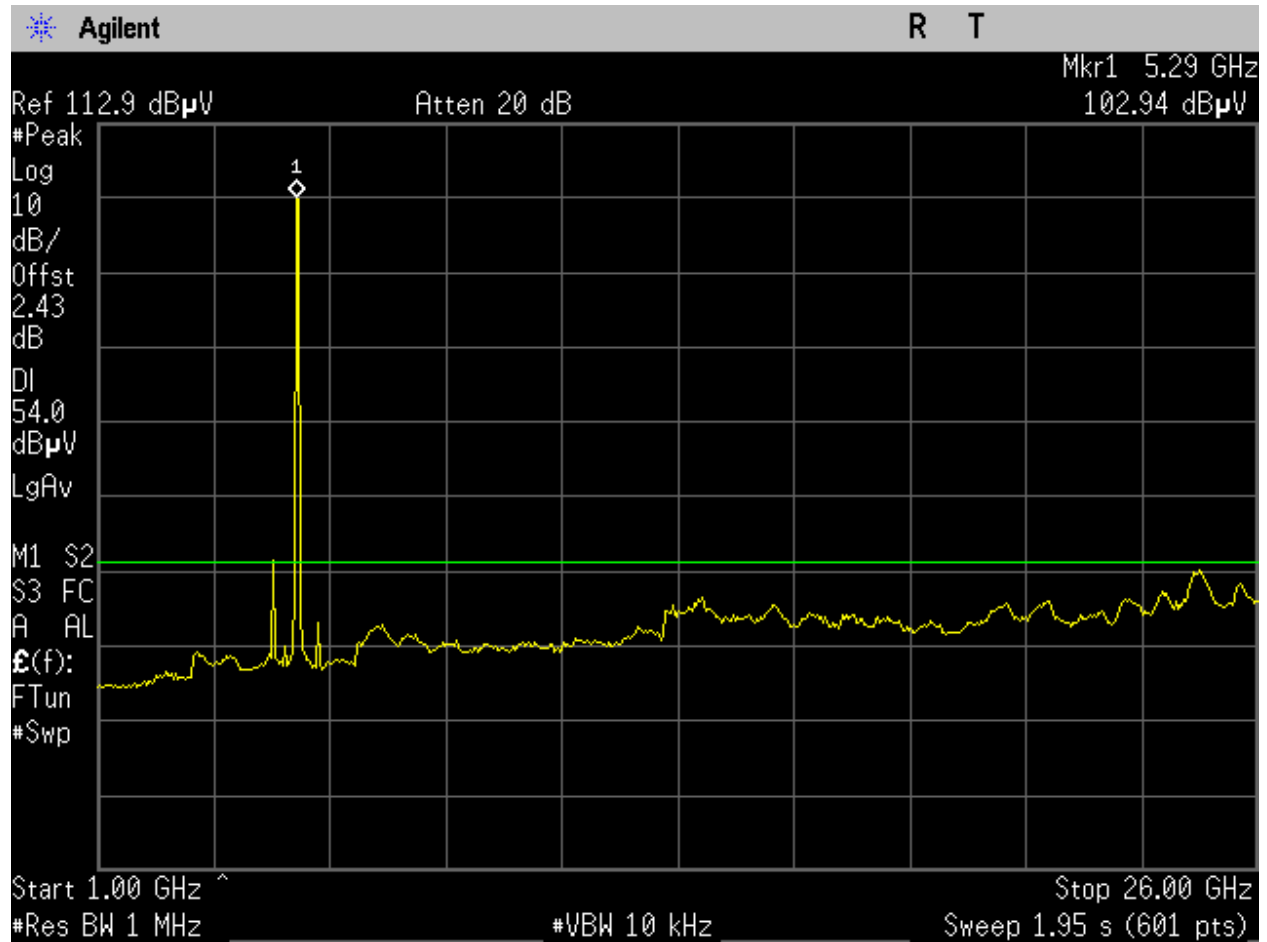


Figure 608: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_1-26GHz avg_Port 2.

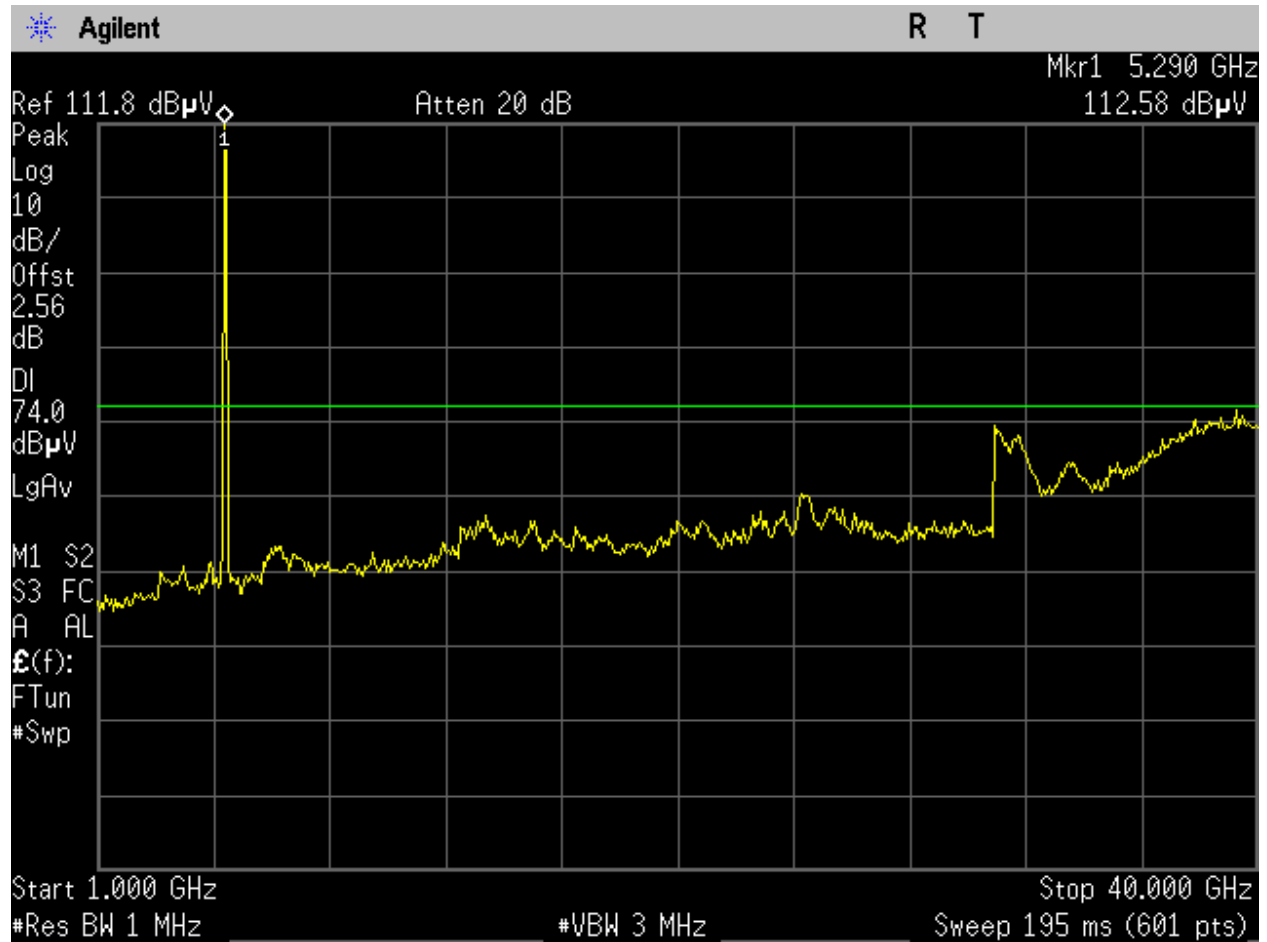


Figure 609: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 1.

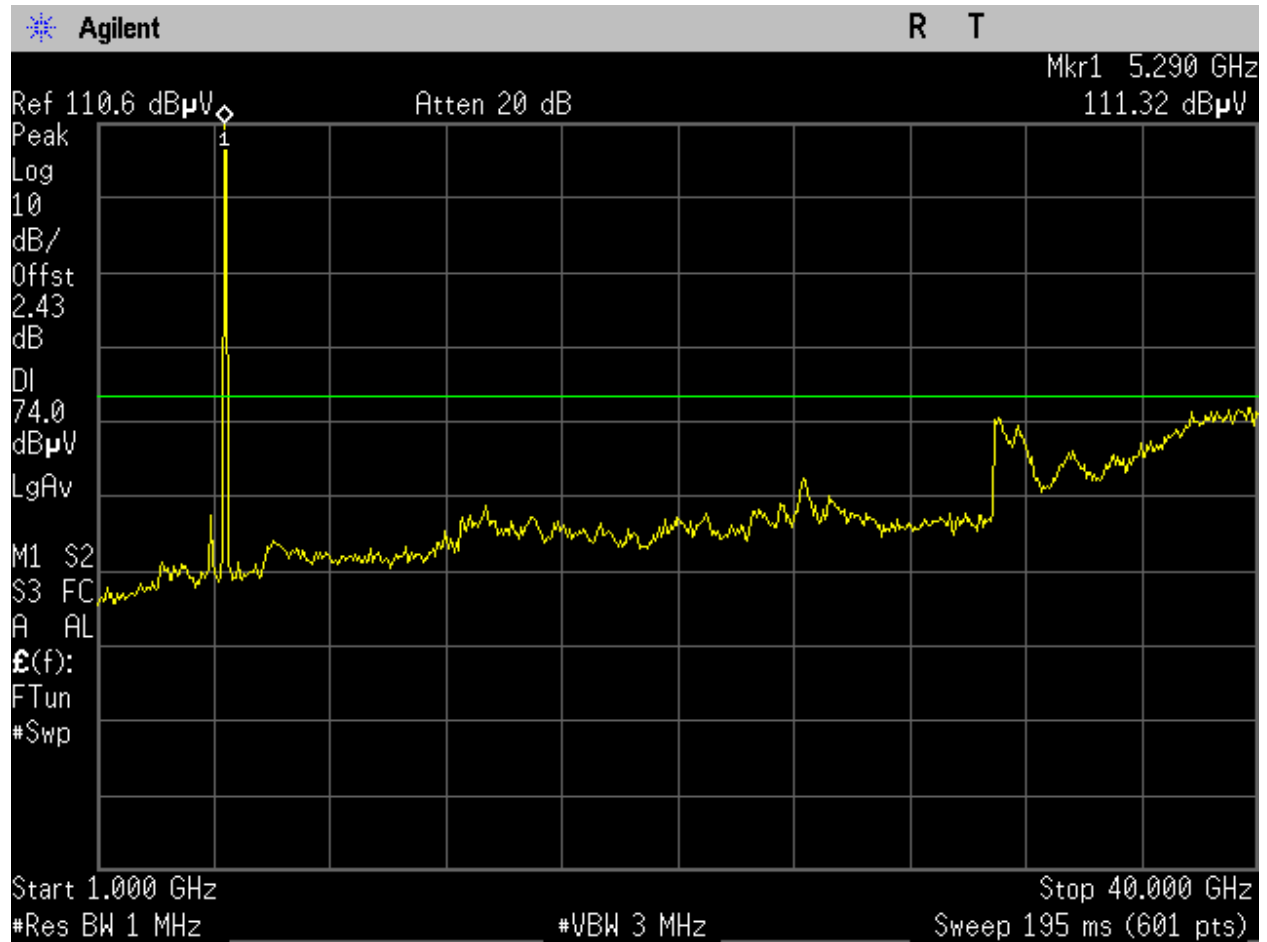


Figure 610: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 2.

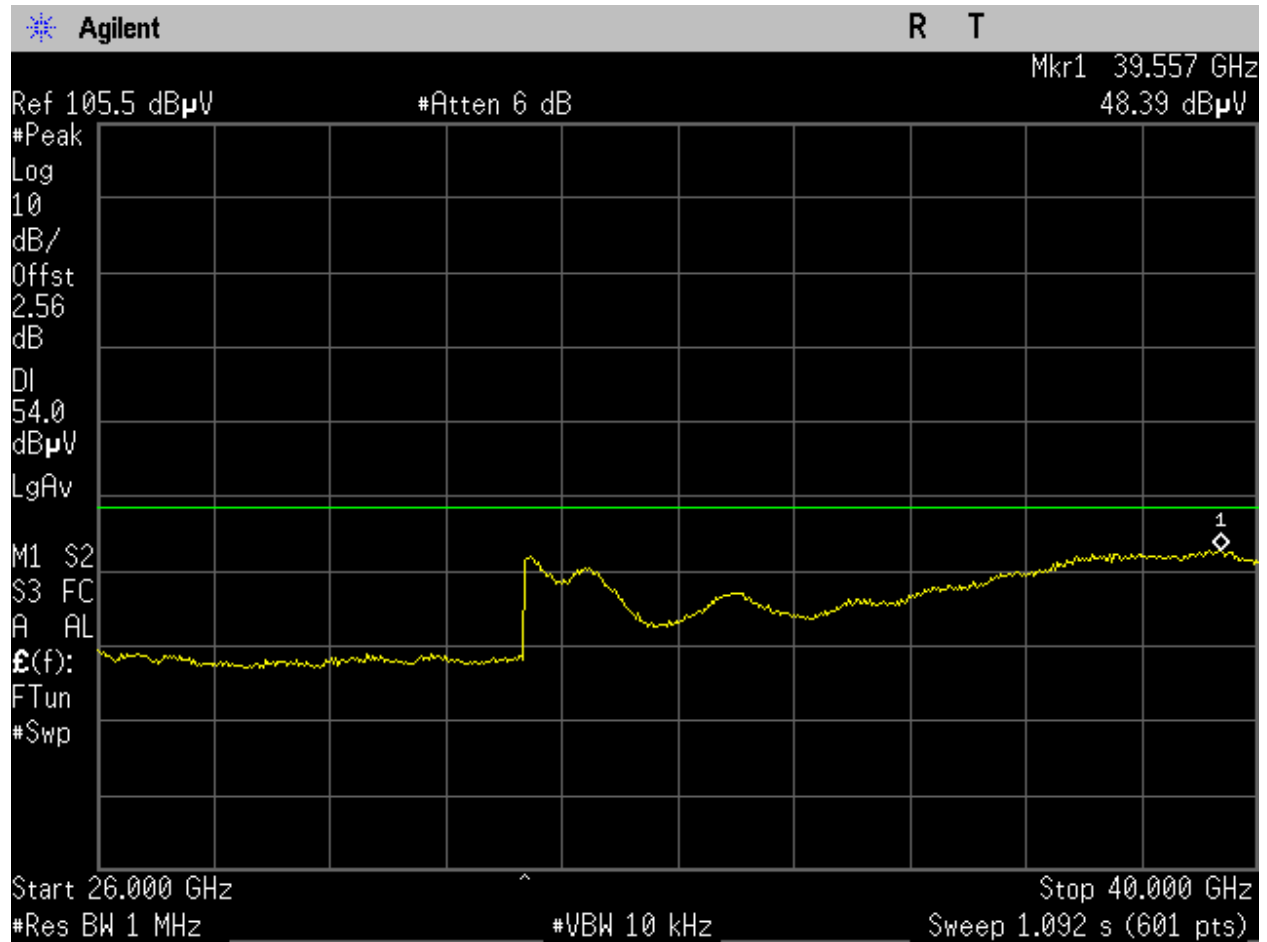


Figure 611: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 1.

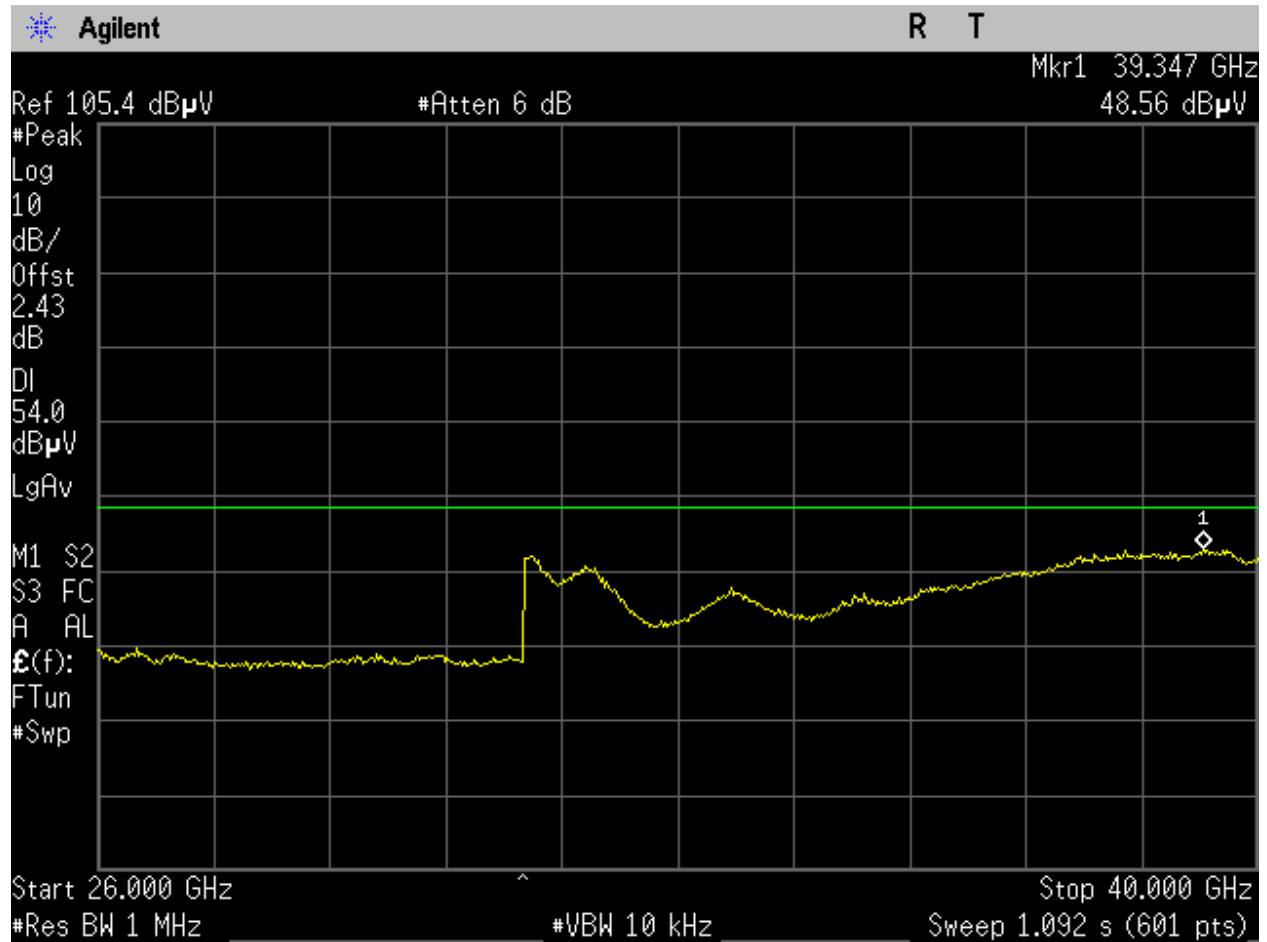


Figure 612: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 2.

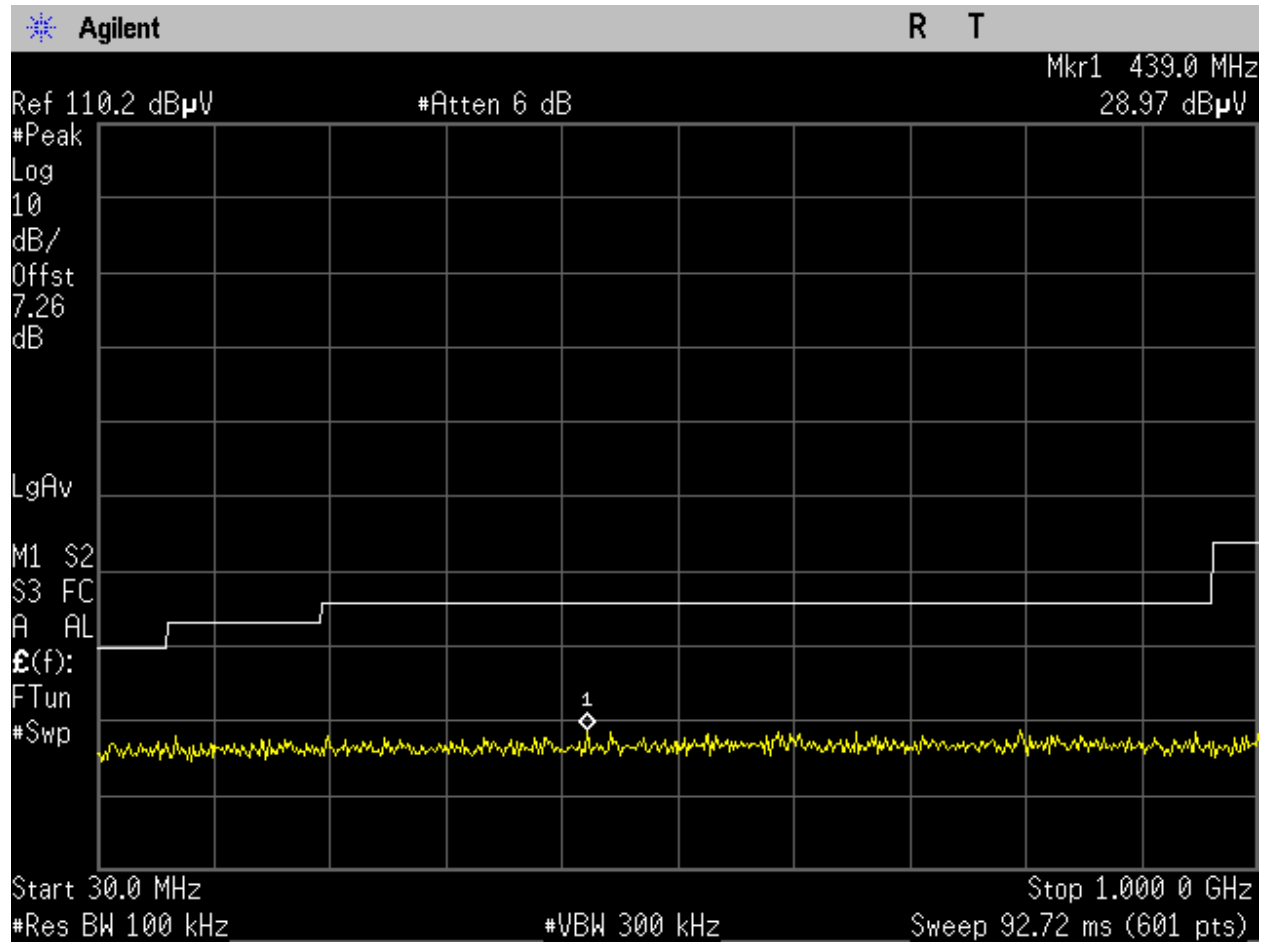


Figure 613: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 1.

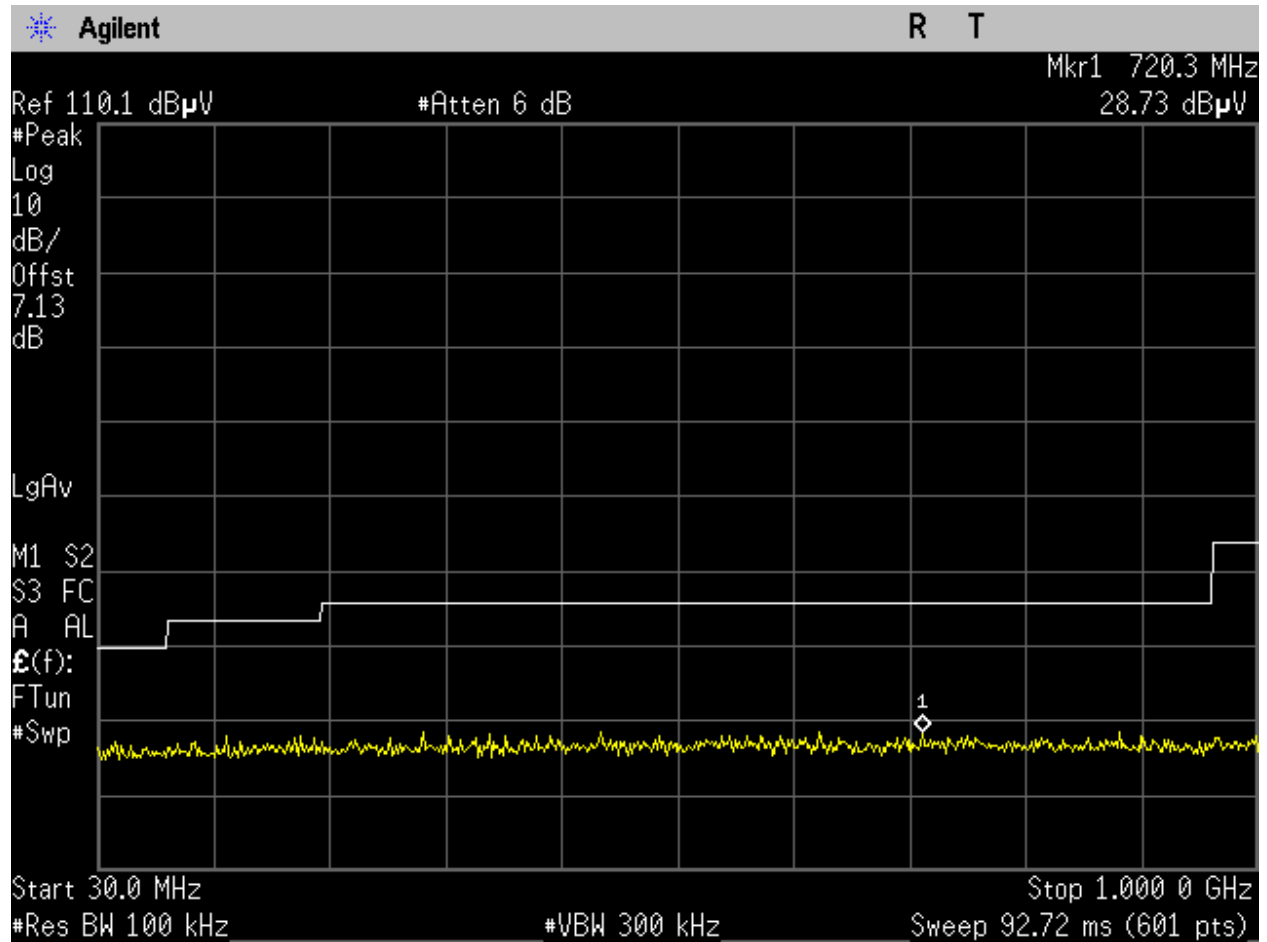


Figure 614: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 2.

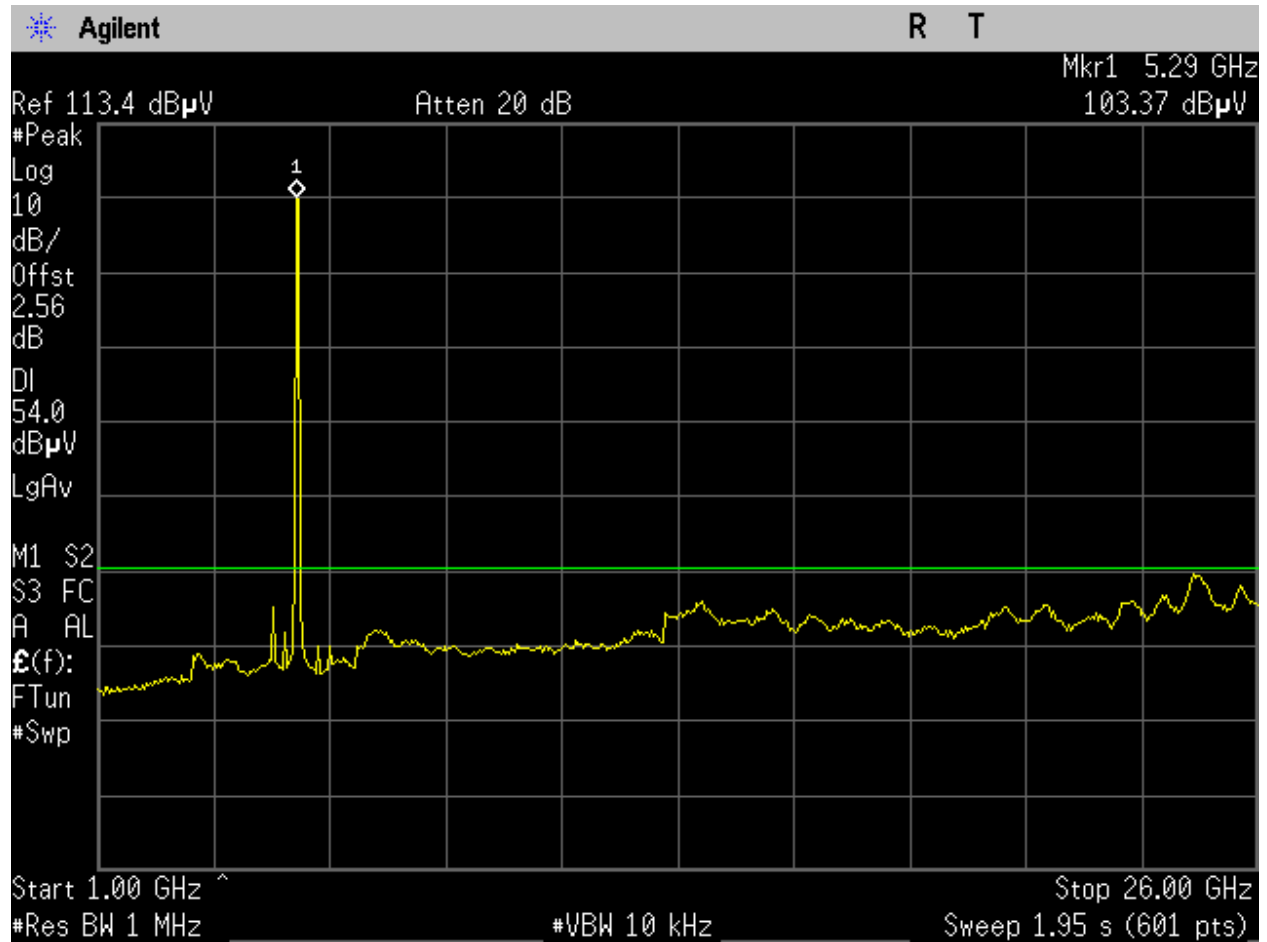


Figure 615: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_1-26GHz avg_Port 1.

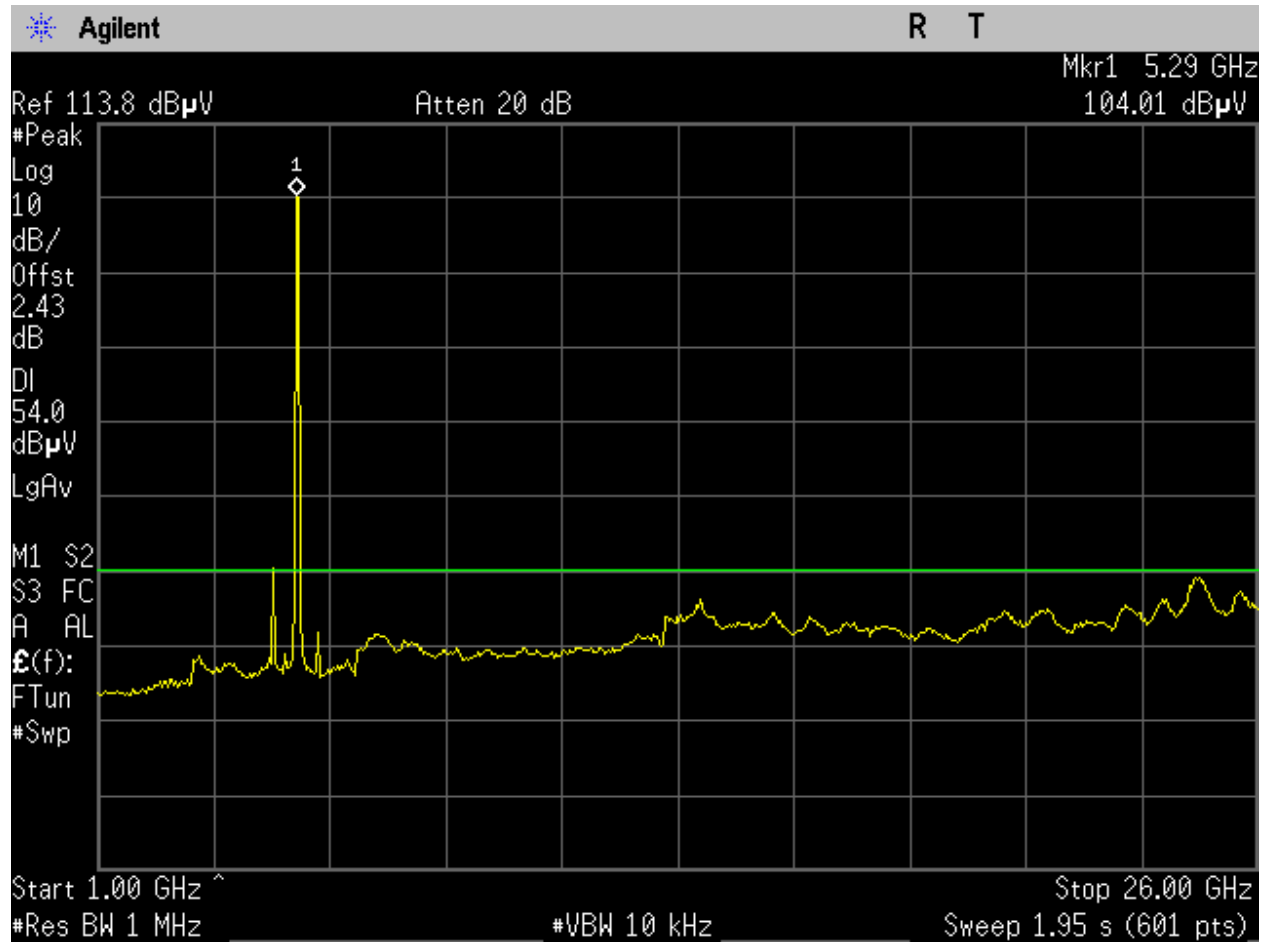


Figure 616: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_1-26GHz avg_Port 2.

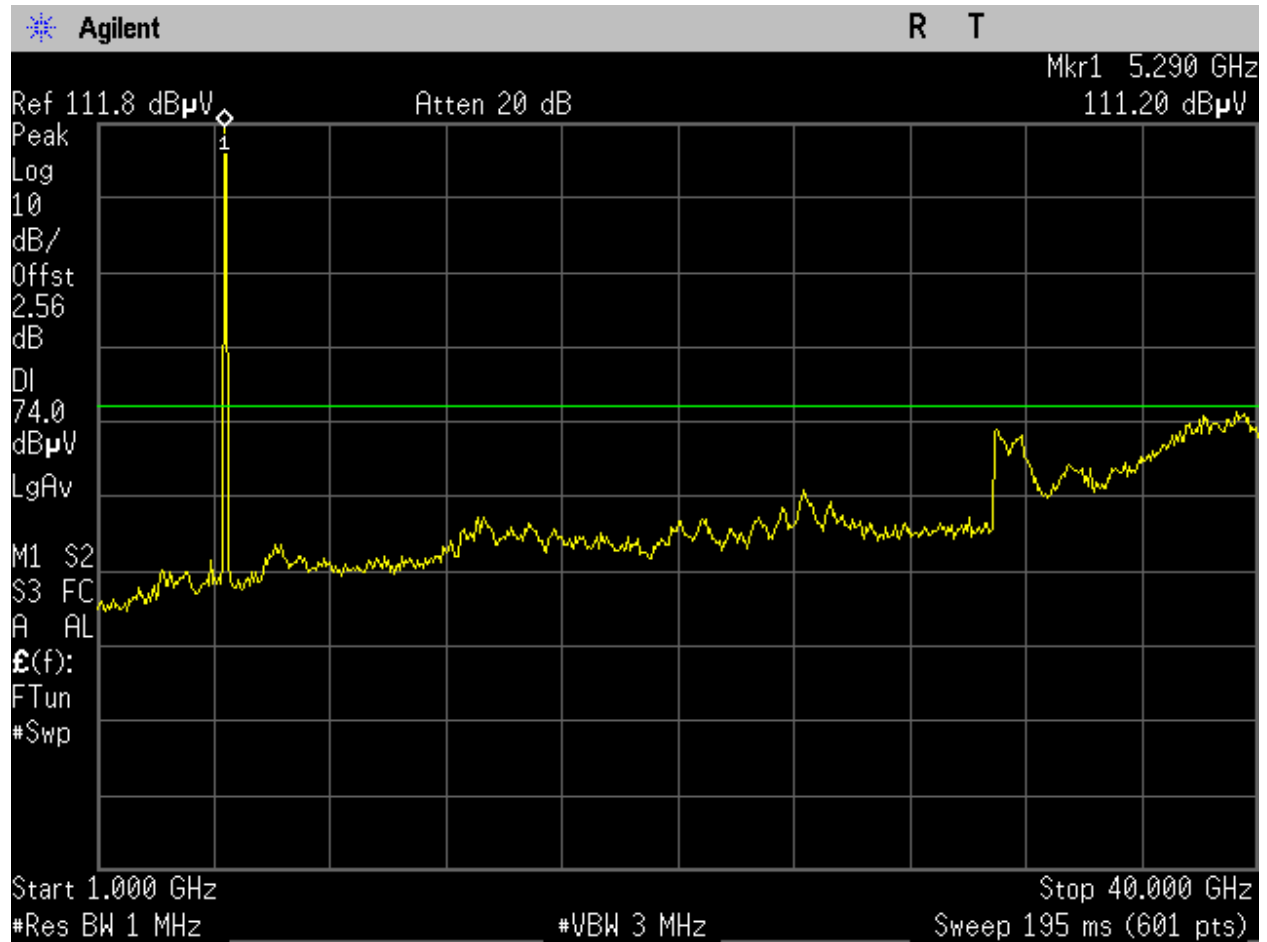


Figure 617: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_1-40GHz_Peak_Port 1.

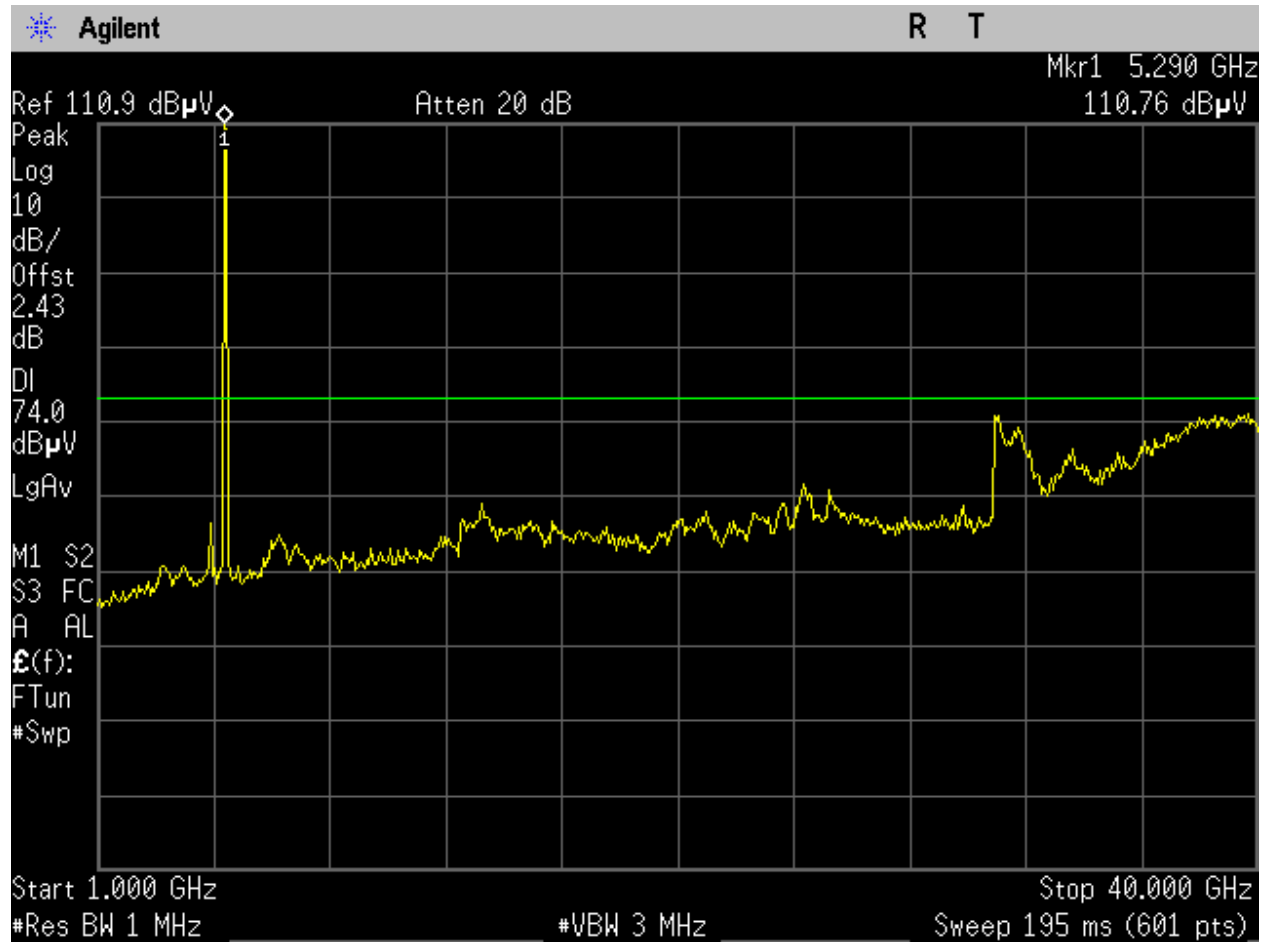


Figure 618: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_1-40GHz_Peak_Port 2.

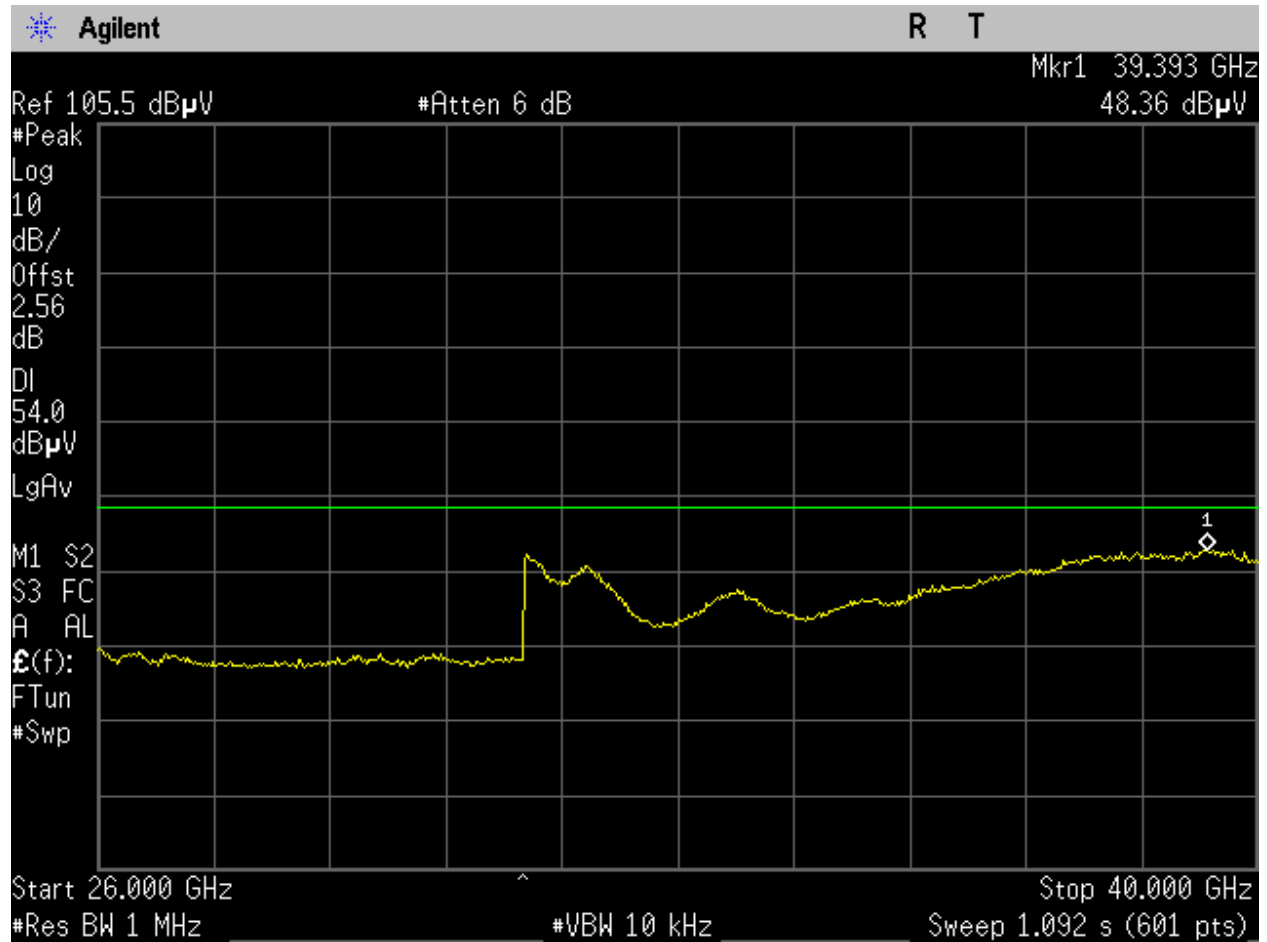


Figure 619: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_26-40GHz_Avg_Port 1.

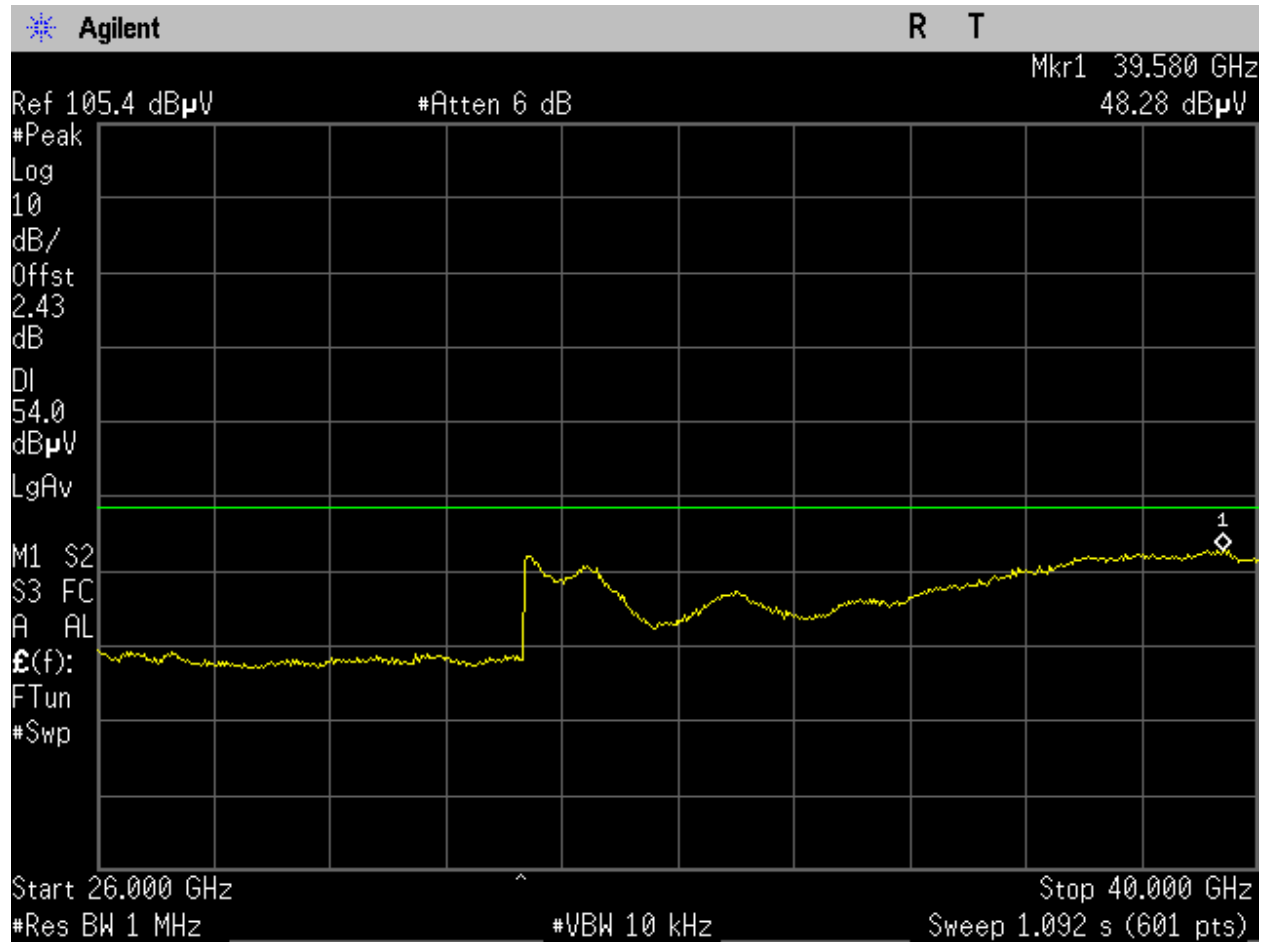


Figure 620: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_26-40GHz_Avg_Port 2.

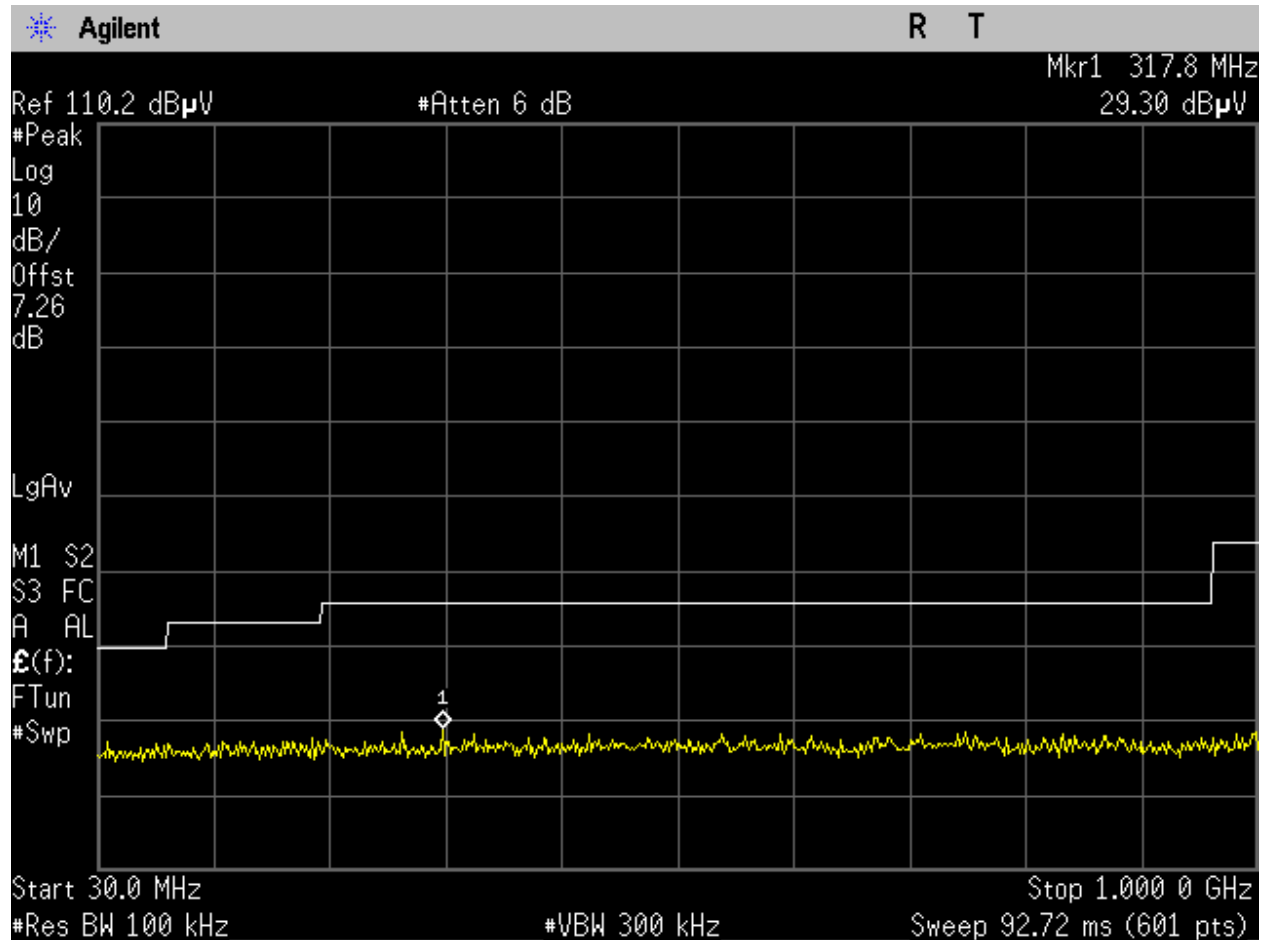


Figure 621: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 1.

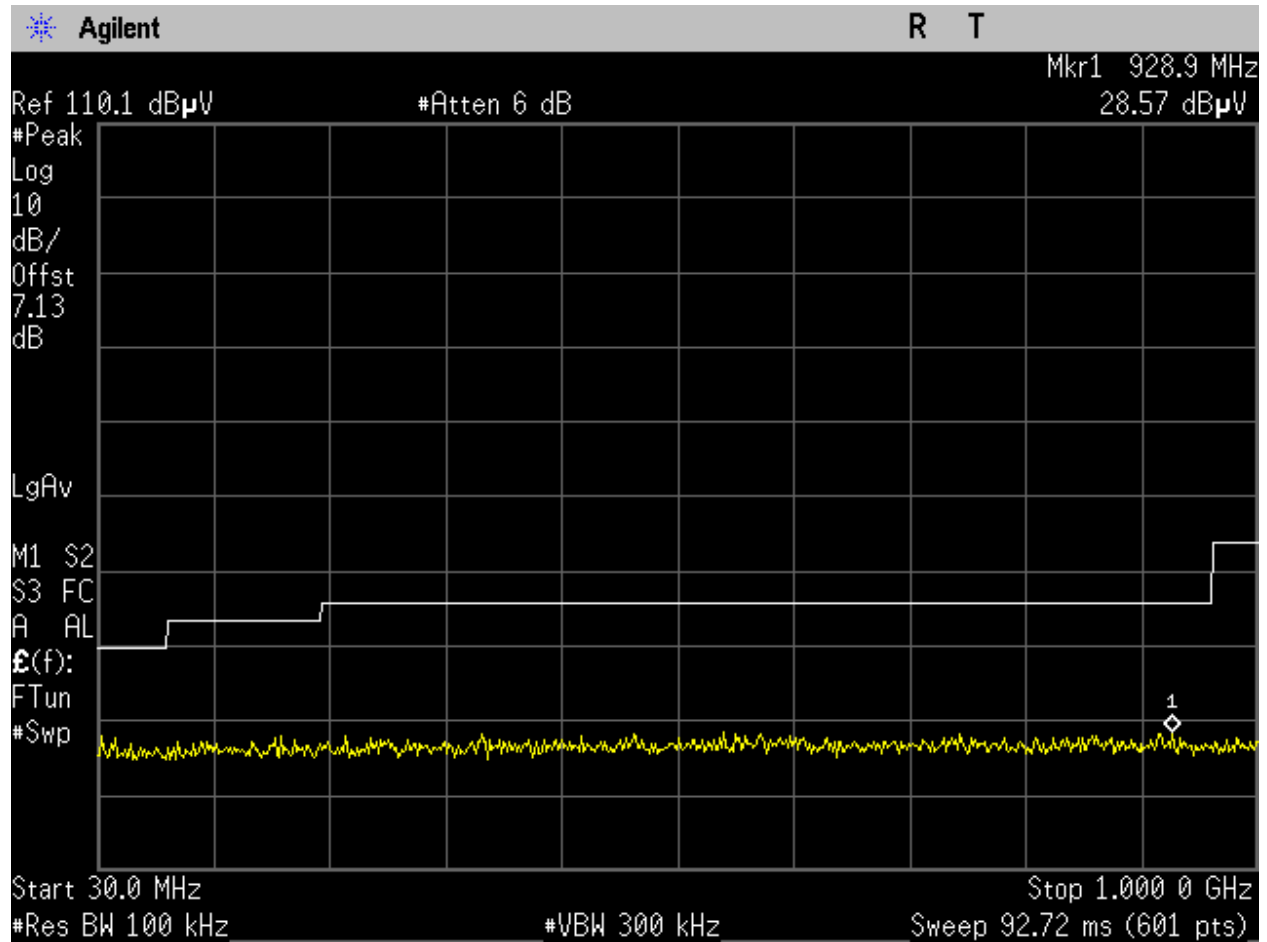


Figure 622: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 2.

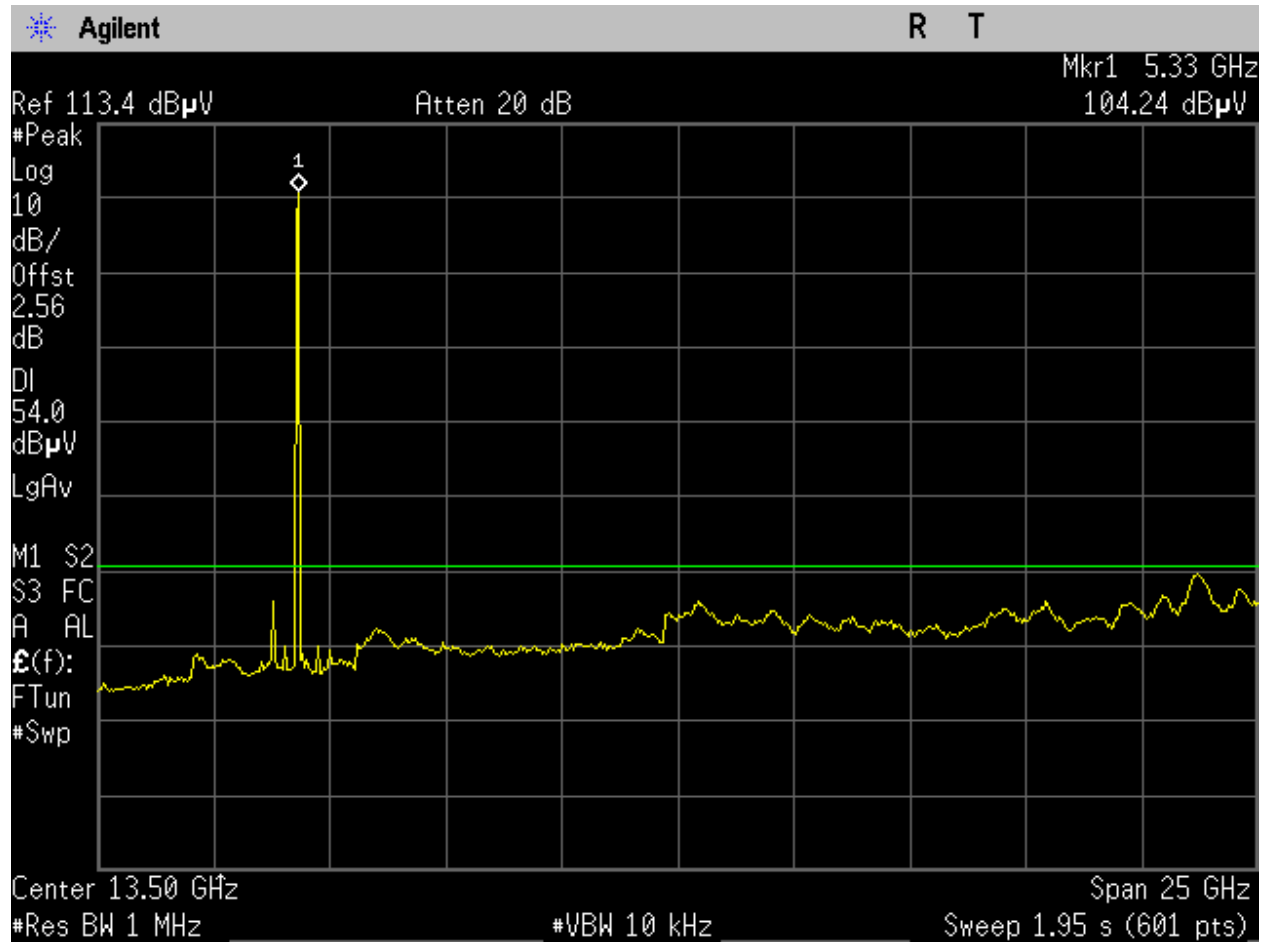


Figure 623: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_1-26GHz avg_Port 1.

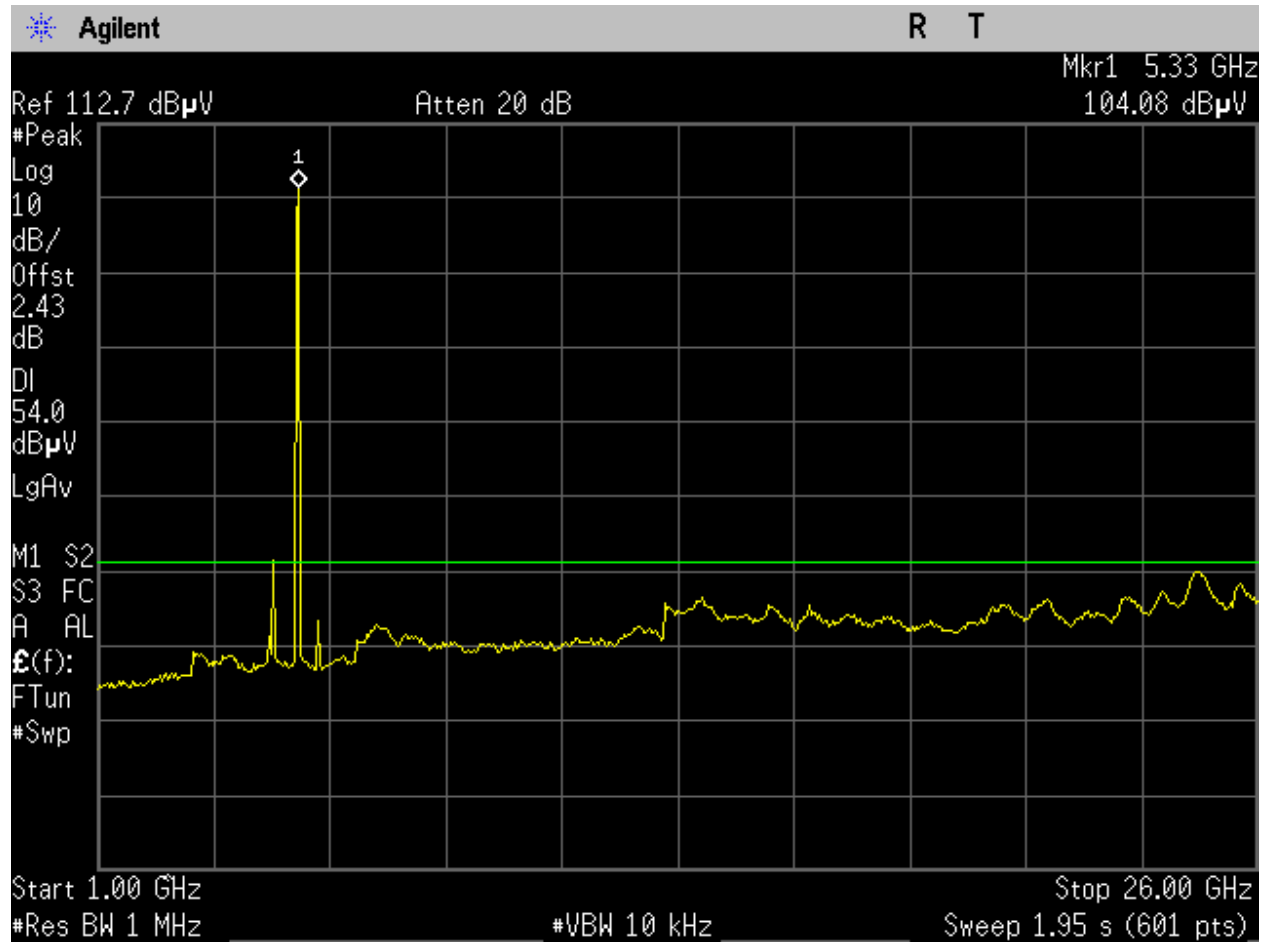


Figure 624: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_1-26GHz avg_Port 2.

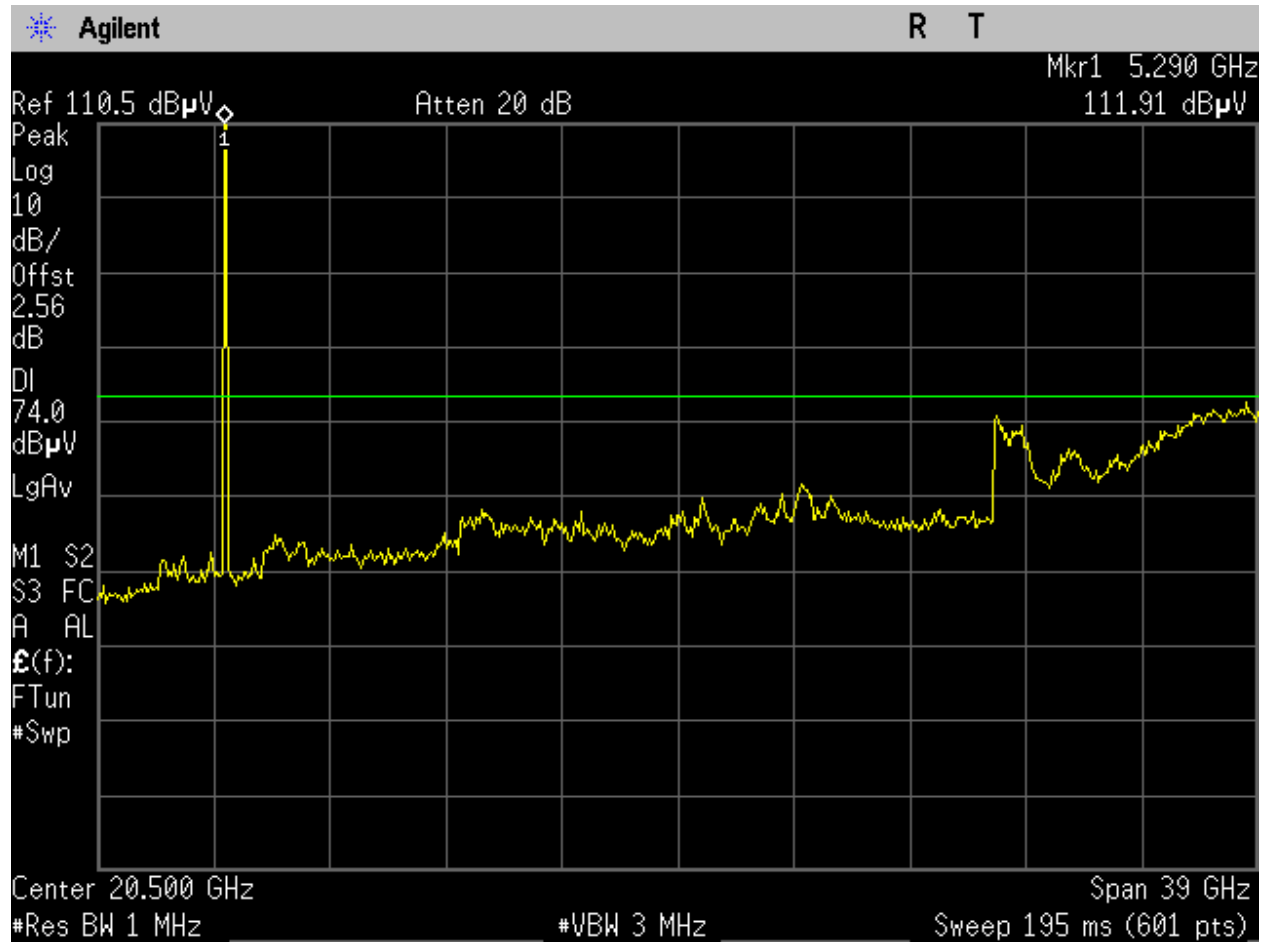


Figure 625: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_1-40GHz_Peak_Port 1.

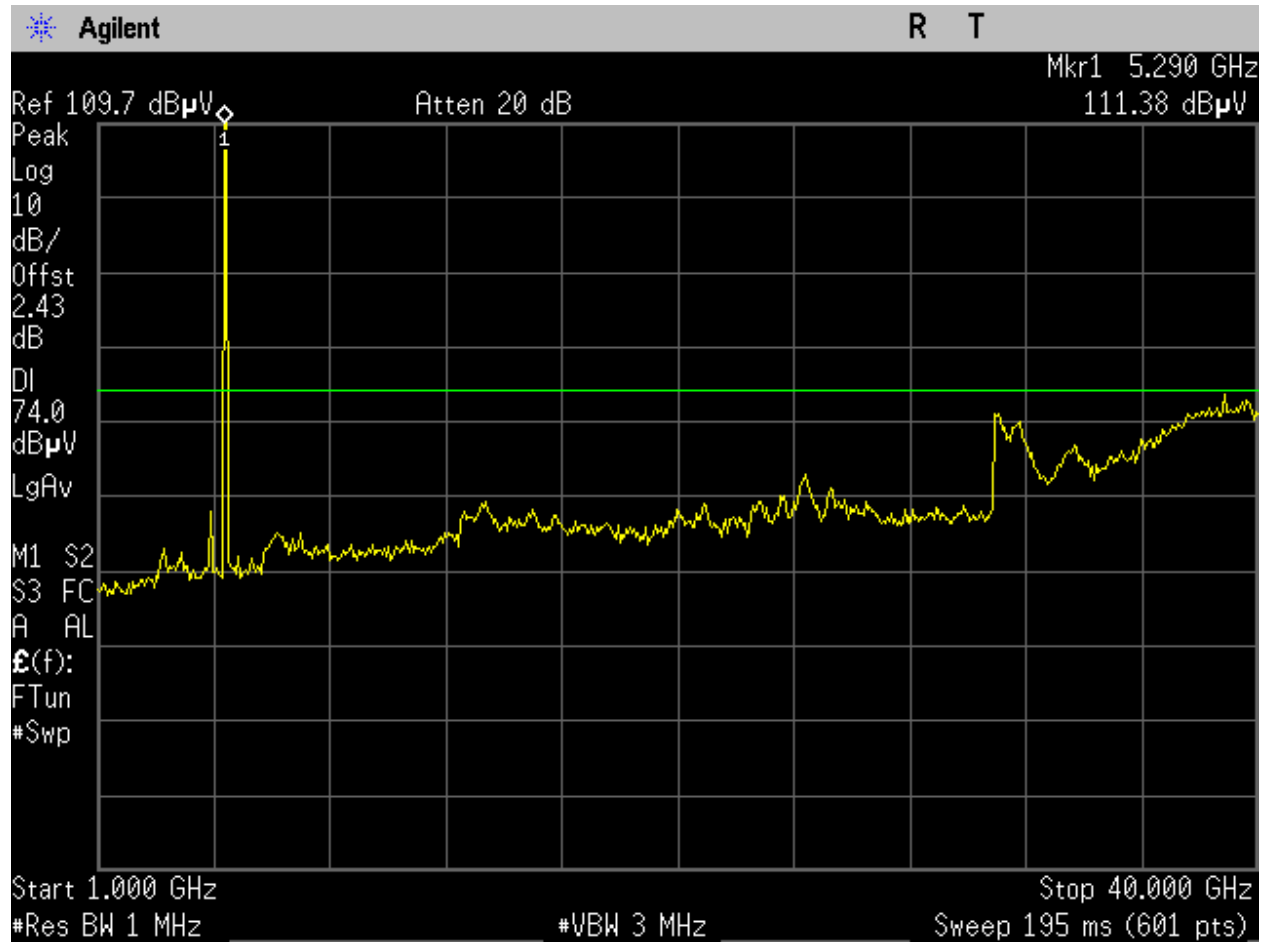


Figure 626: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_1-40GHz_Peak_Port 2.

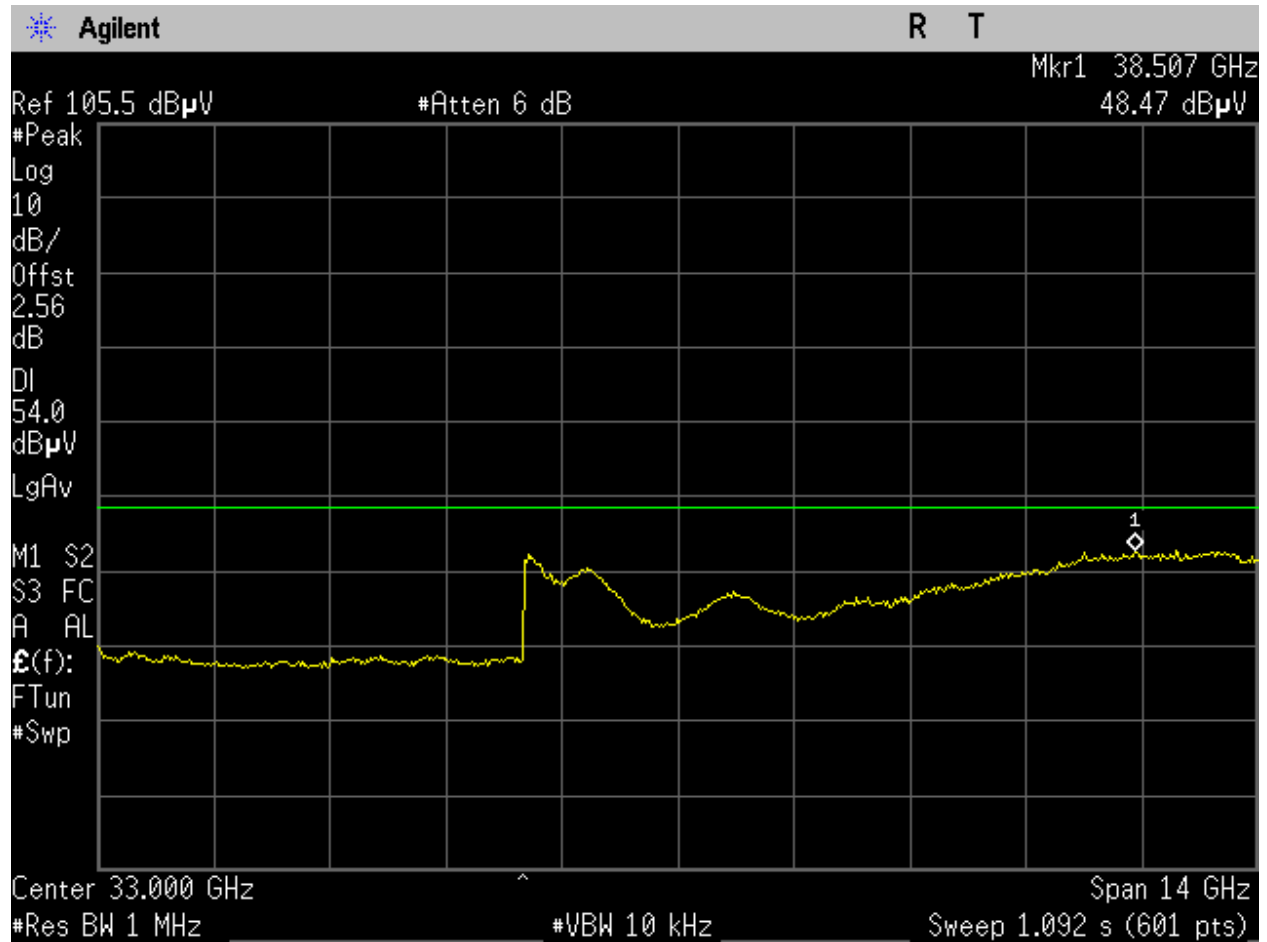


Figure 627: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 1.

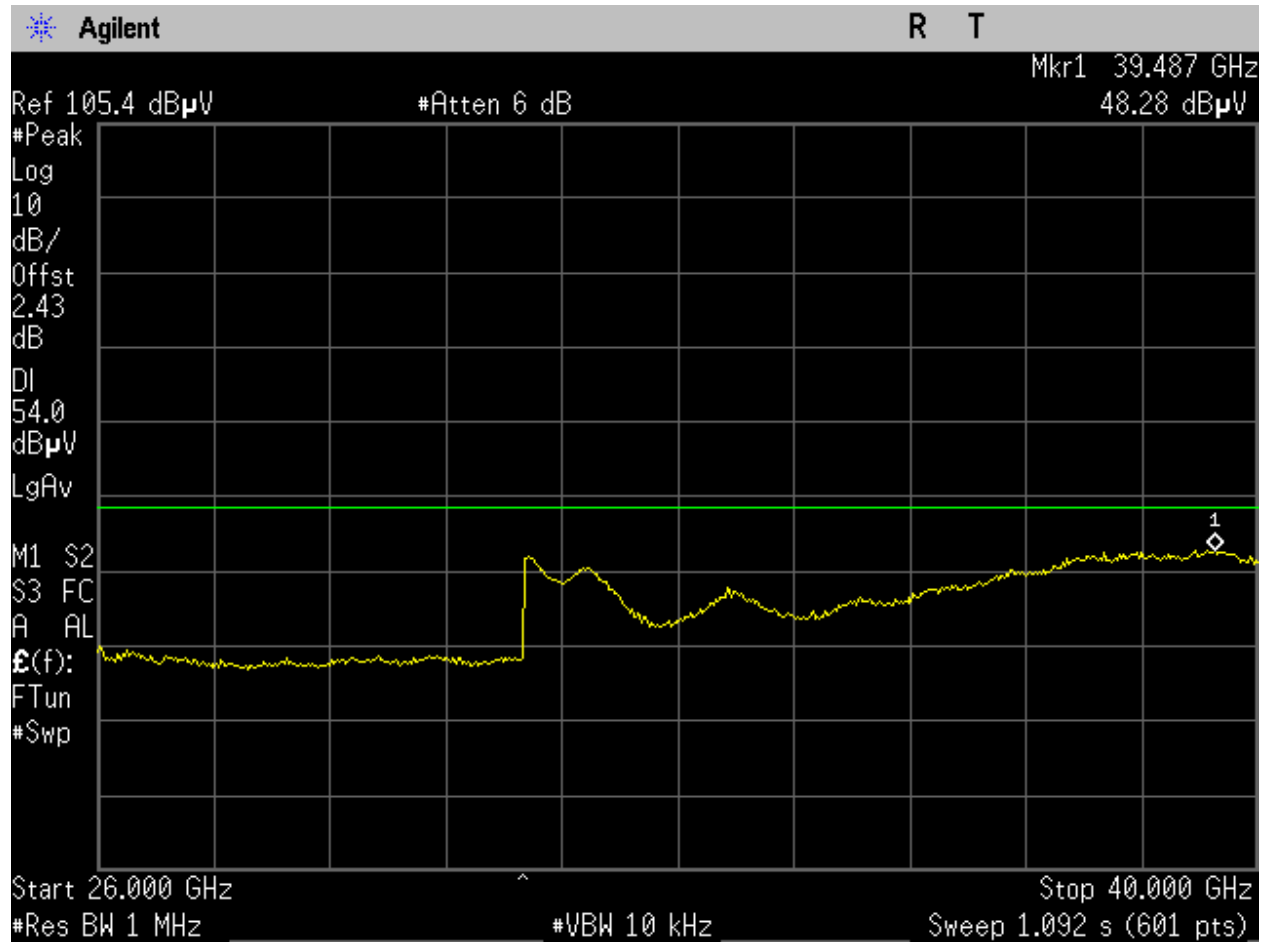


Figure 628: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 2.

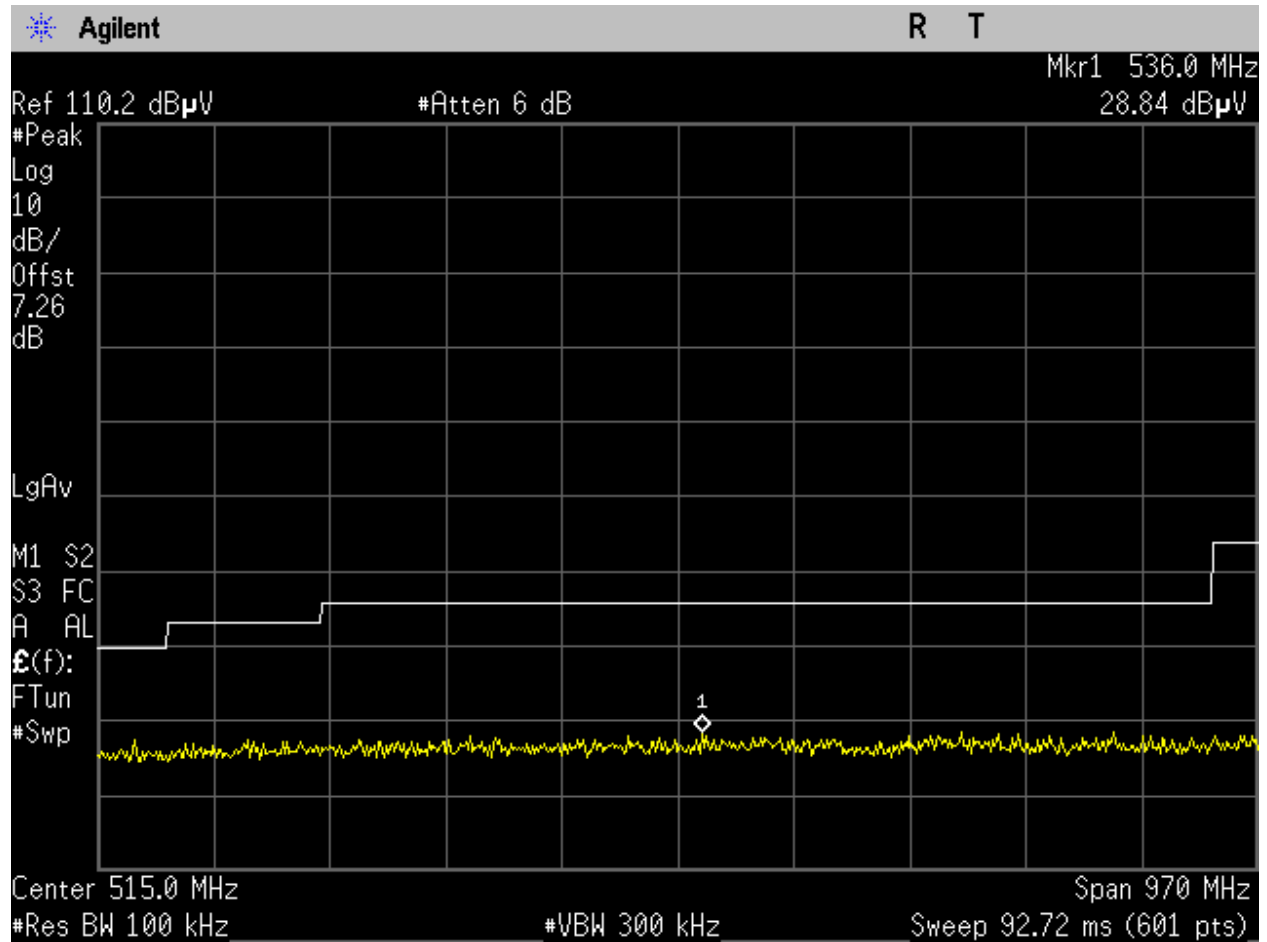


Figure 629: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 1.

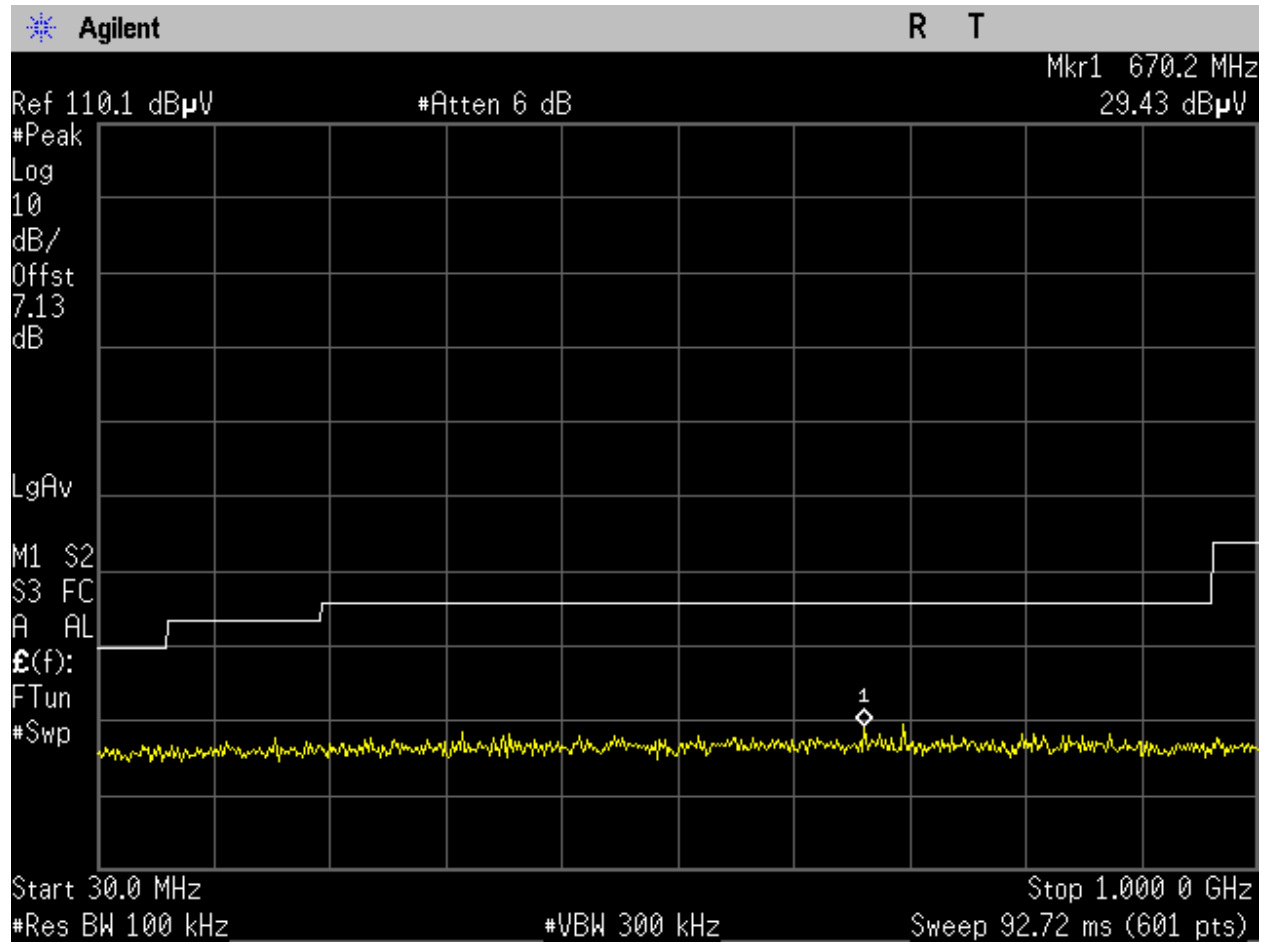


Figure 630: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 2.

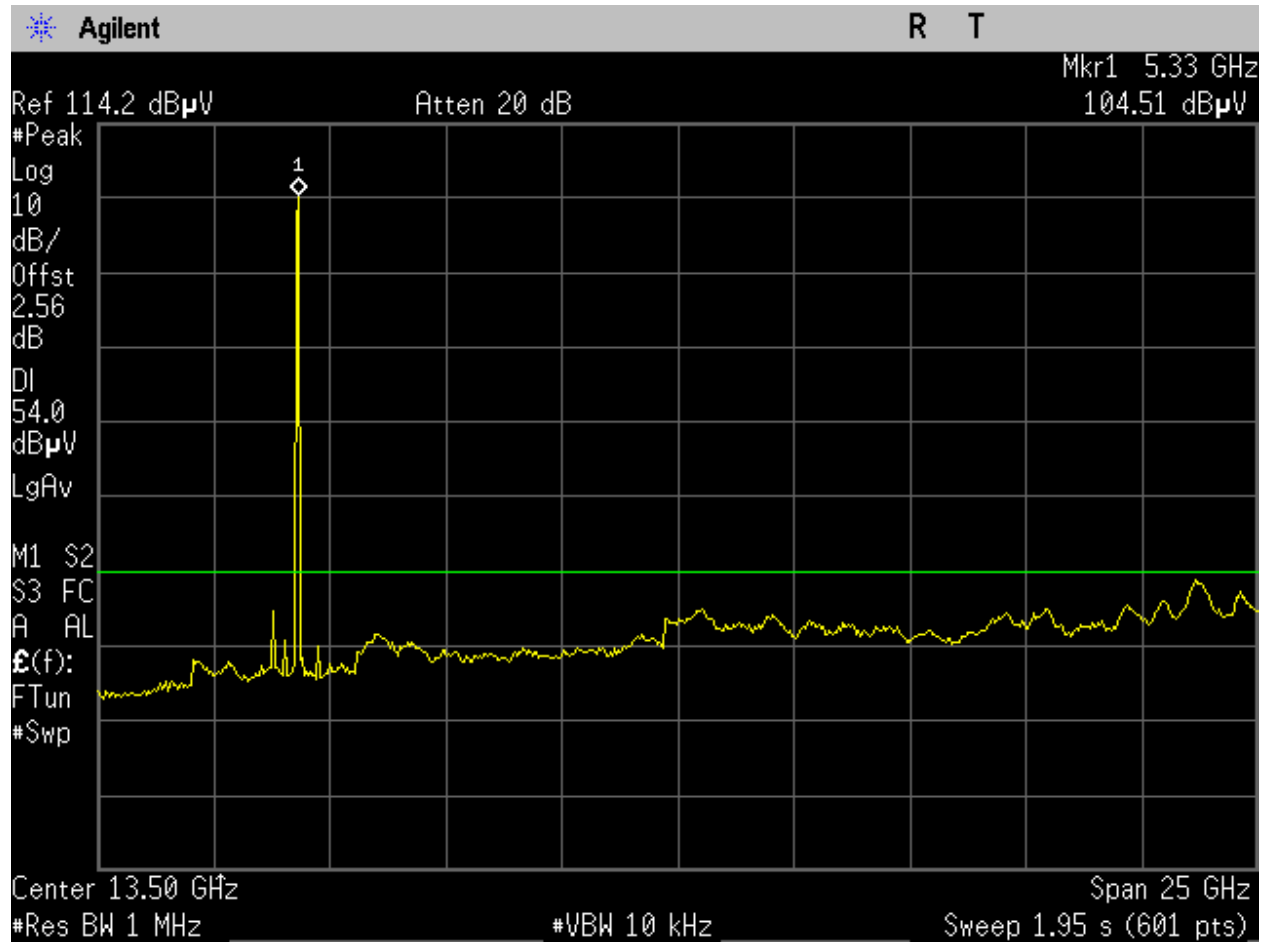


Figure 631: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

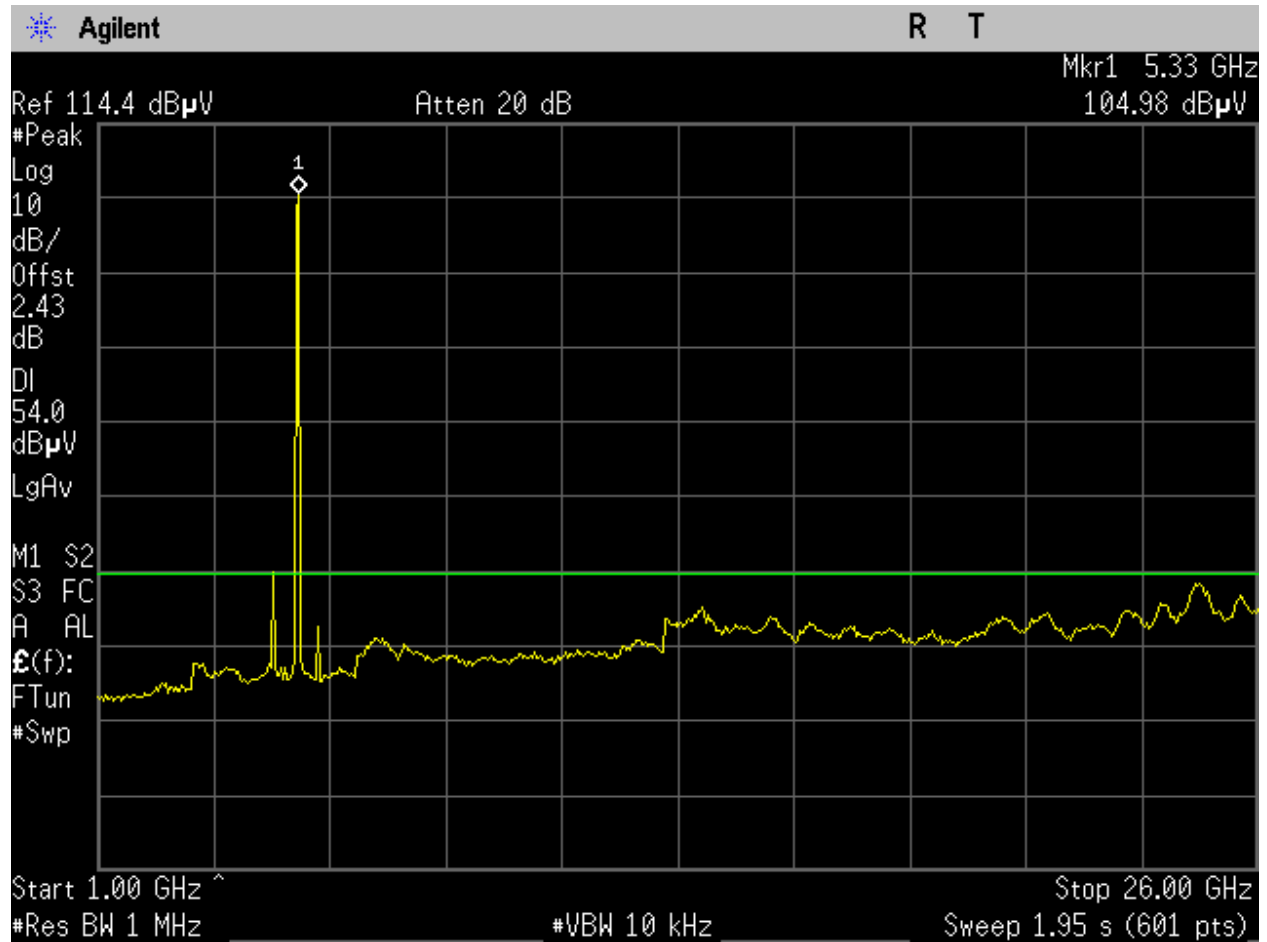


Figure 632: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

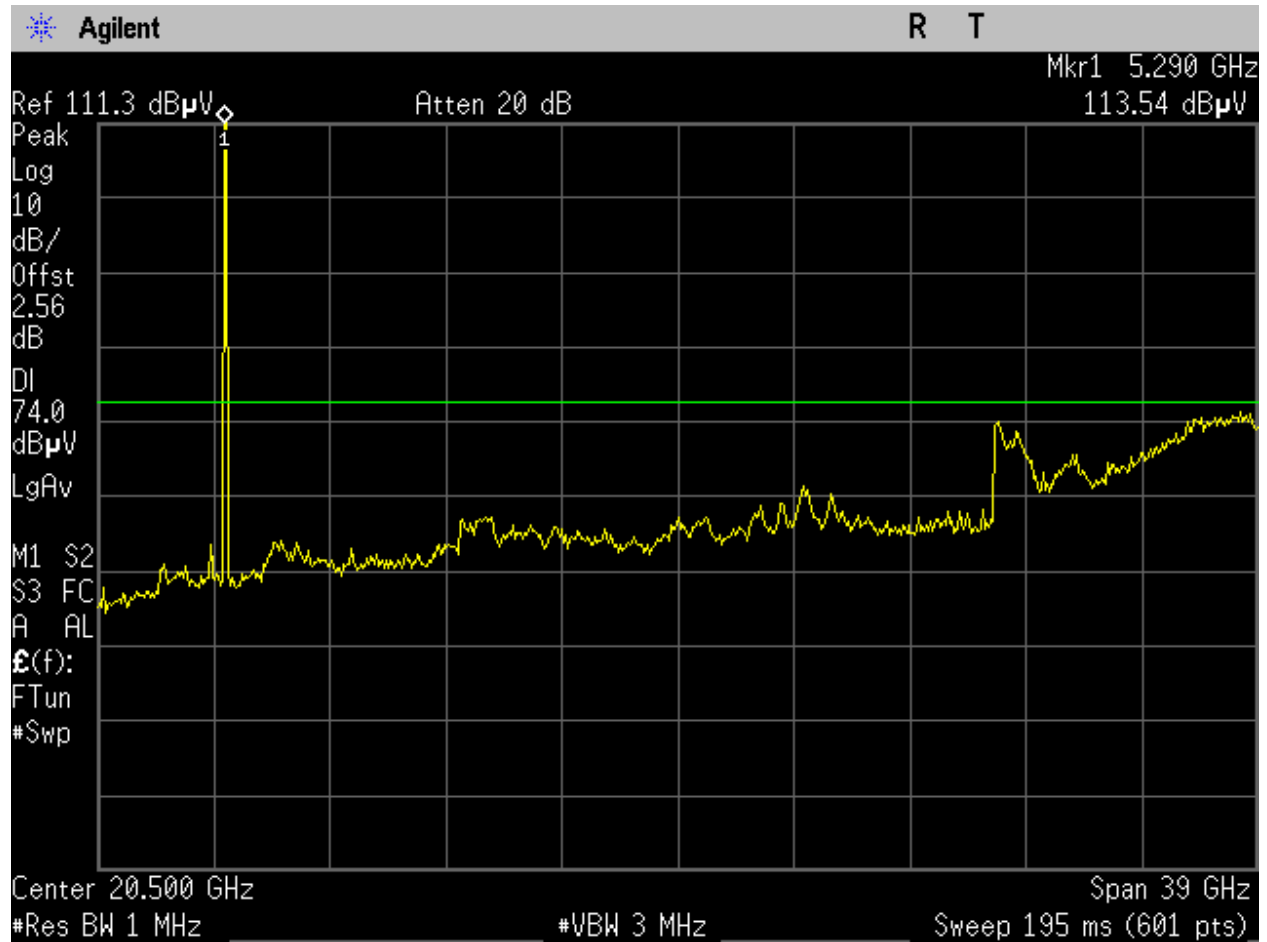


Figure 633: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 1.

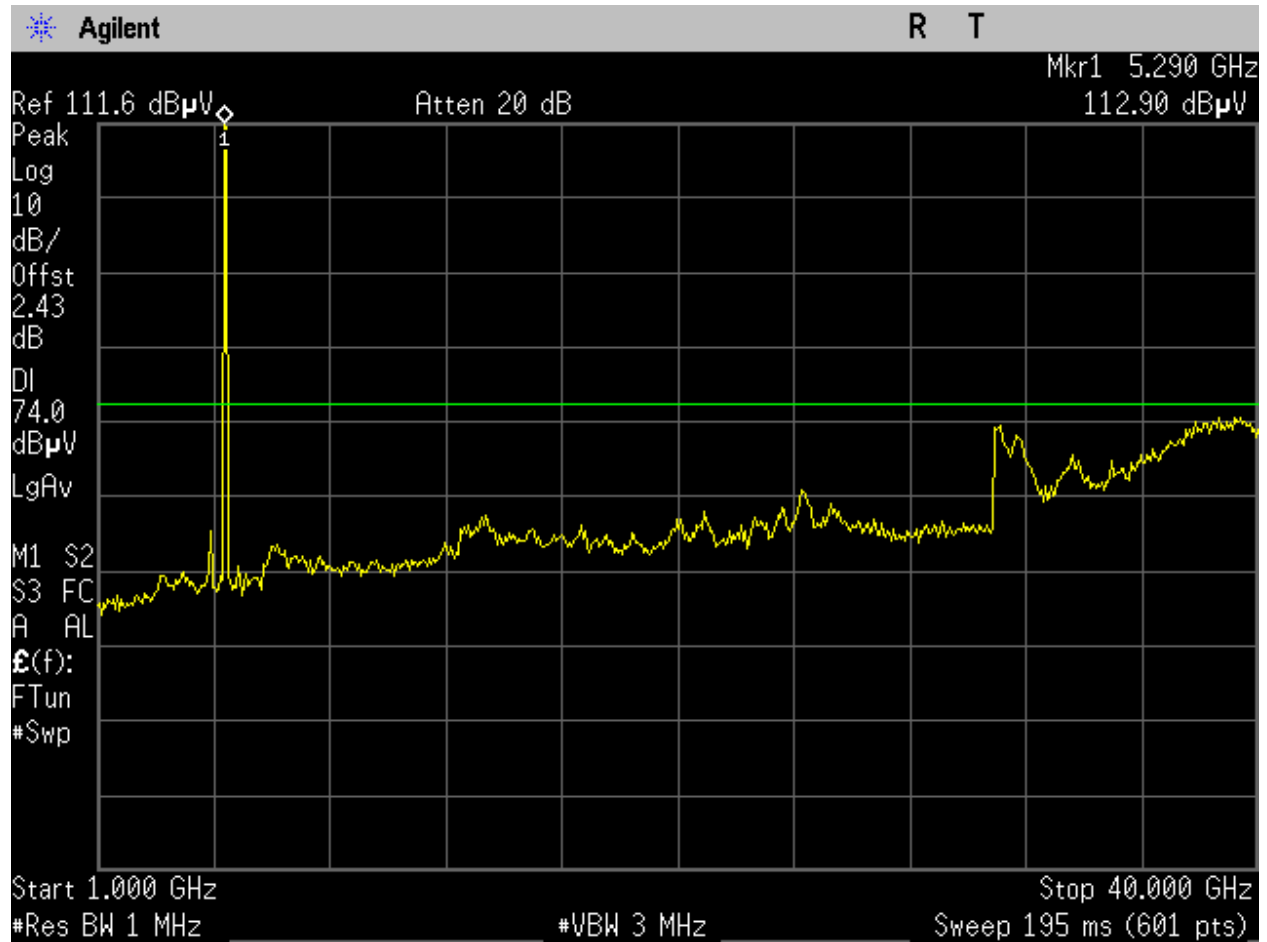


Figure 634: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 2.

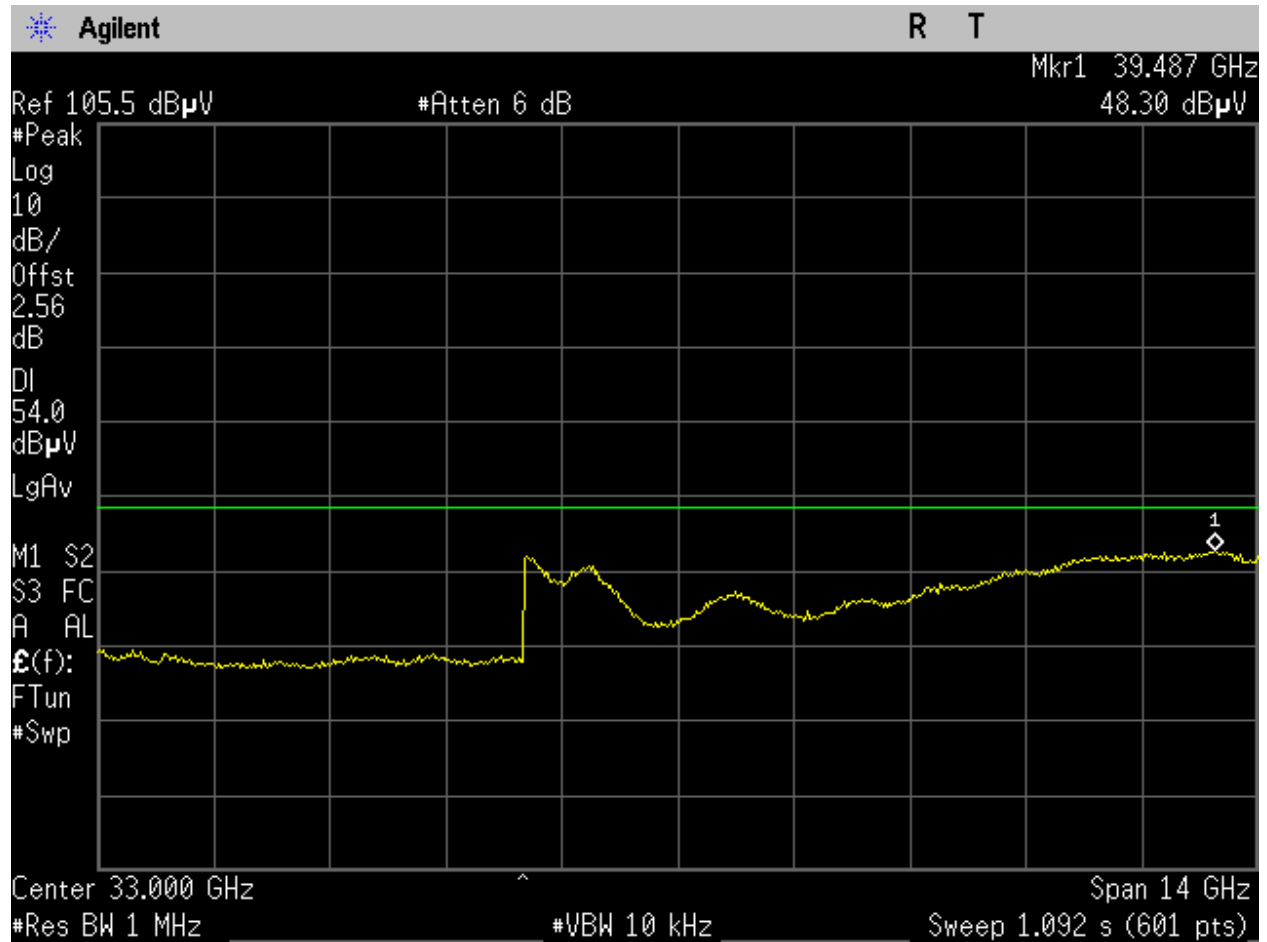


Figure 635: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

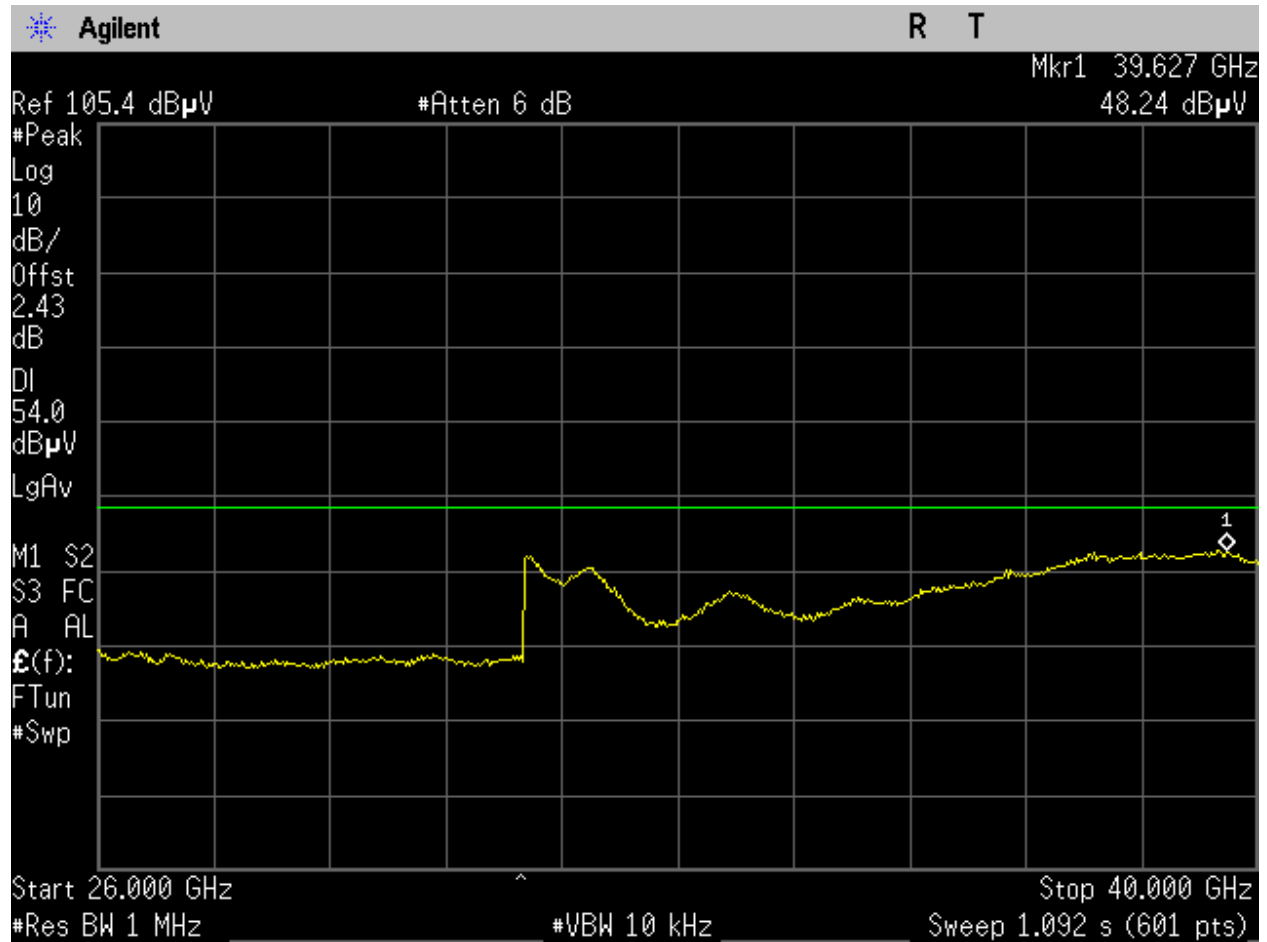


Figure 636: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

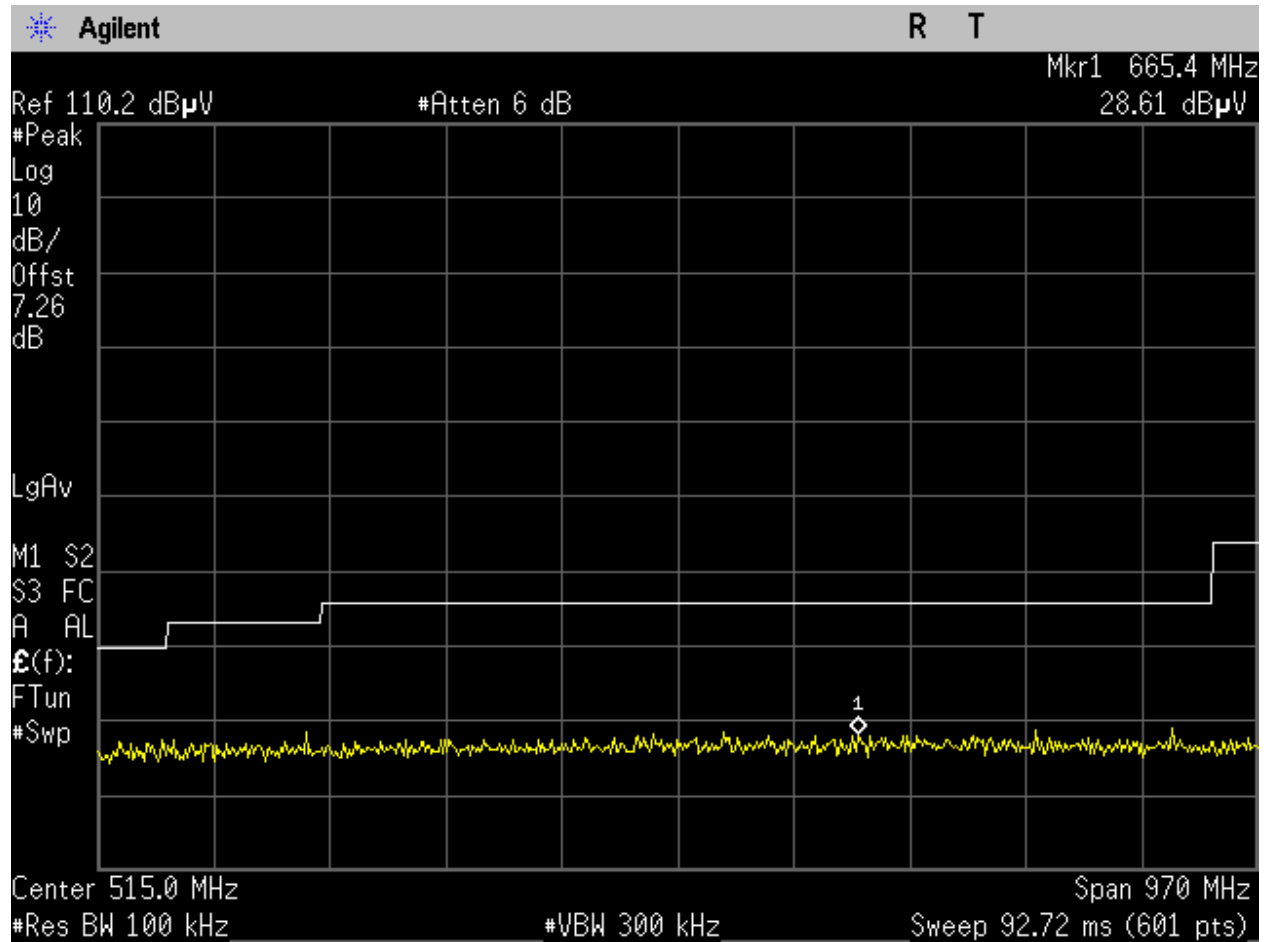


Figure 637: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

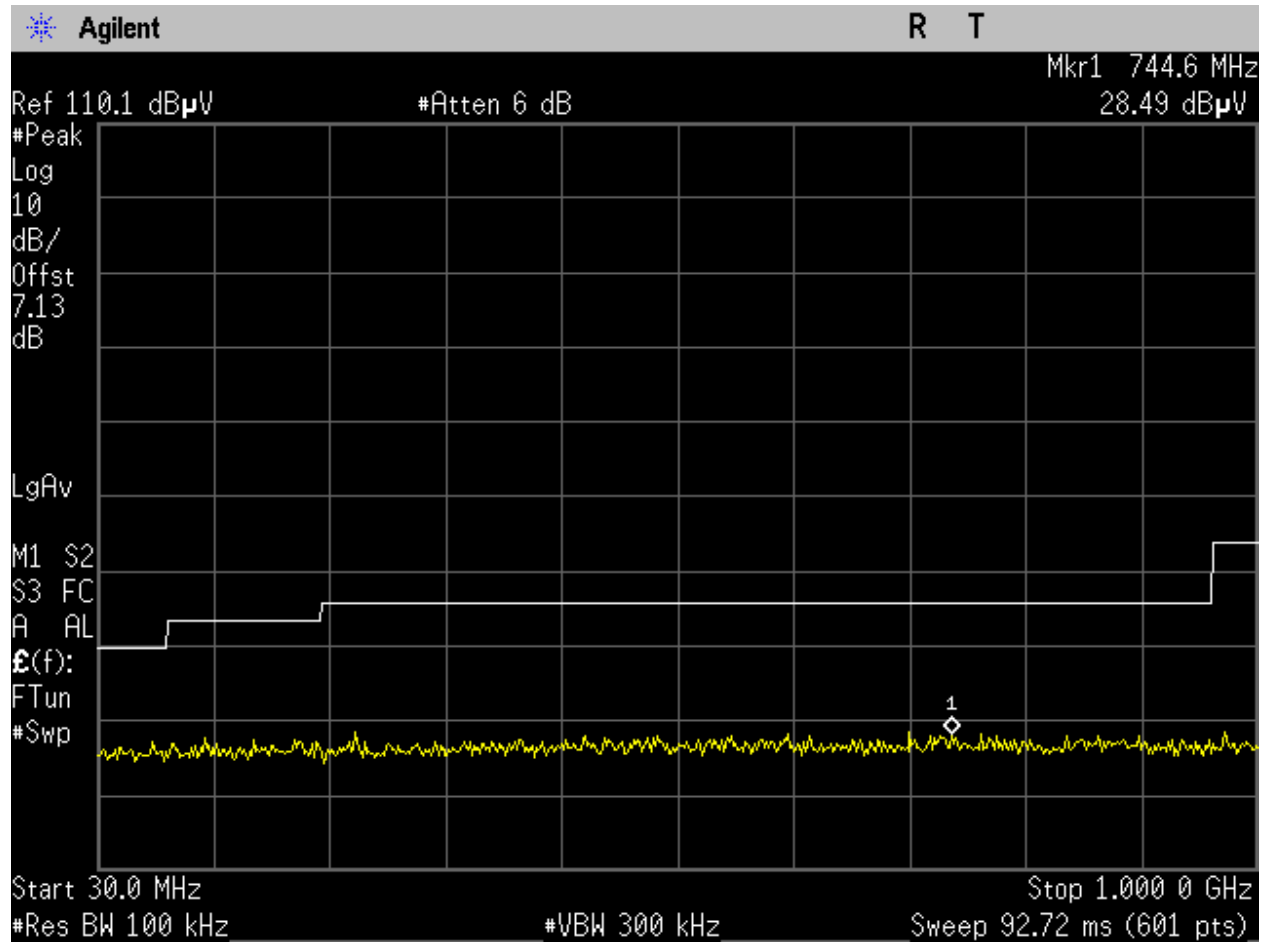


Figure 638: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

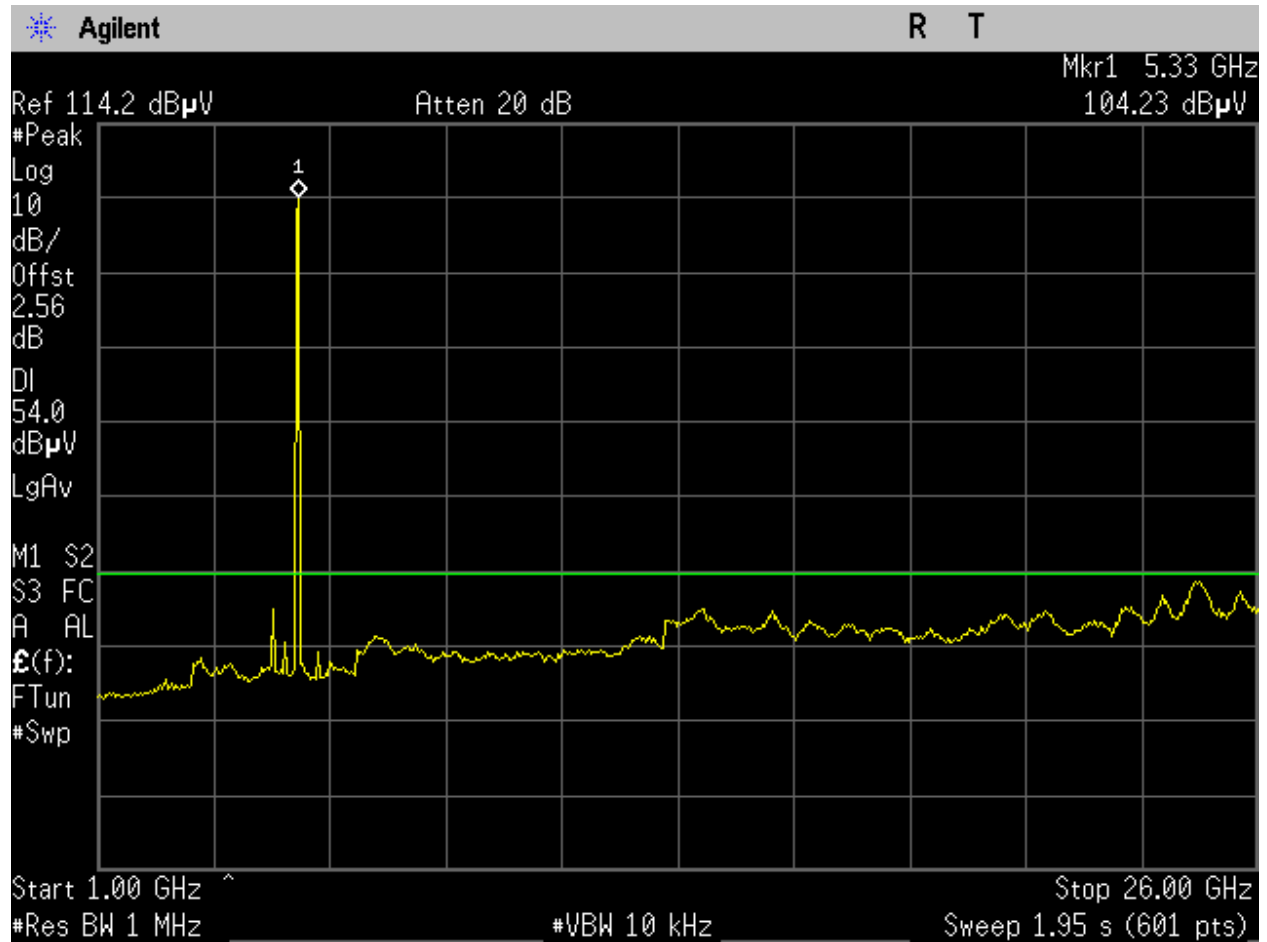


Figure 639: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_1-26GHz avg_Port 1.

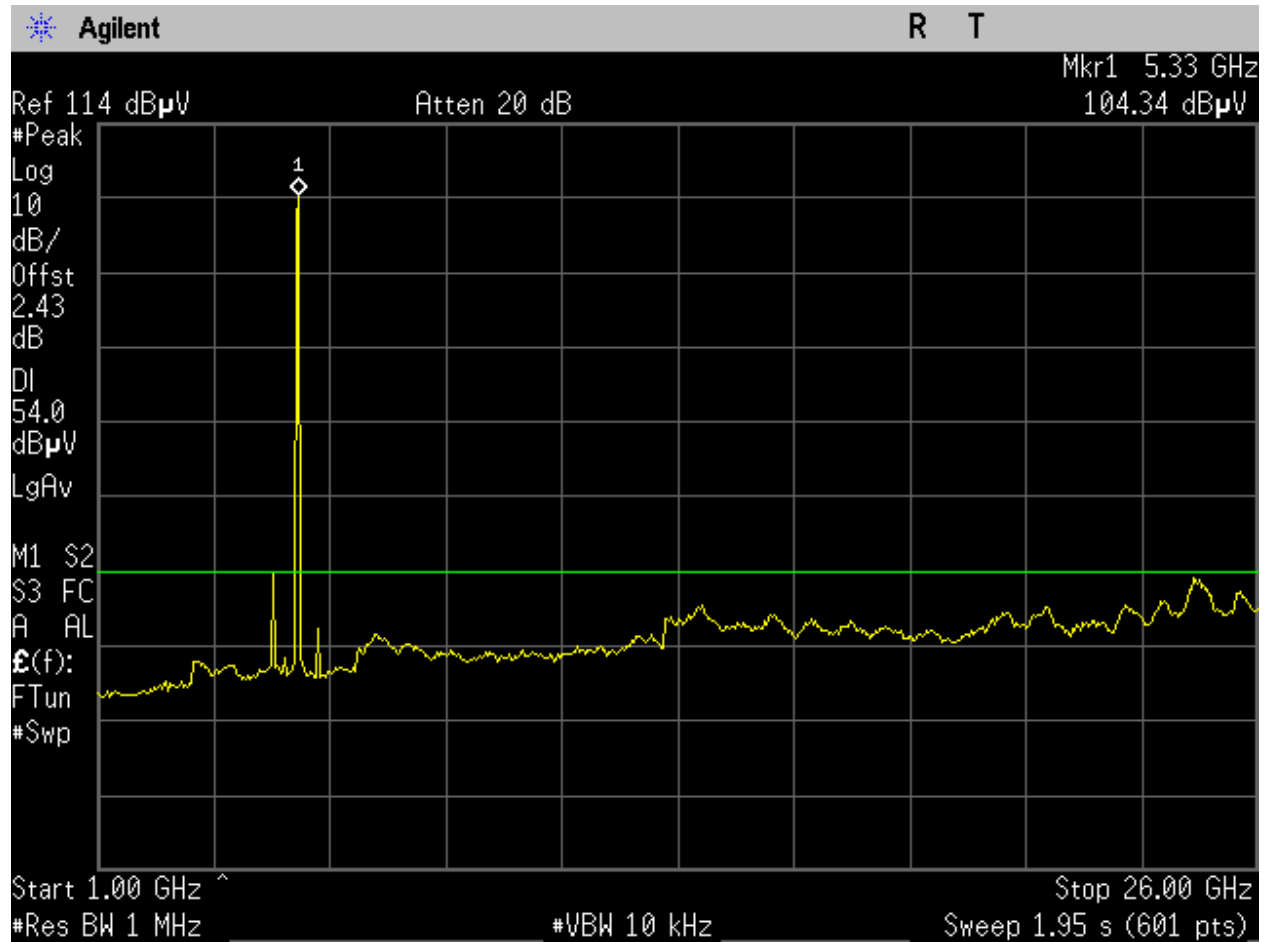


Figure 640: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_1-26GHz avg_Port 2.

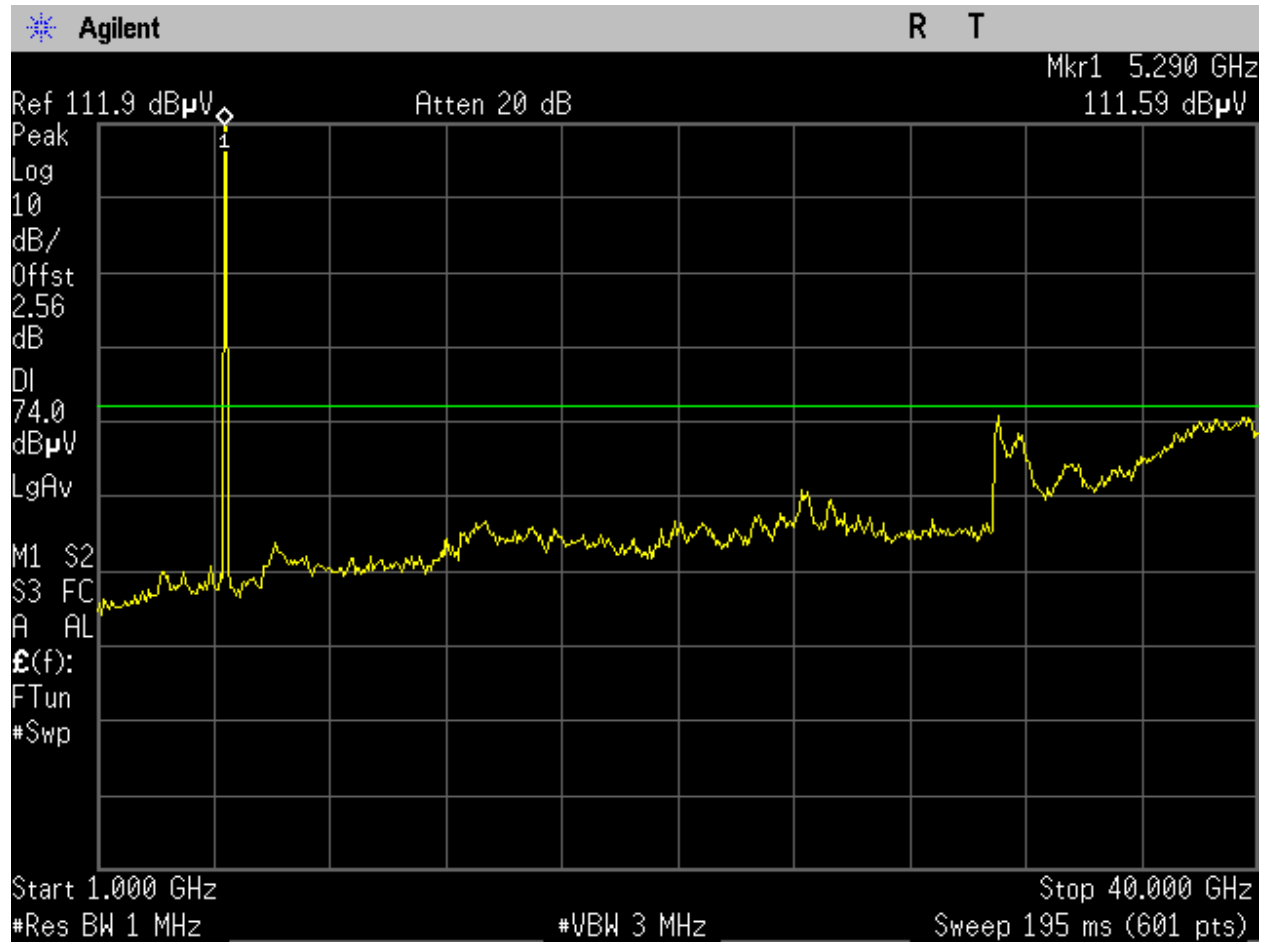


Figure 641: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 1.

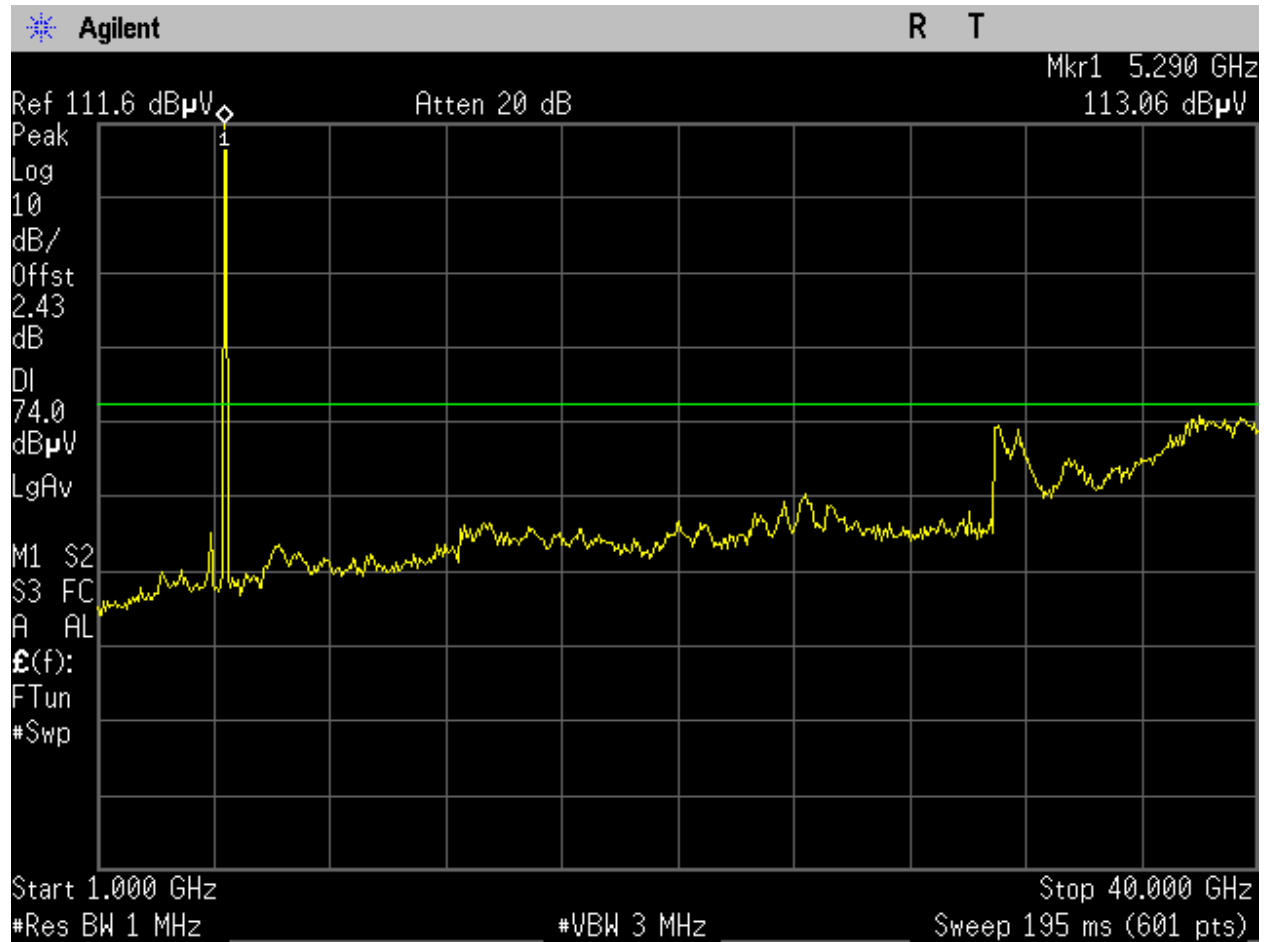


Figure 642: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 2.

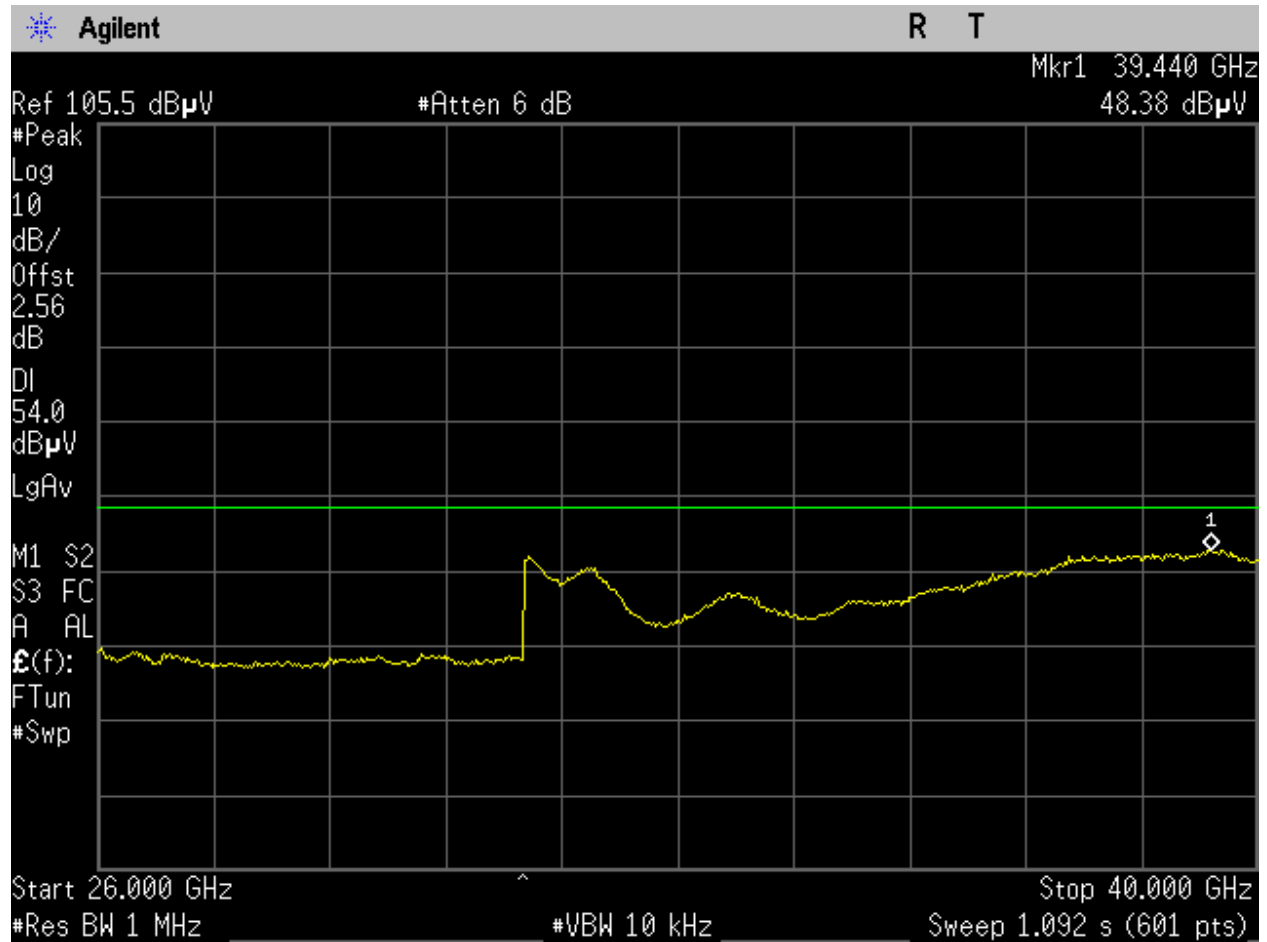


Figure 643: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 1.

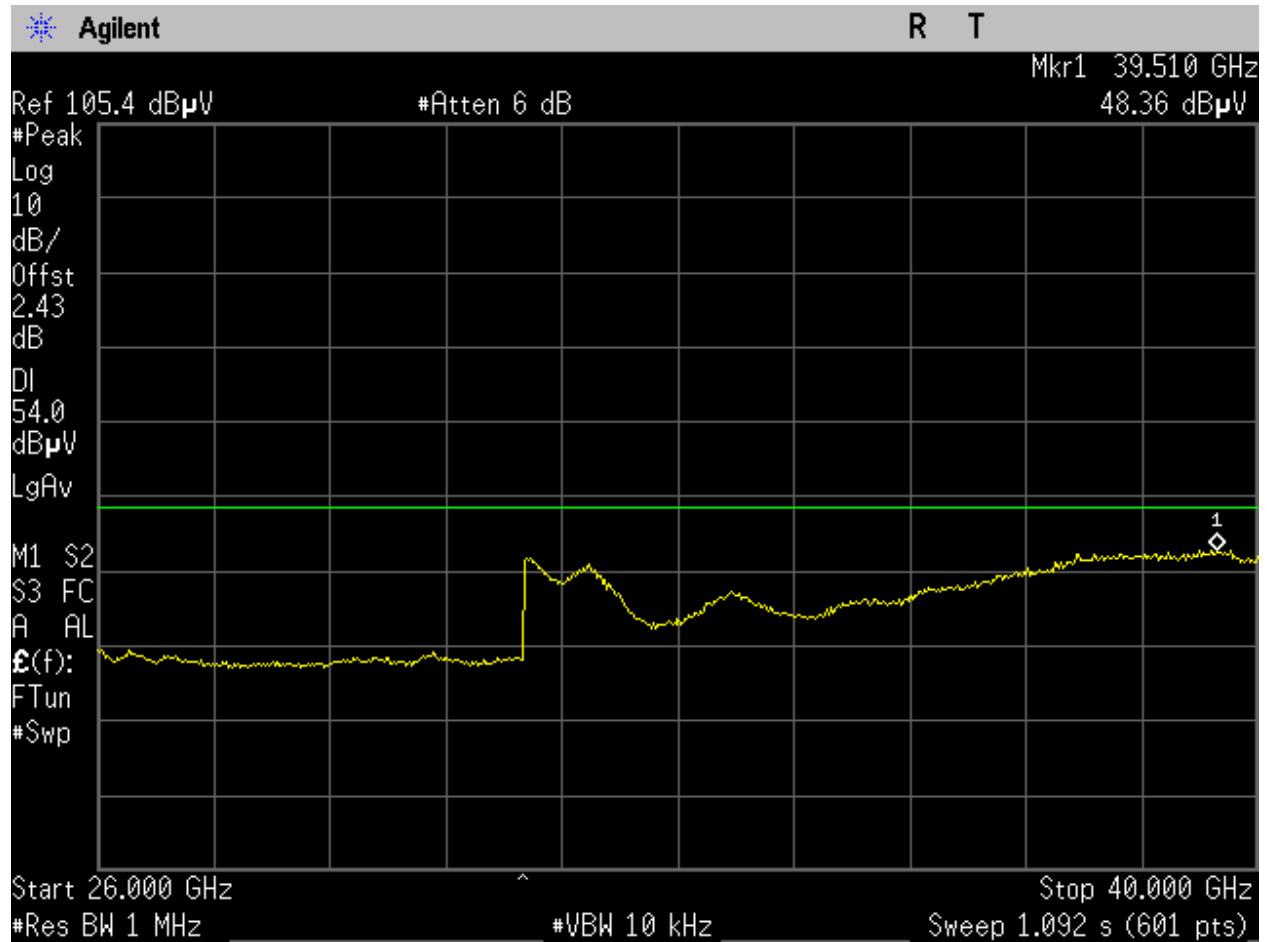


Figure 644: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 2.

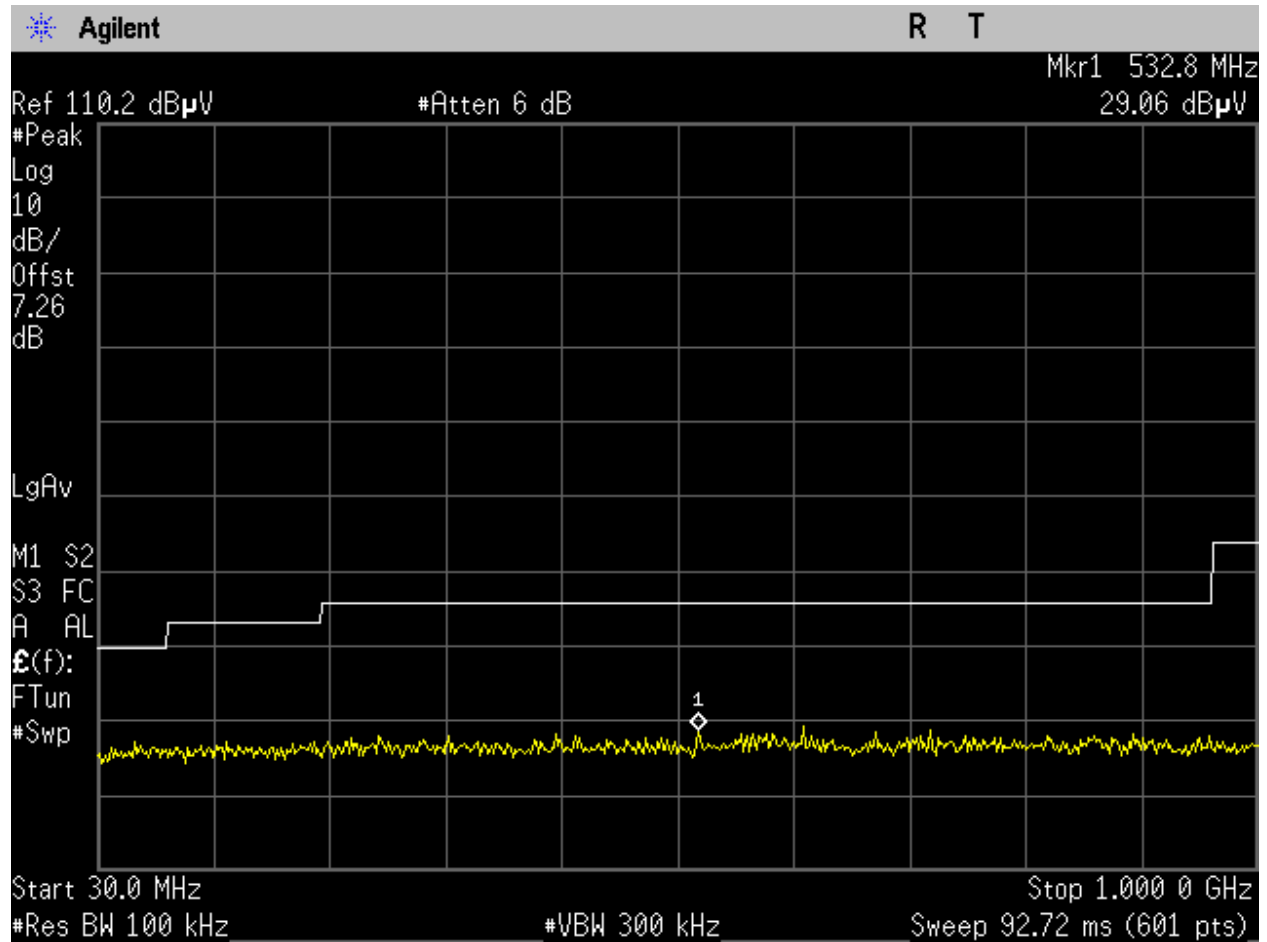


Figure 645: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 1.

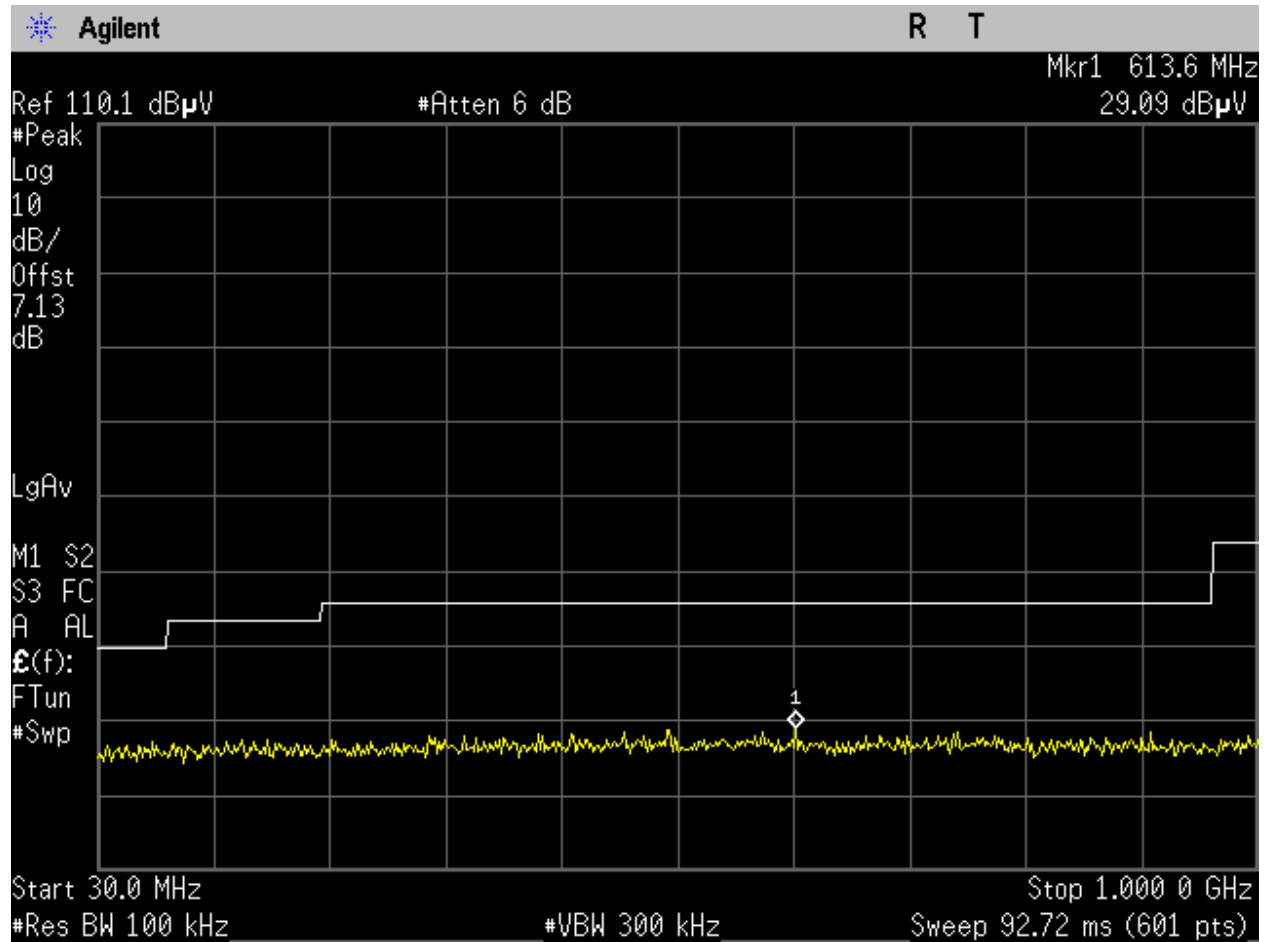


Figure 646: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 2.

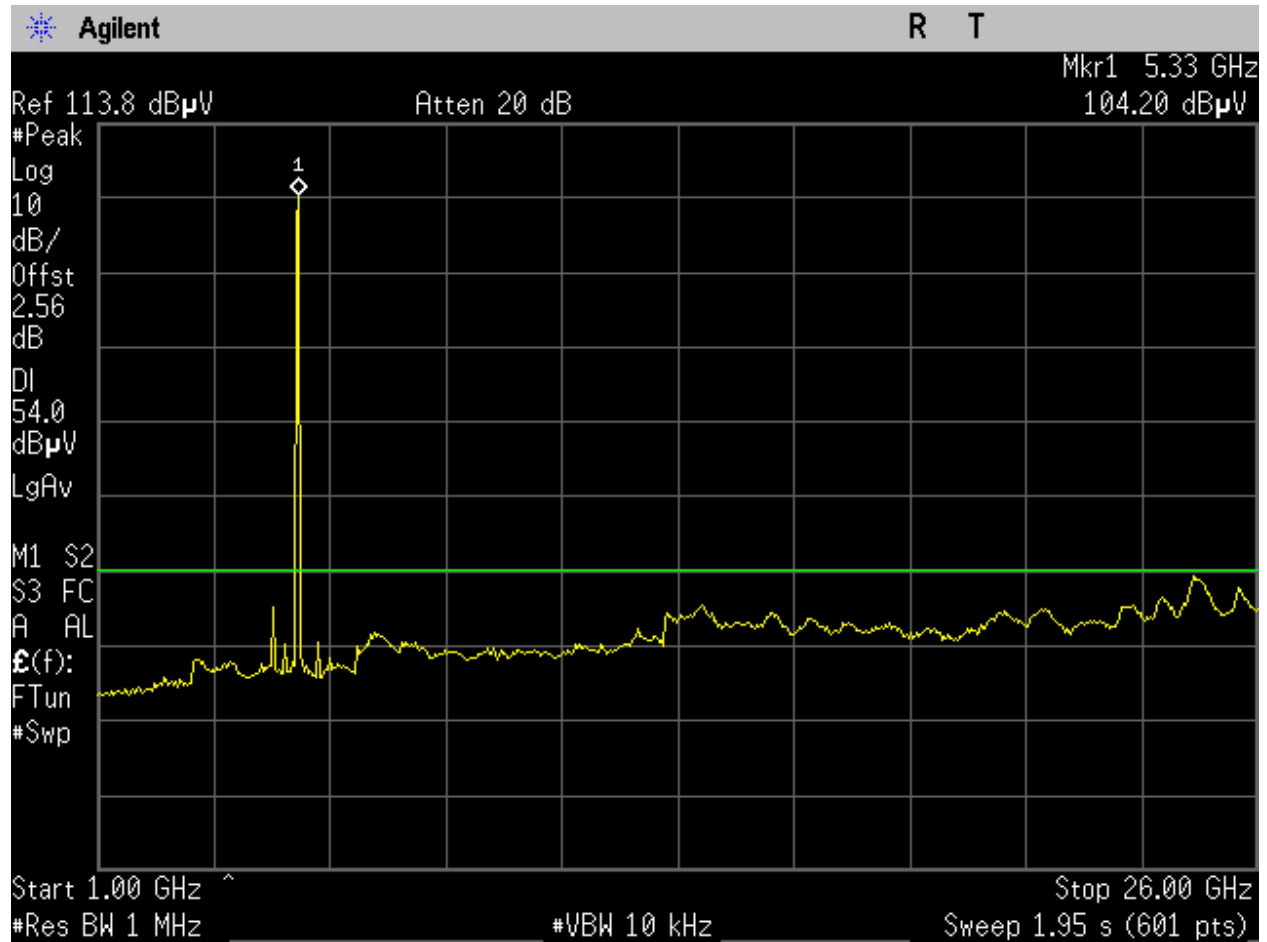


Figure 647: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_1-26GHz avg_Port 1.

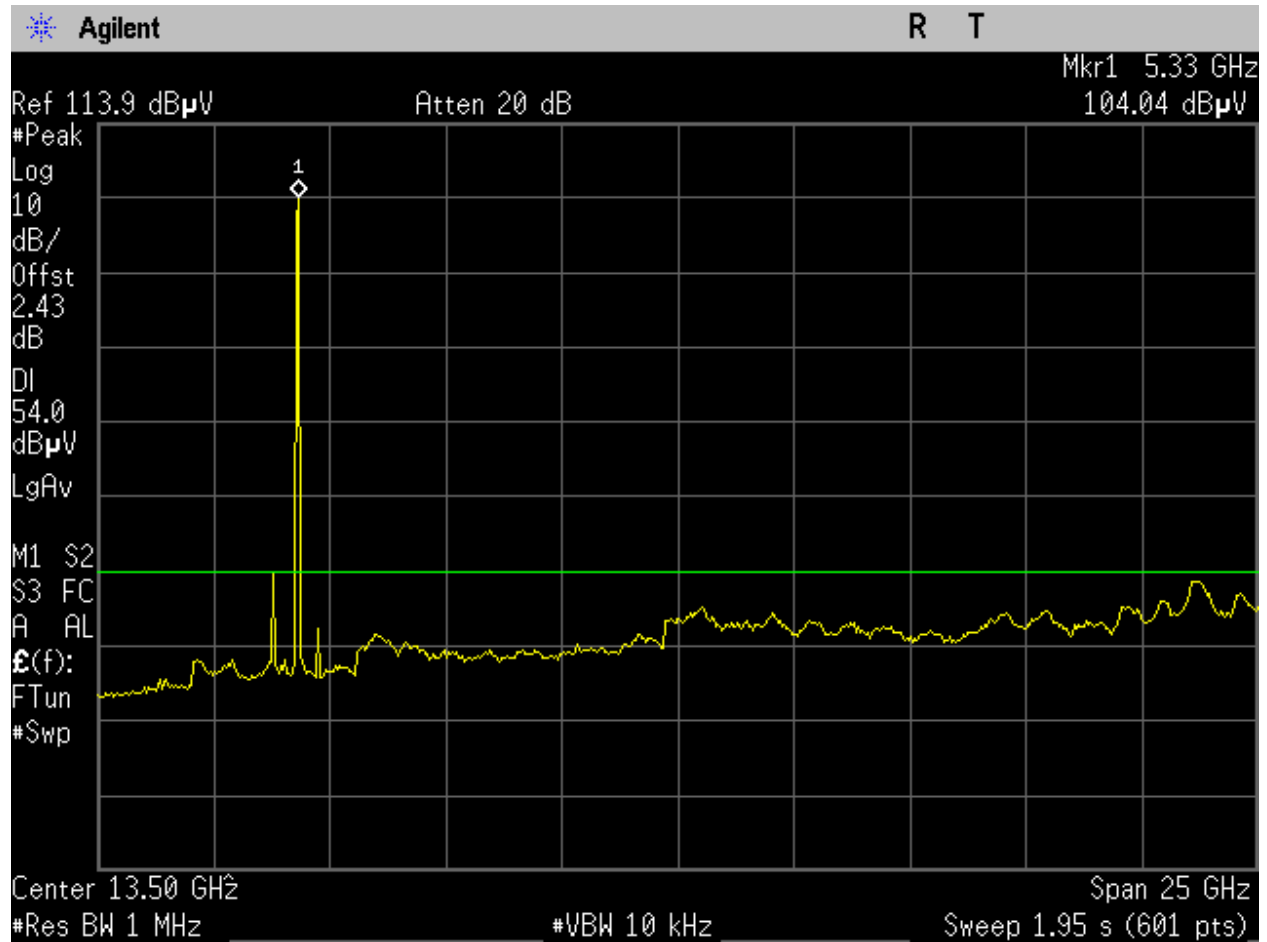


Figure 648: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_1-26GHz avg_Port 2.

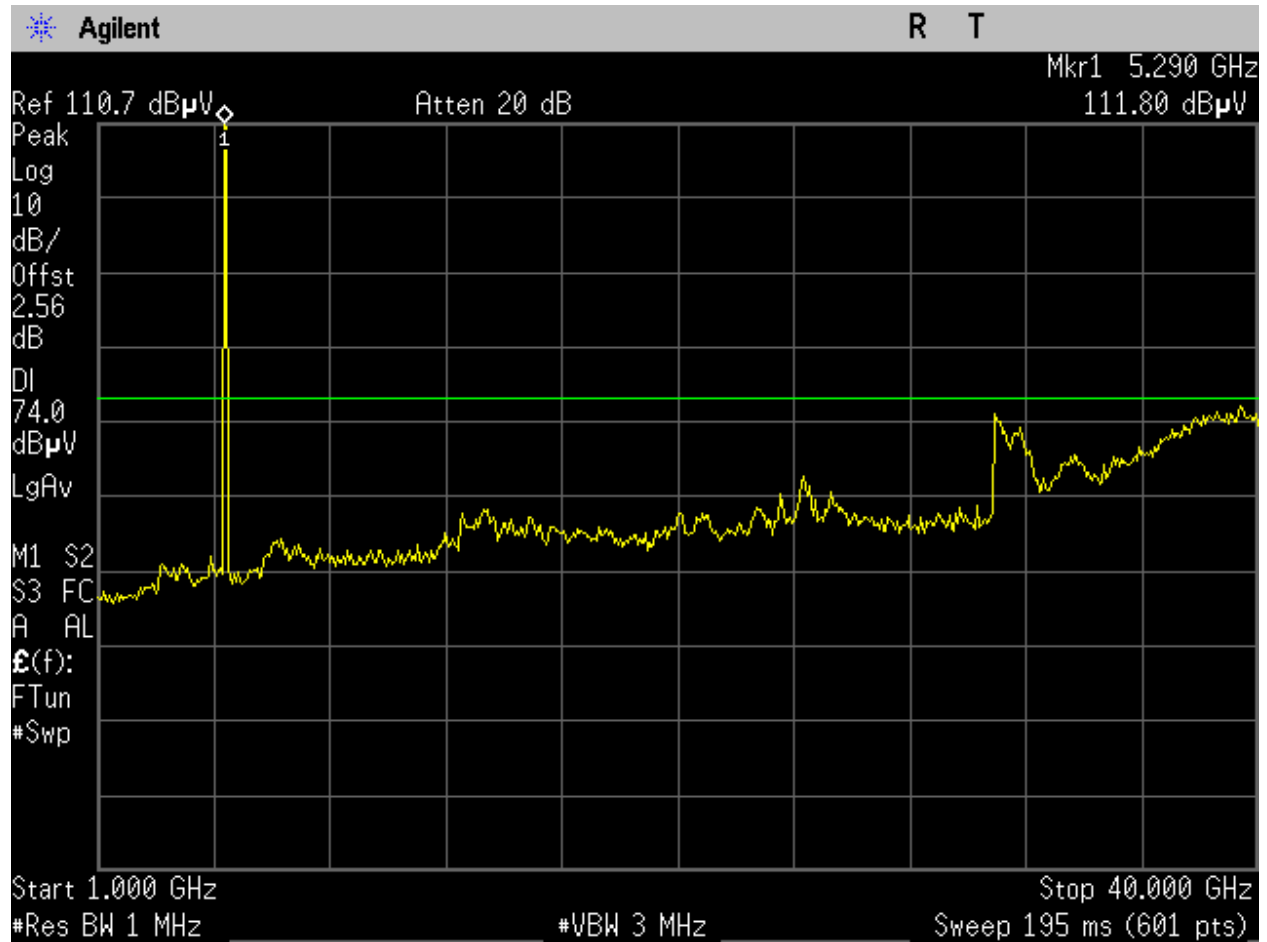


Figure 649: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_1-40GHz_Peak_Port 1.

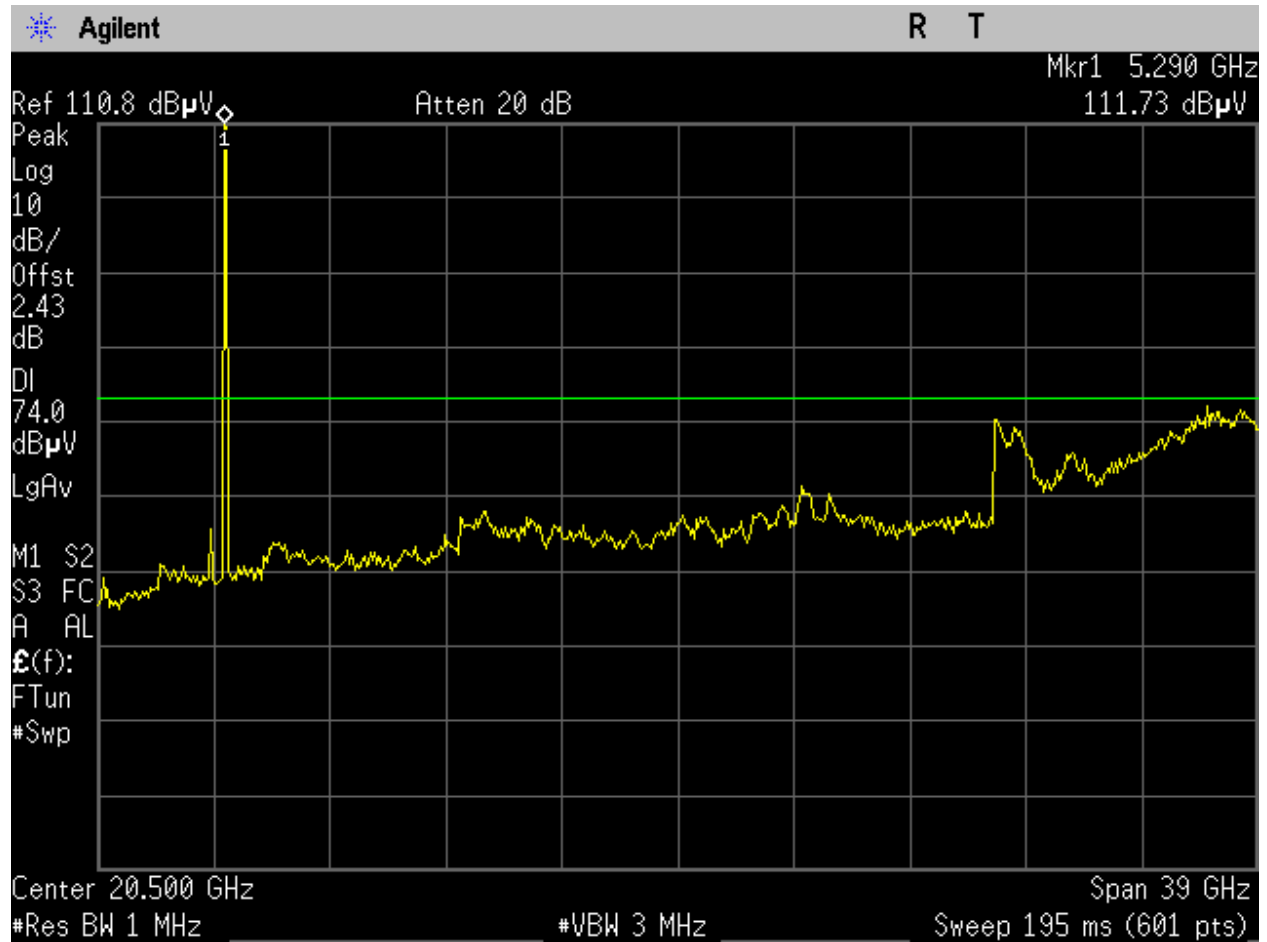


Figure 650: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_1-40GHz_Peak_Port 2.

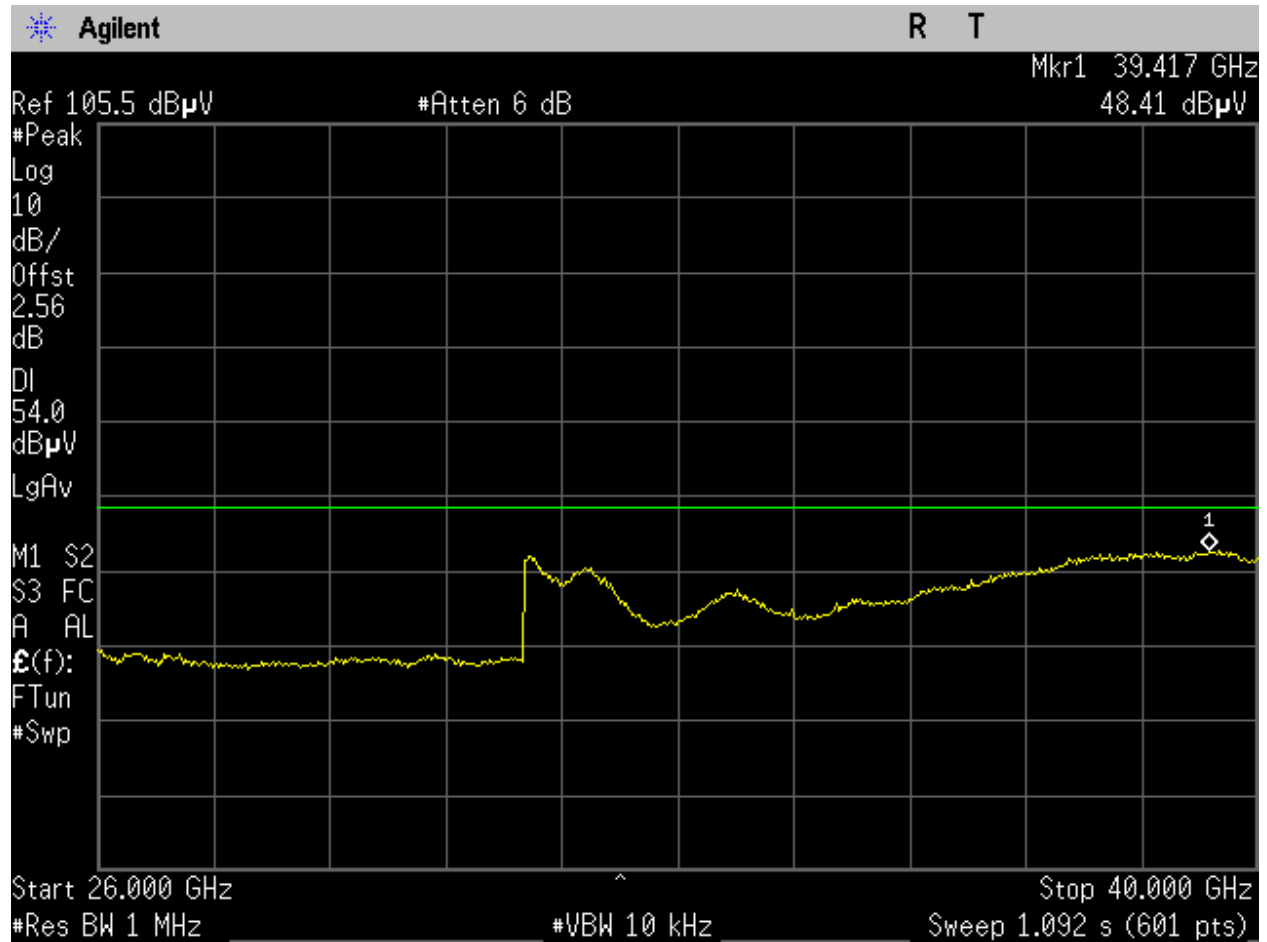


Figure 651: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_26-40GHz_Avg_Port 1.

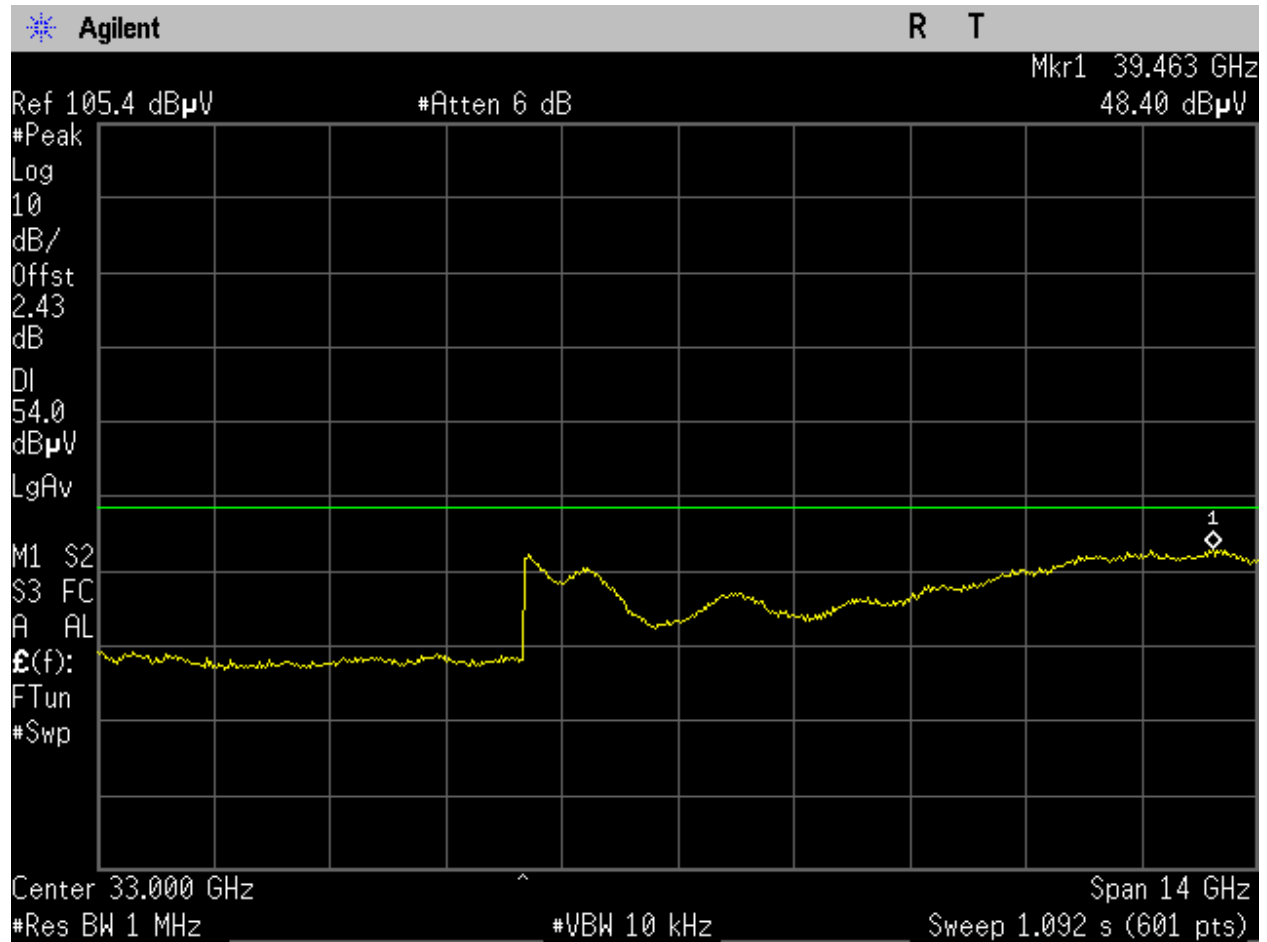


Figure 652: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_26-40GHz_Avg_Port 2.

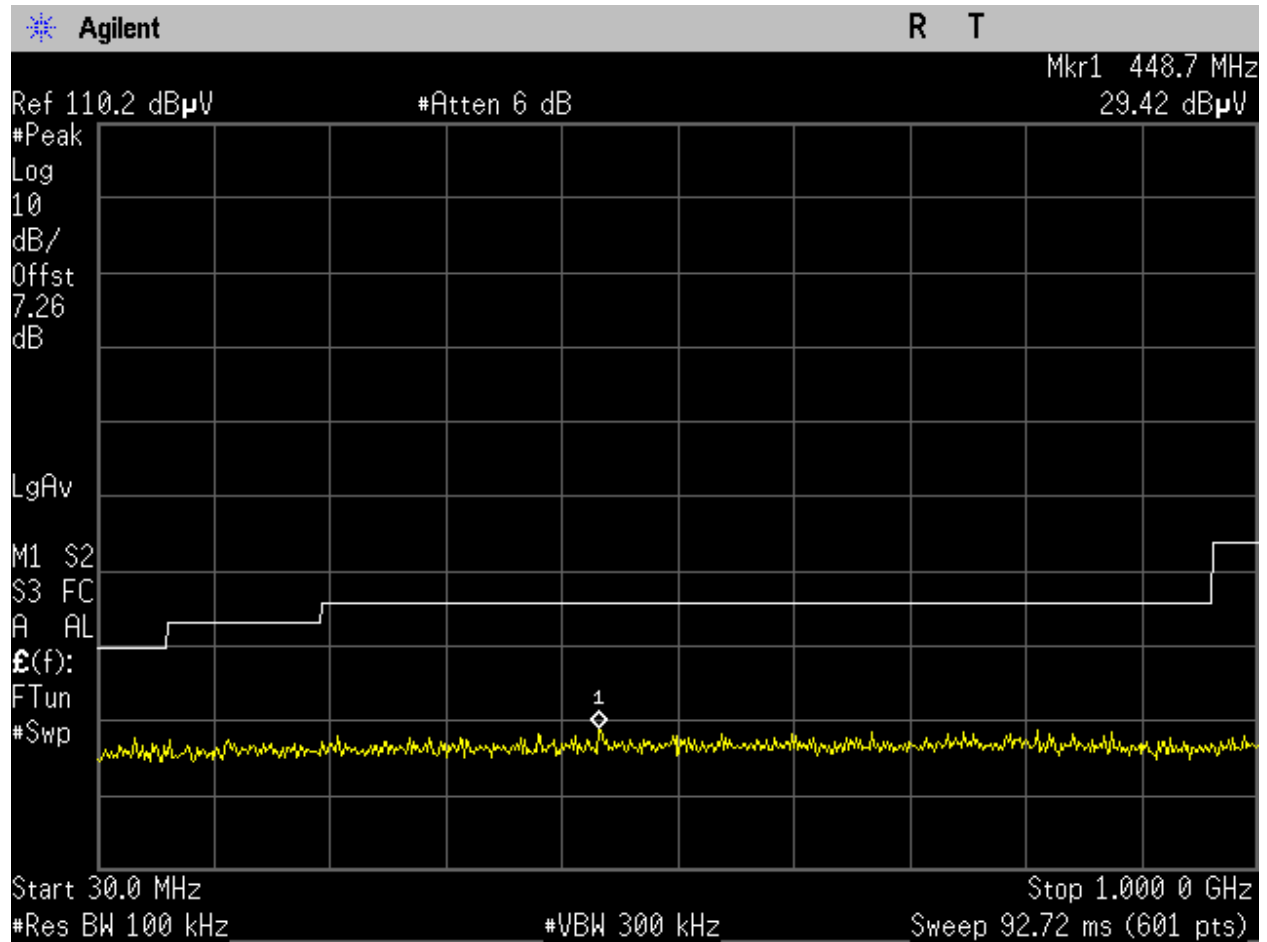


Figure 653: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 1.

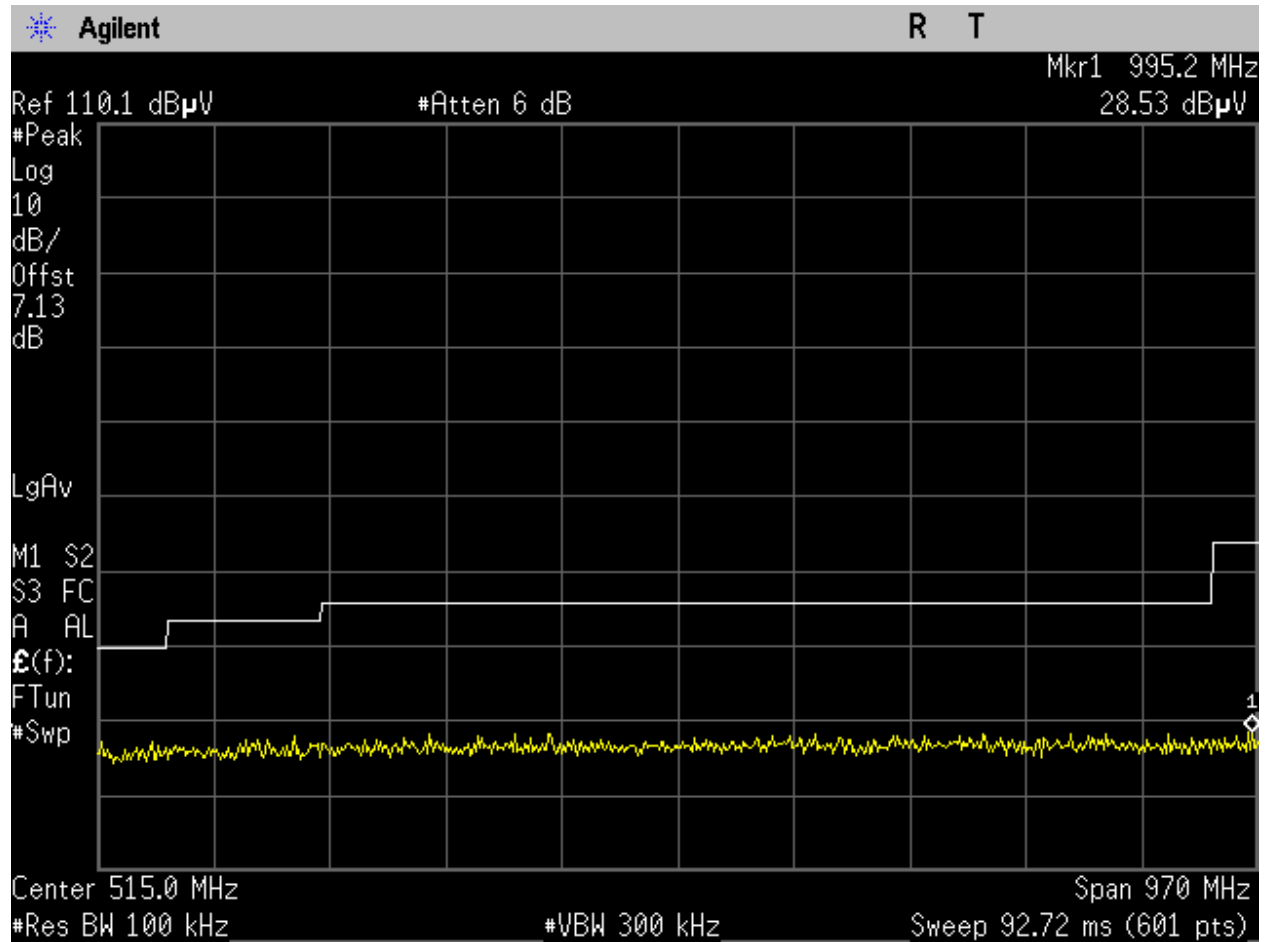


Figure 654: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 2.

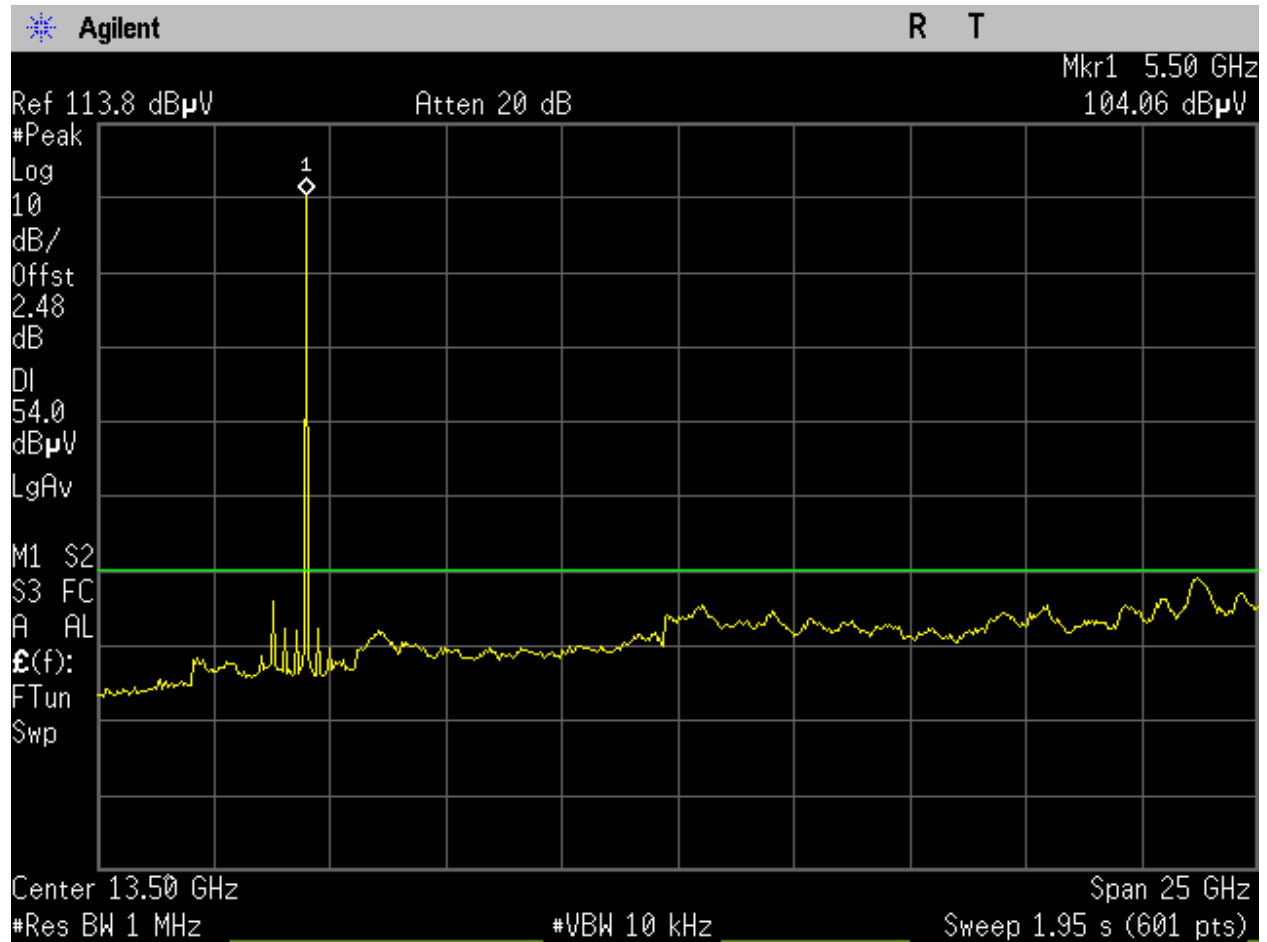


Figure 655: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_1-26GHz avg_Port 1.

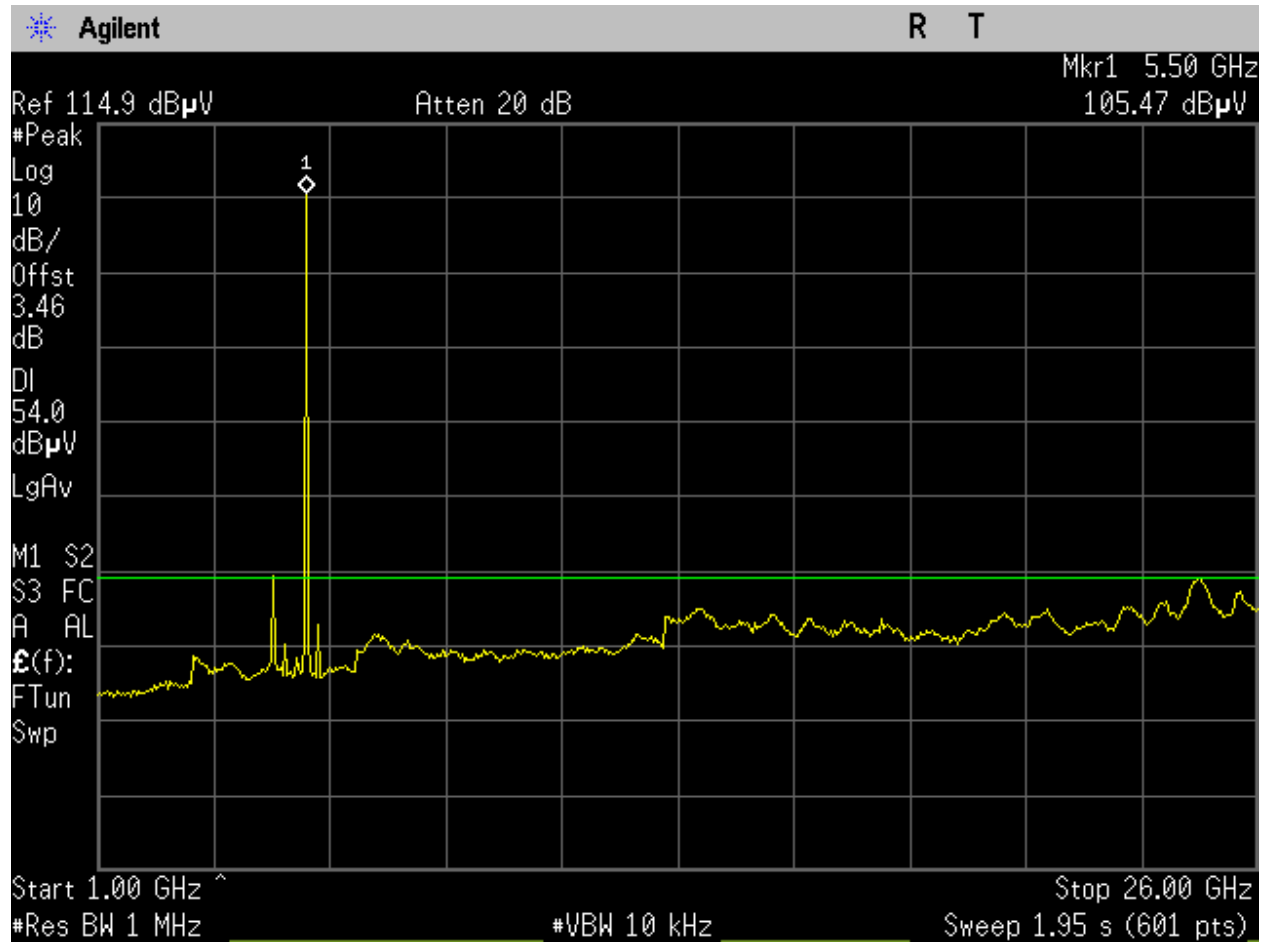


Figure 656: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_1-26GHz avg_Port 2.

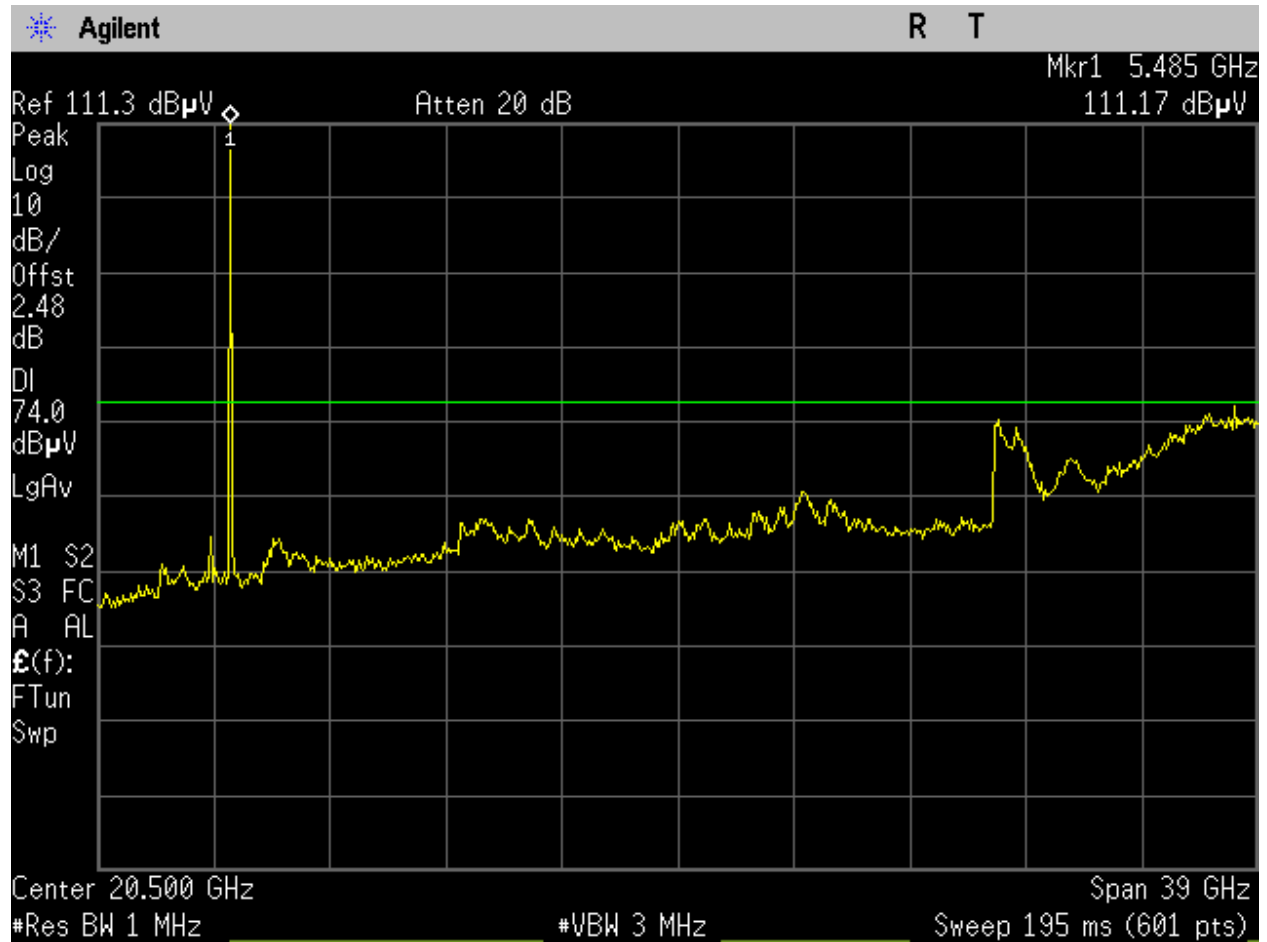


Figure 657: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_1-40GHz_Peak_Port 1.

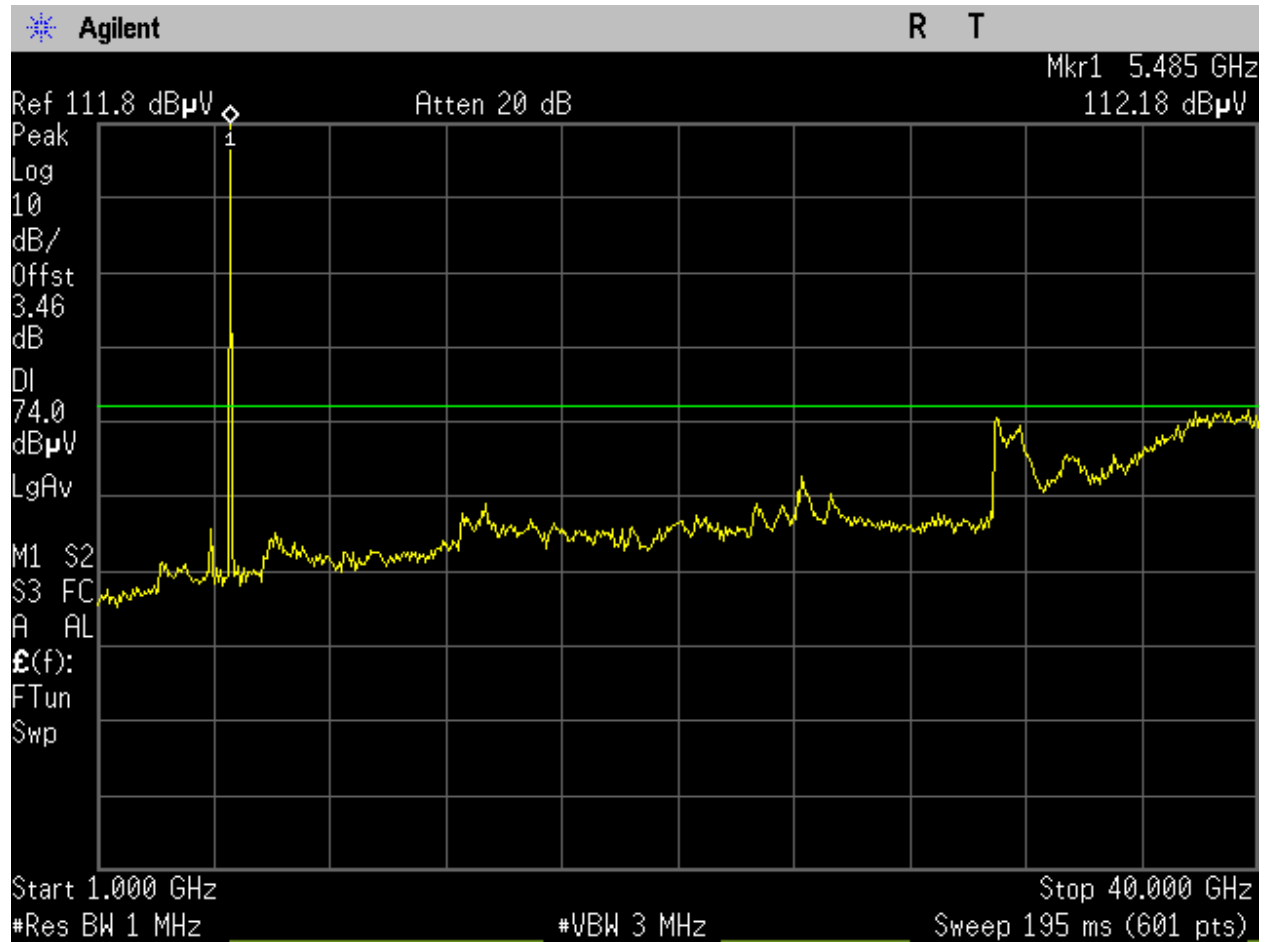


Figure 658: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_1-40GHz_Peak_Port 2.

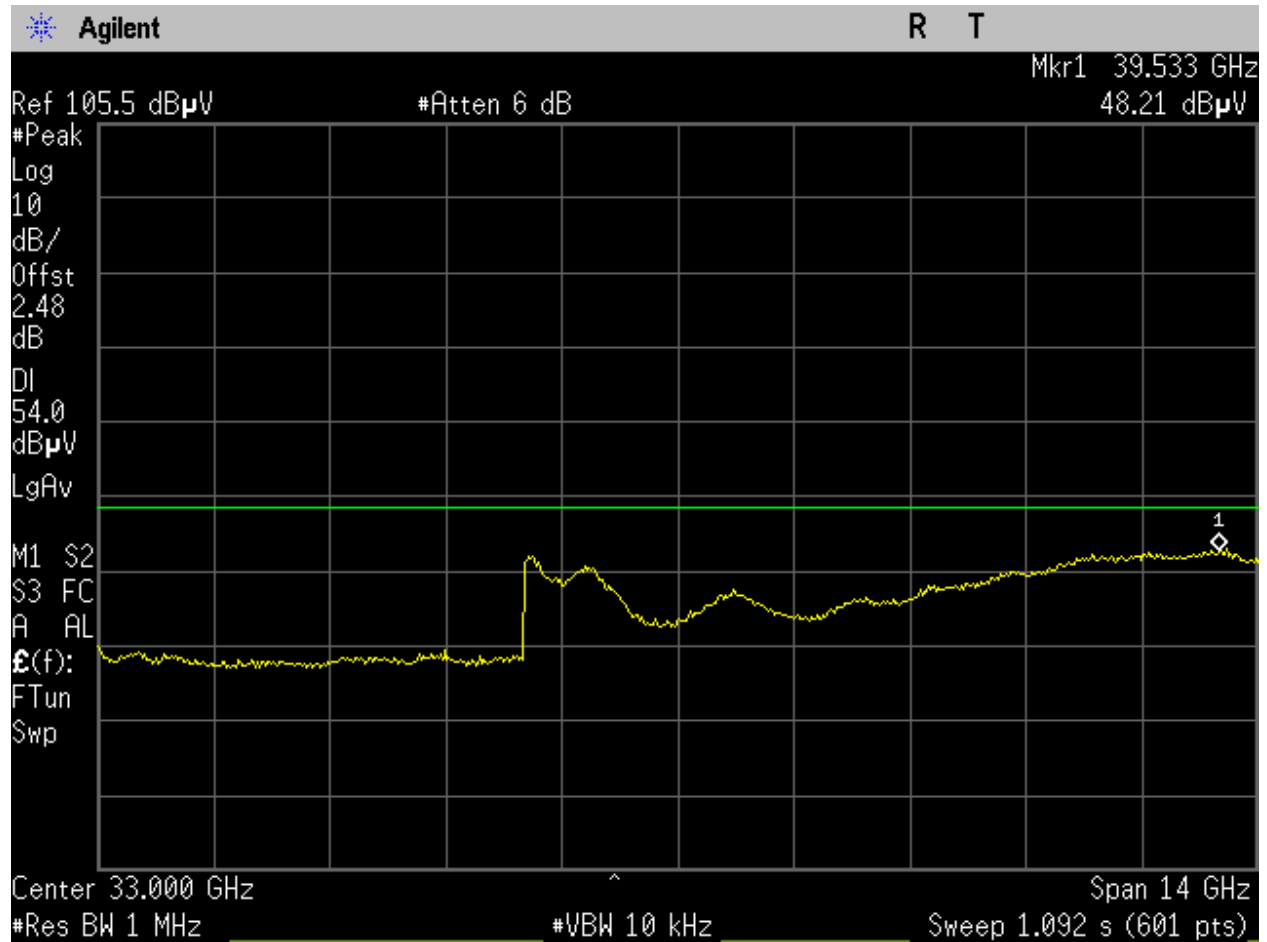


Figure 659: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 1.

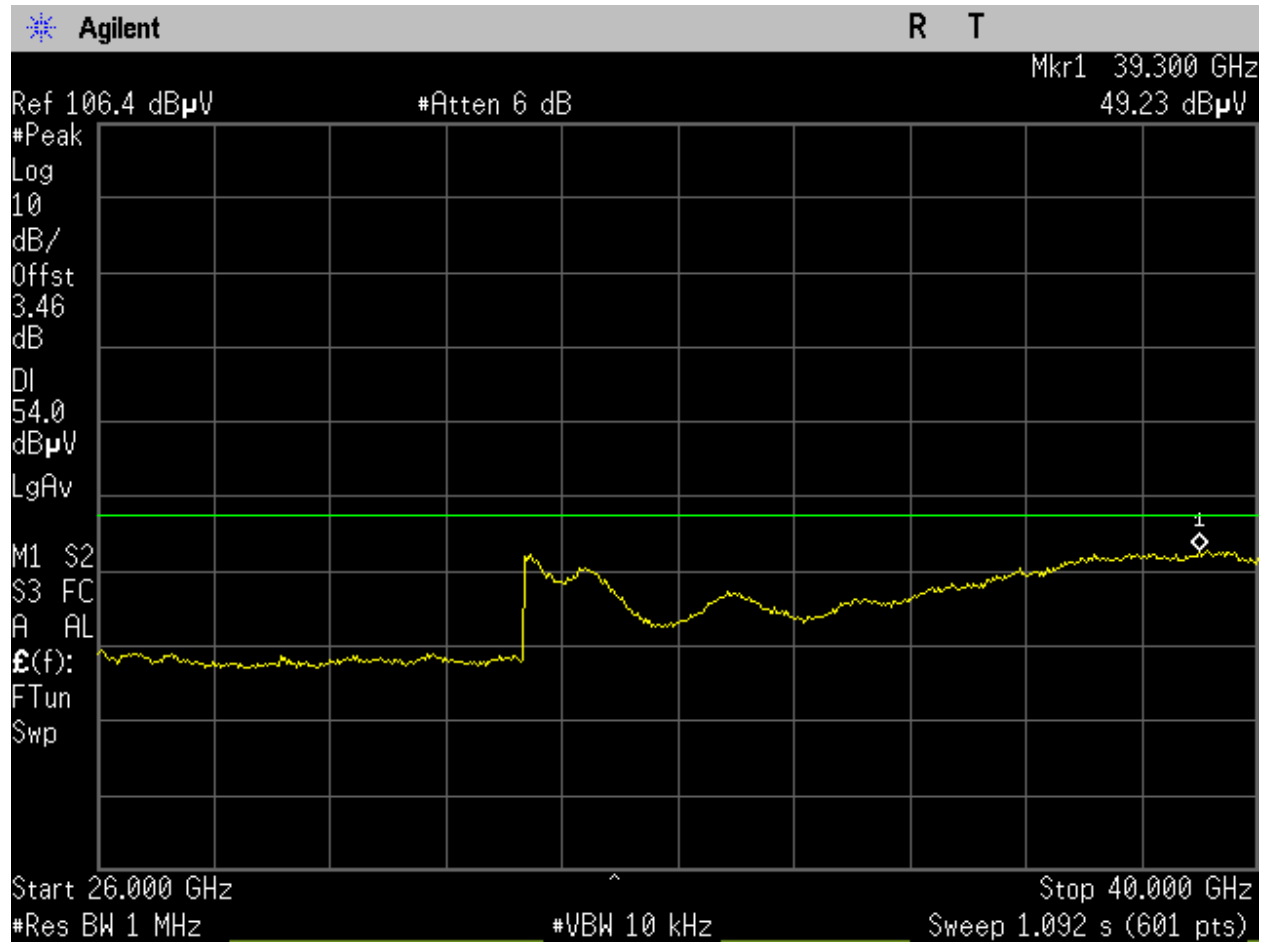


Figure 660: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 2.

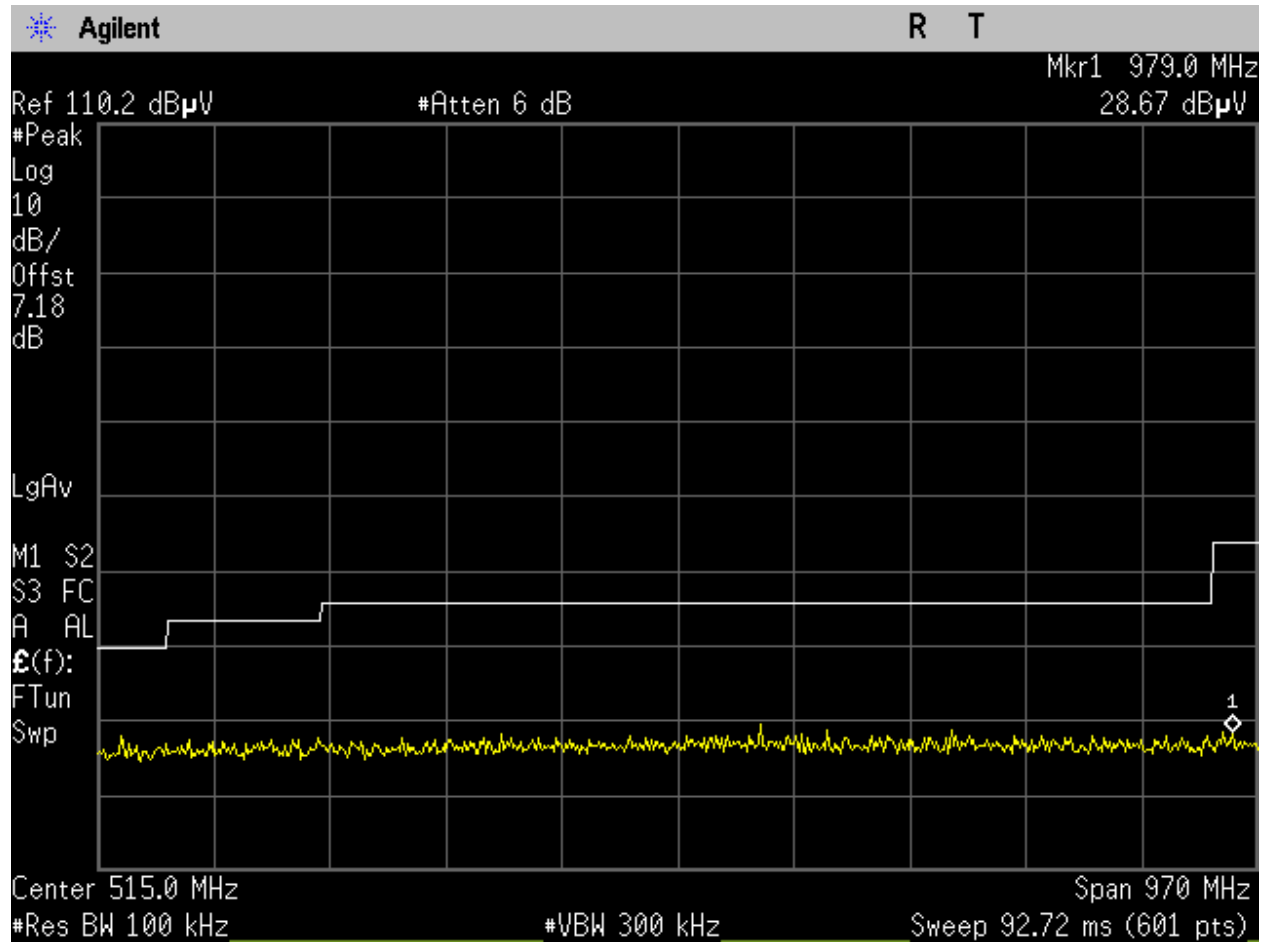


Figure 661: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 1.

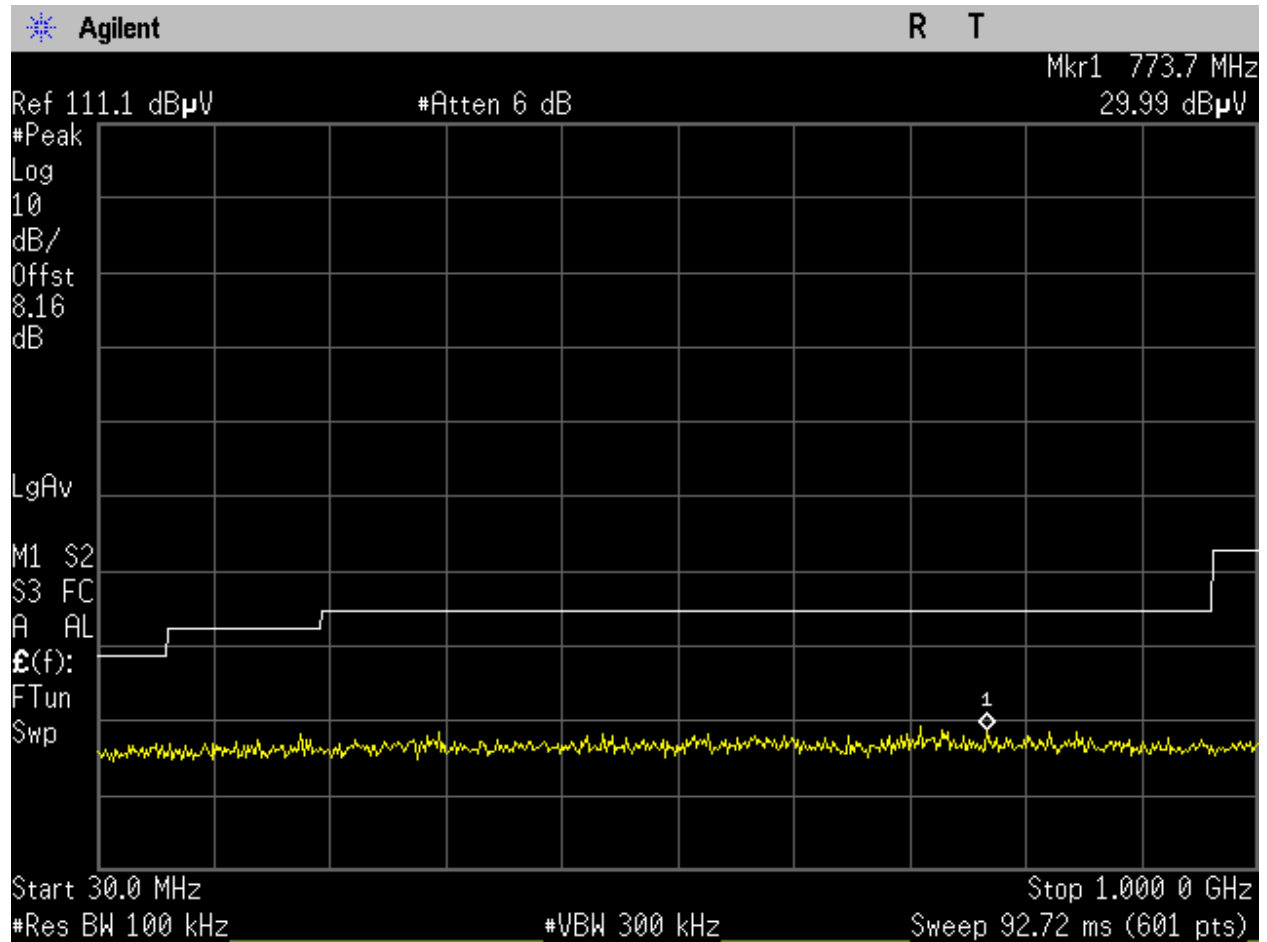


Figure 662: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 2.

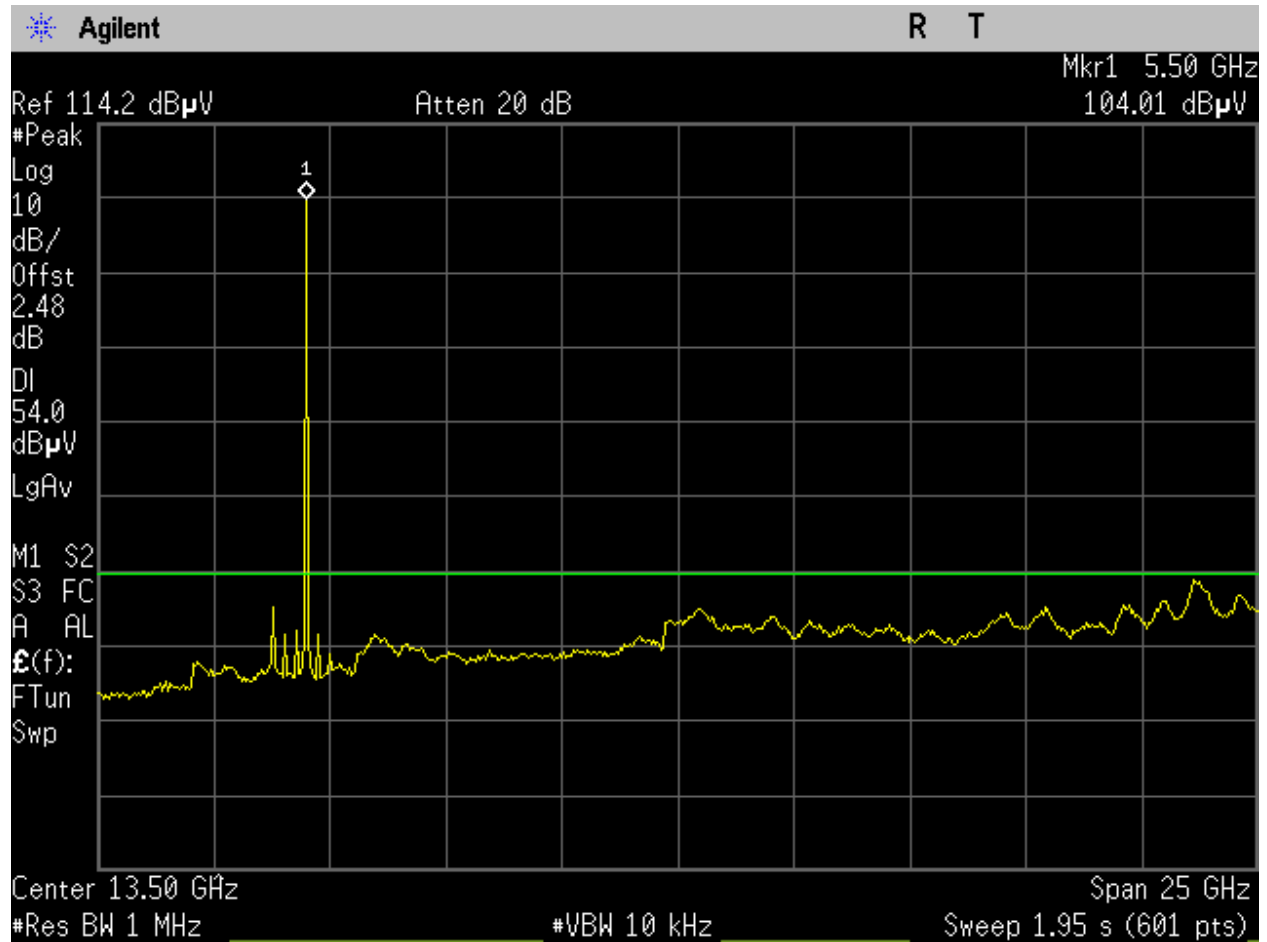


Figure 663: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

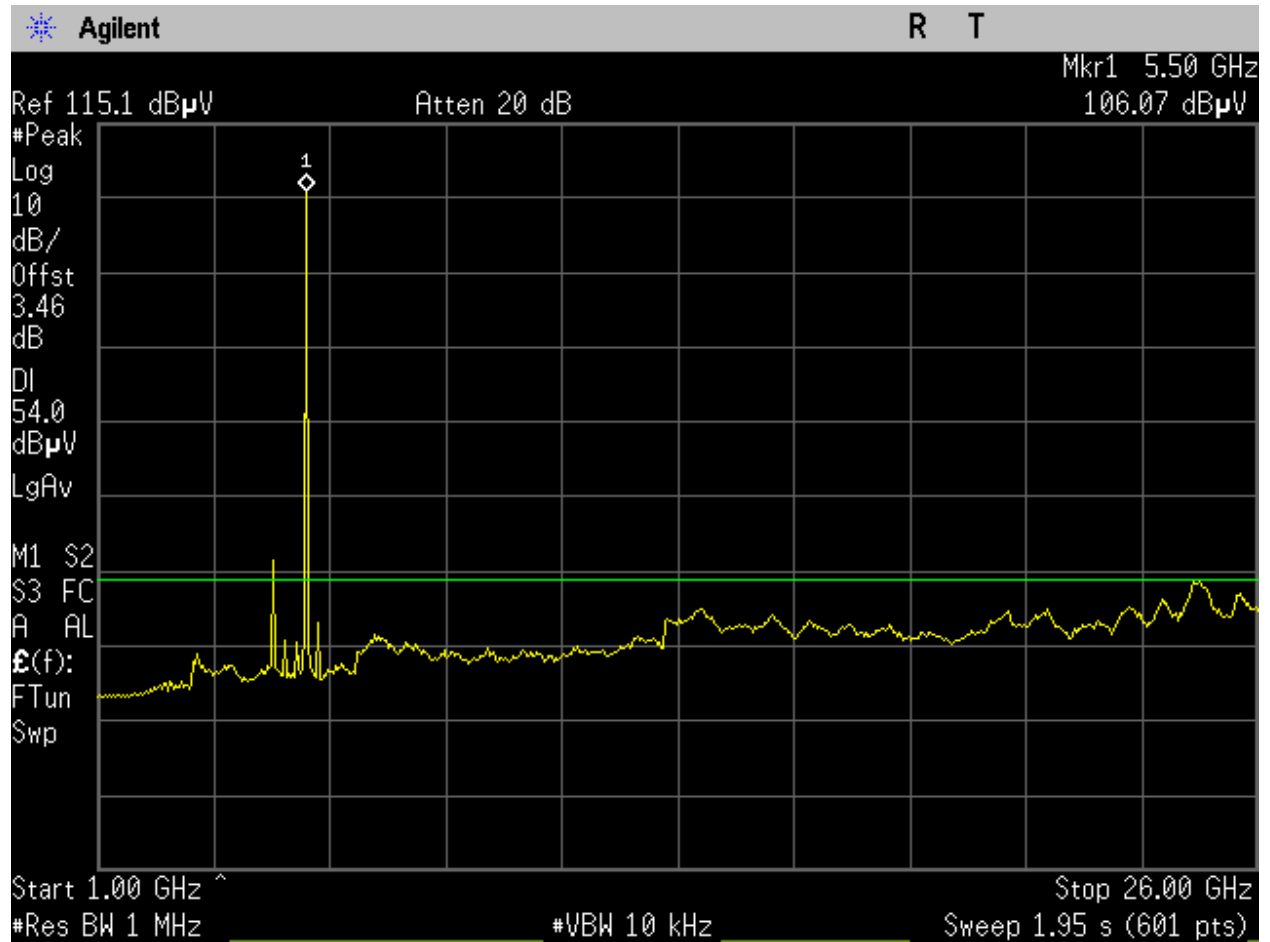


Figure 664: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

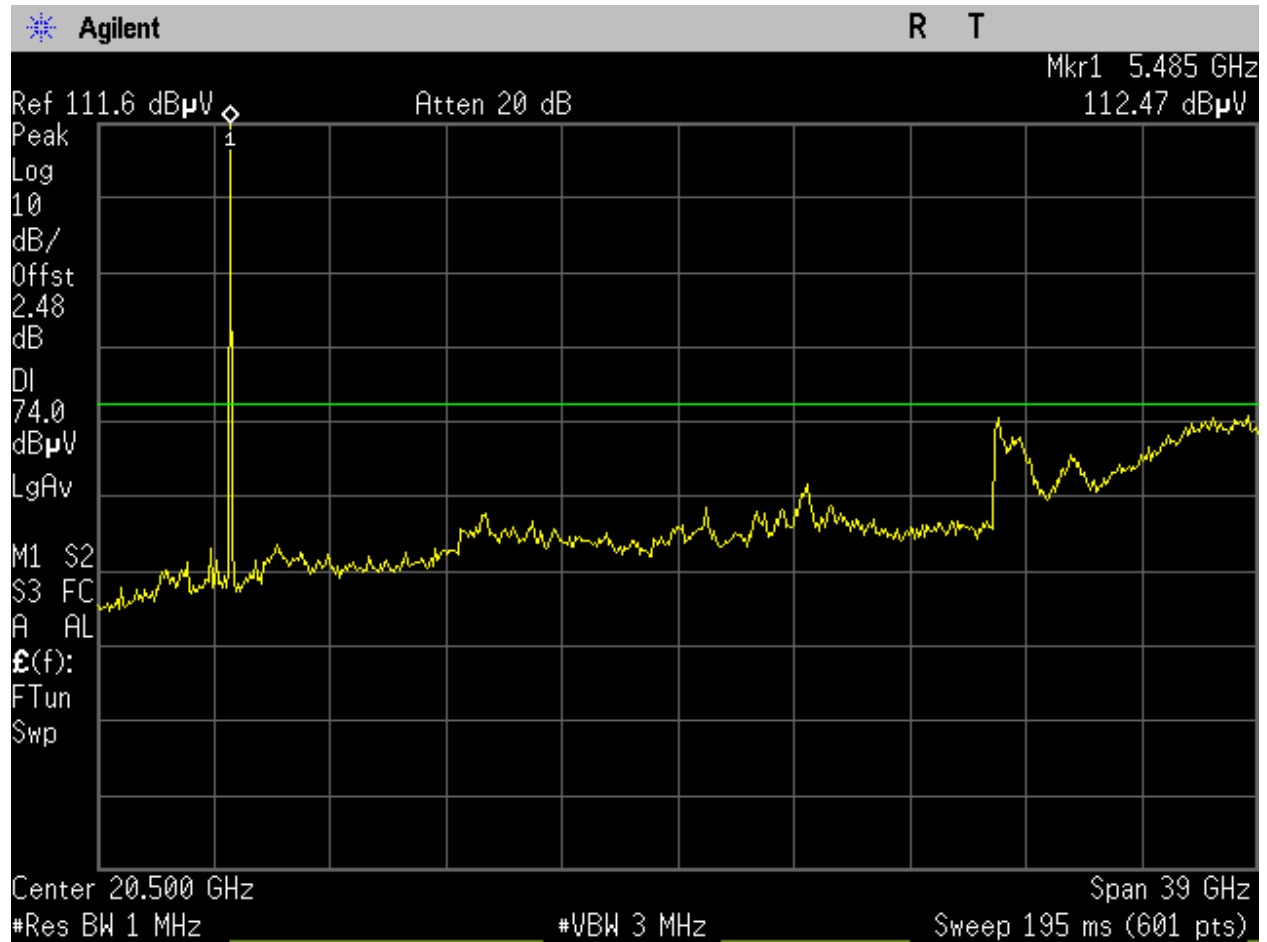


Figure 665: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_1-40GHz _Peak_Port 1.

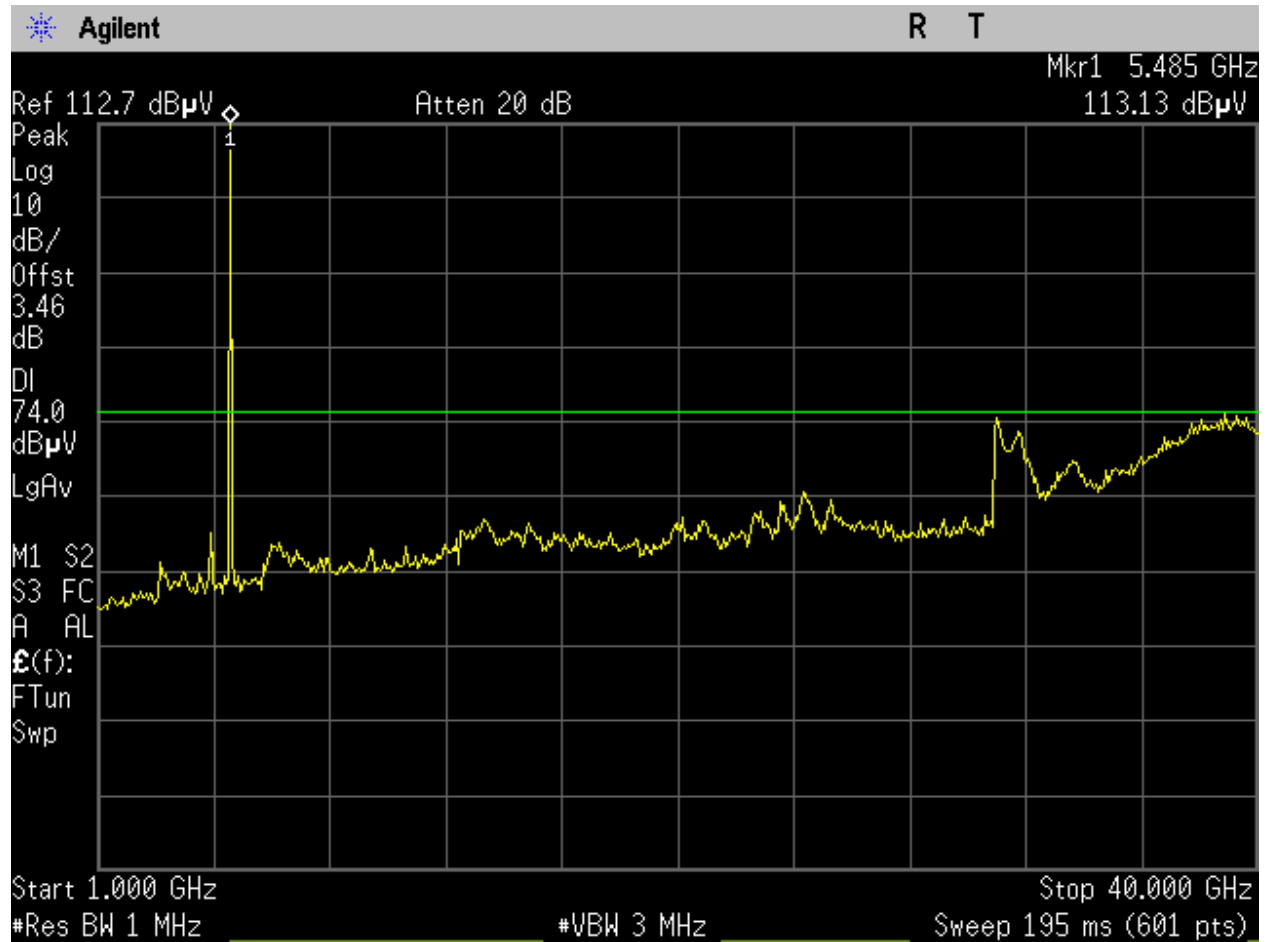


Figure 666: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_1-40GHz _Peak_Port 2.

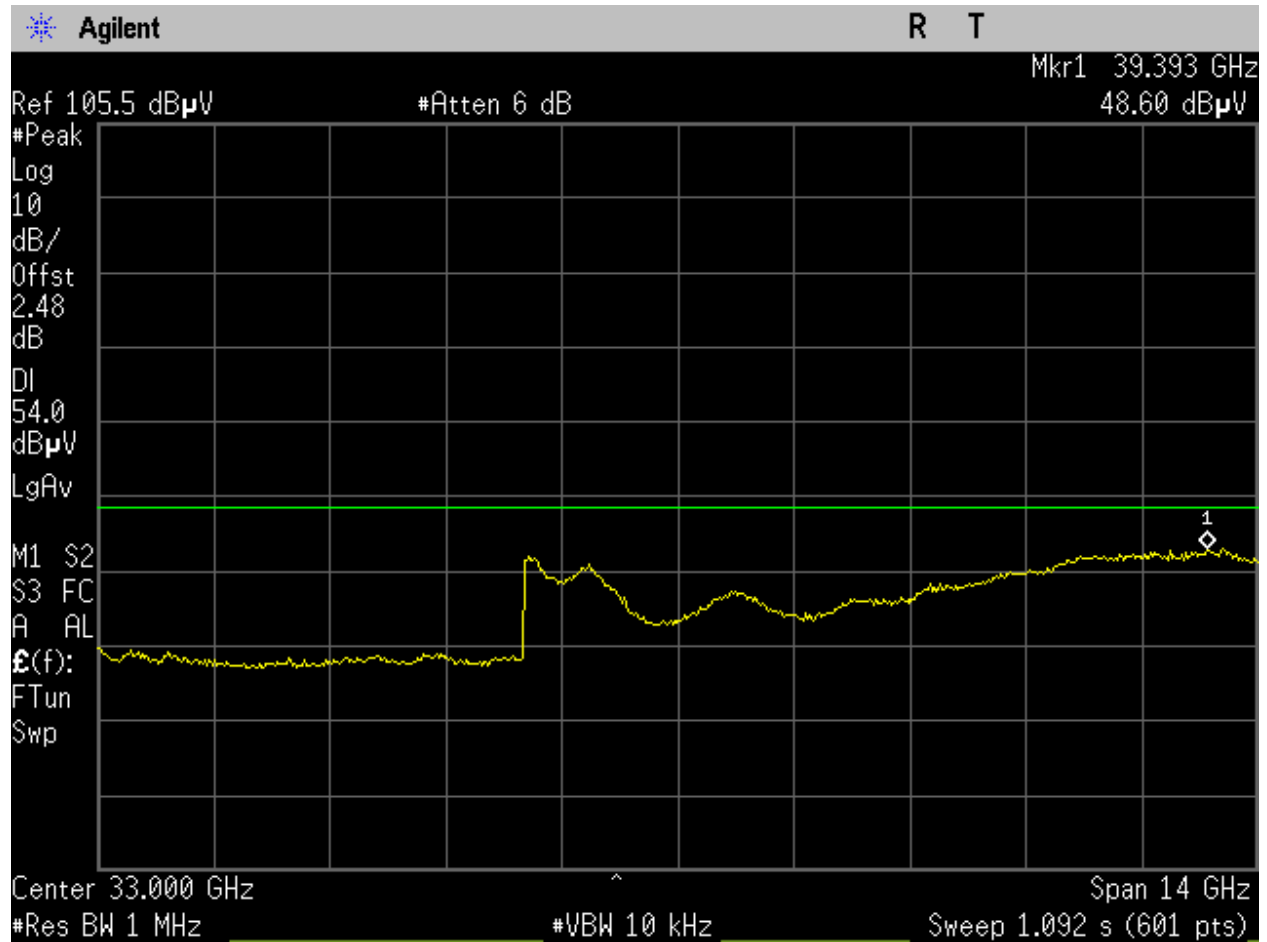


Figure 667: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

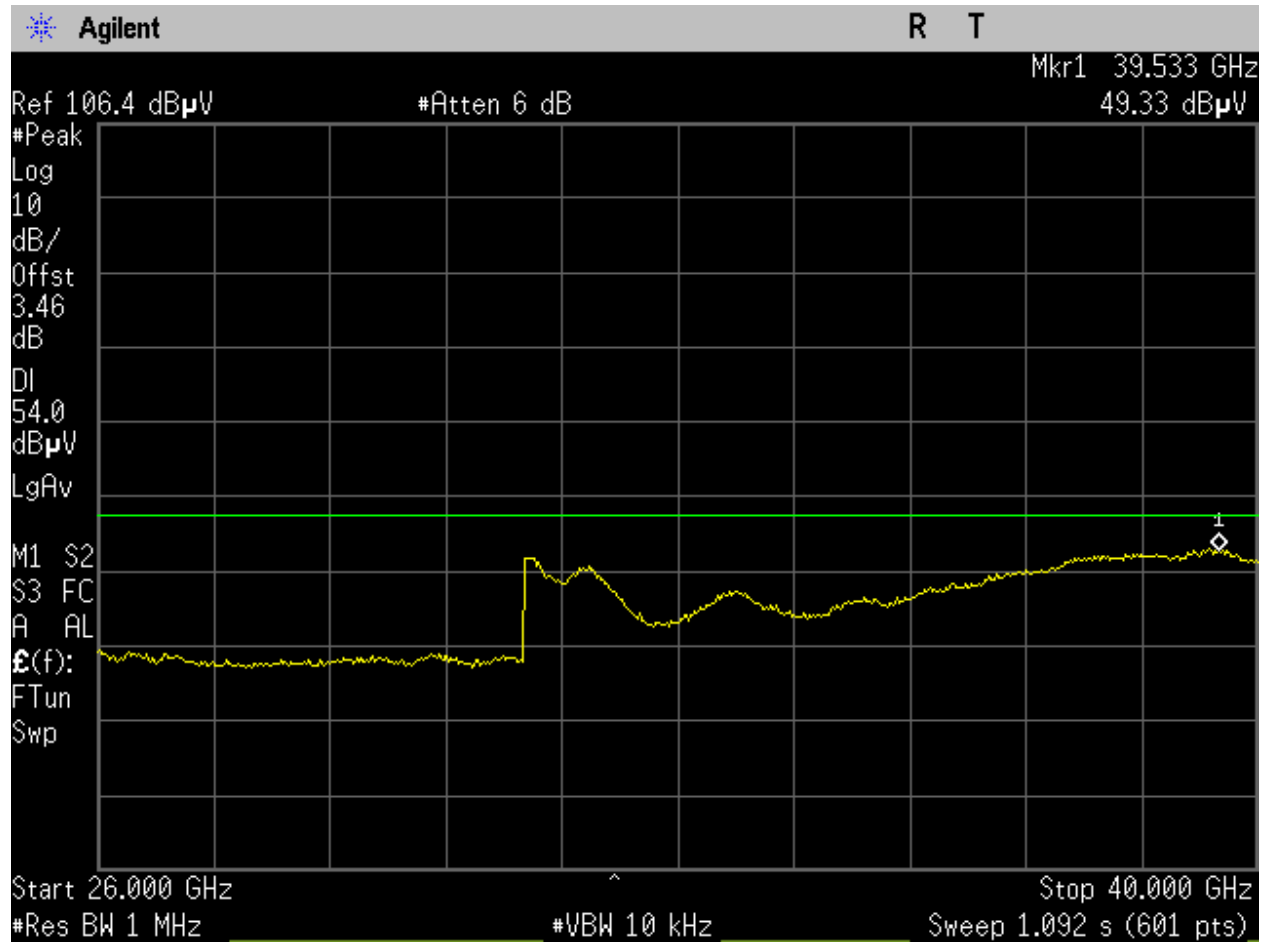


Figure 668: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

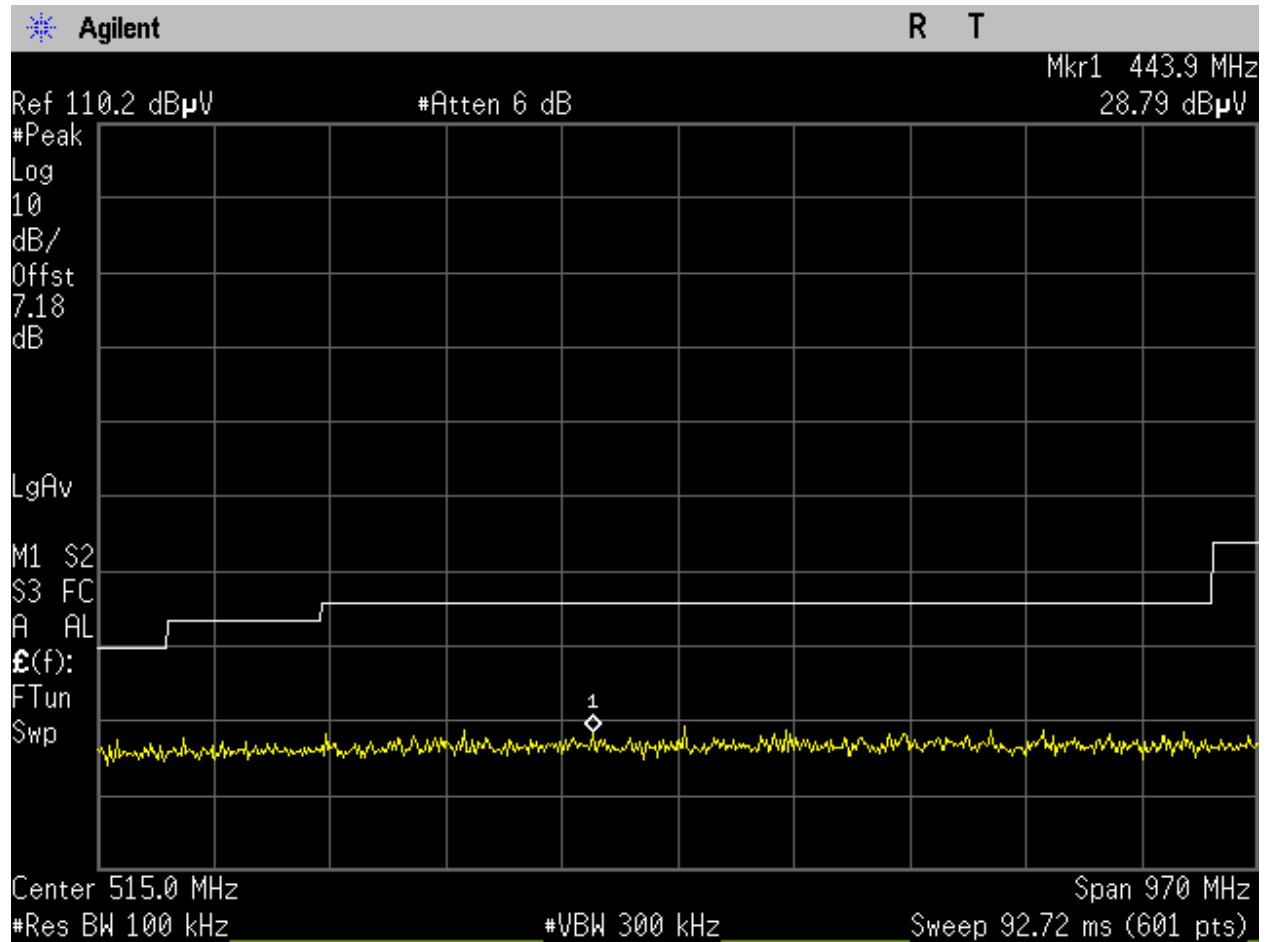


Figure 669: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

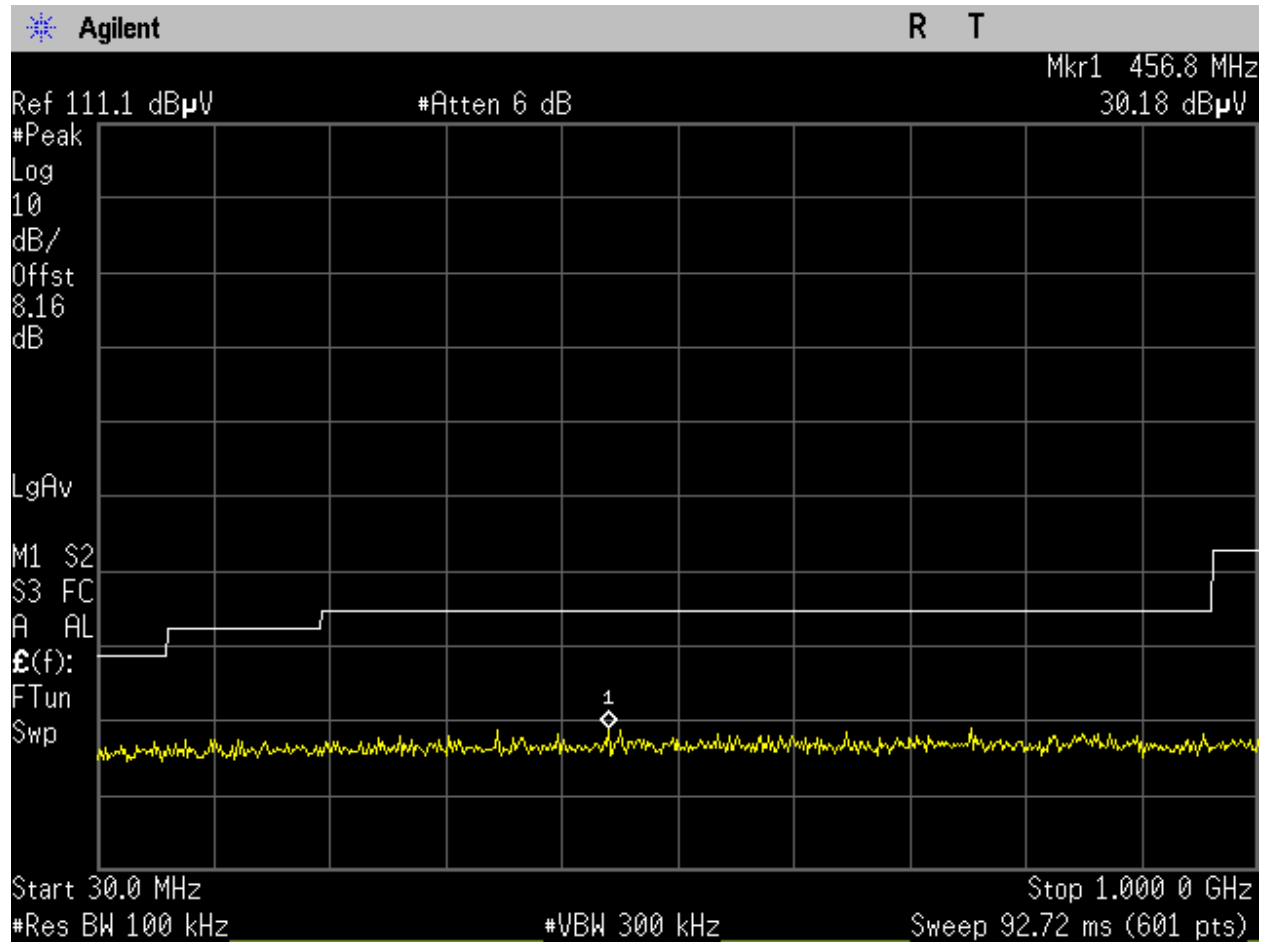


Figure 670: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

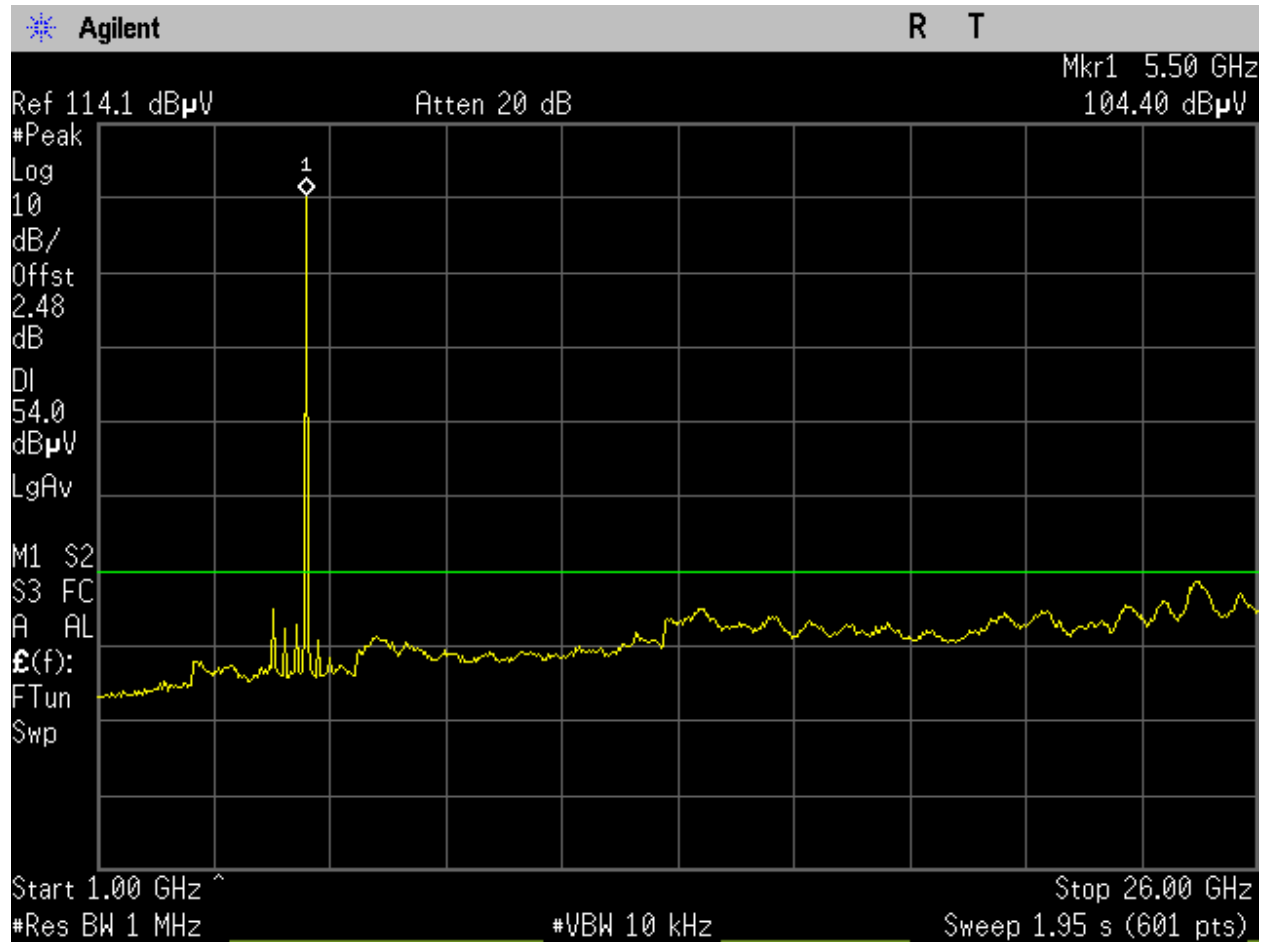


Figure 671: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_1-26GHz avg_Port 1.

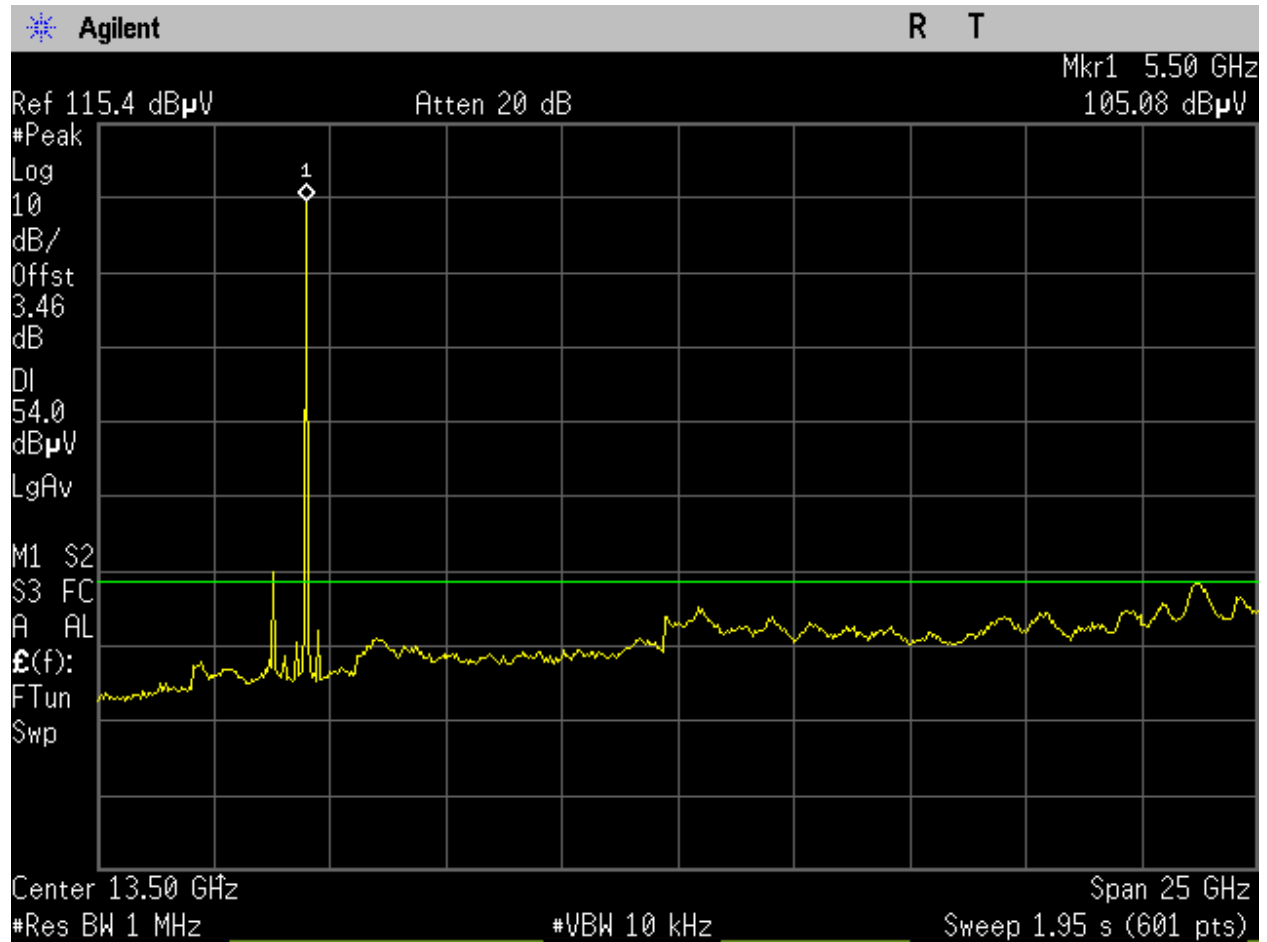


Figure 672: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_1-26GHz avg_Port 2.

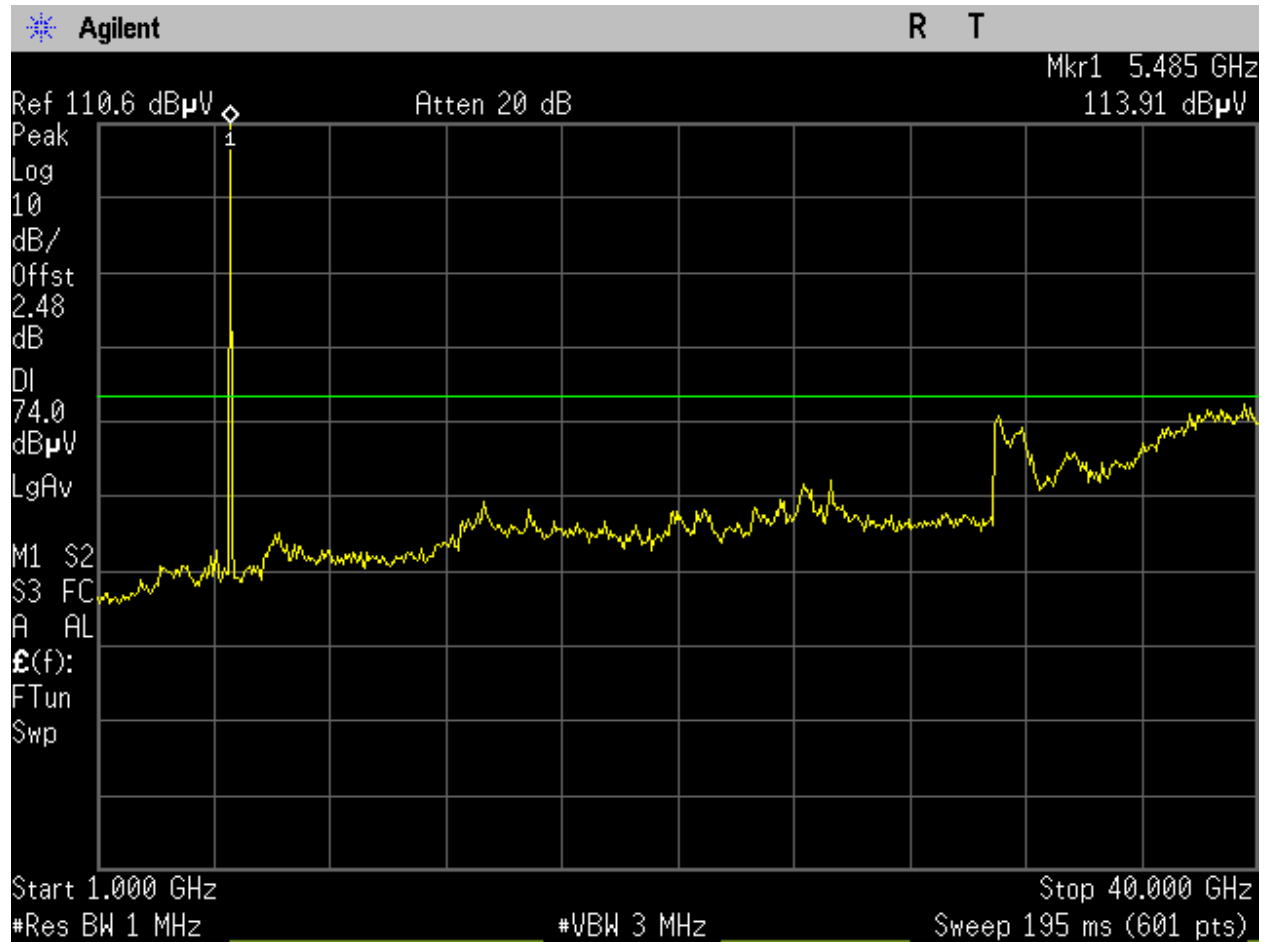


Figure 673: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_1-40GHz_Peak_Port 1.

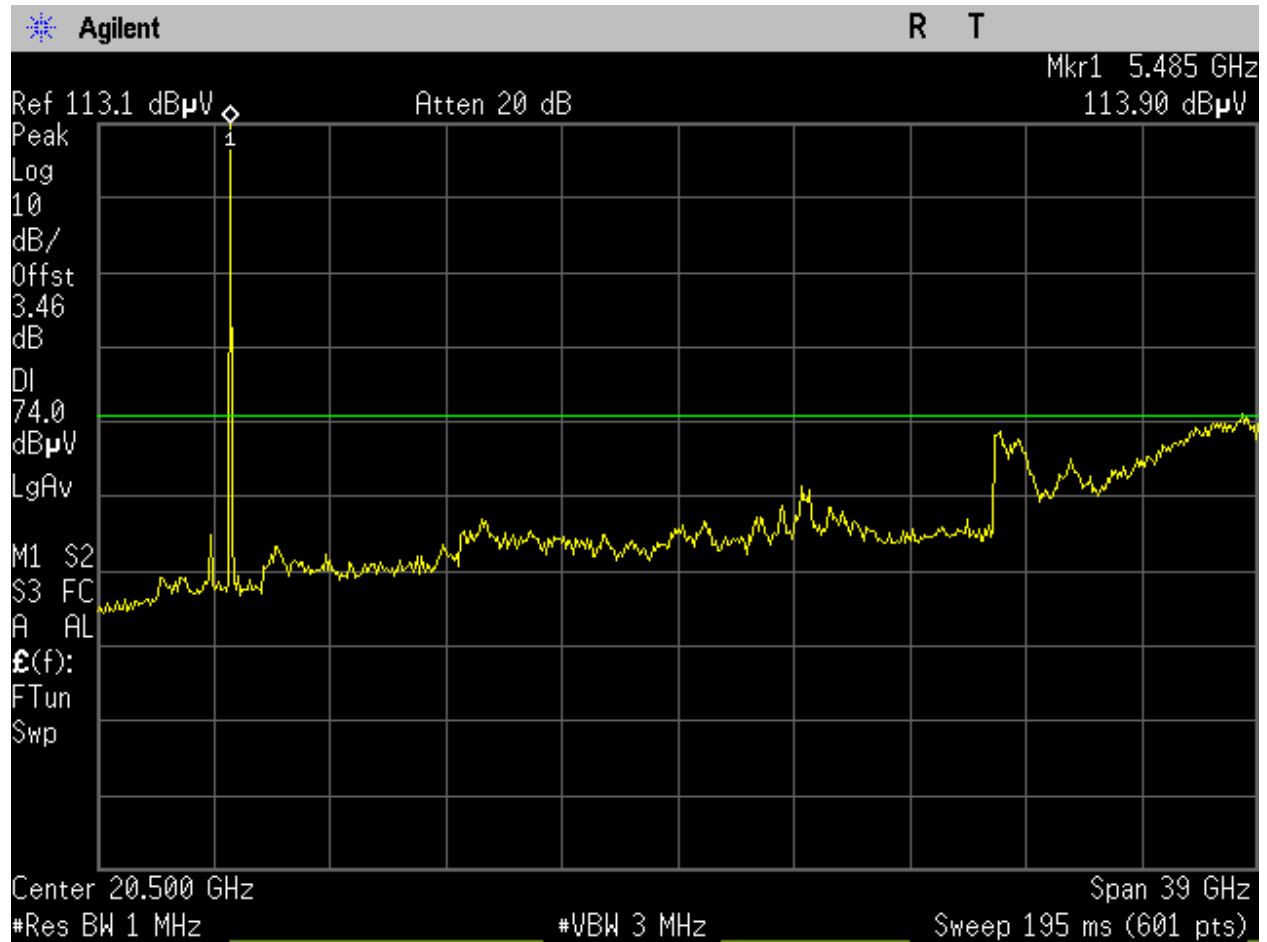


Figure 674: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_1-40GHz _Peak_Port 2.

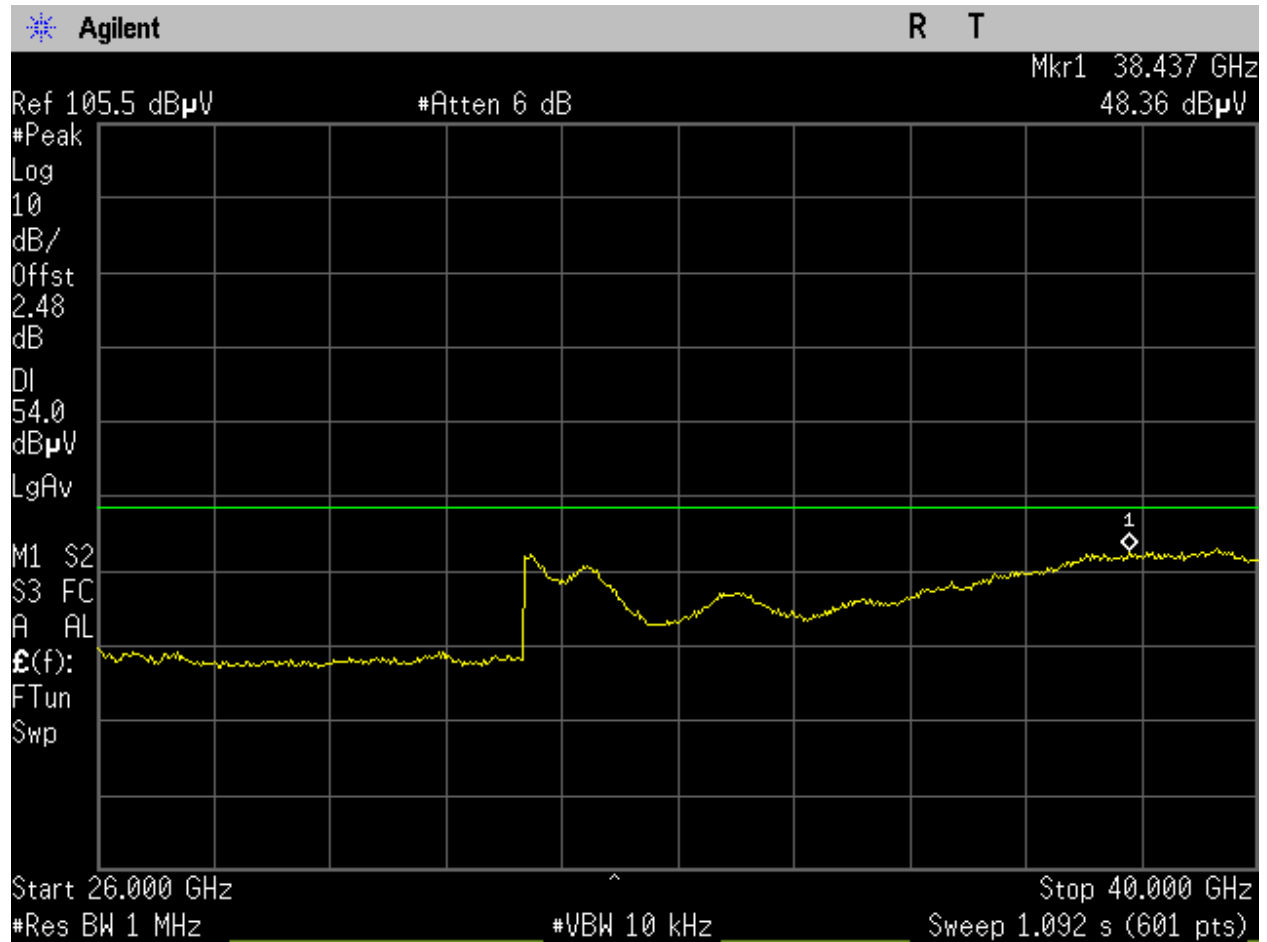


Figure 675: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 1.

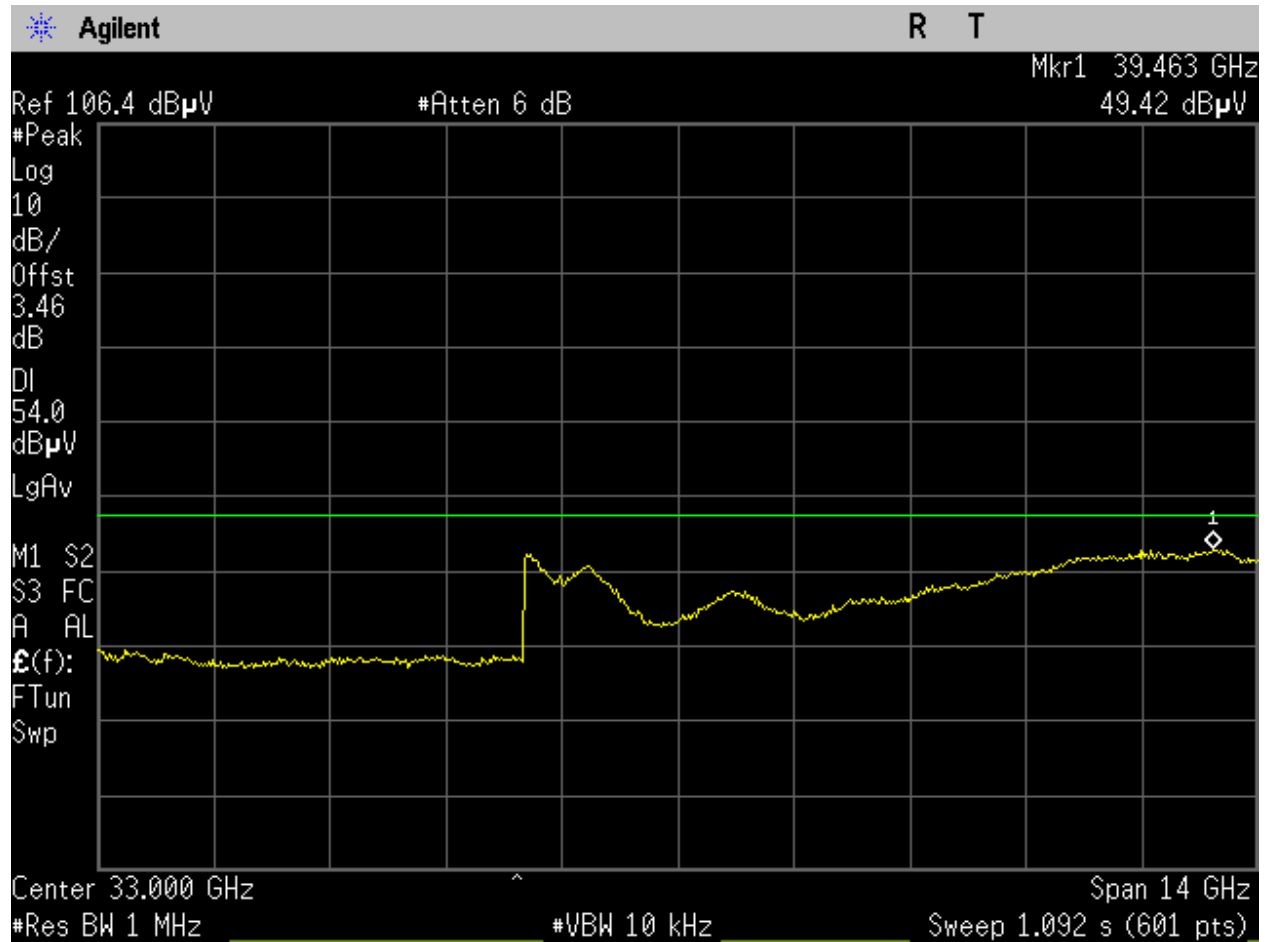


Figure 676: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_26-40GHz_Avg_Port 2.

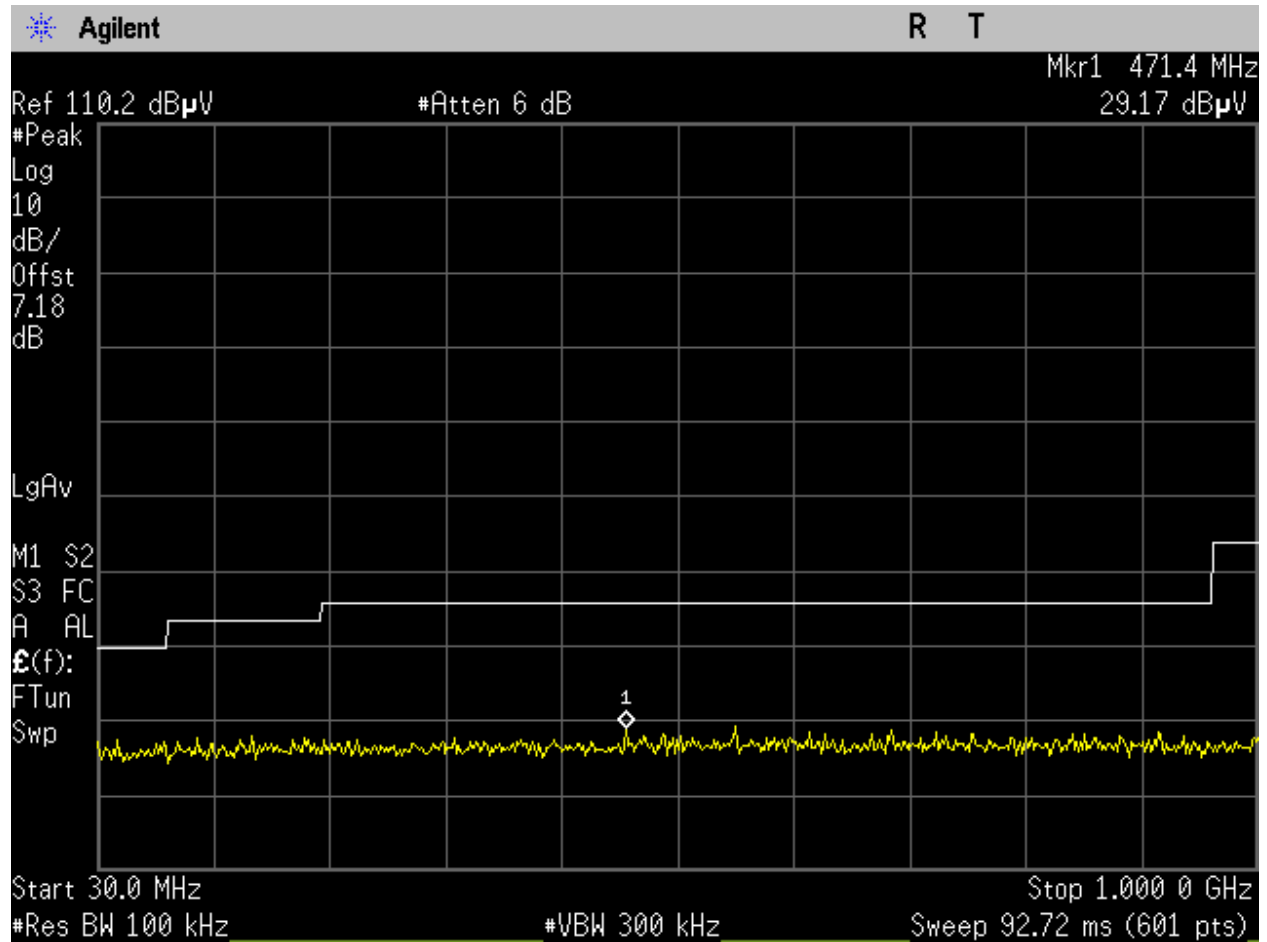


Figure 677: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 1.

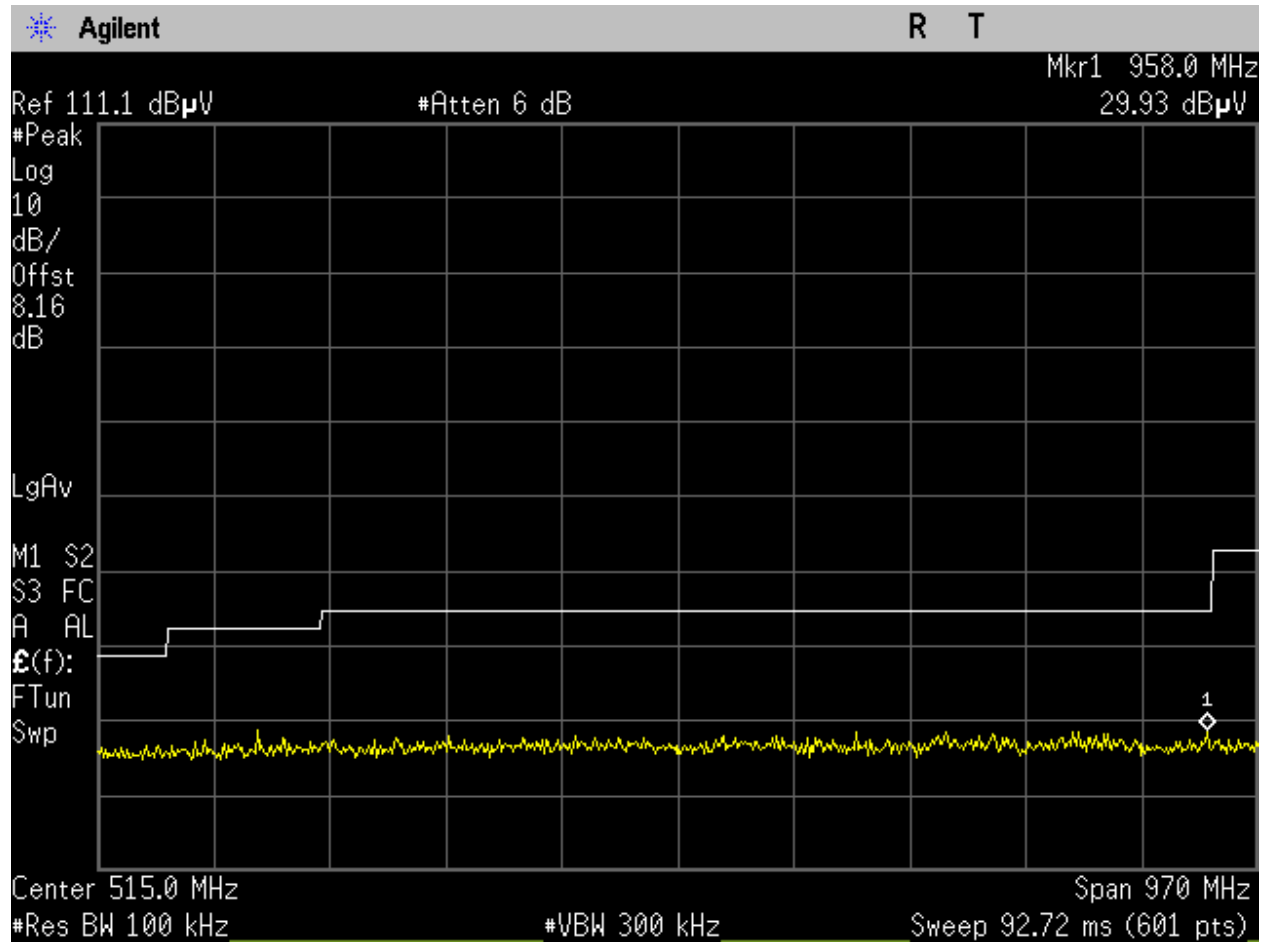


Figure 678: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_15.209_30-1000MHz_Peak_Port 2.

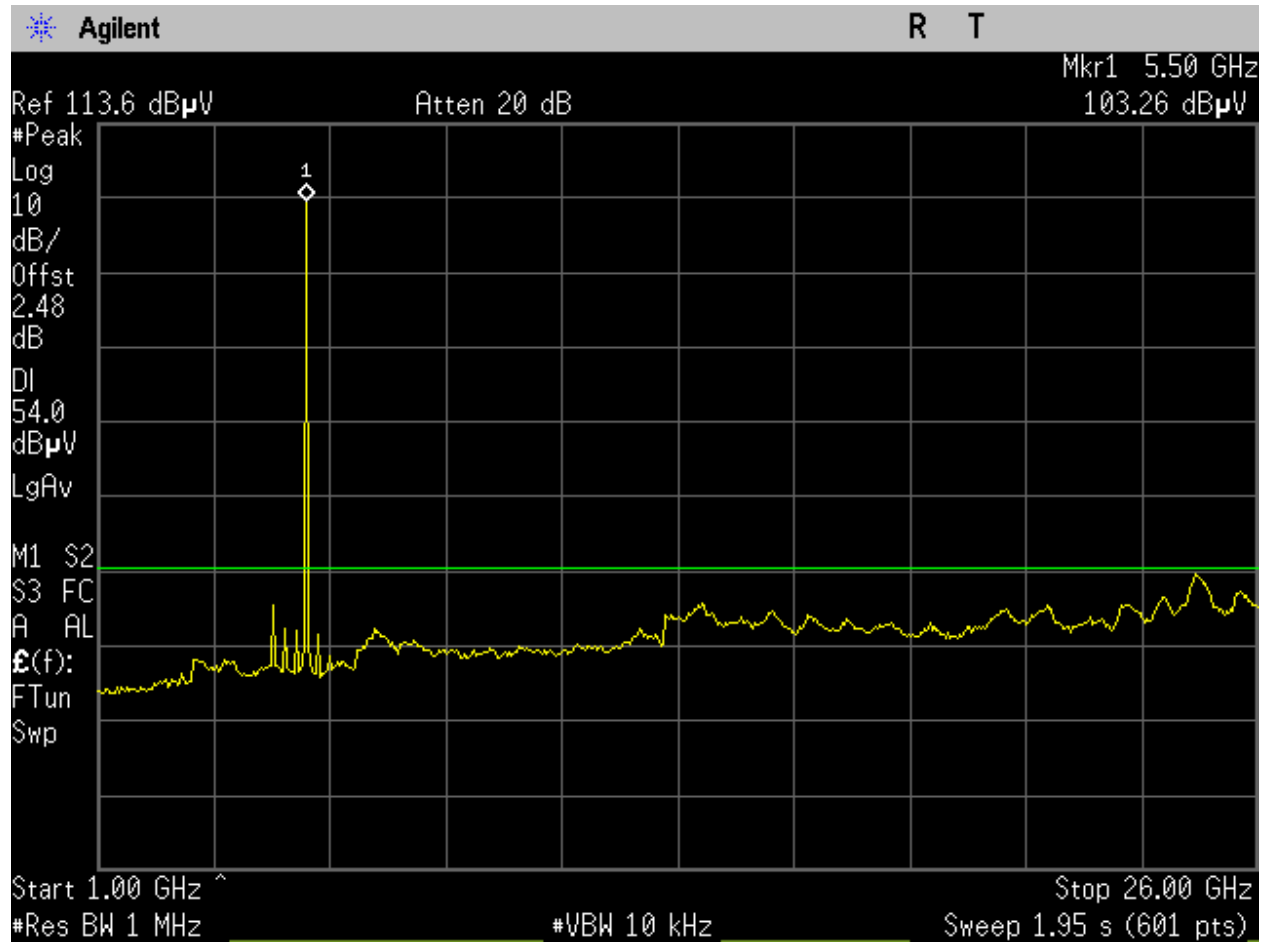


Figure 679: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_1-26GHz avg_Port 1.

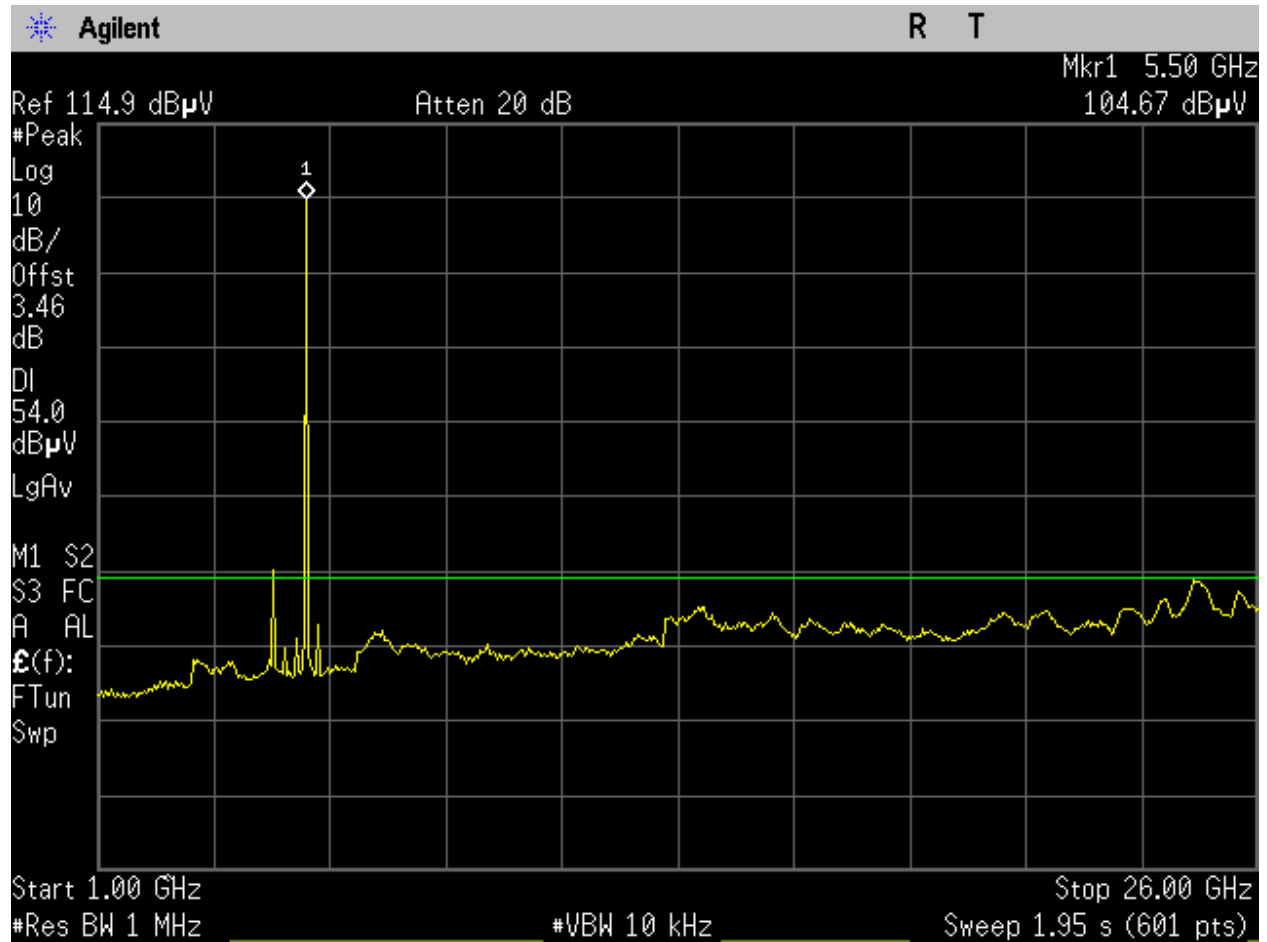


Figure 680: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_1-26GHz avg_Port 2.

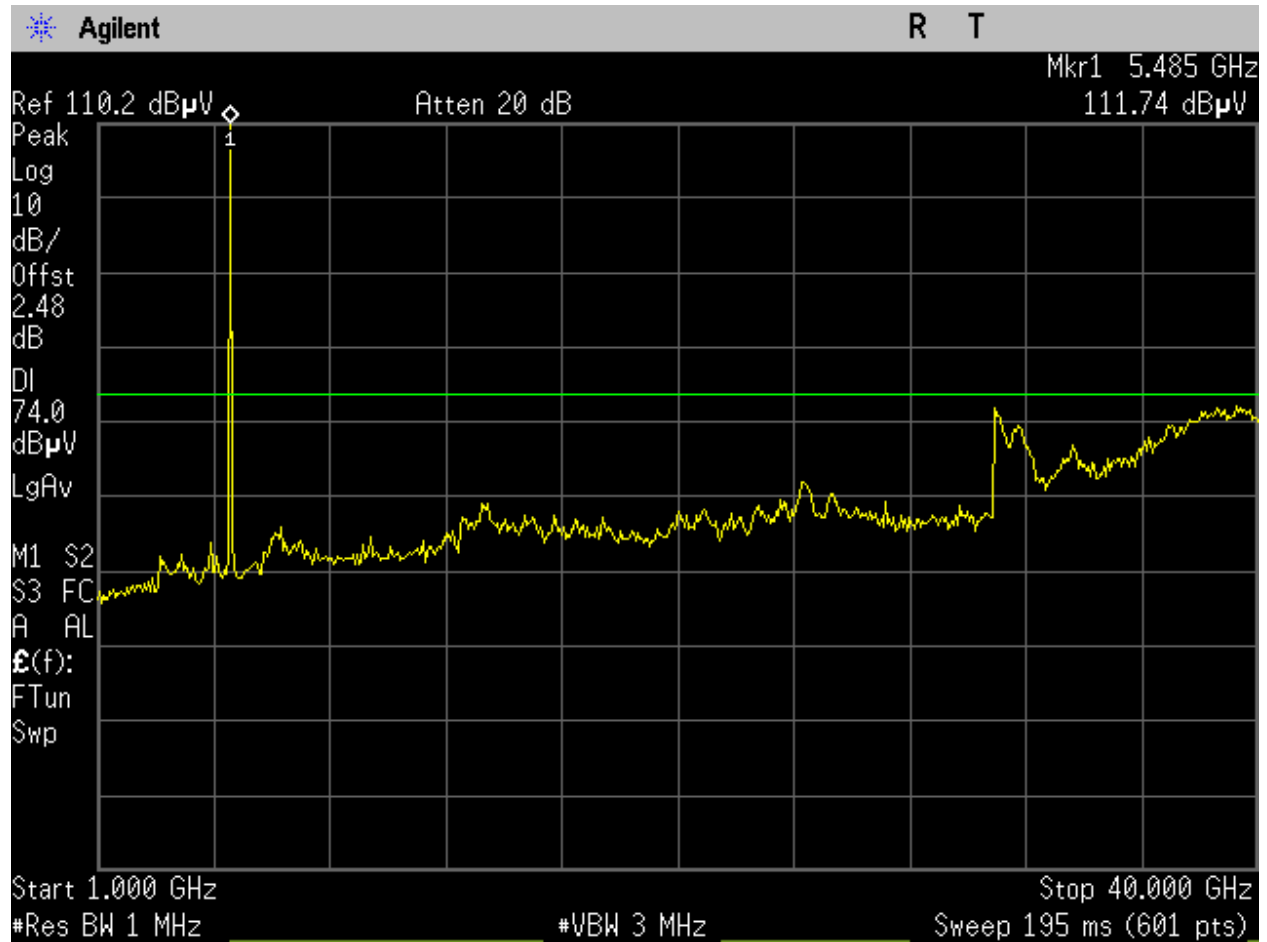


Figure 681: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_1-40GHz_Peak_Port 1.

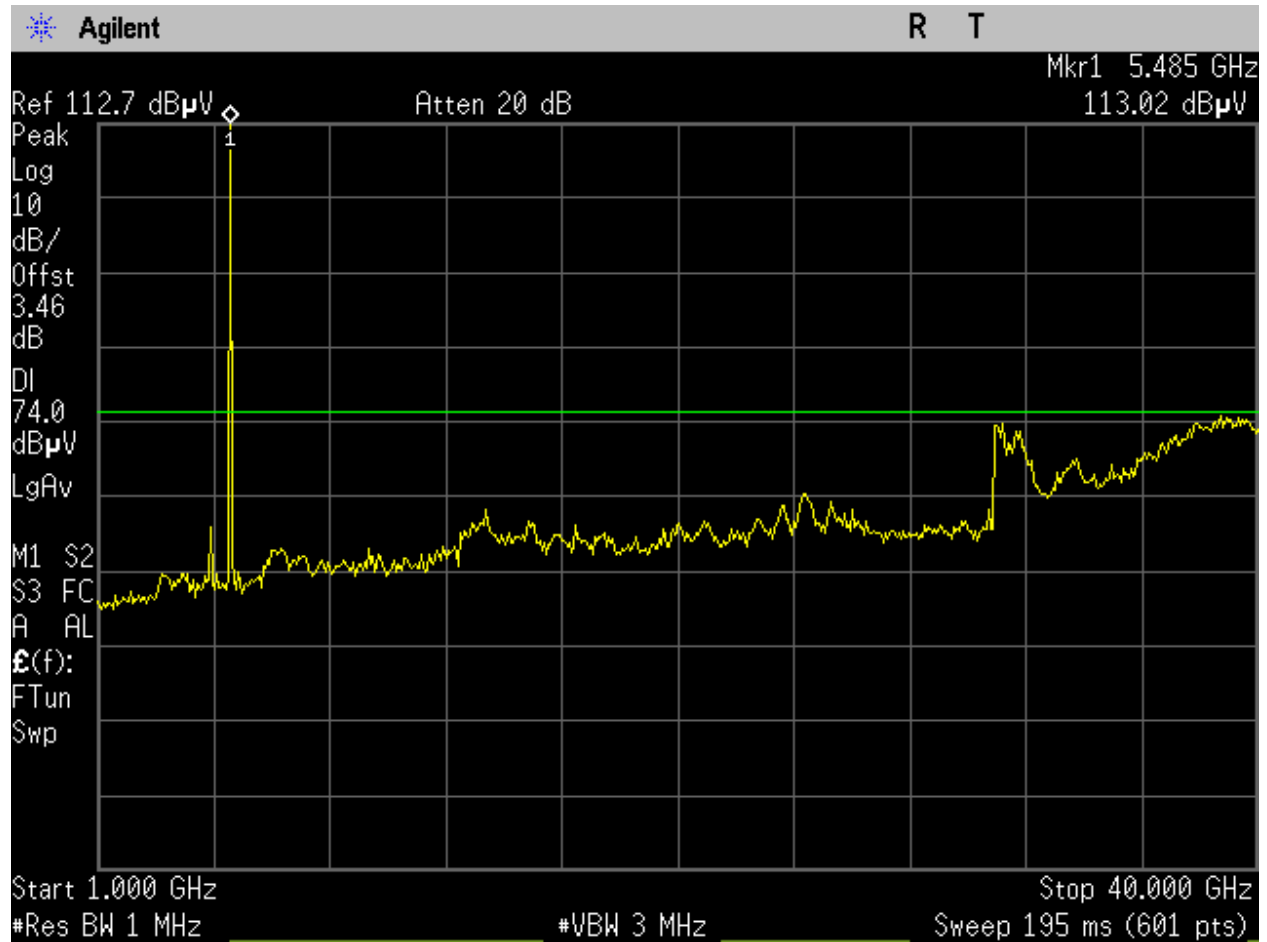


Figure 682: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_1-40GHz_Peak_Port 2.

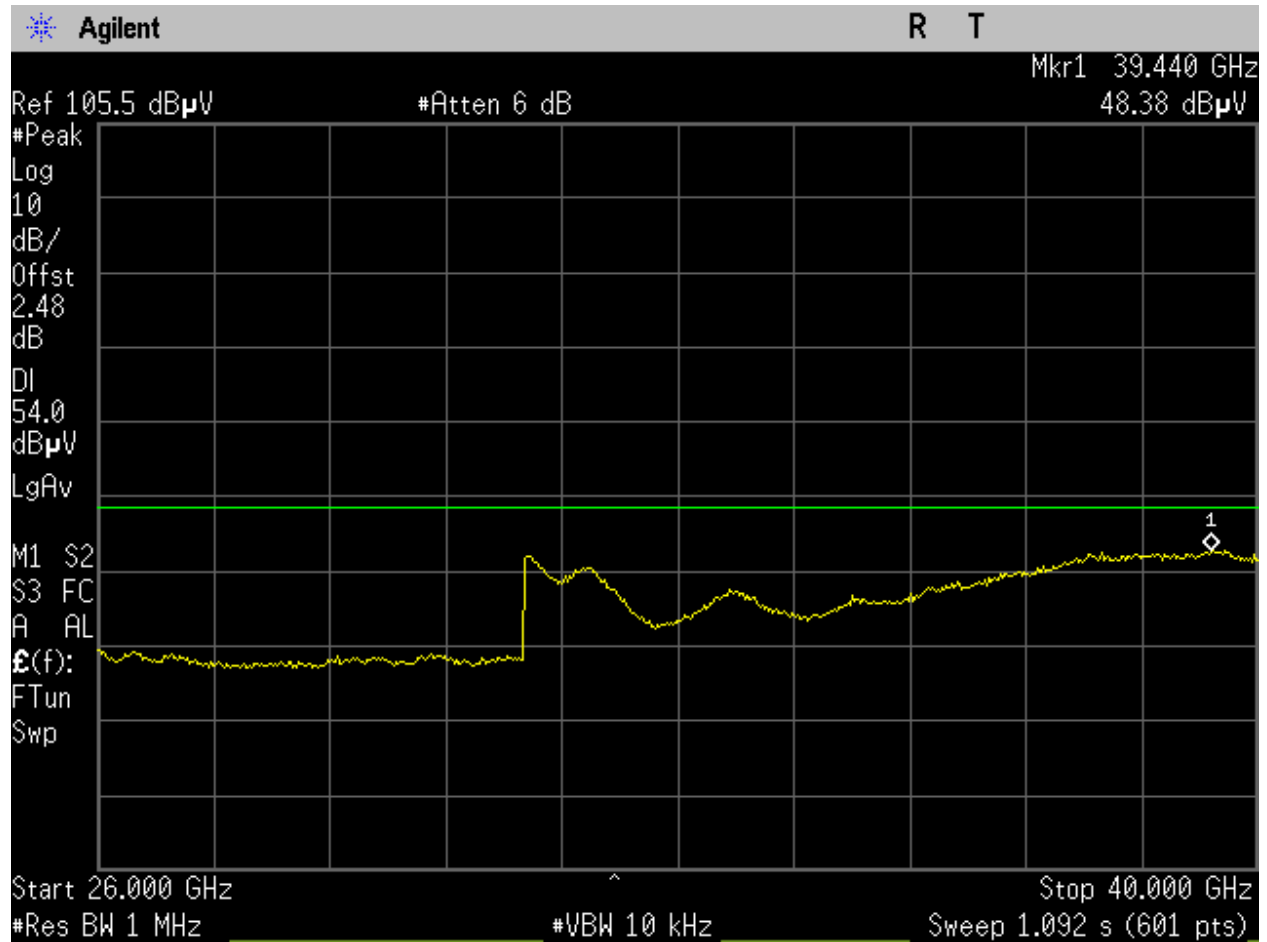


Figure 683: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_26-40GHz_Avg_Port 1.

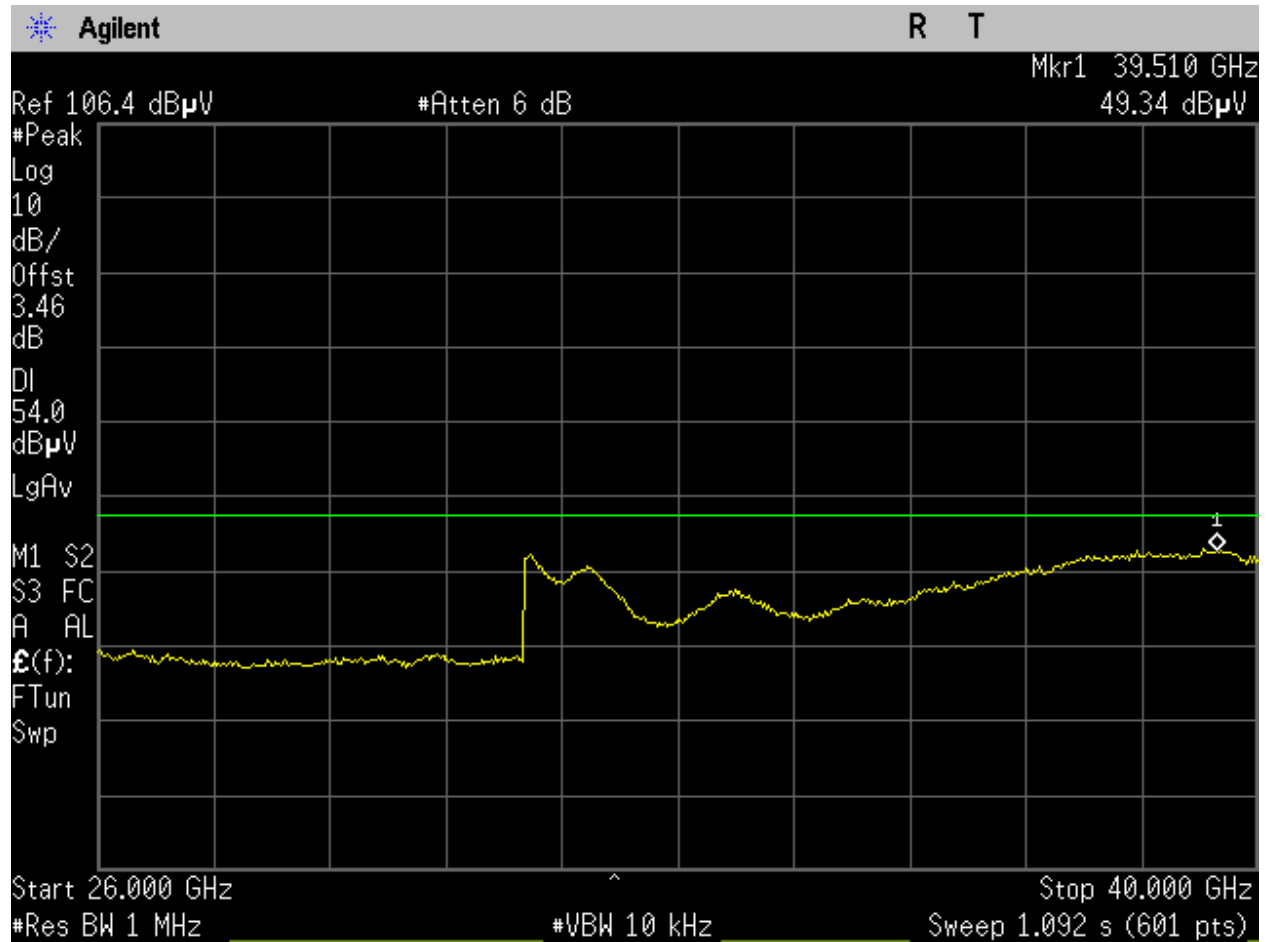


Figure 684: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_26-40GHz_Avg_Port 2.

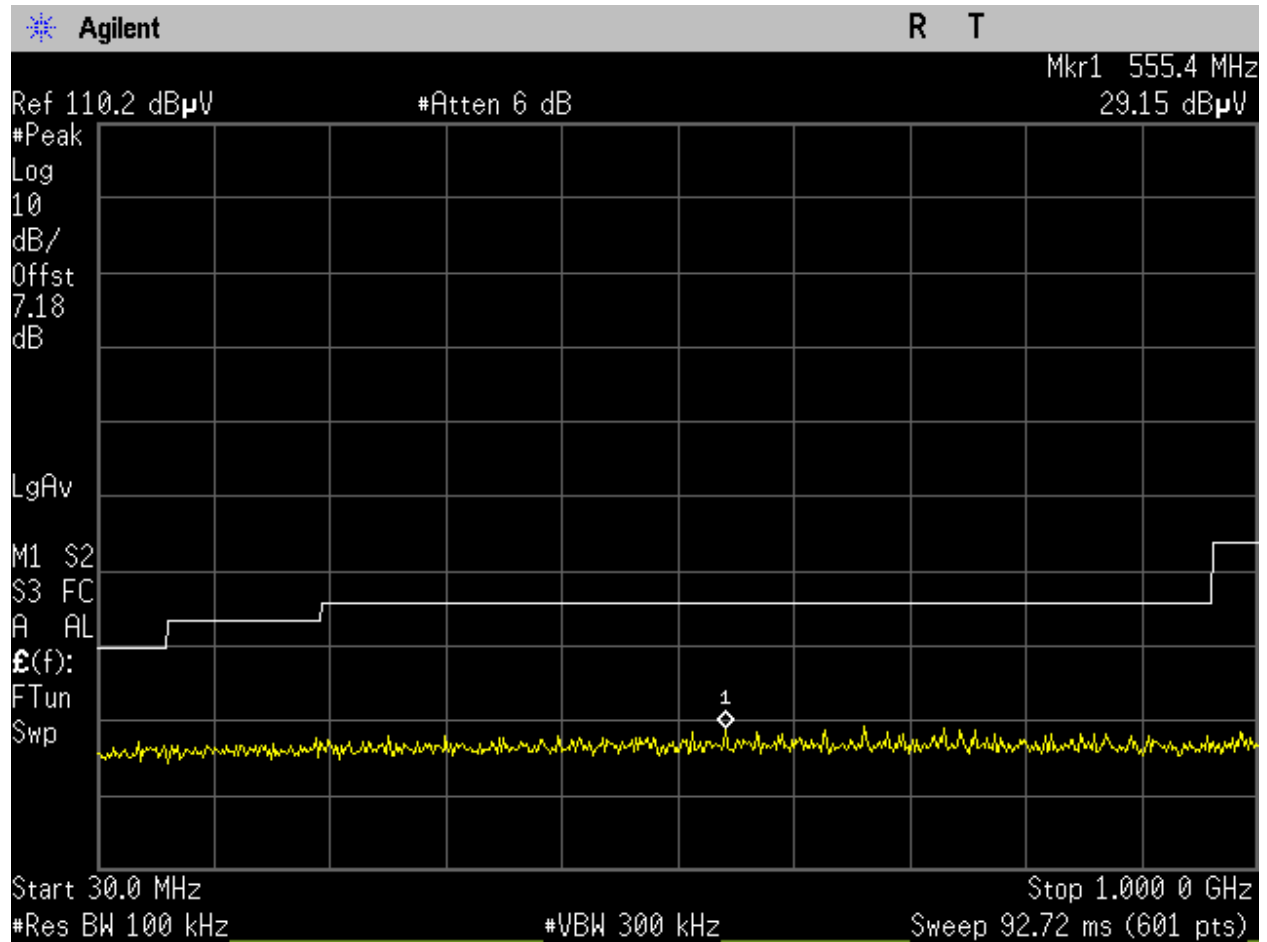


Figure 685: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 1.

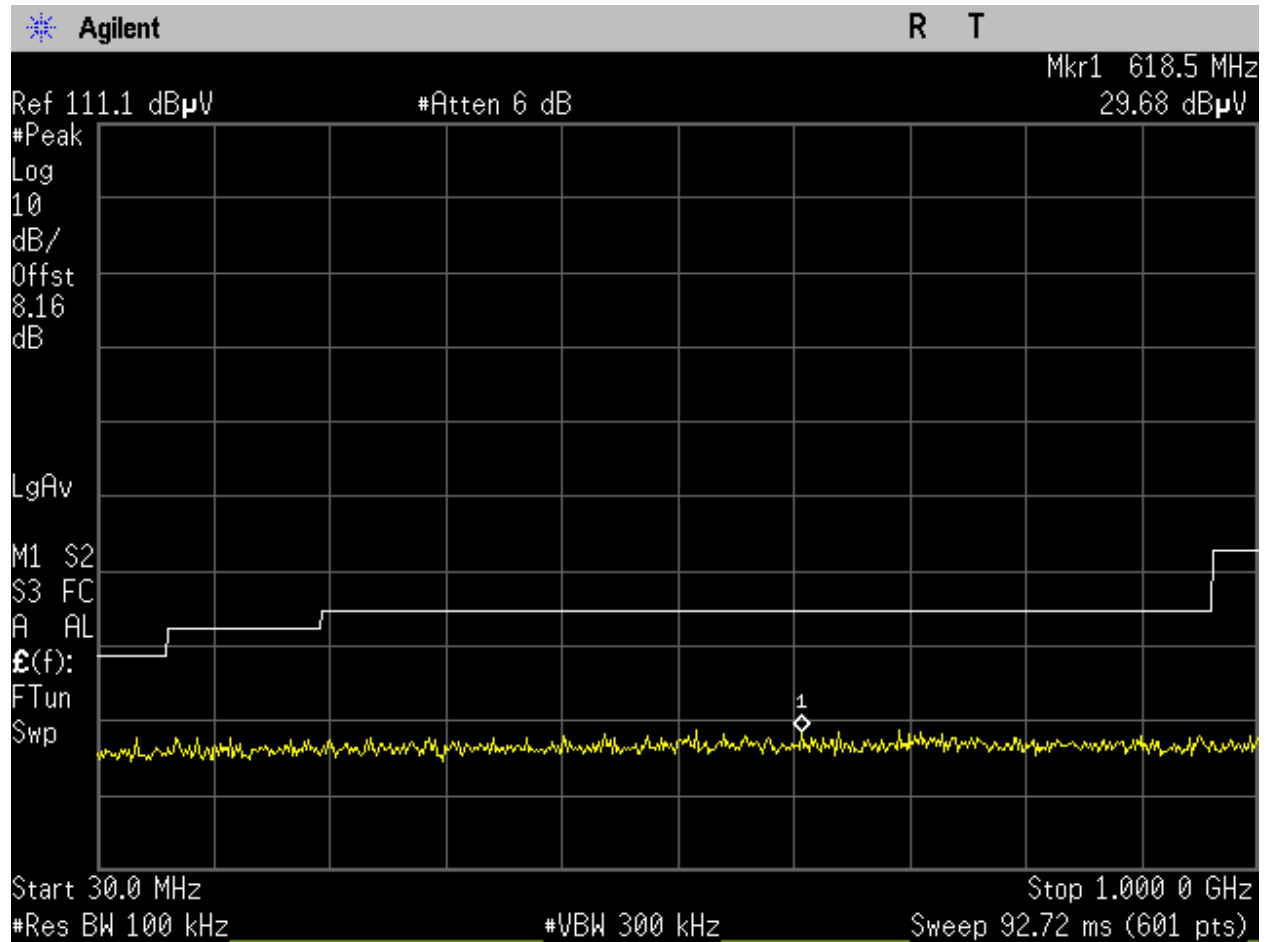


Figure 686: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_15.209_30-1000MHz_Peak_Port 2.

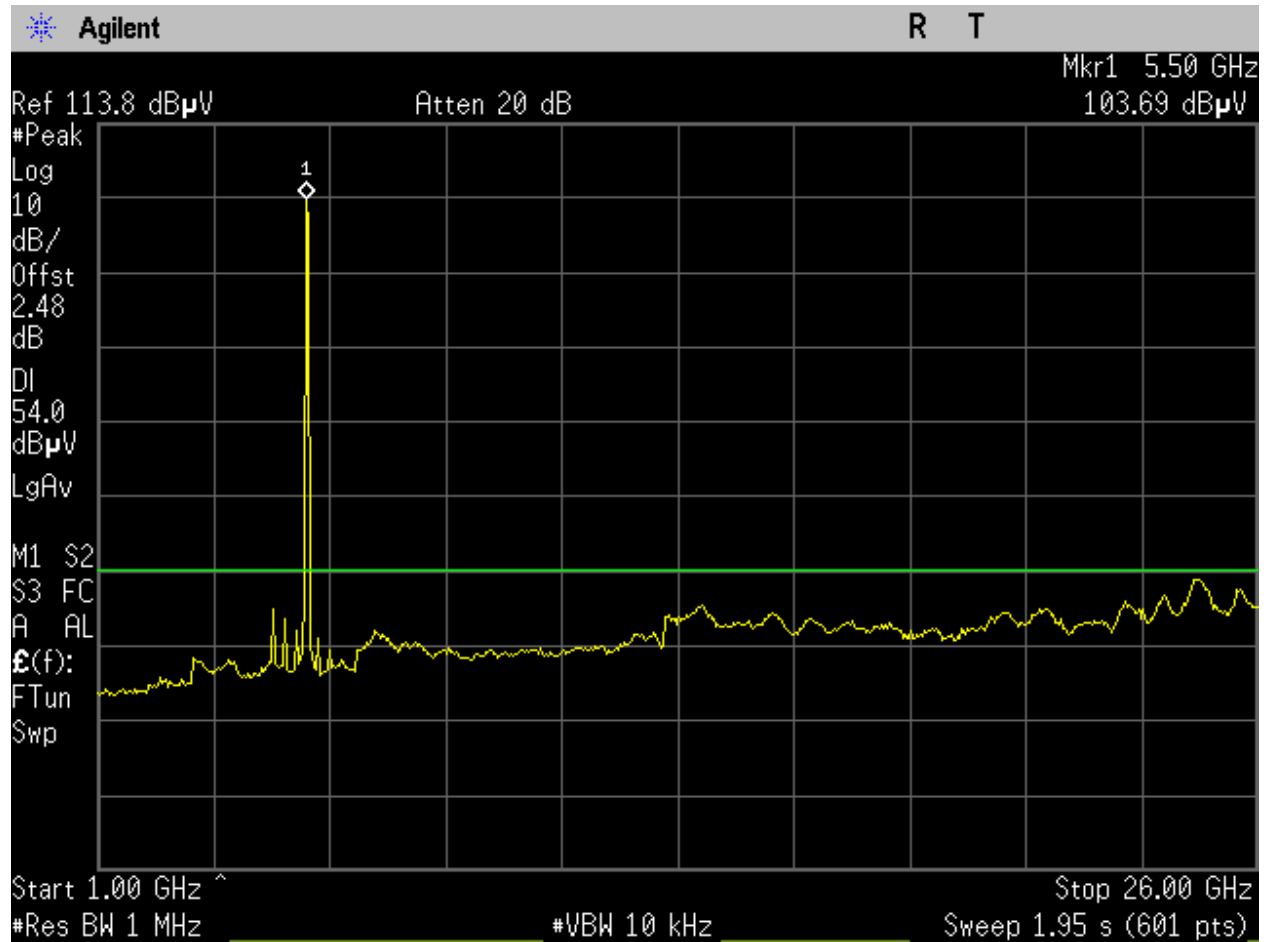


Figure 687: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

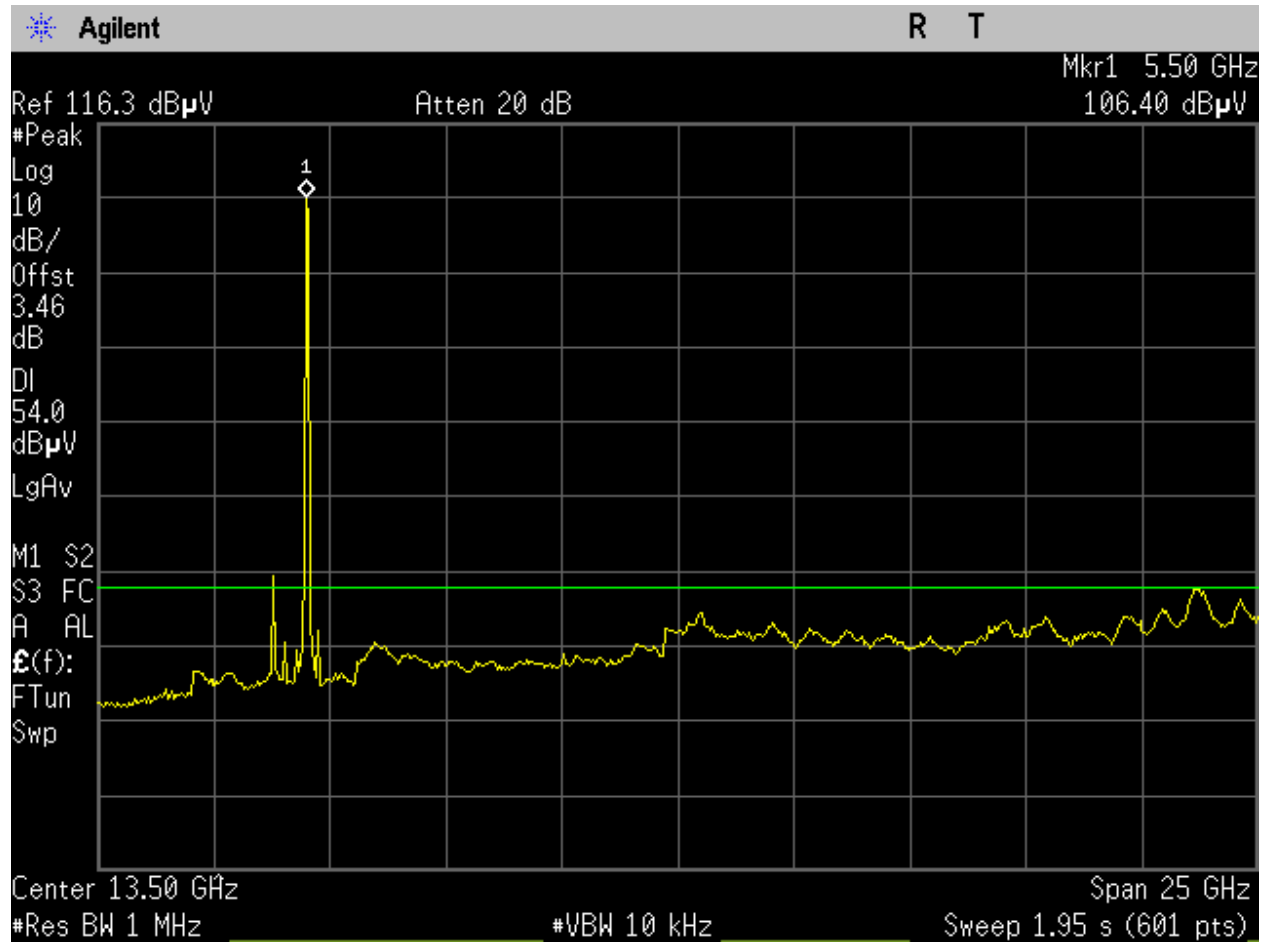


Figure 688: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

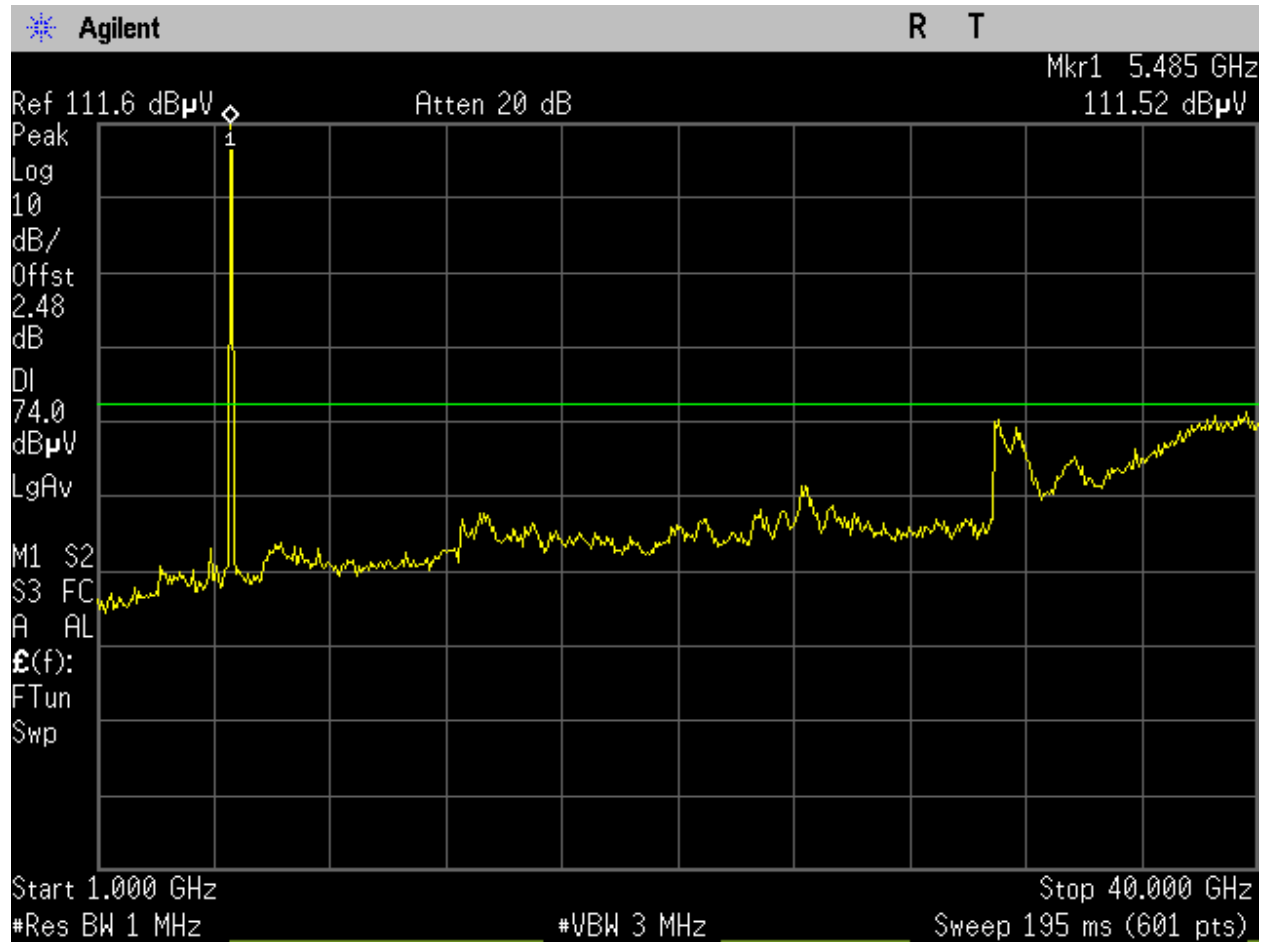


Figure 689: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_1-40GHz_Peak_Port 1.

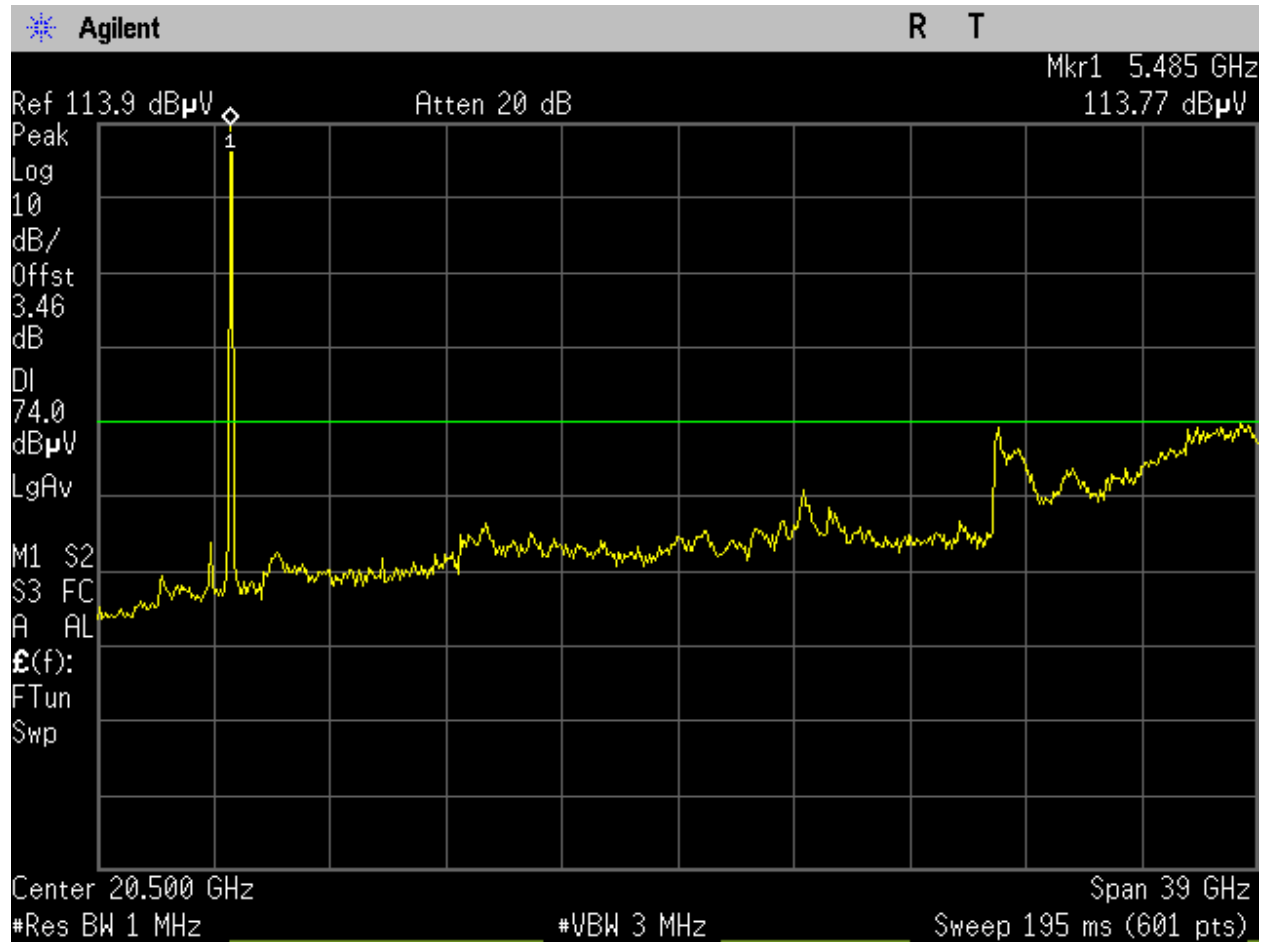


Figure 690: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_1-40GHz _Peak_Port 2.

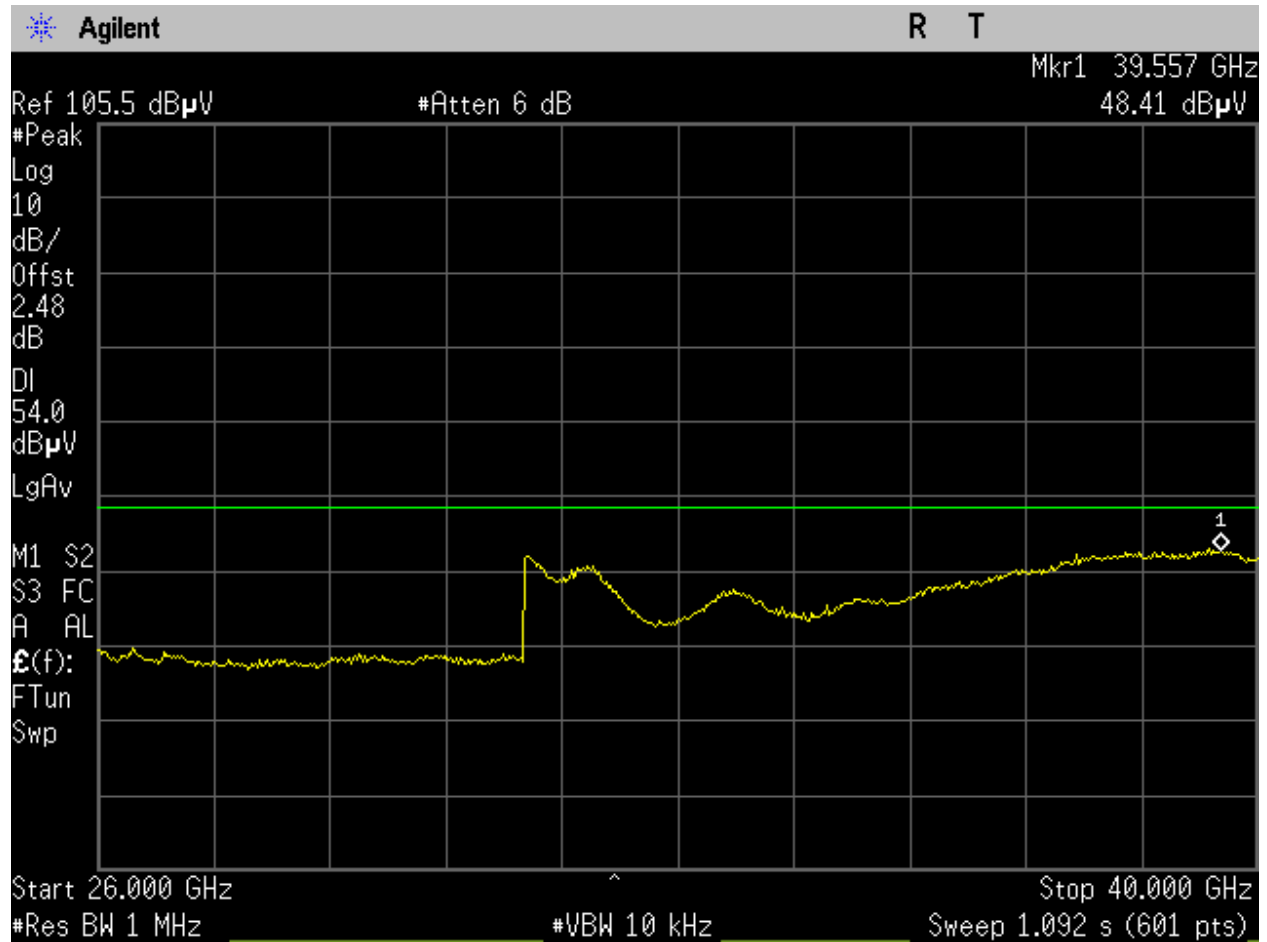


Figure 691: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

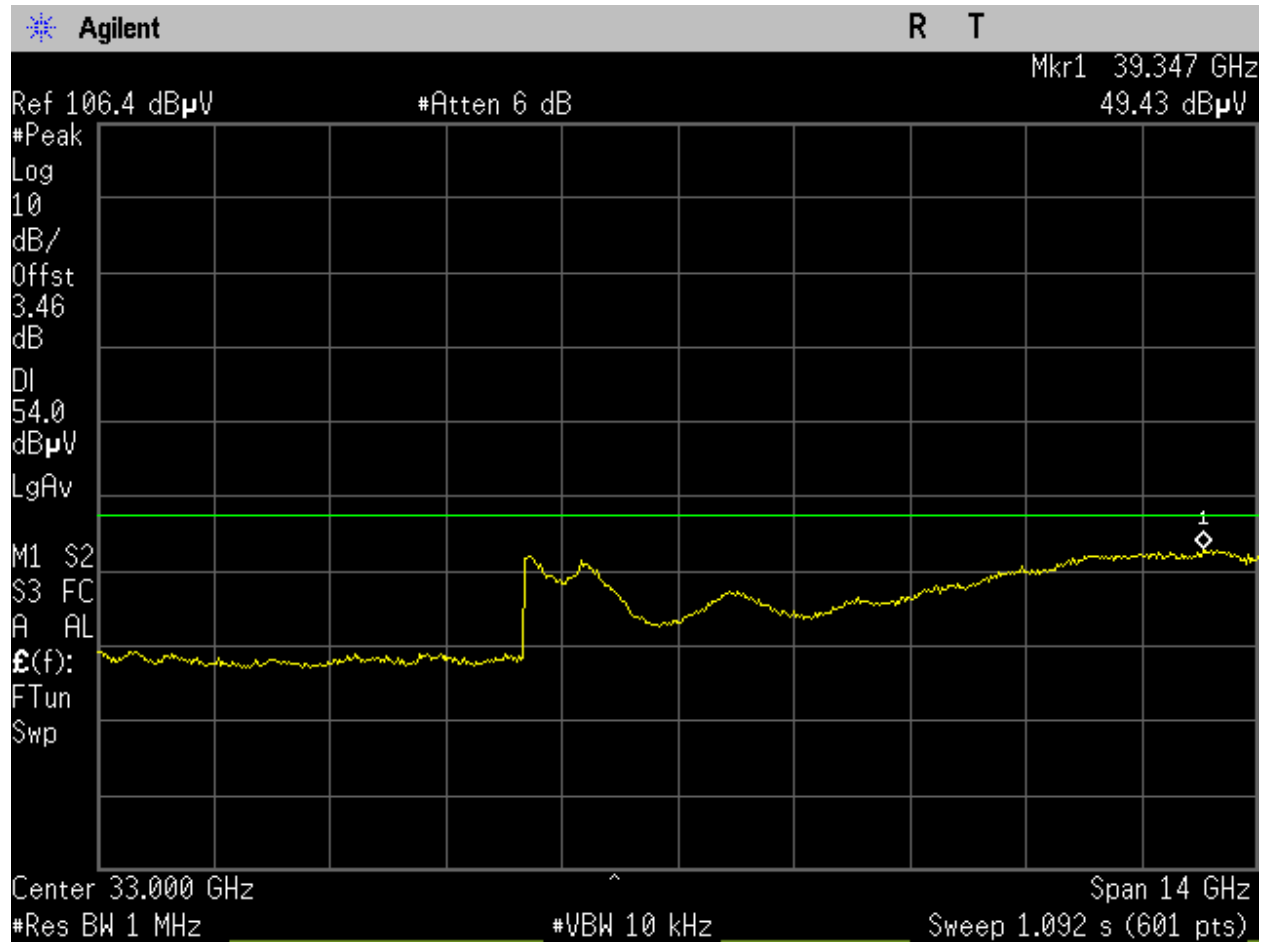


Figure 692: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

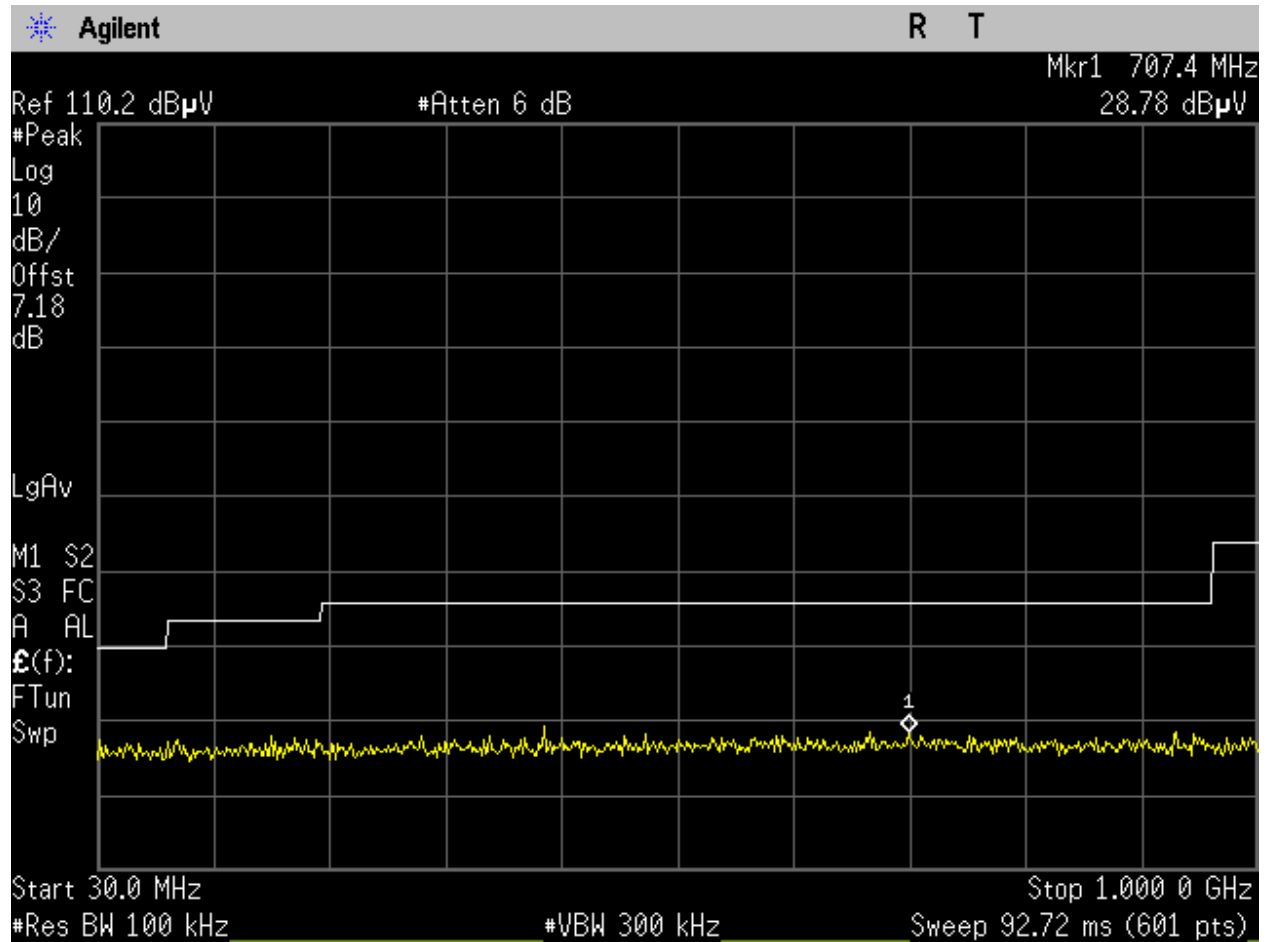


Figure 693: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.

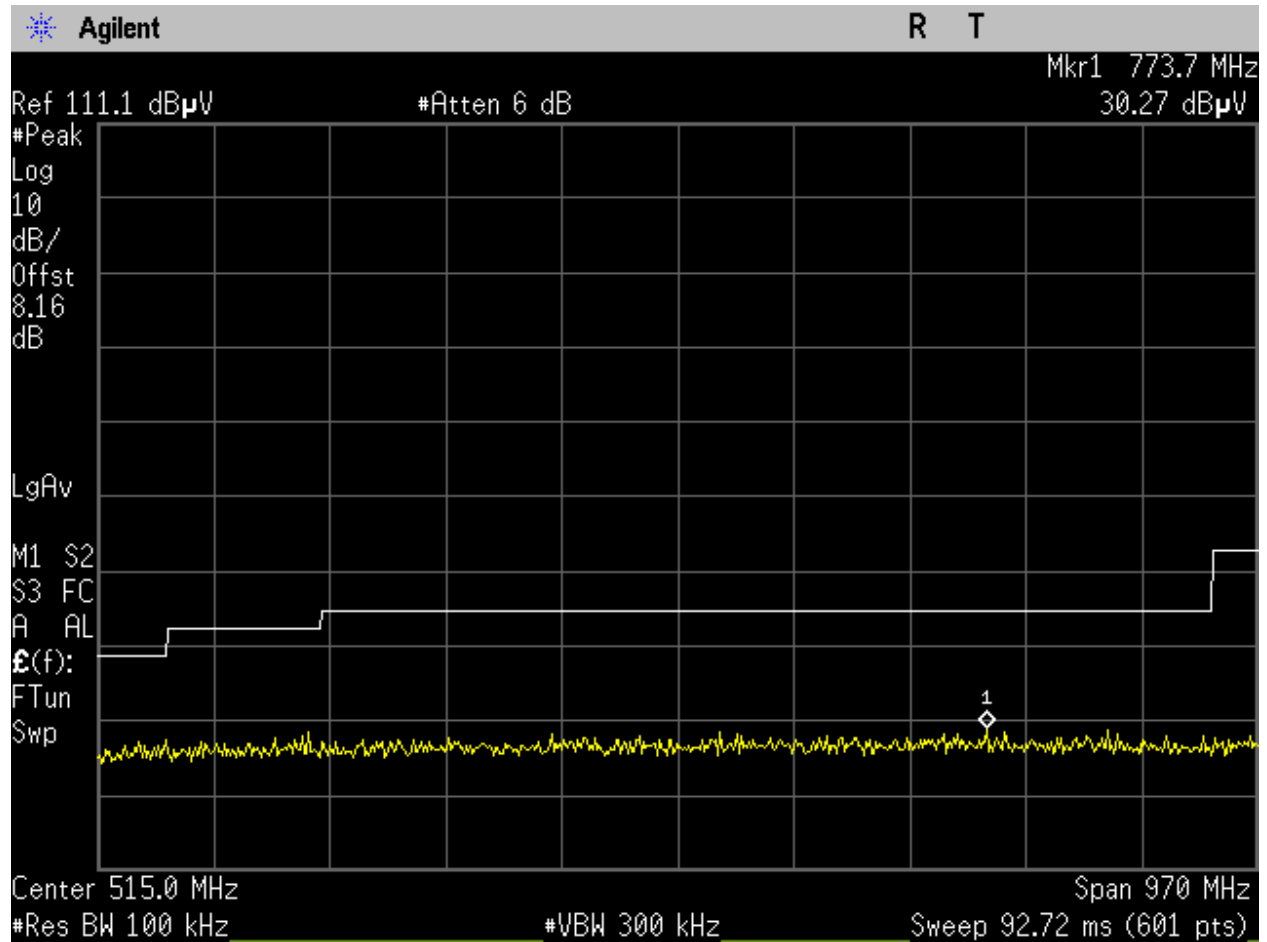


Figure 694: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 2.

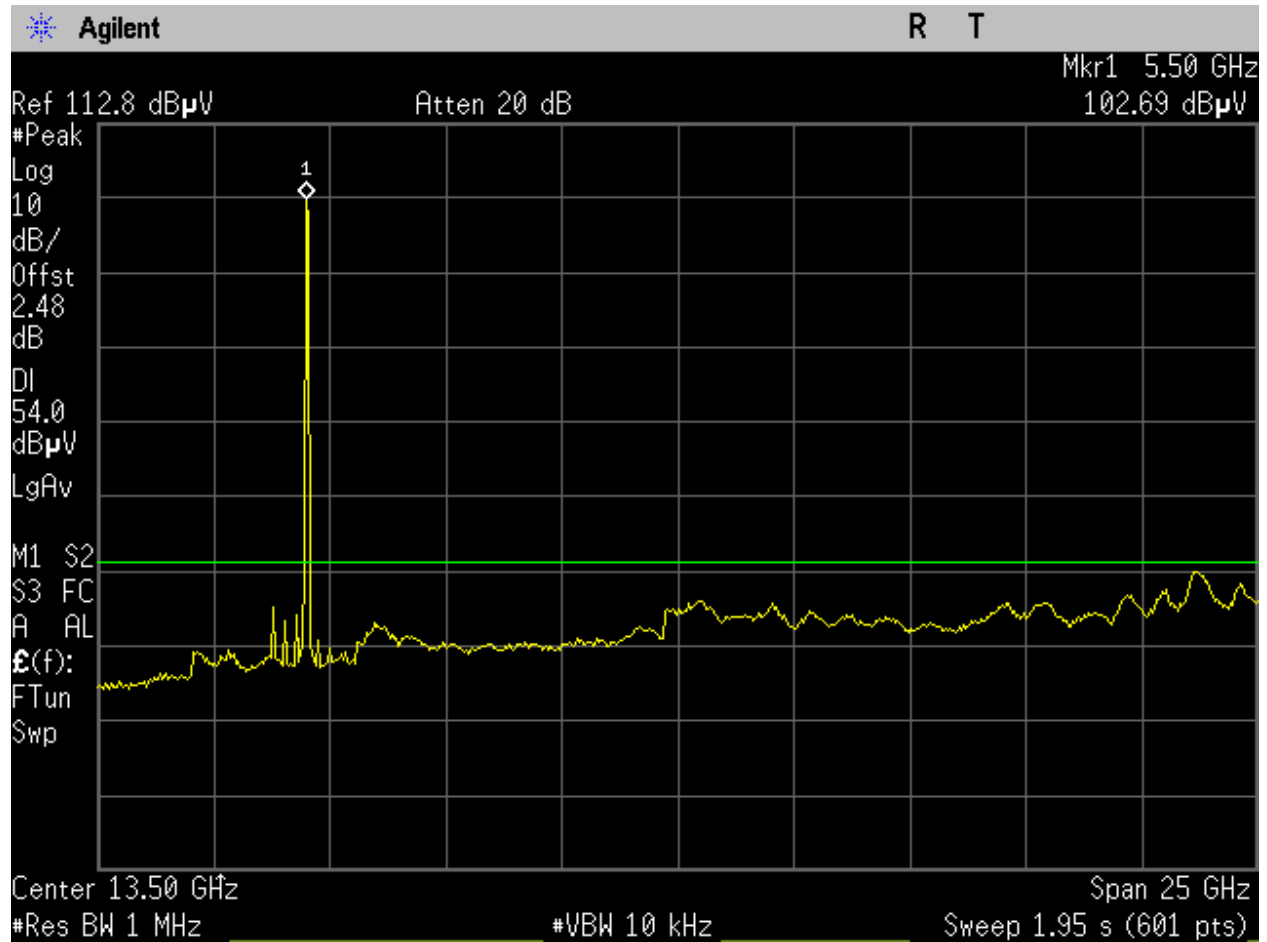


Figure 695: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_15.209_1-26GHz avg_Port 1.

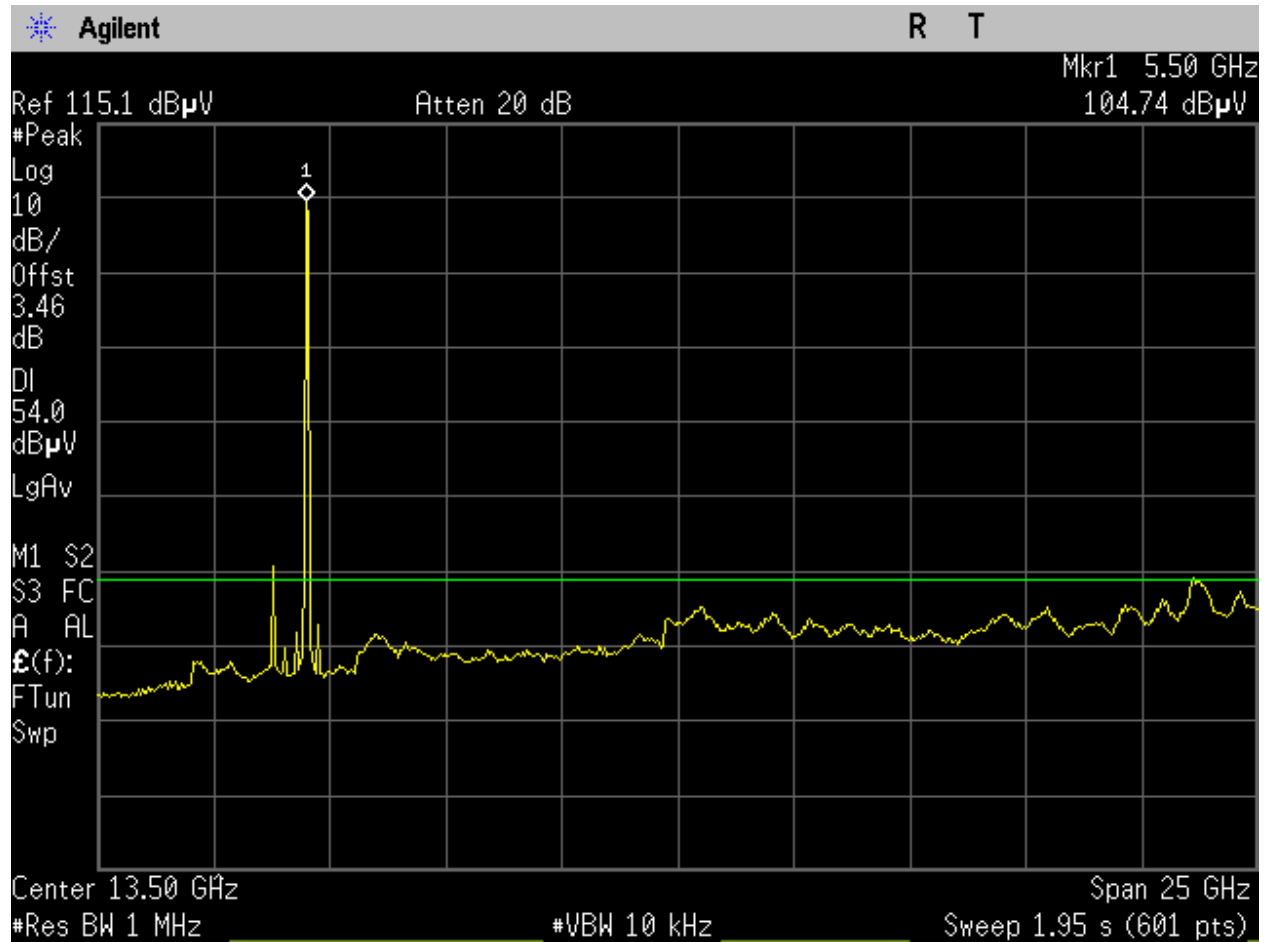


Figure 696: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_15.209_1-26GHz avg_Port 2.

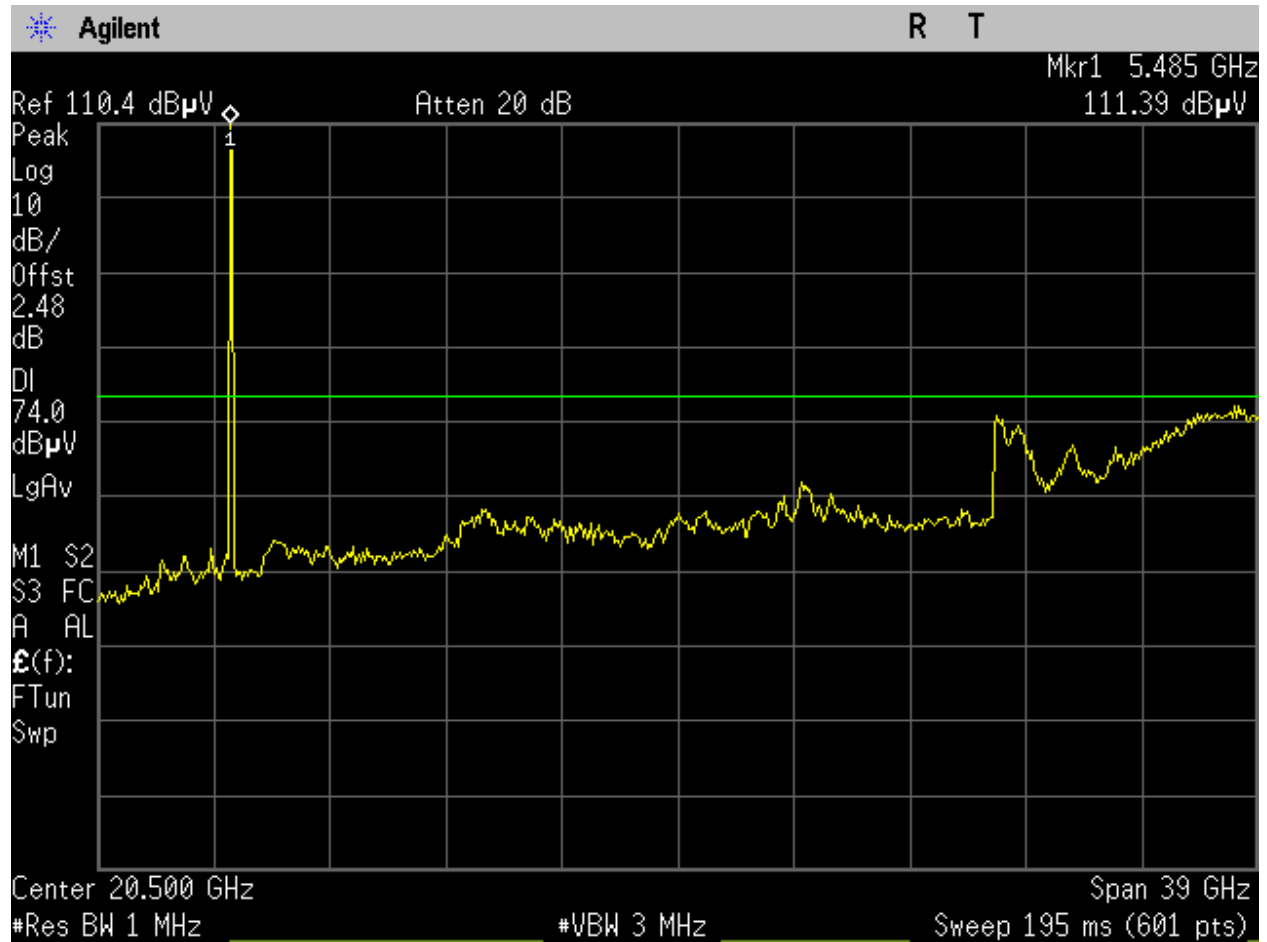


Figure 697: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_15.209_1-40GHz _Peak_Port 1.