

APPENDIX FCC407 UNII 2

WIR128375

FCC407 UNII 2 Data

§ 15.203 Antenna Requirement

No appendix data.

§ 15. 403 26dB Bandwidth

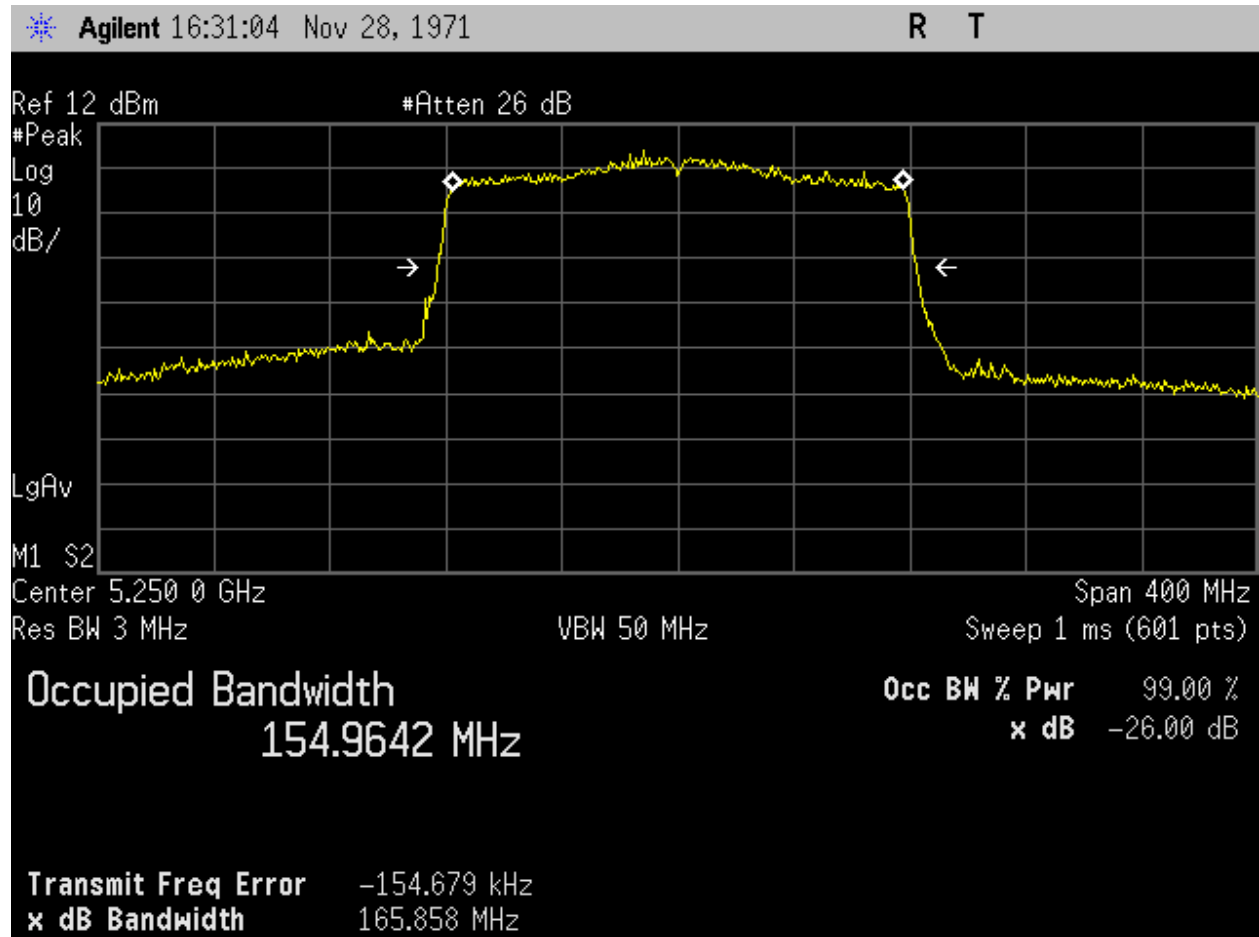


Figure 1: U-NII-1,2A_5250MHz_Mid_Mid Ch_50_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

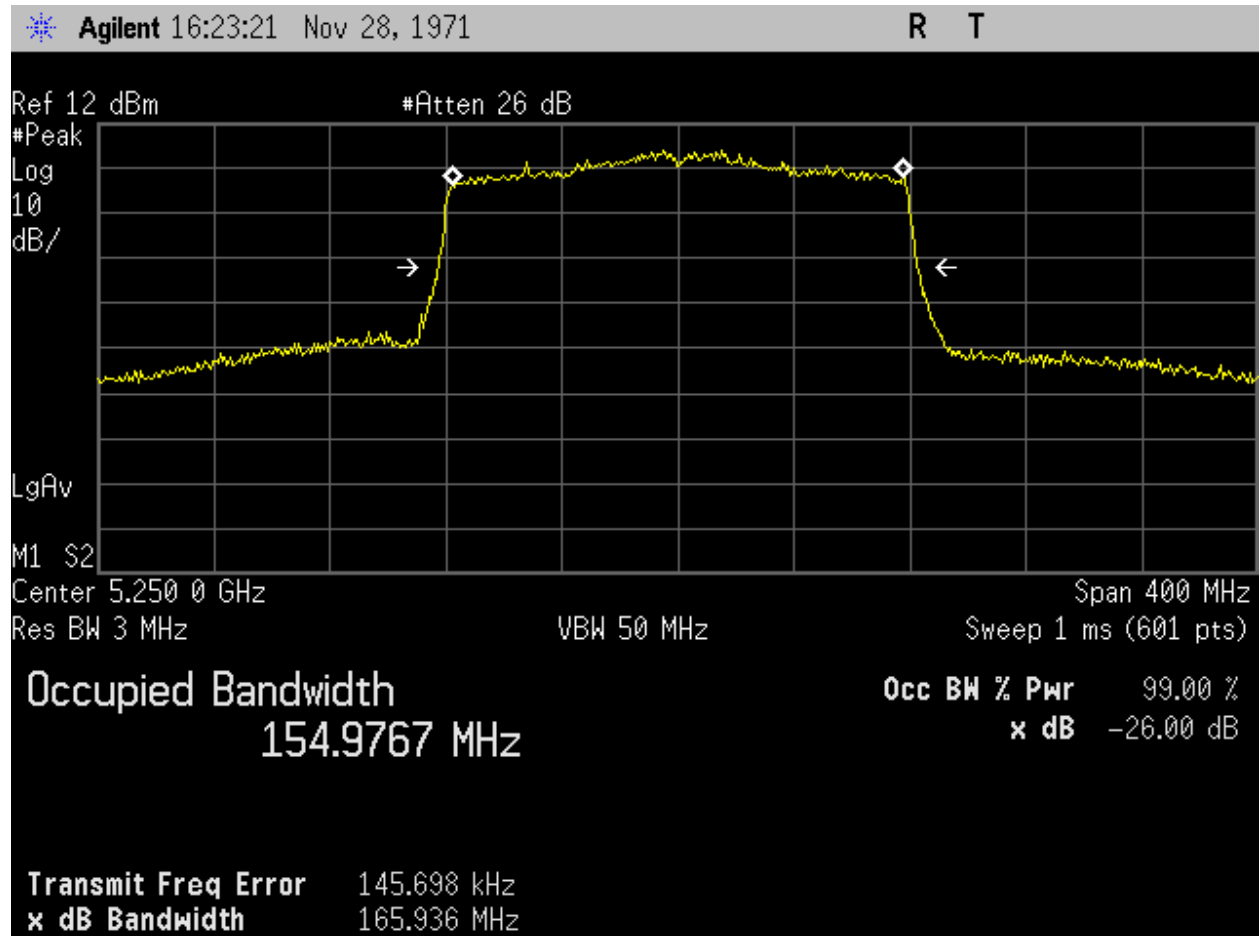


Figure 2: U-NII-1,2A_5250MHz_Mid_Mid Ch_50_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

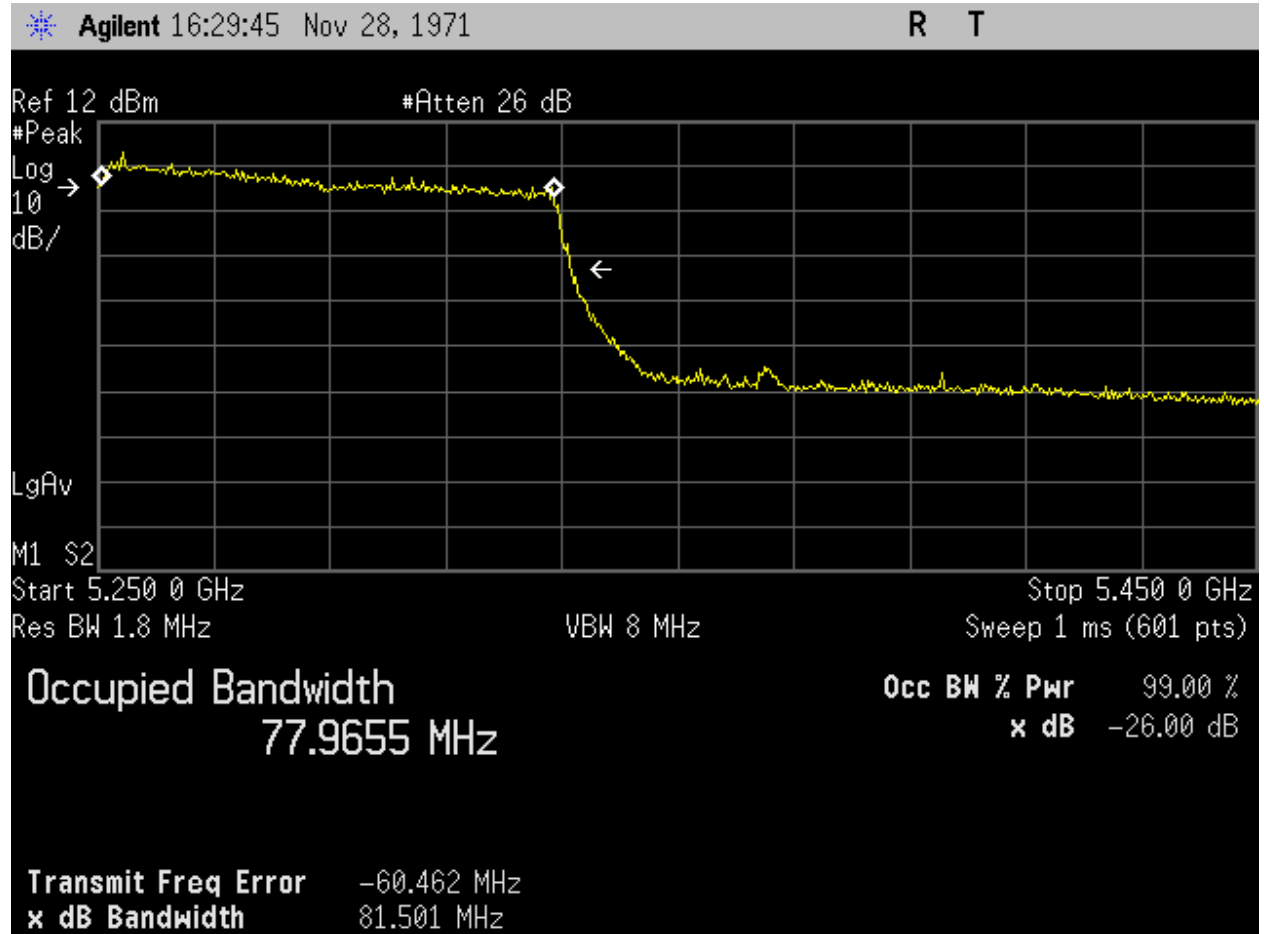


Figure 3: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

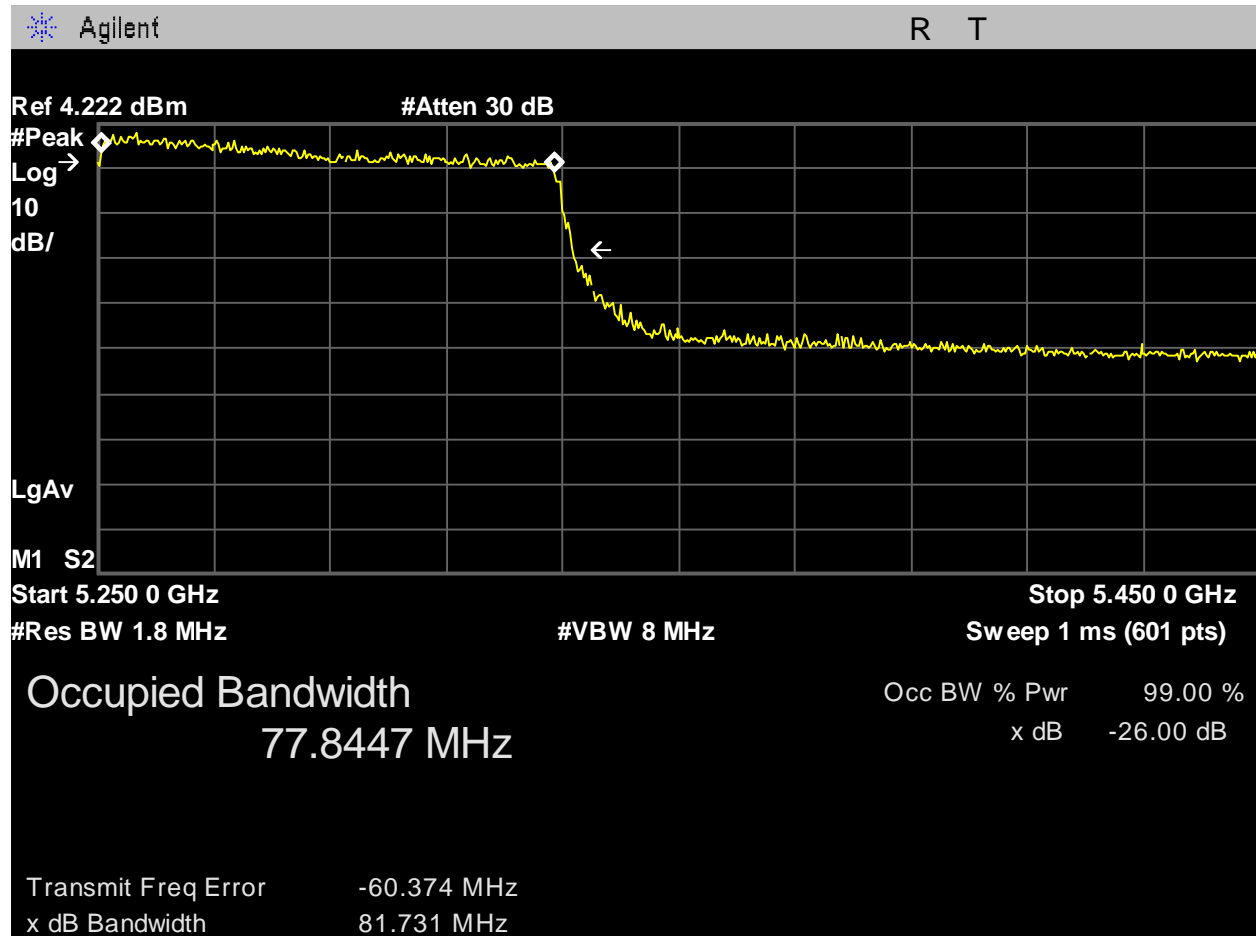


Figure 4: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

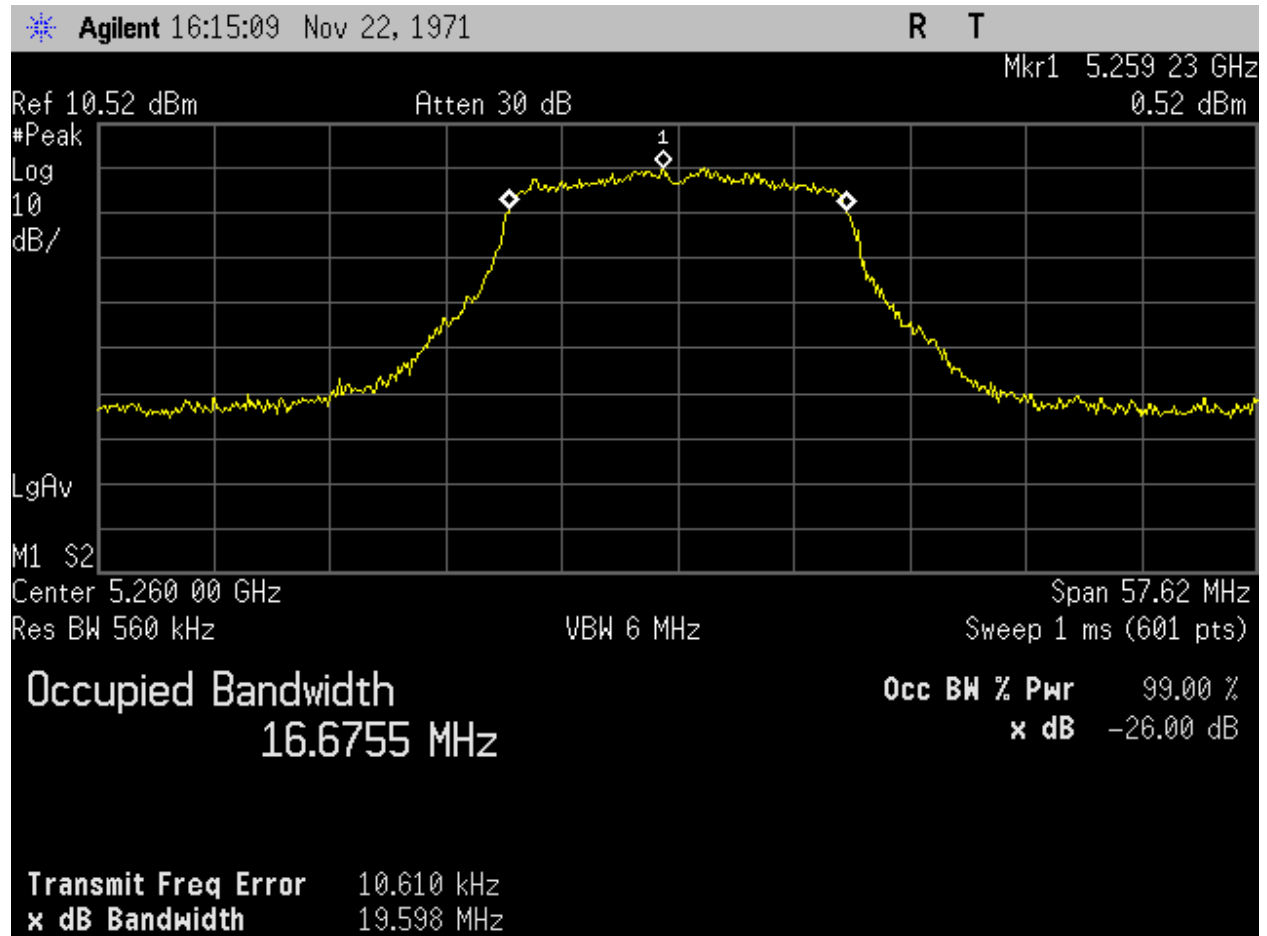


Figure 5: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

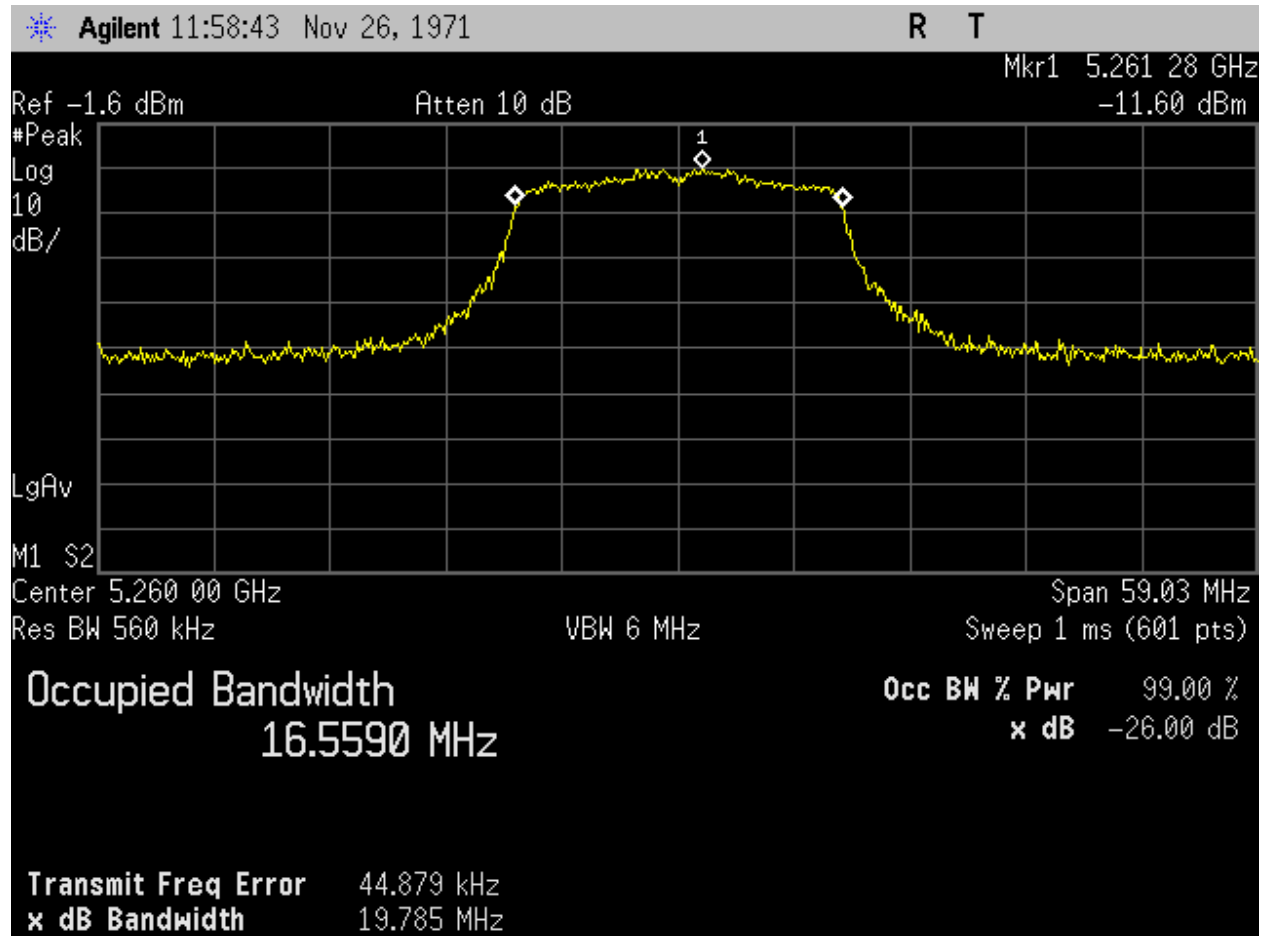


Figure 6: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

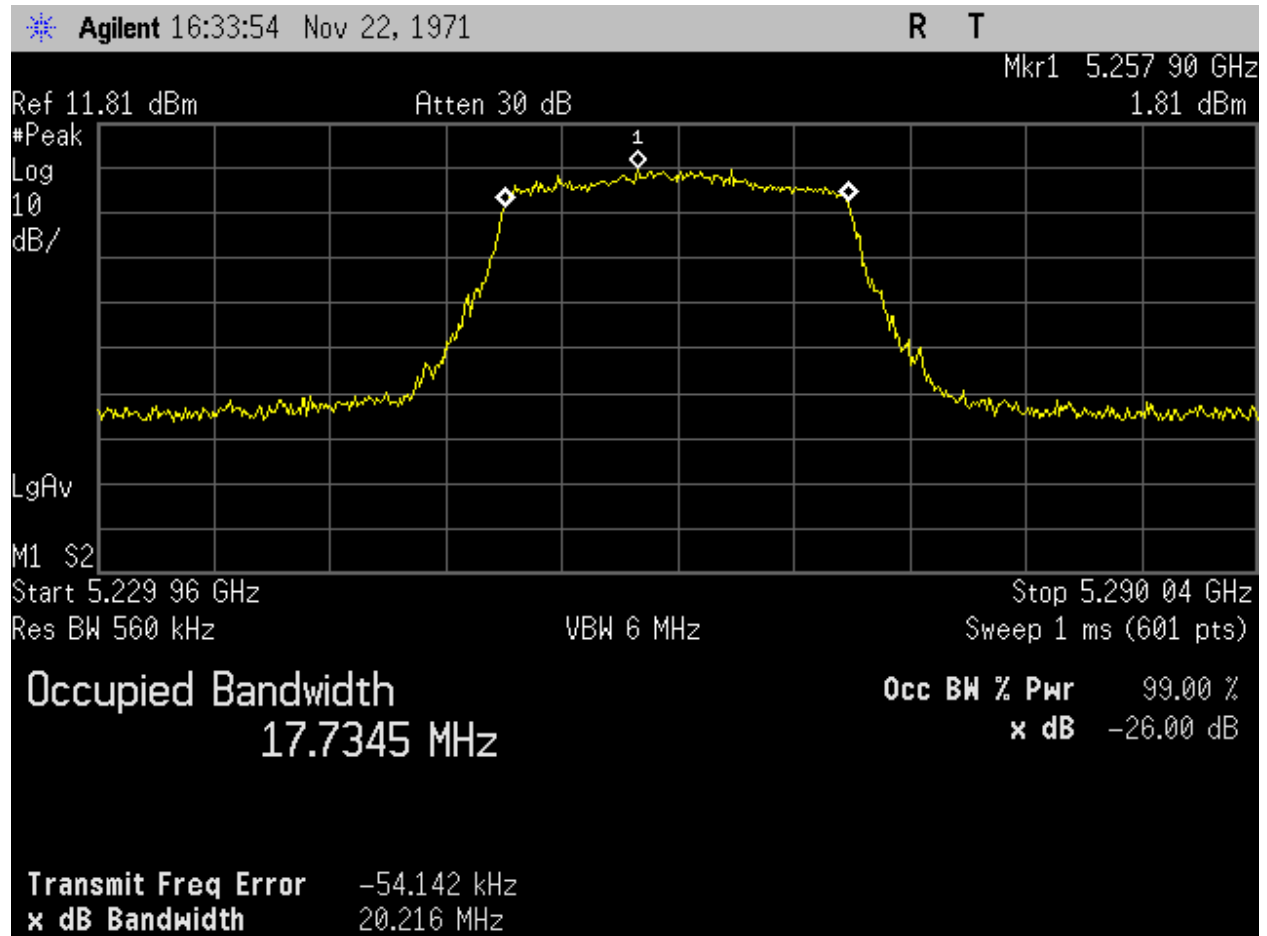


Figure 7: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

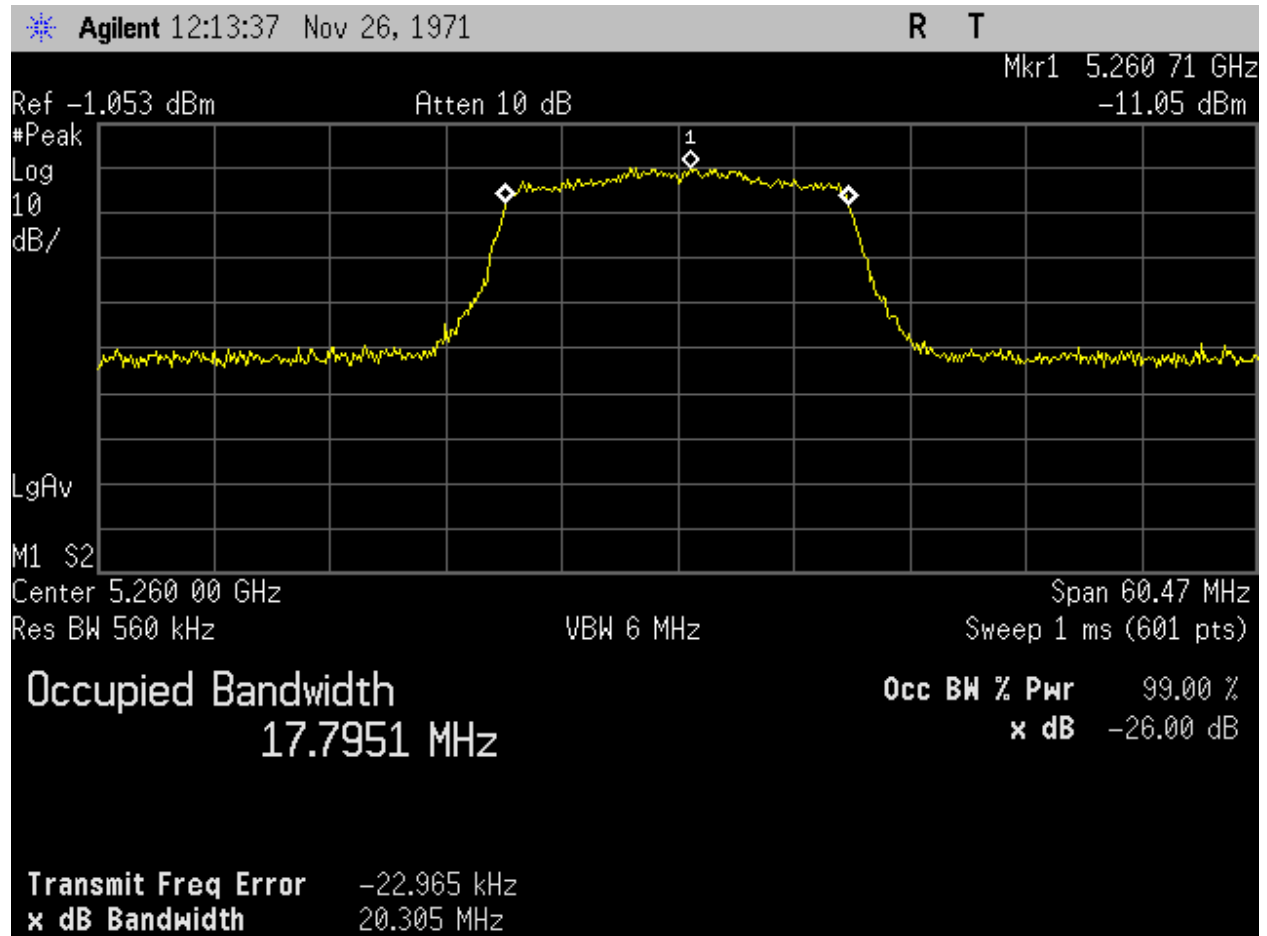


Figure 8: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

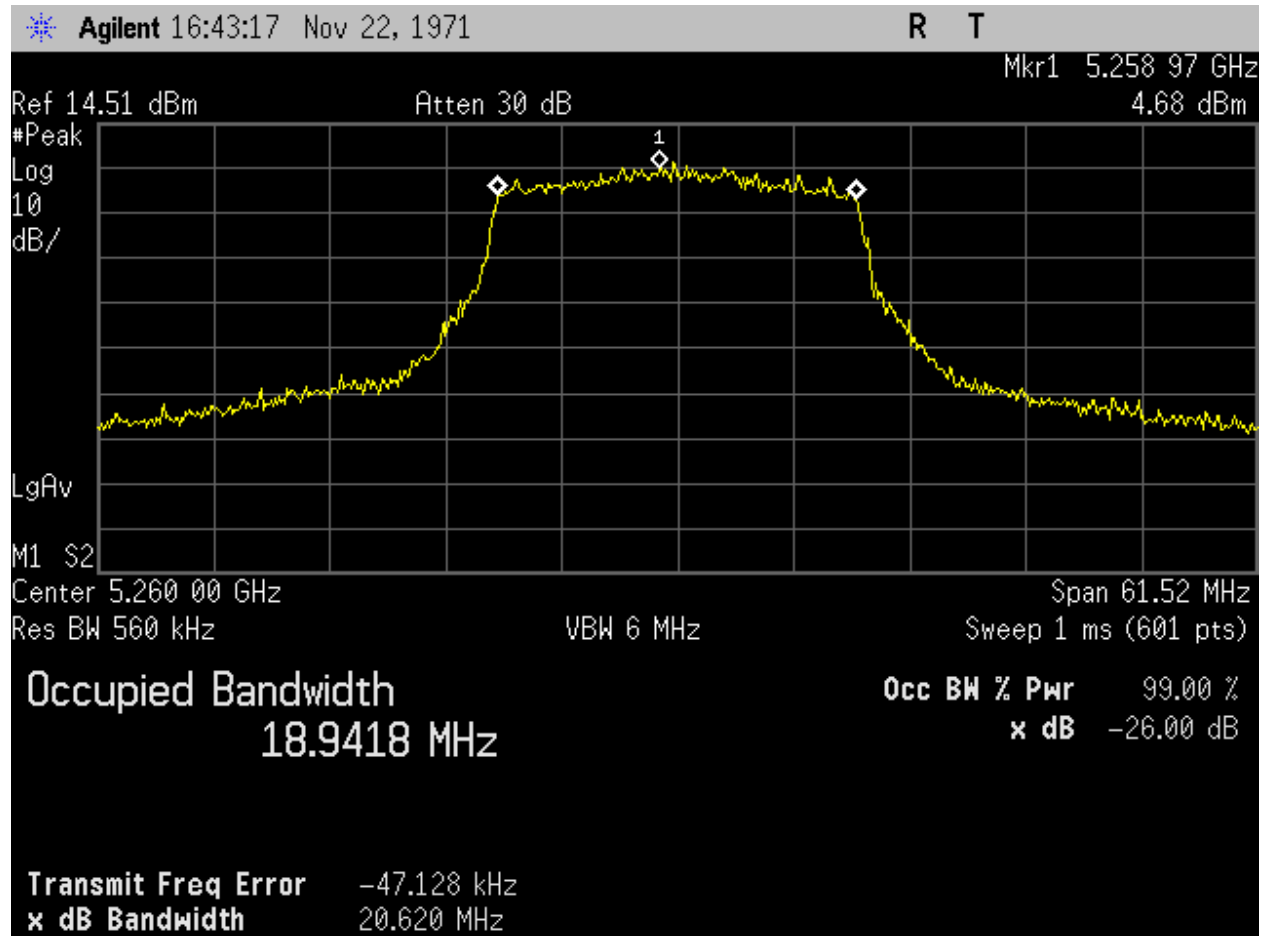


Figure 9: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

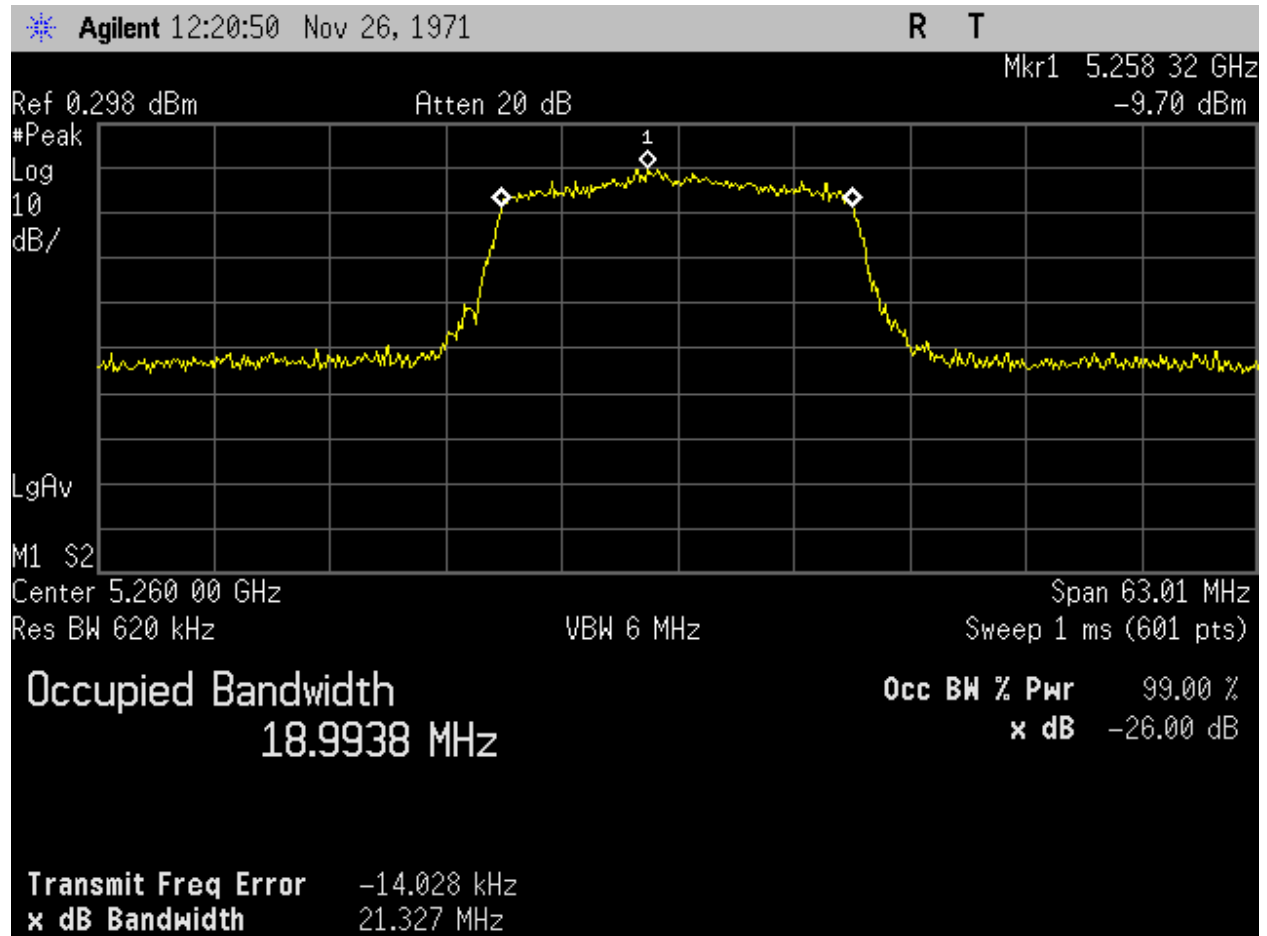


Figure 10: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

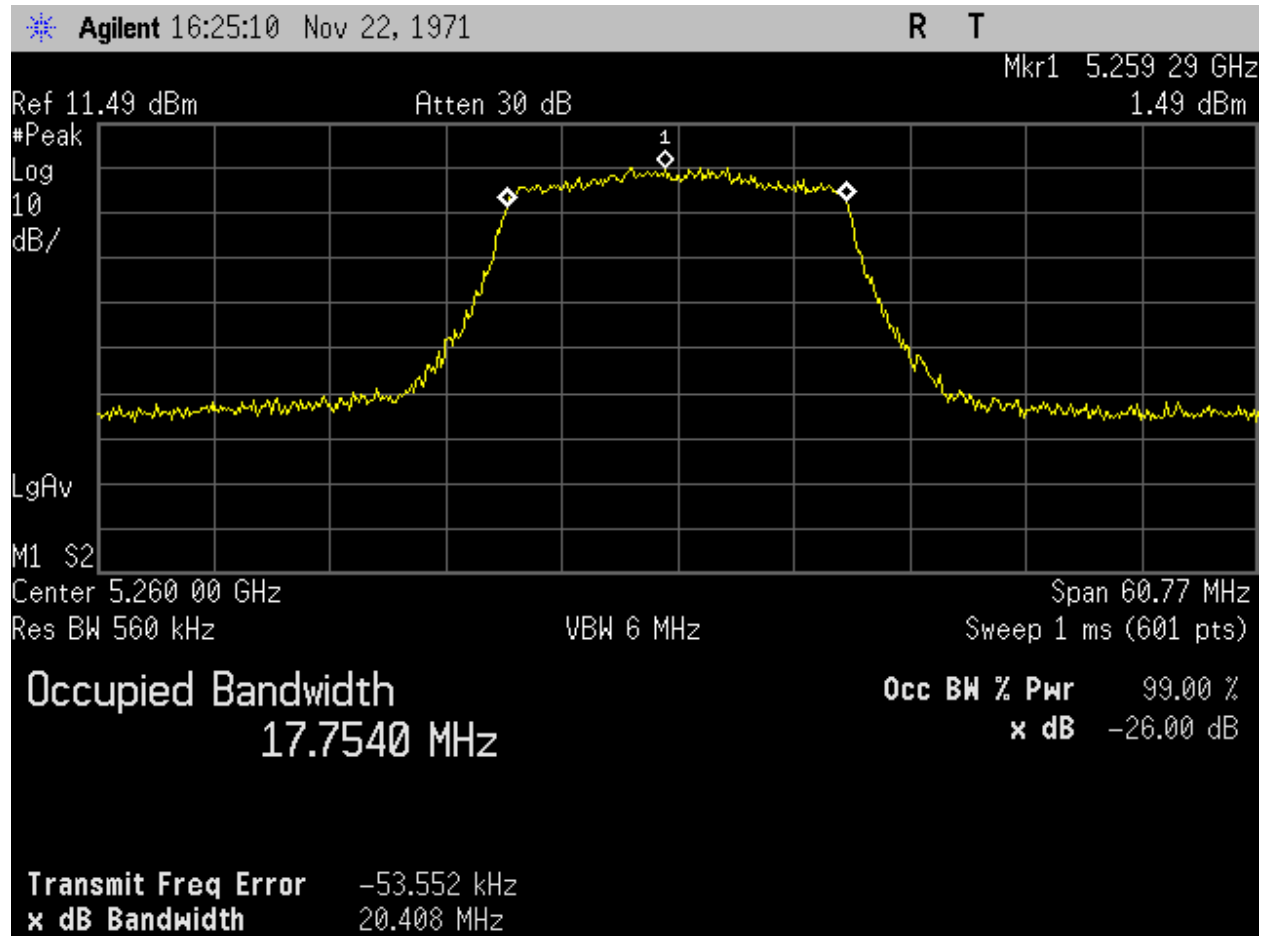


Figure 11: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

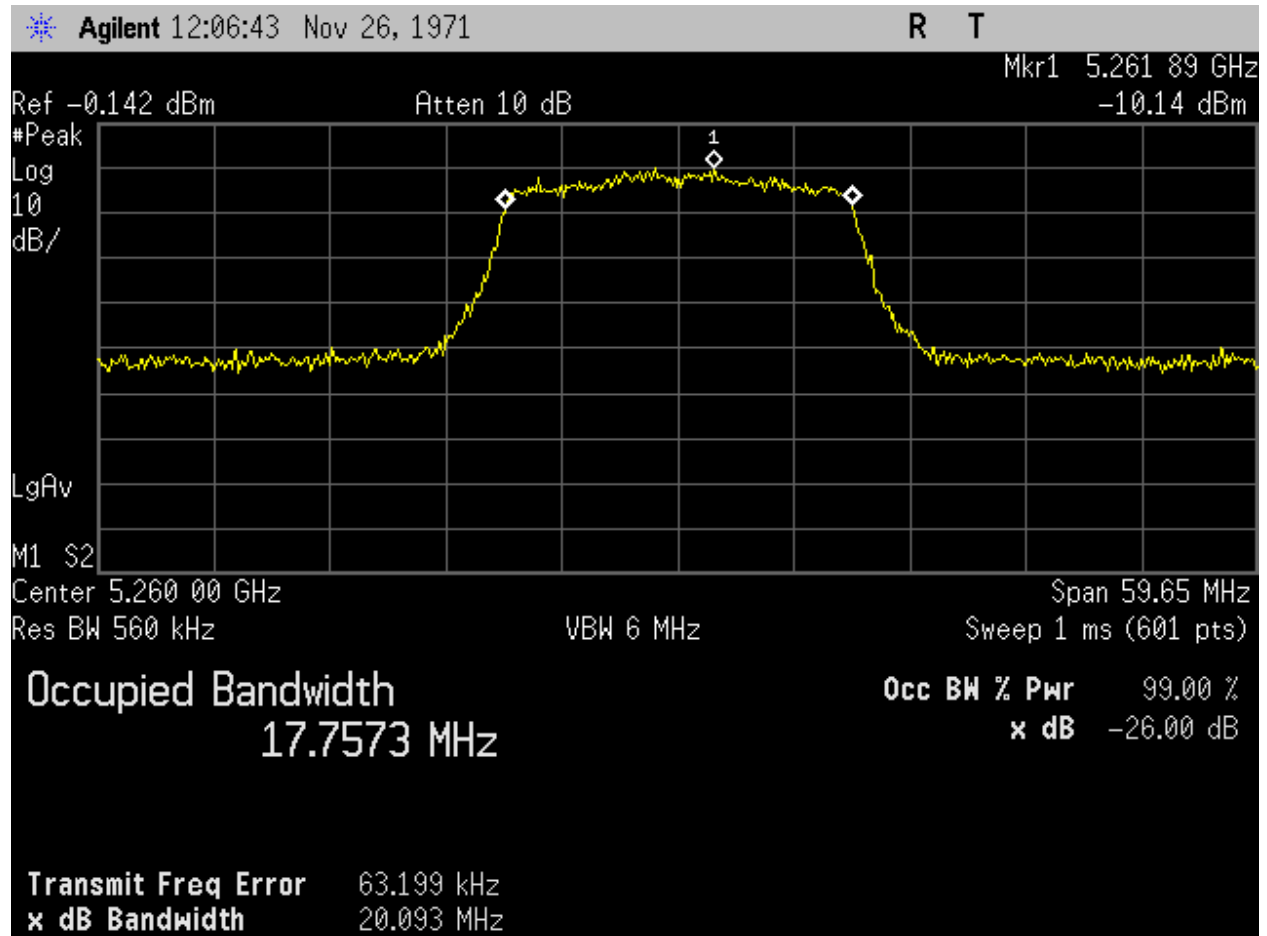


Figure 12: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

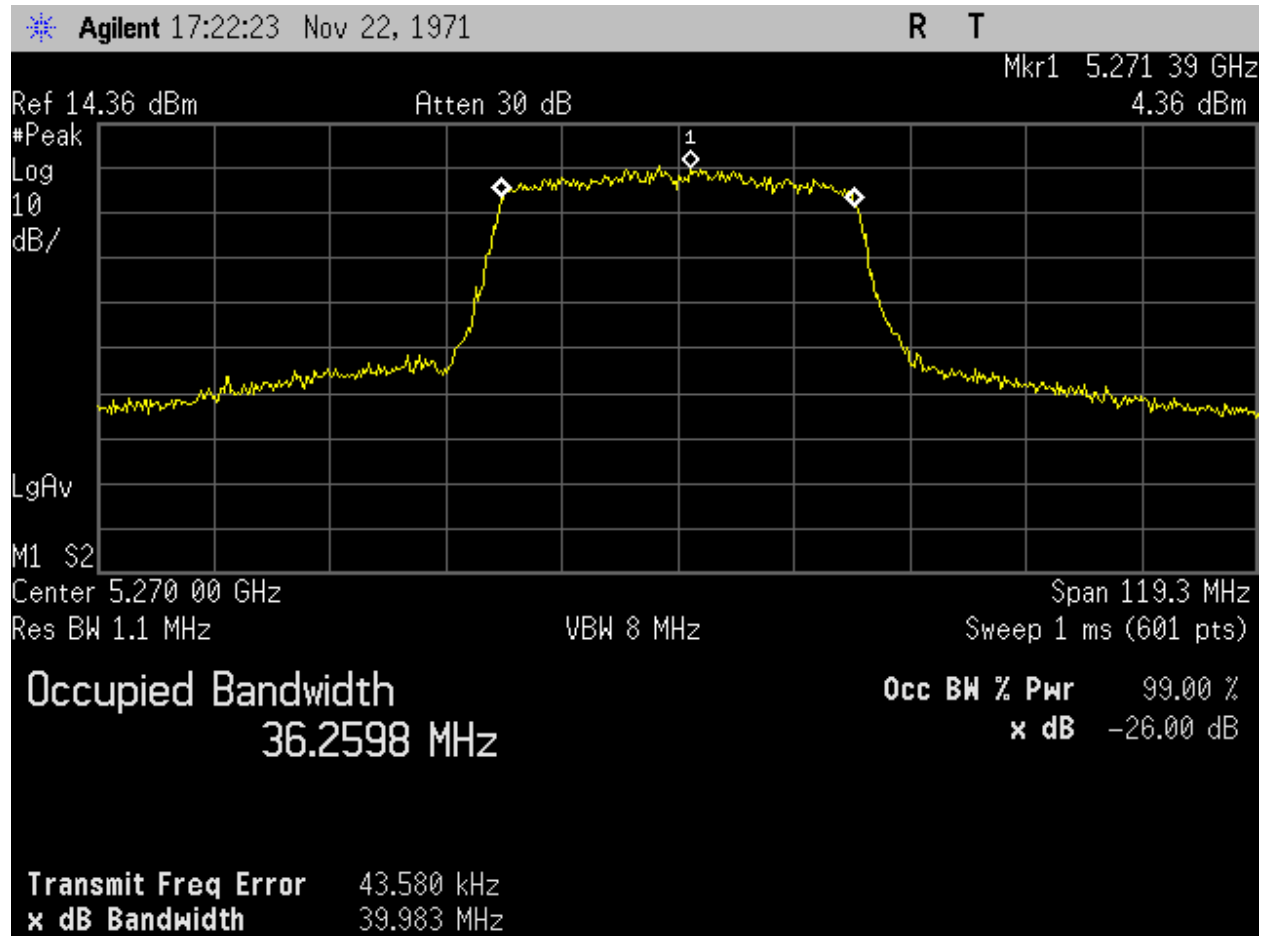


Figure 13: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

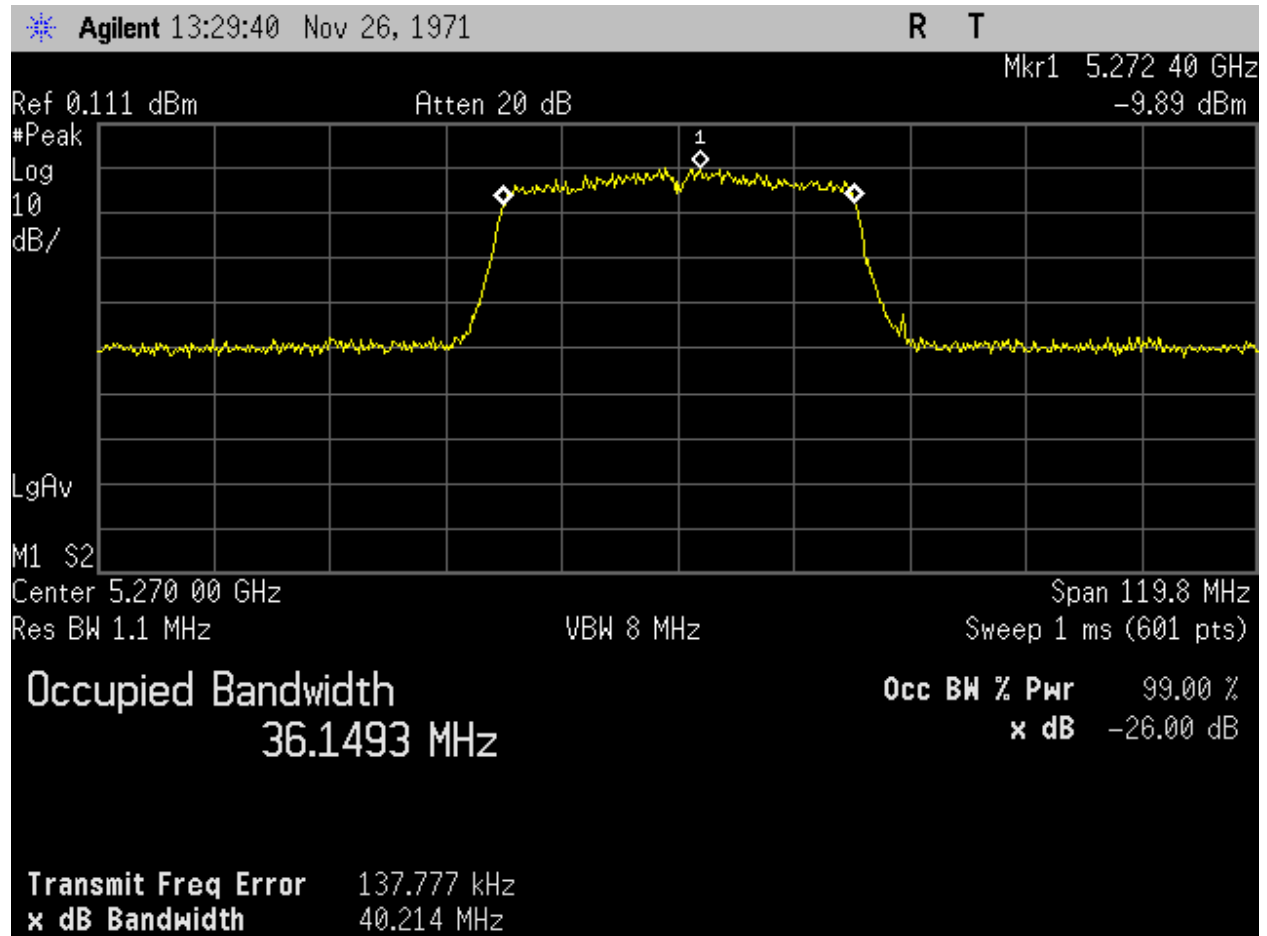


Figure 14: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

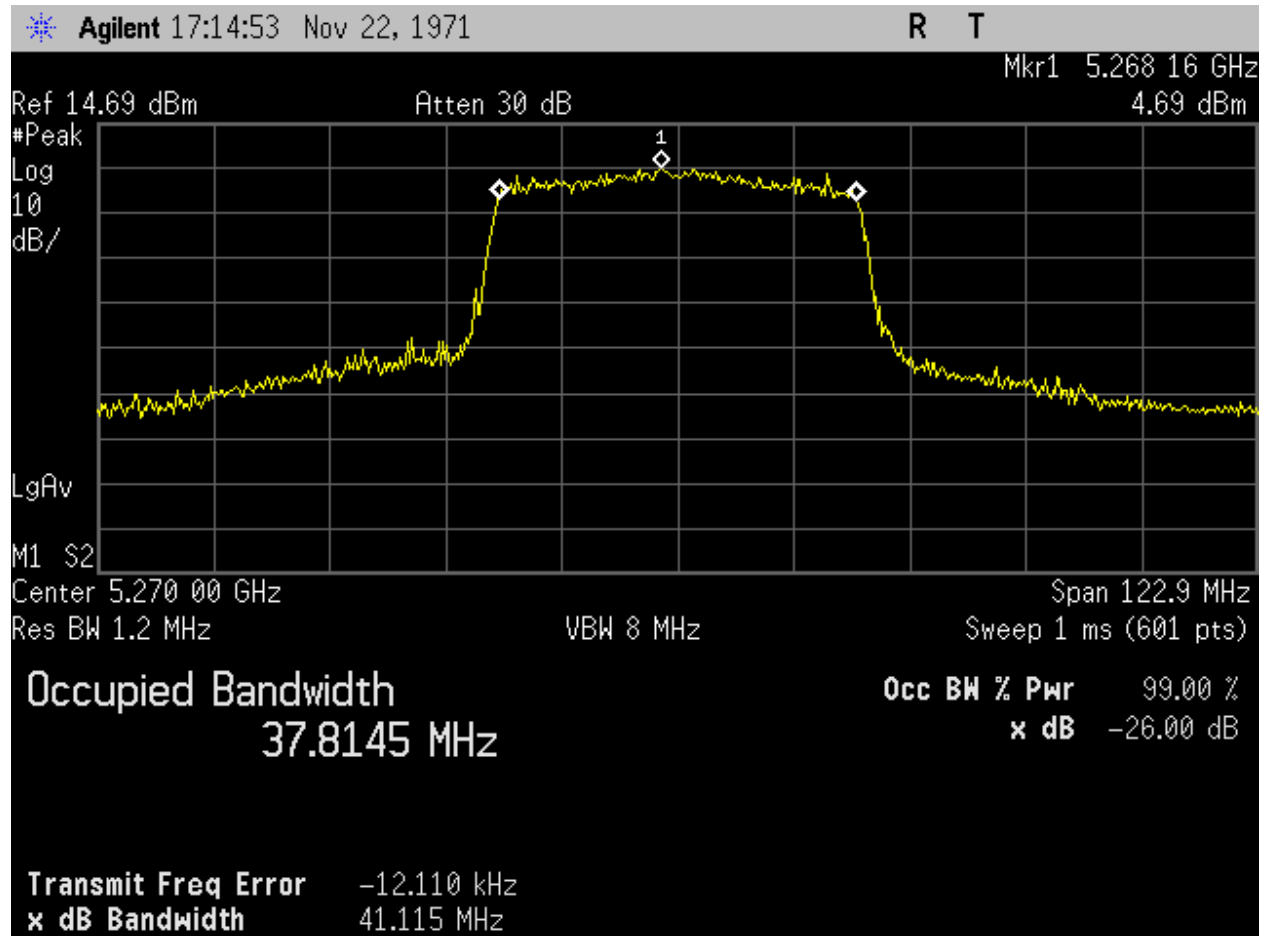


Figure 15: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

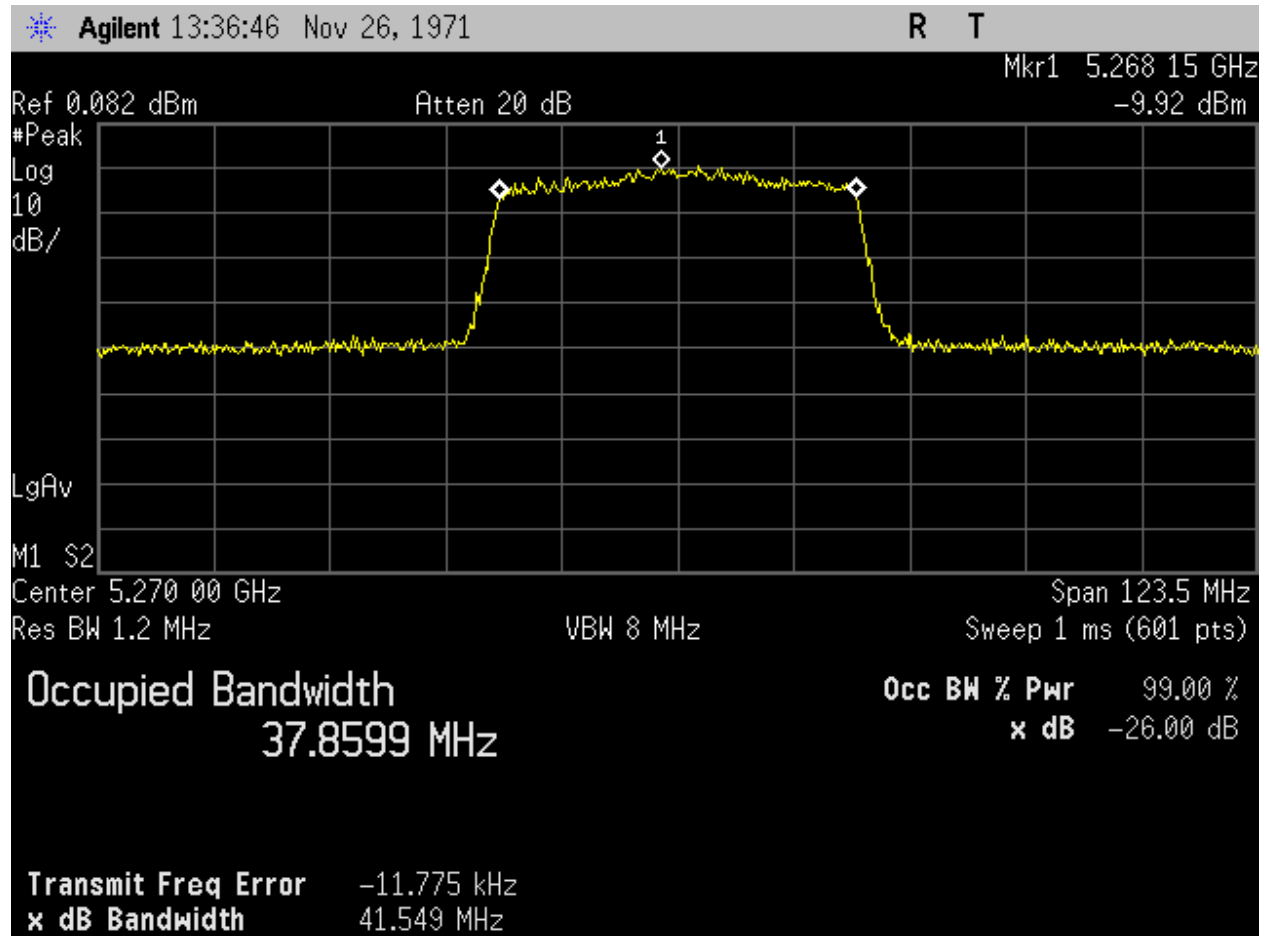


Figure 16: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

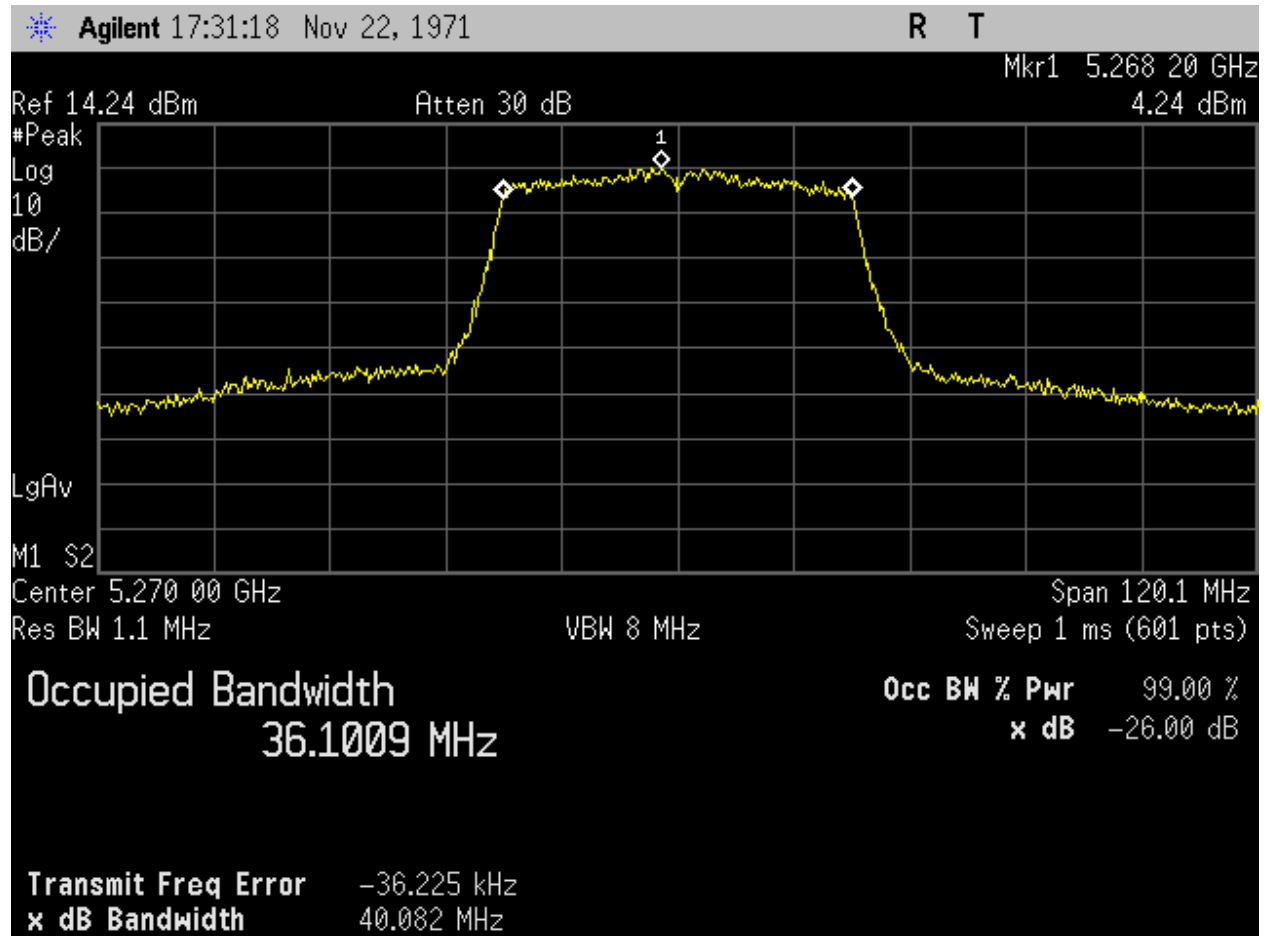


Figure 17: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

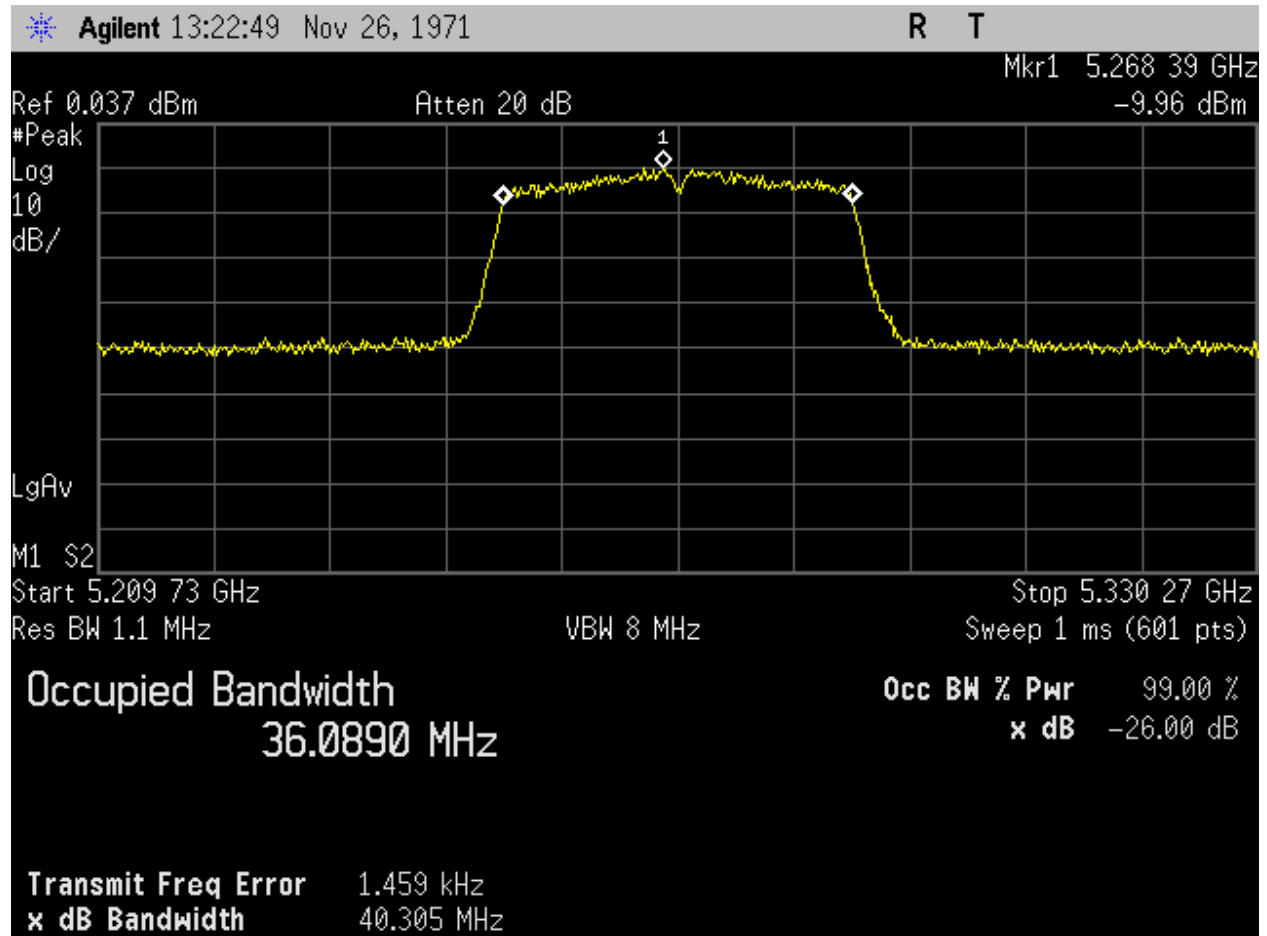


Figure 18: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

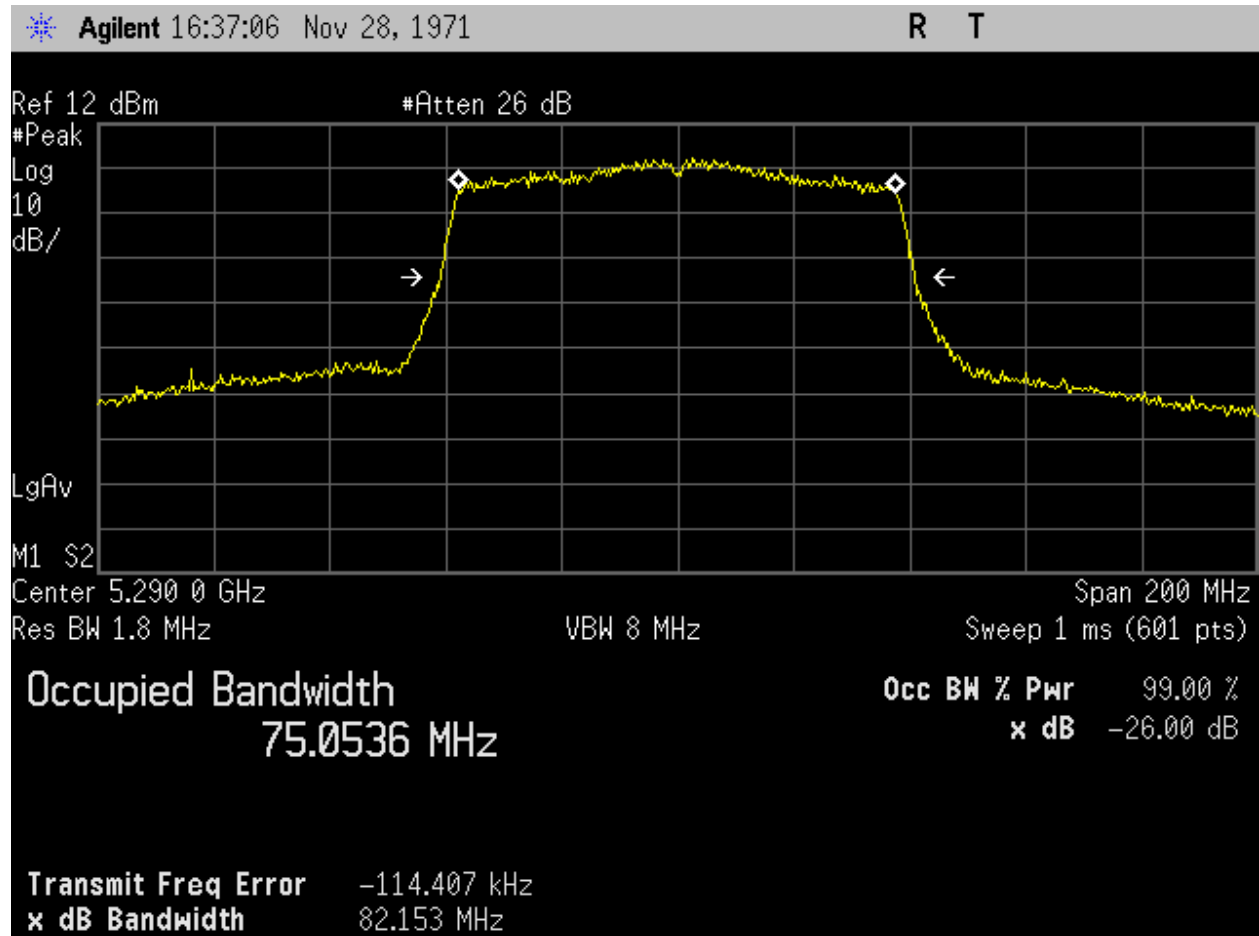


Figure 19: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

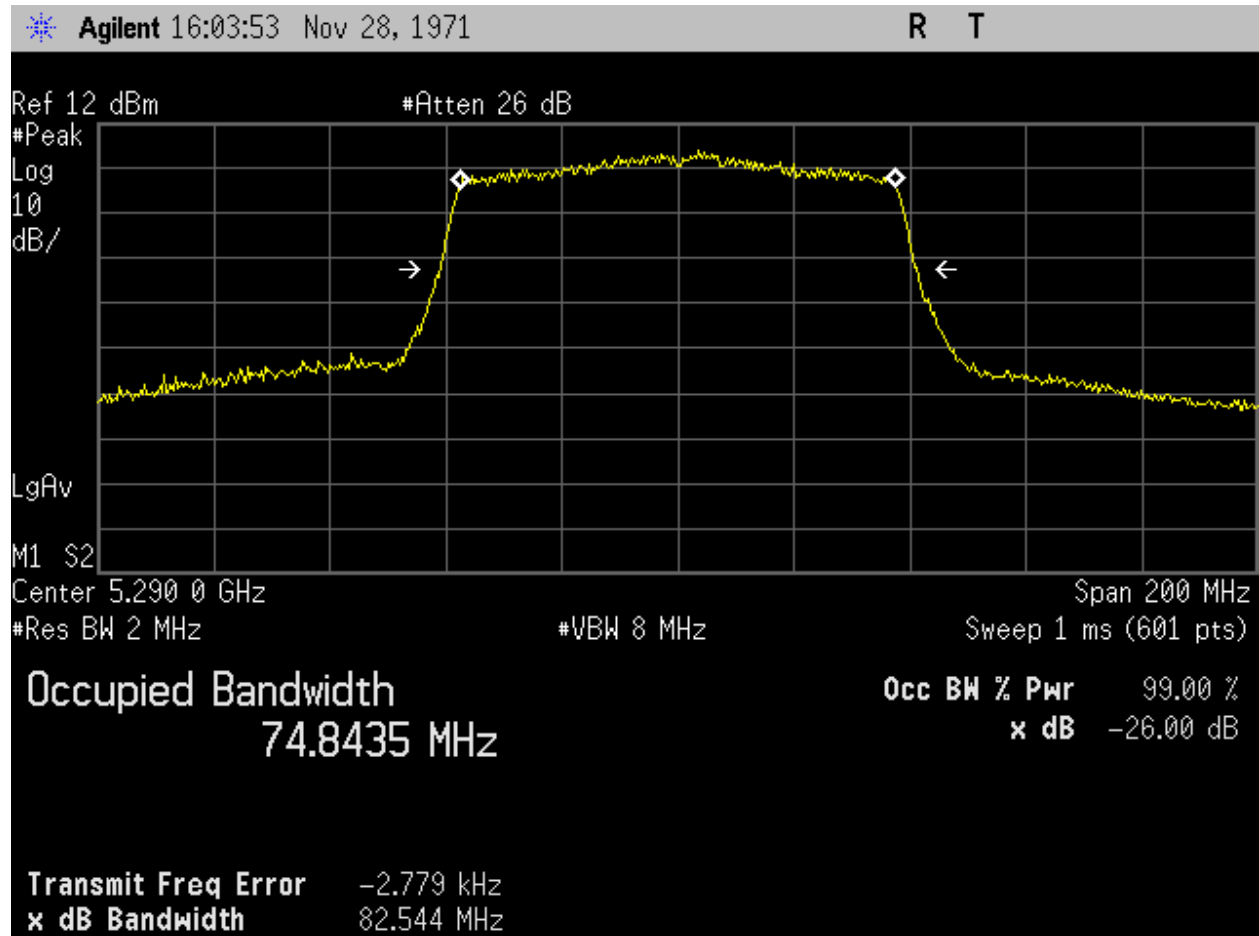


Figure 20: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

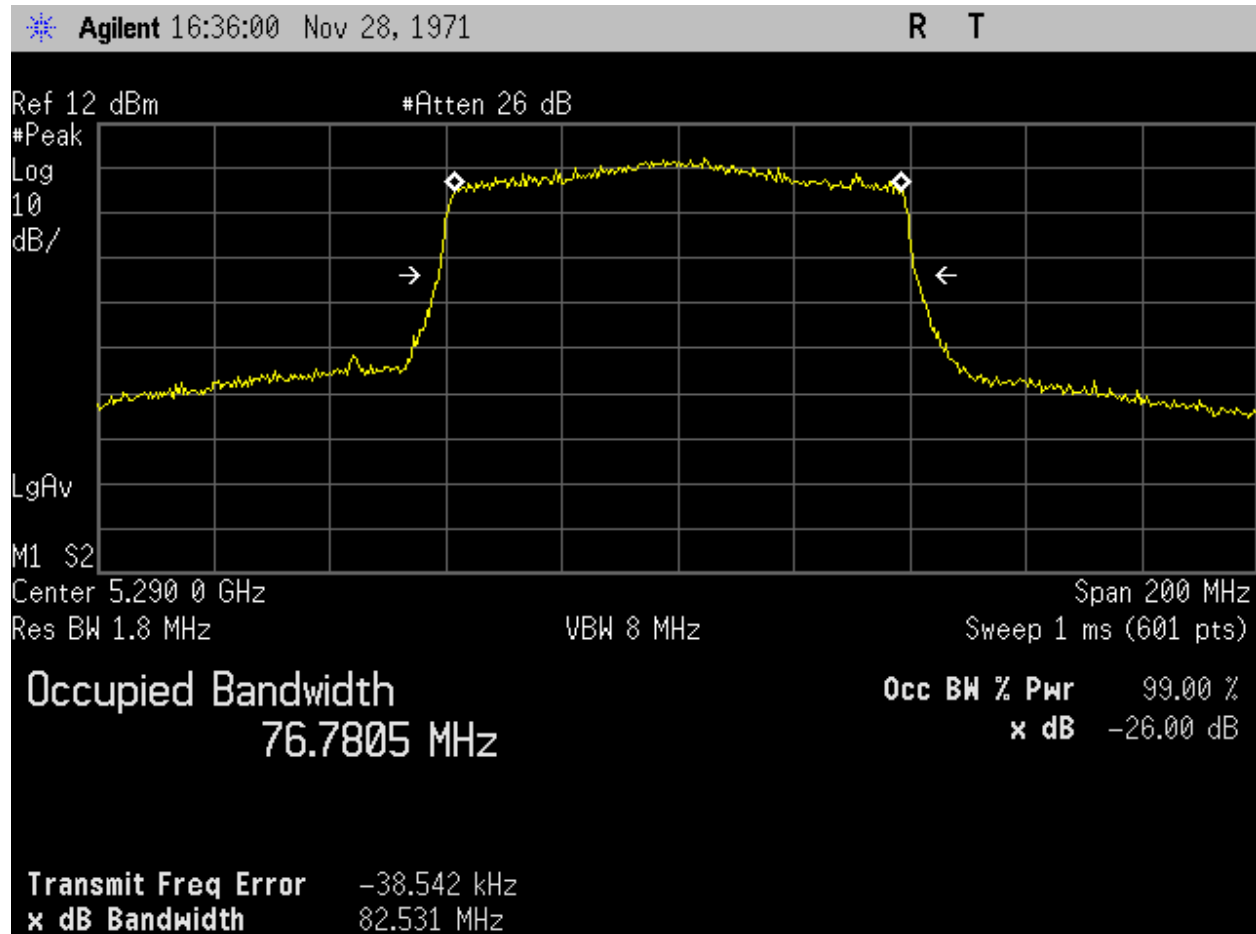


Figure 21: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

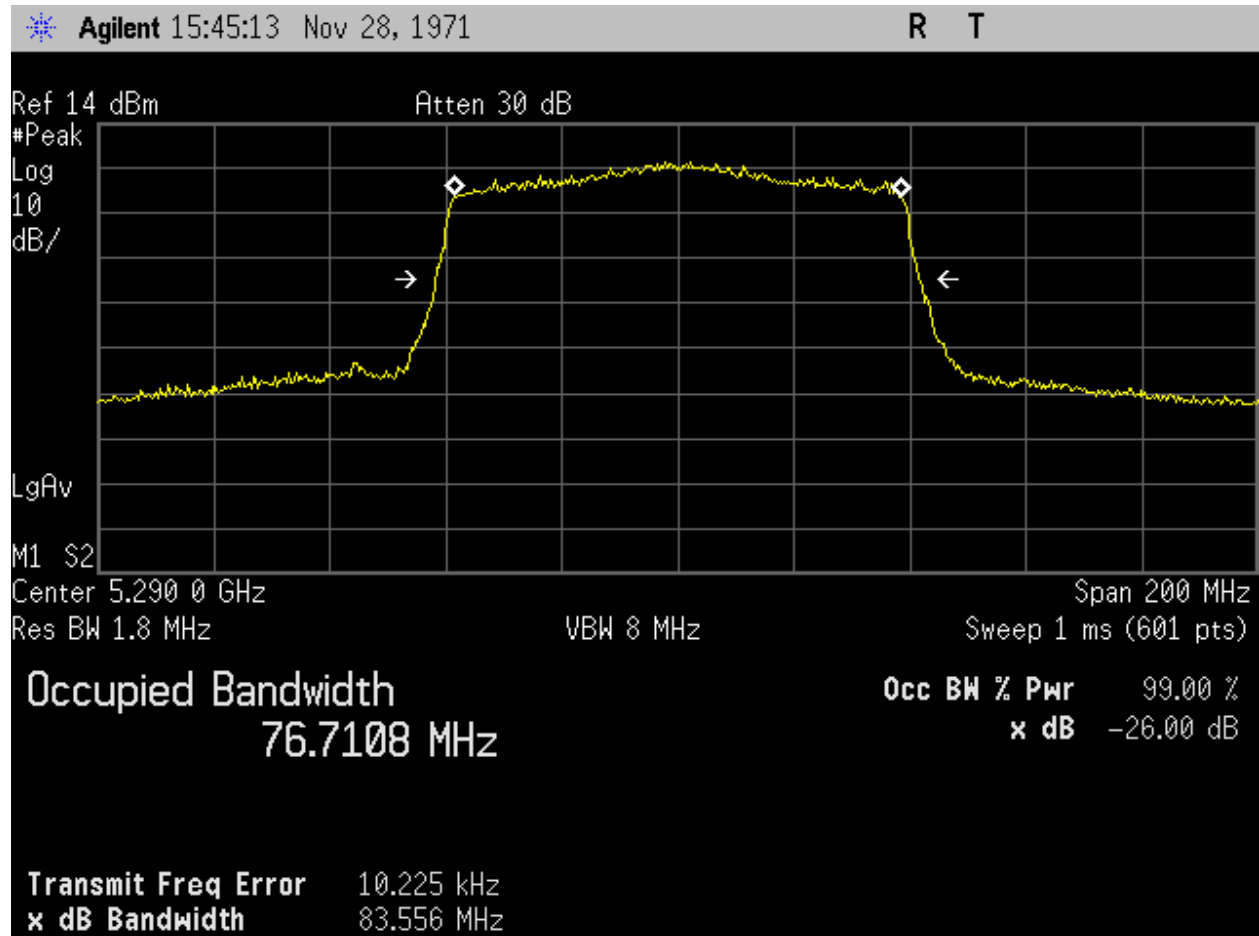


Figure 22: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

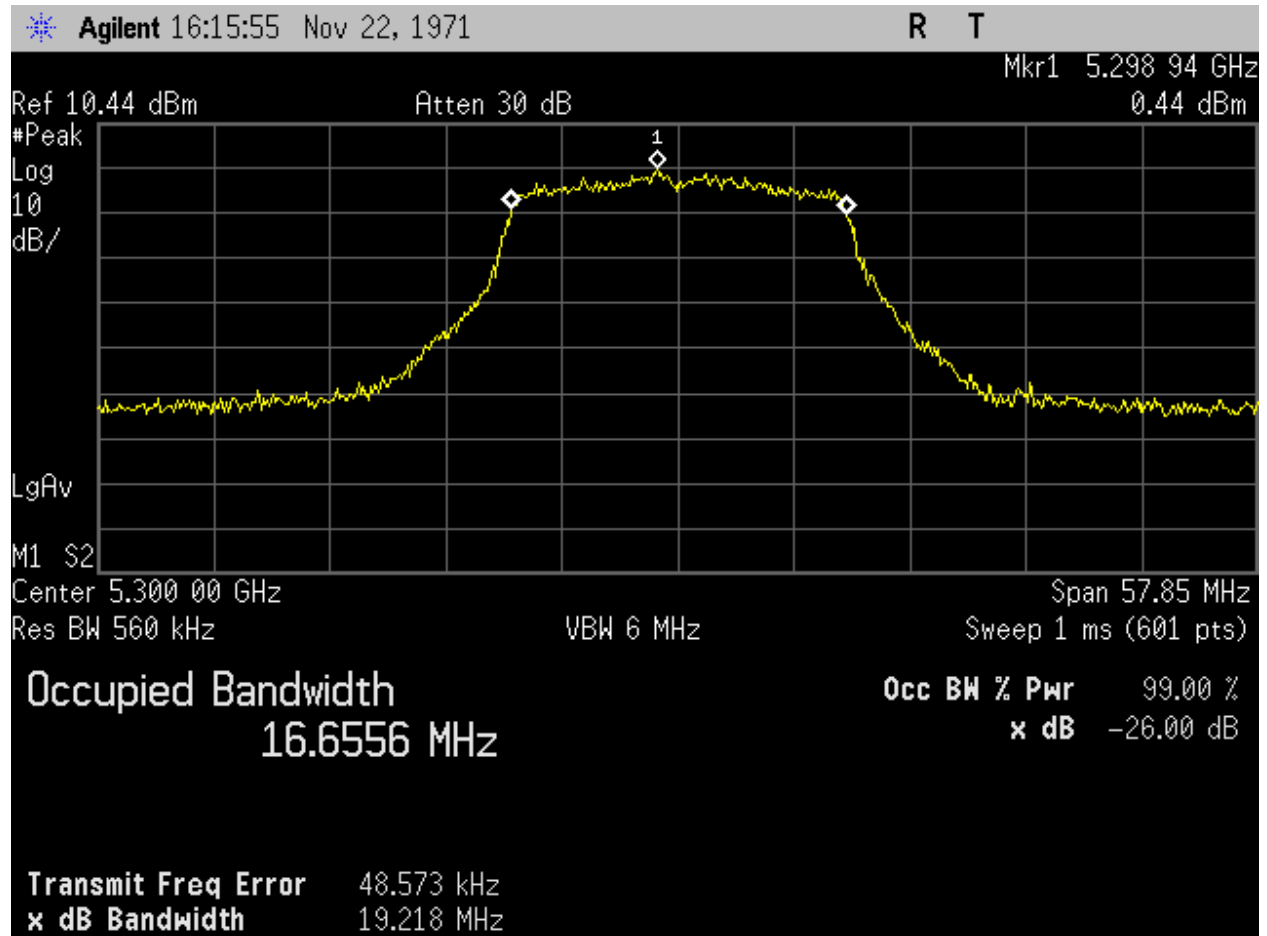


Figure 23: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

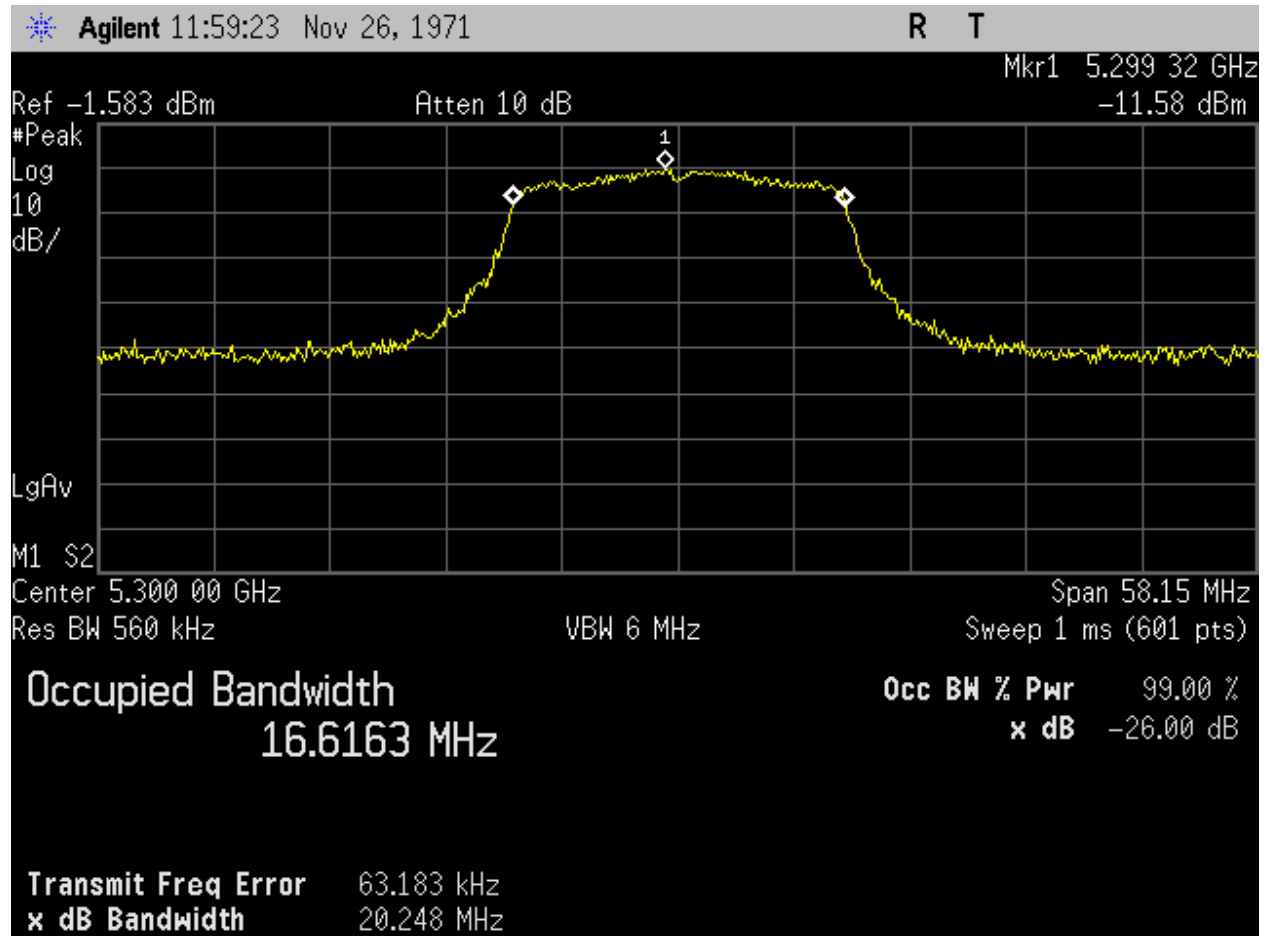


Figure 24: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

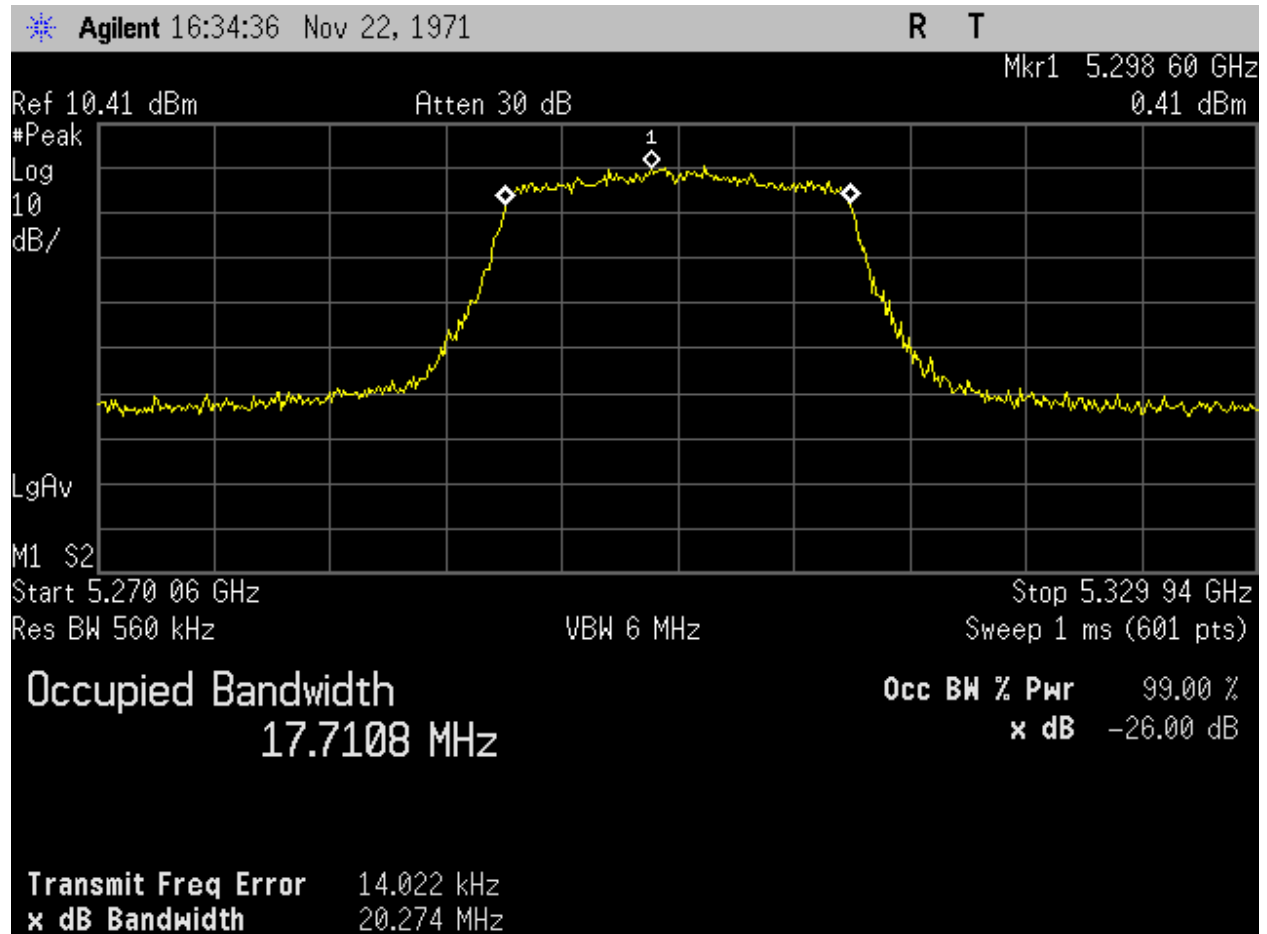


Figure 25: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

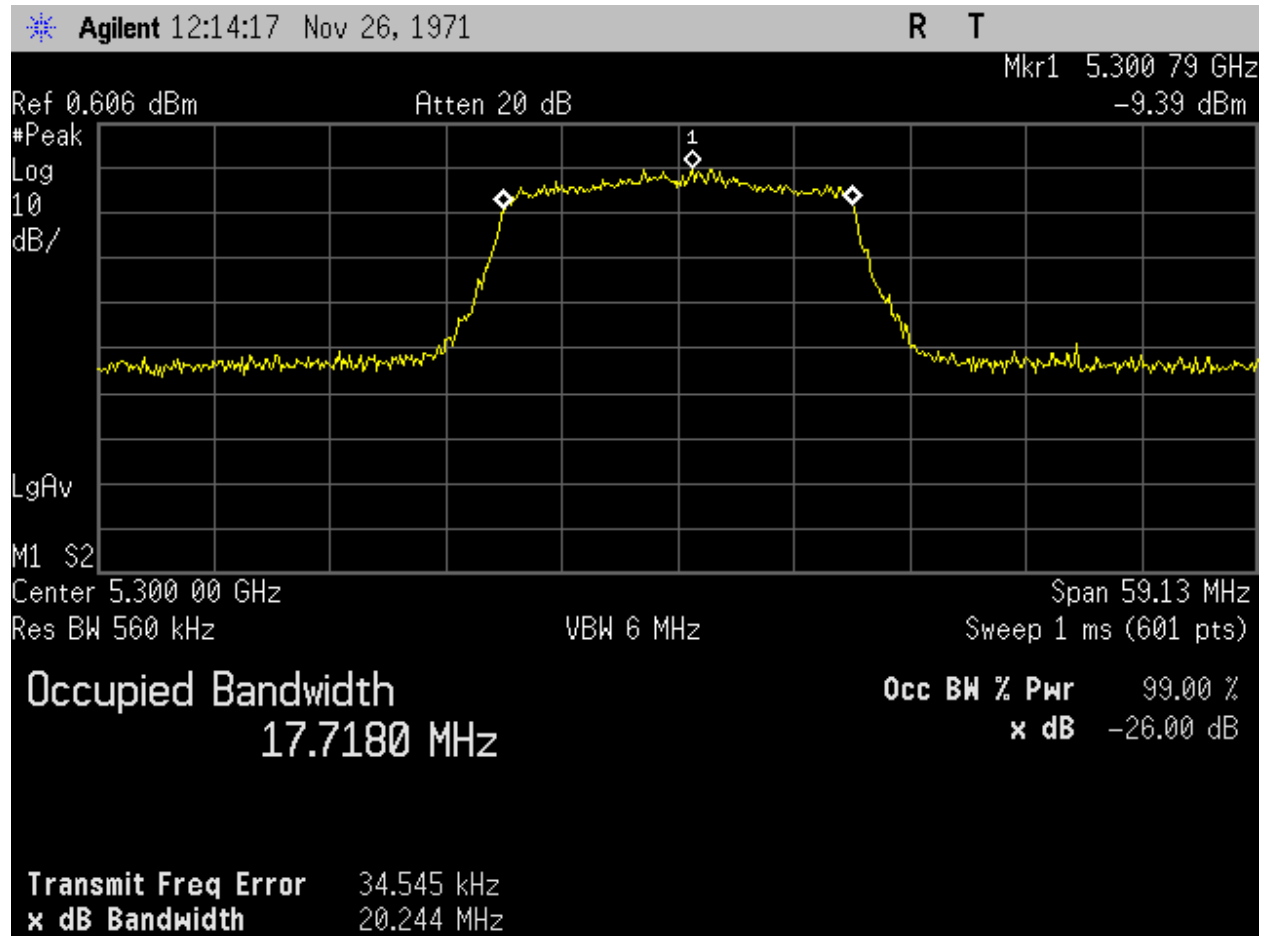


Figure 26: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

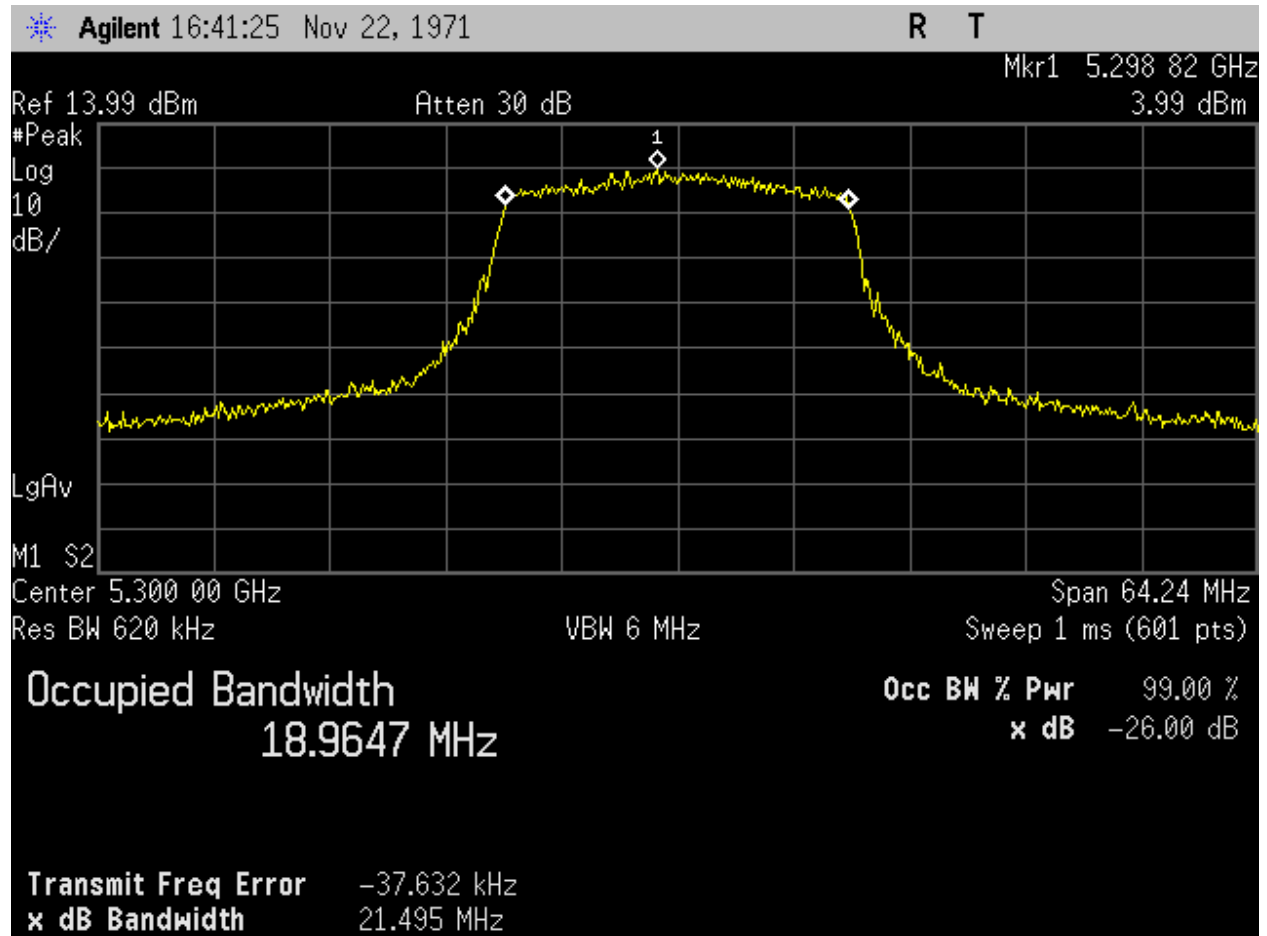


Figure 27: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

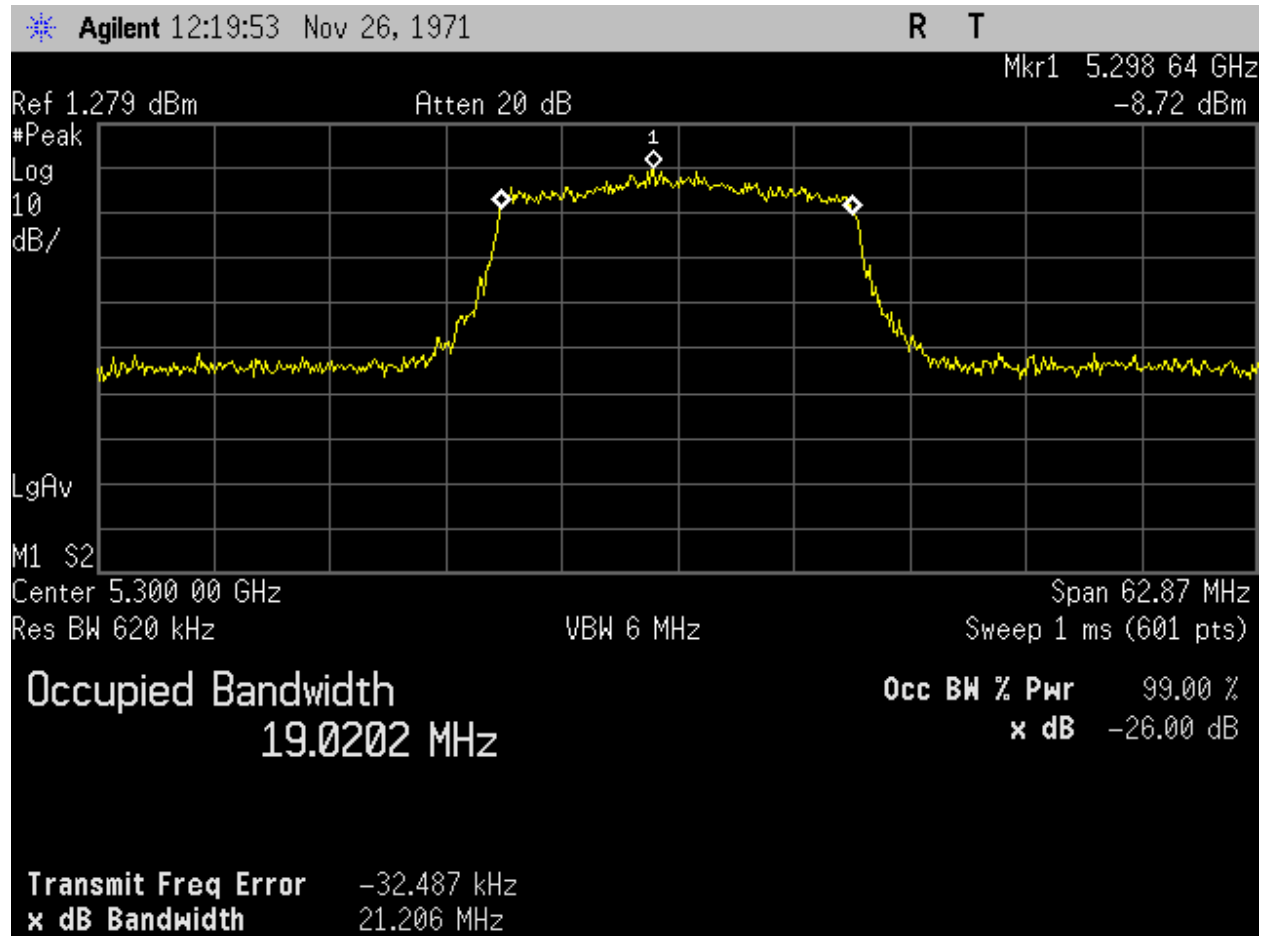


Figure 28: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

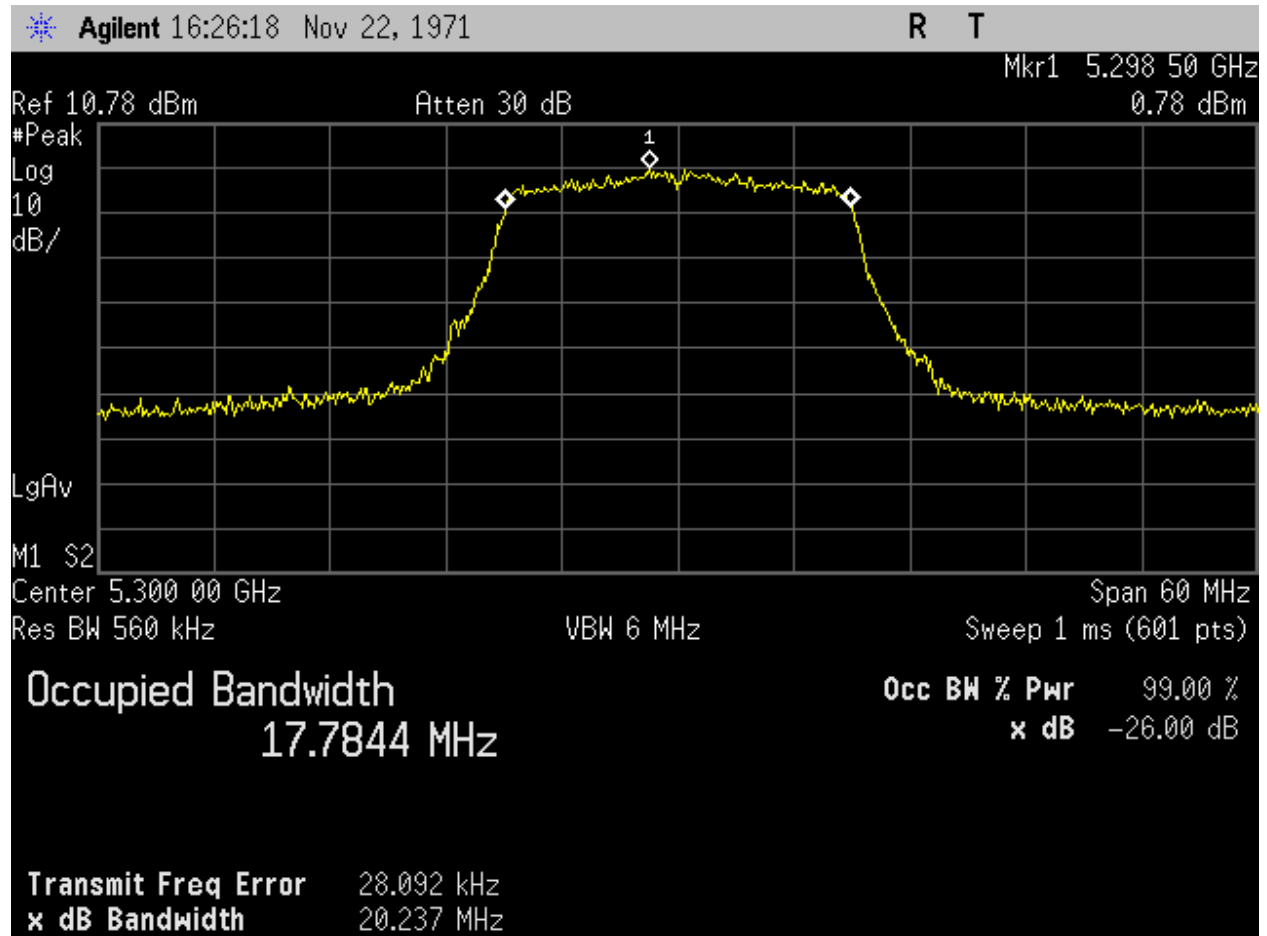


Figure 29: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

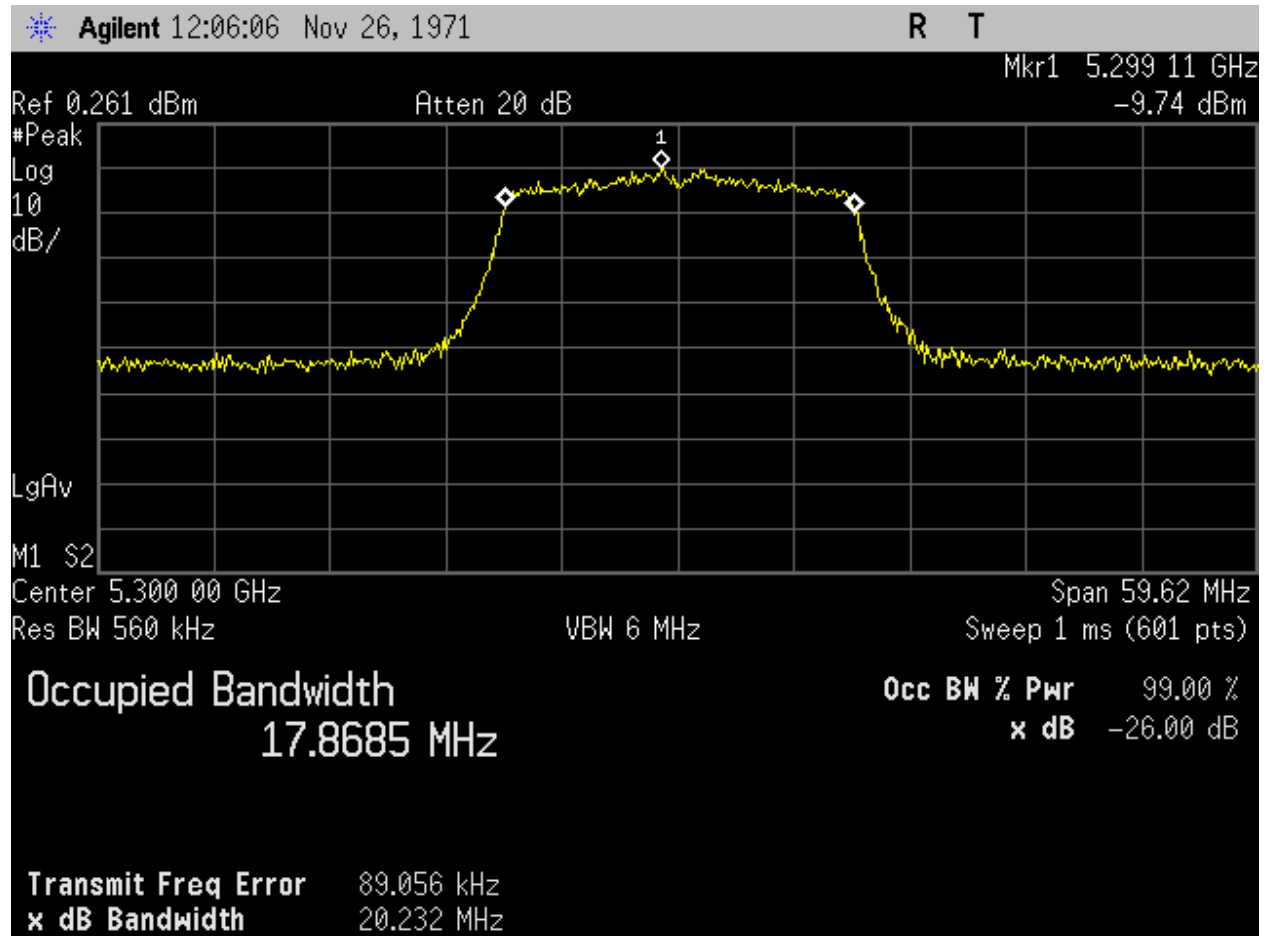


Figure 30: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

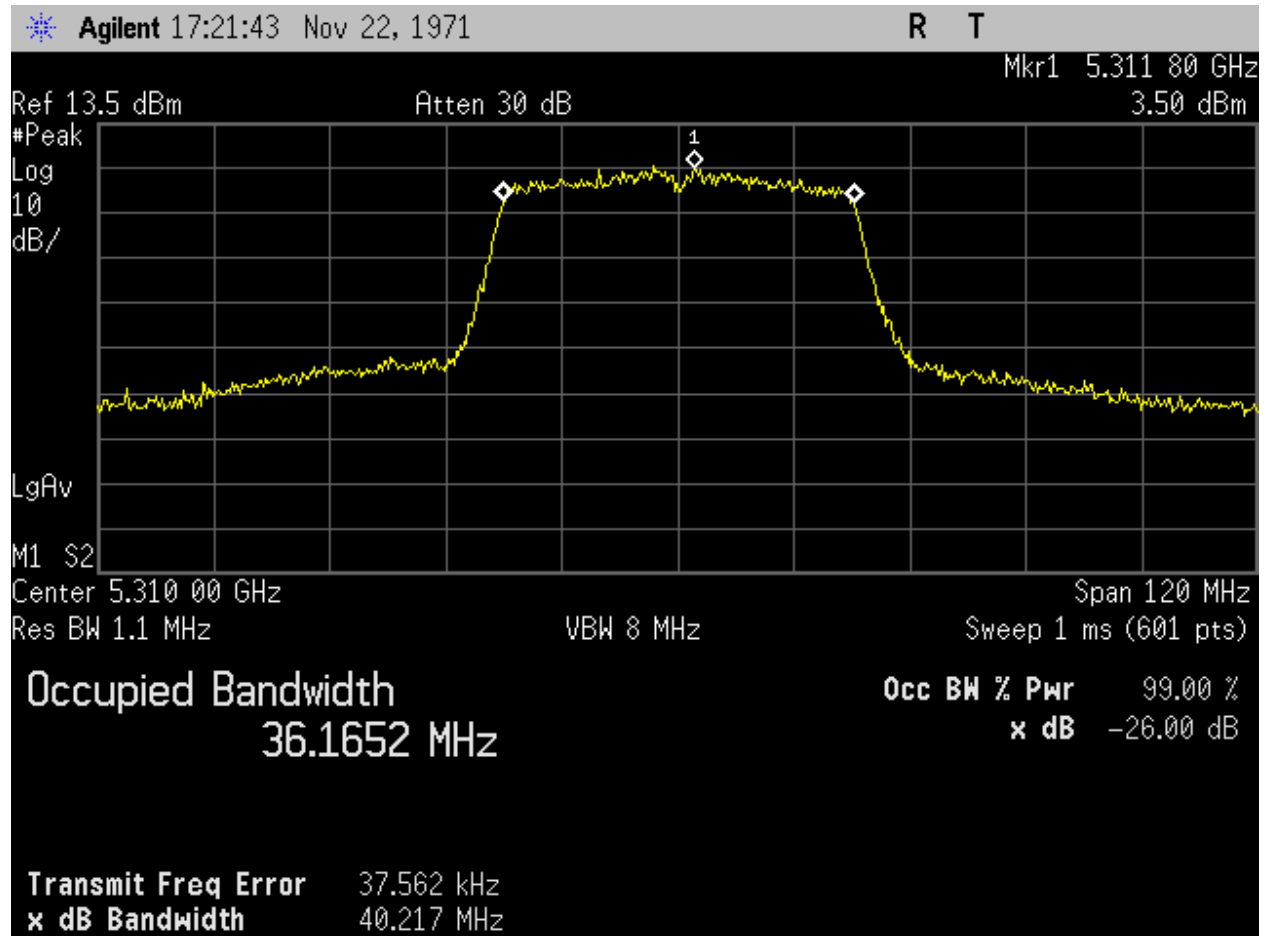


Figure 31: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

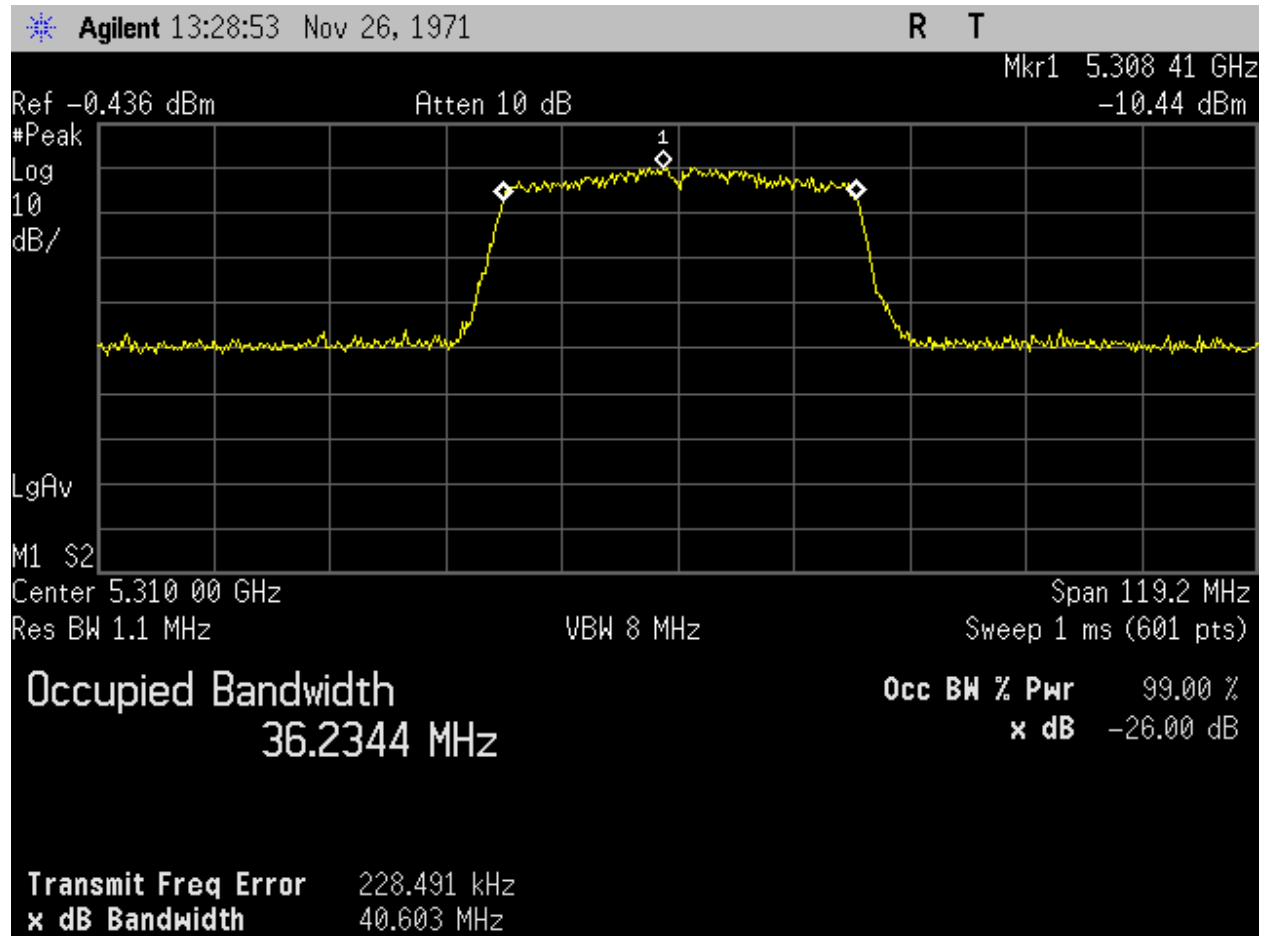


Figure 32: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

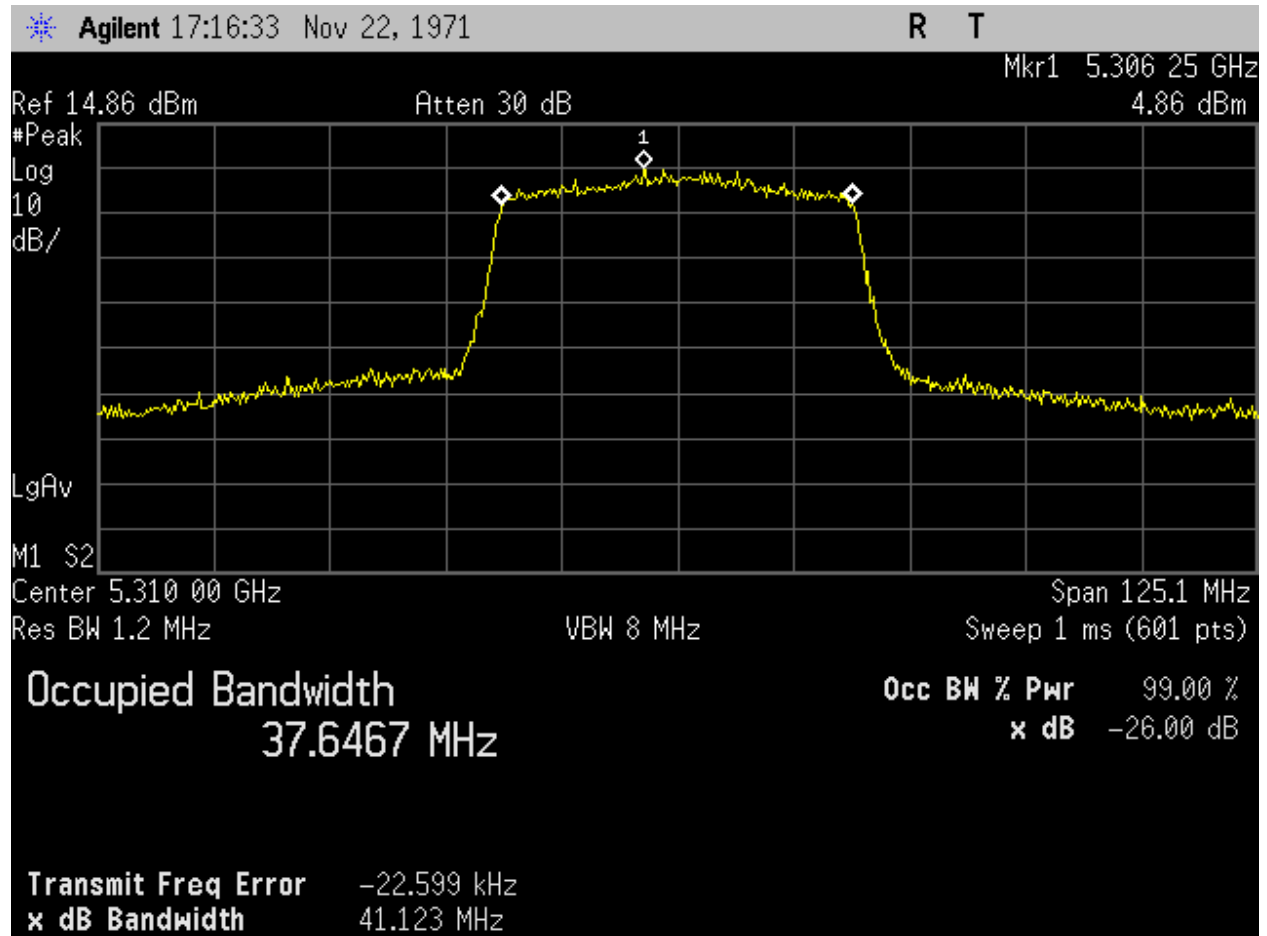


Figure 33: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

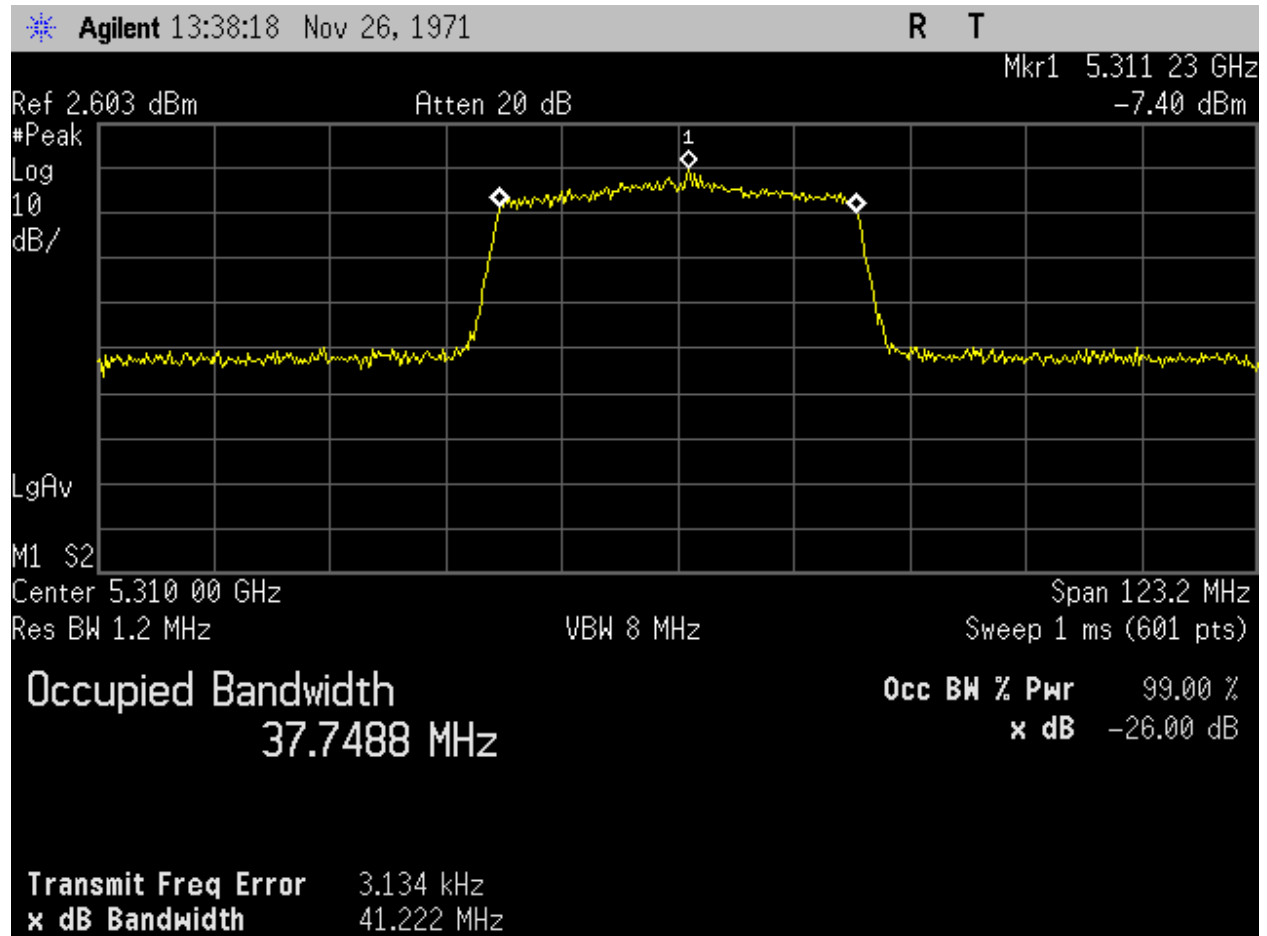


Figure 34: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

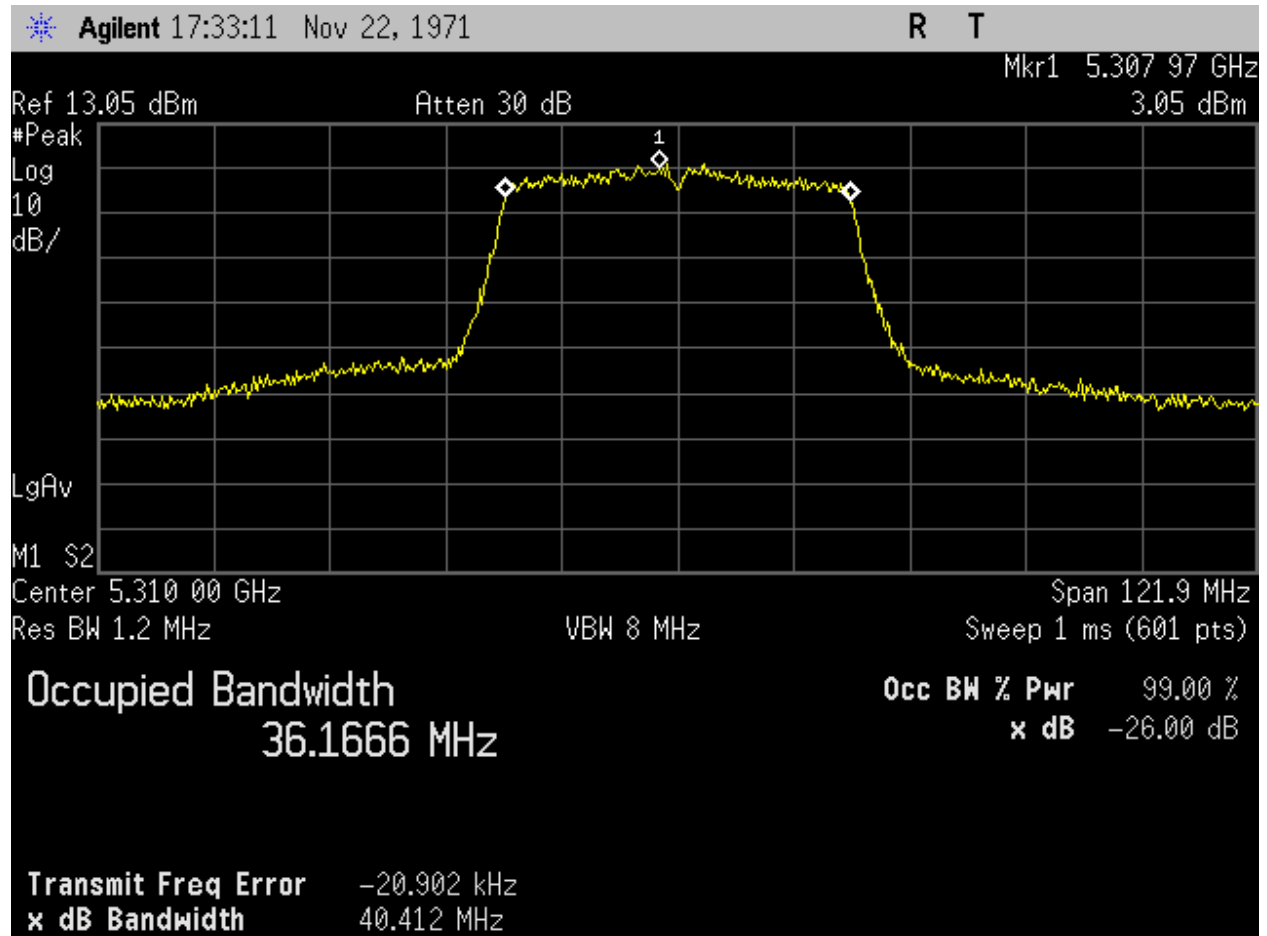


Figure 35: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

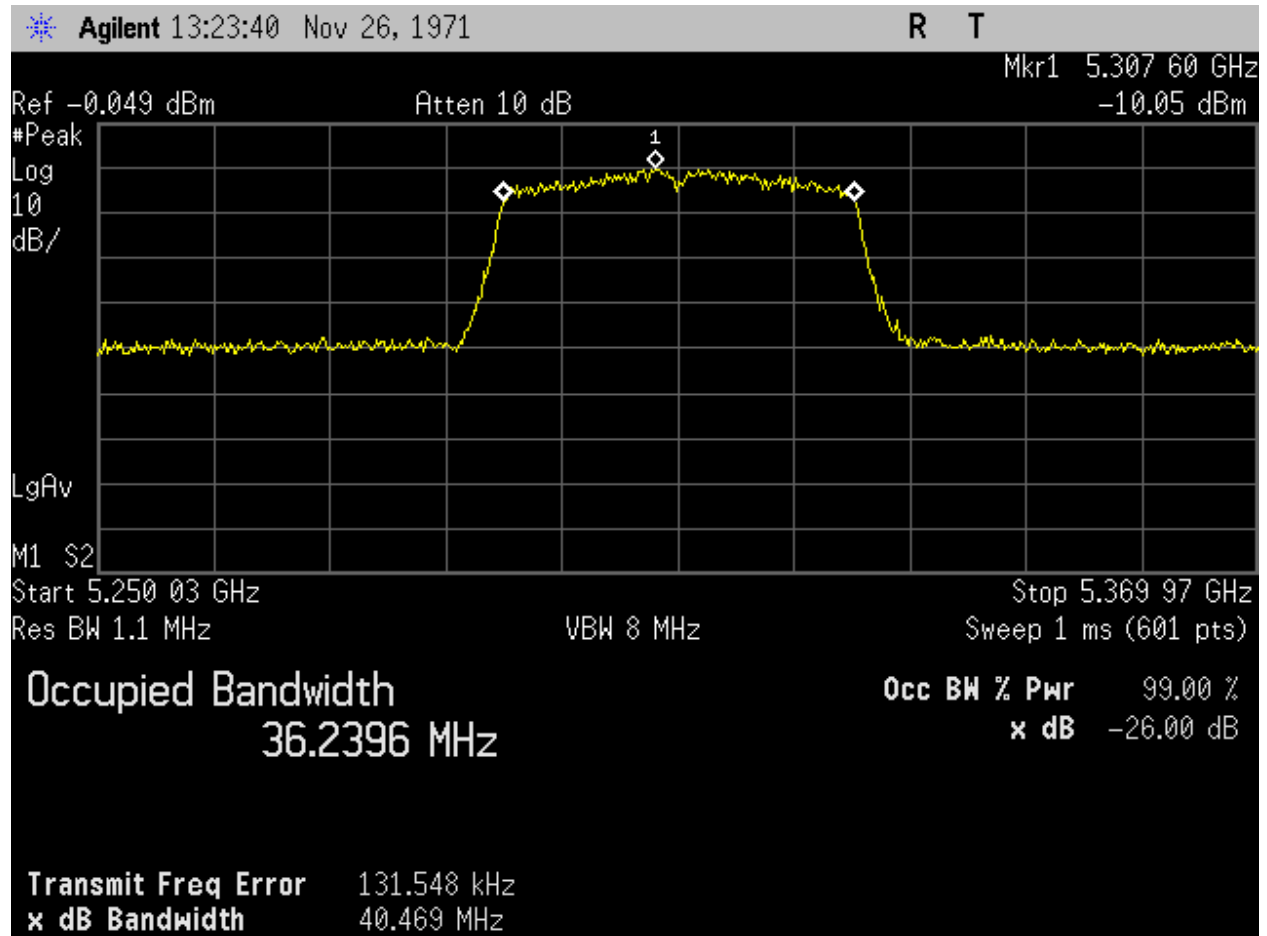


Figure 36: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

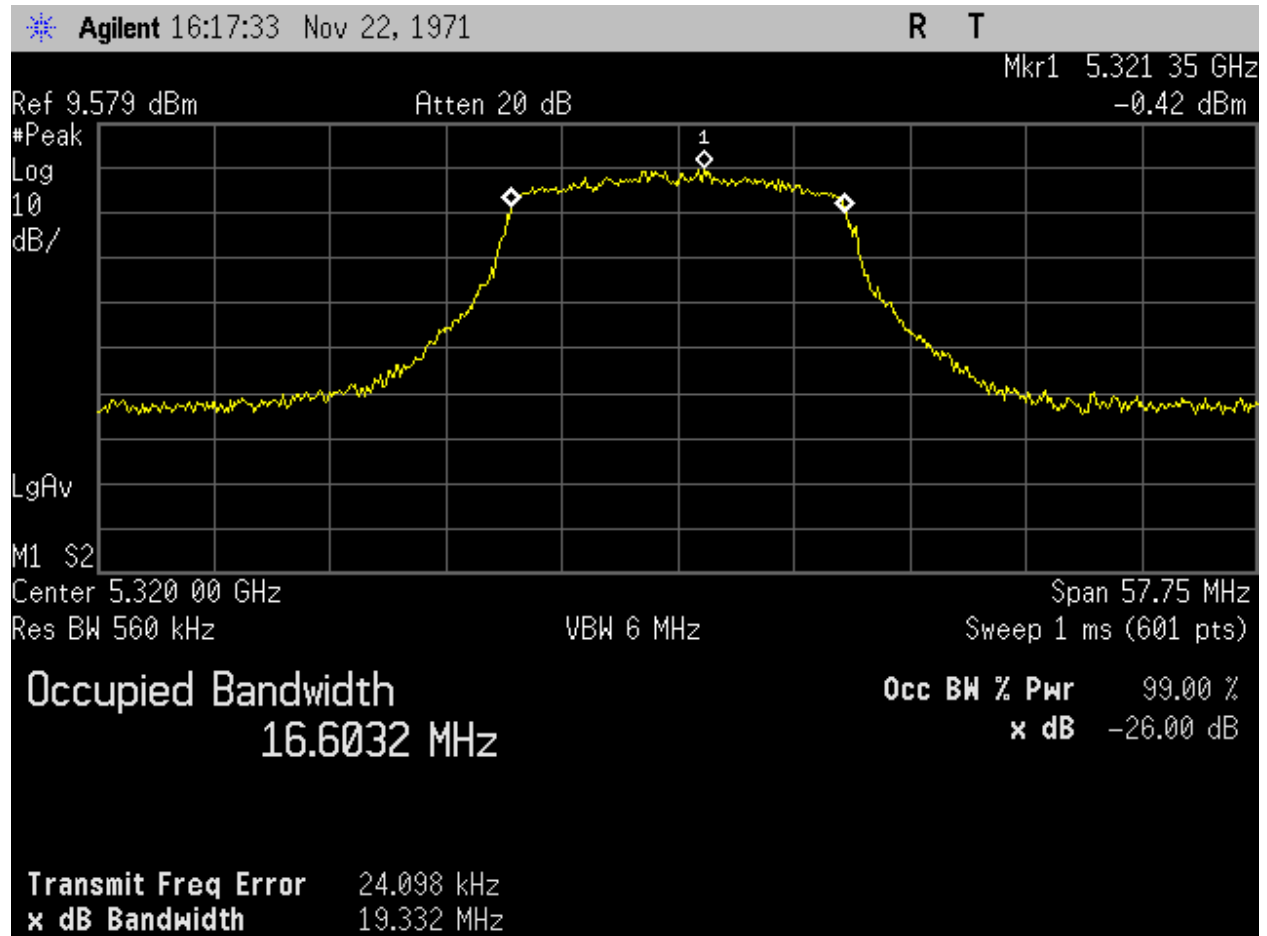


Figure 37: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

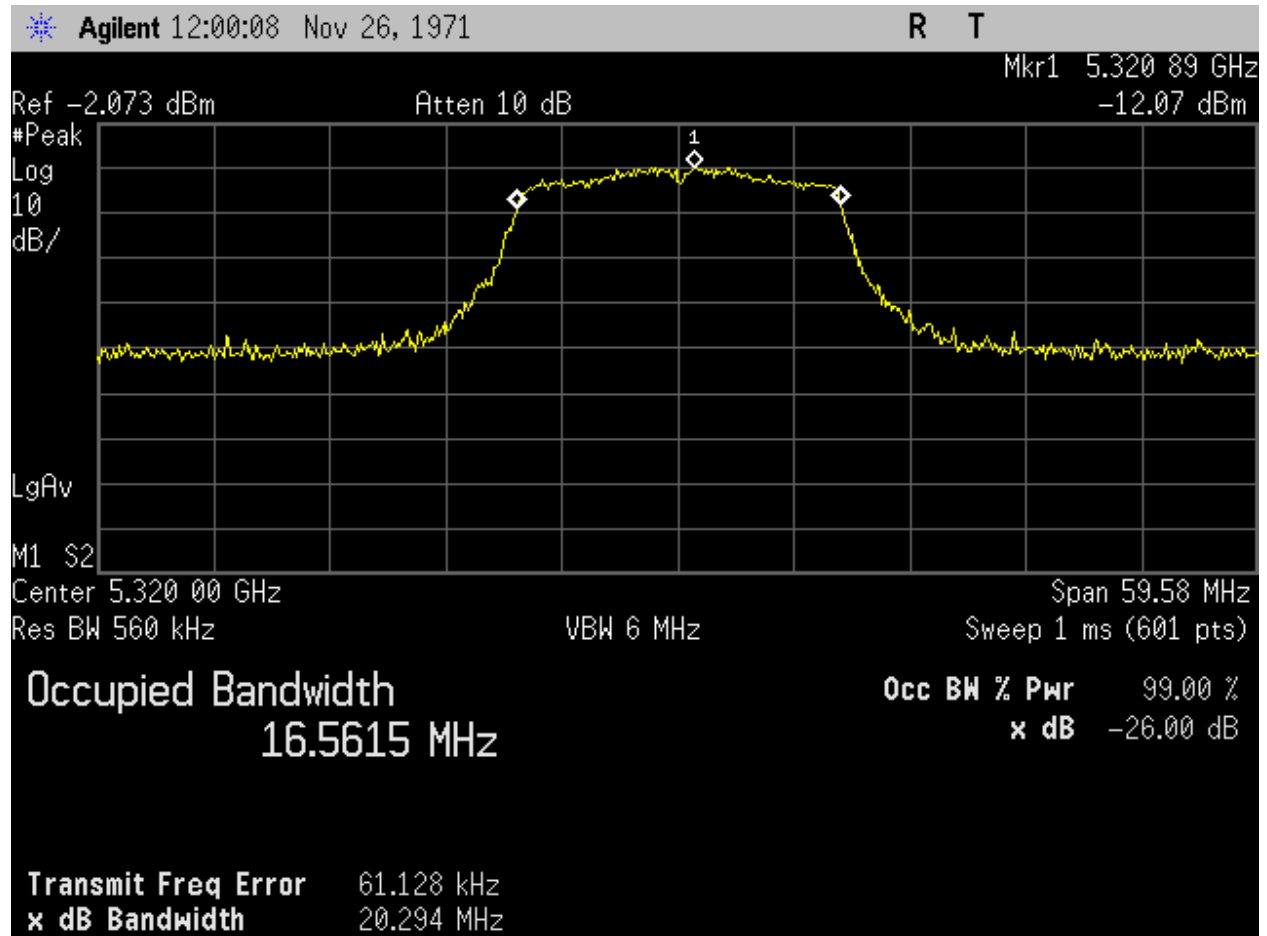


Figure 38: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

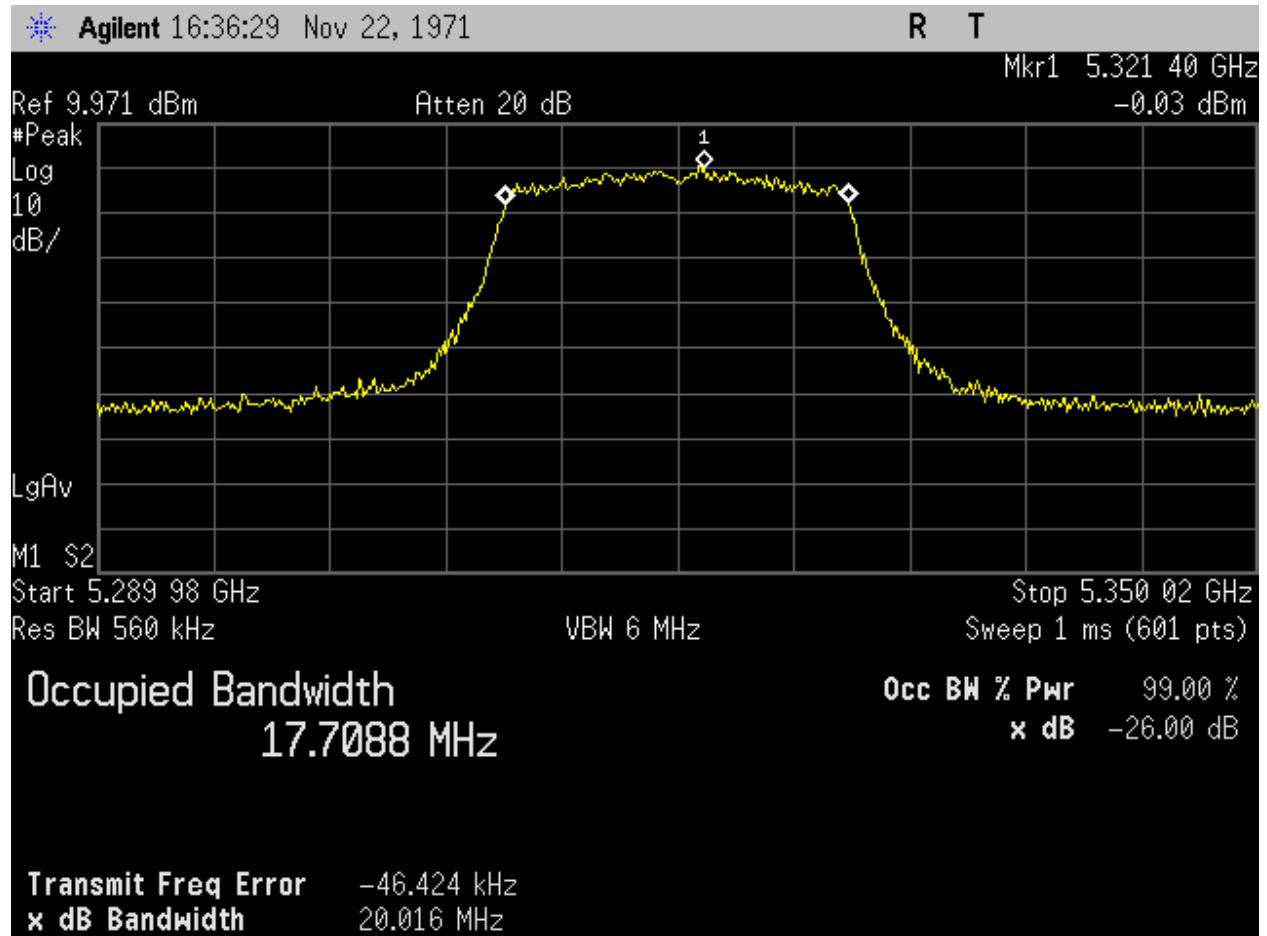


Figure 39: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

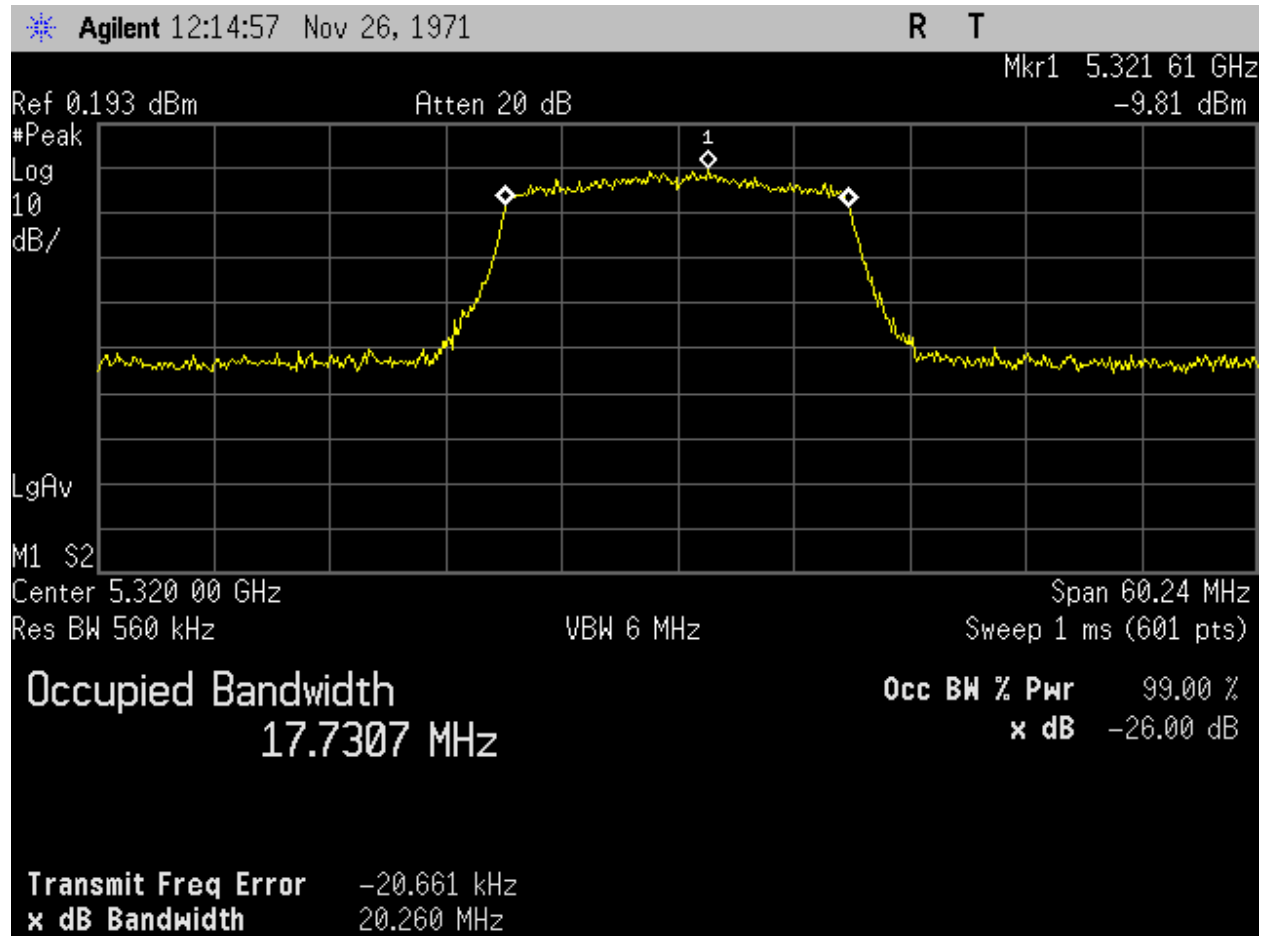


Figure 40: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

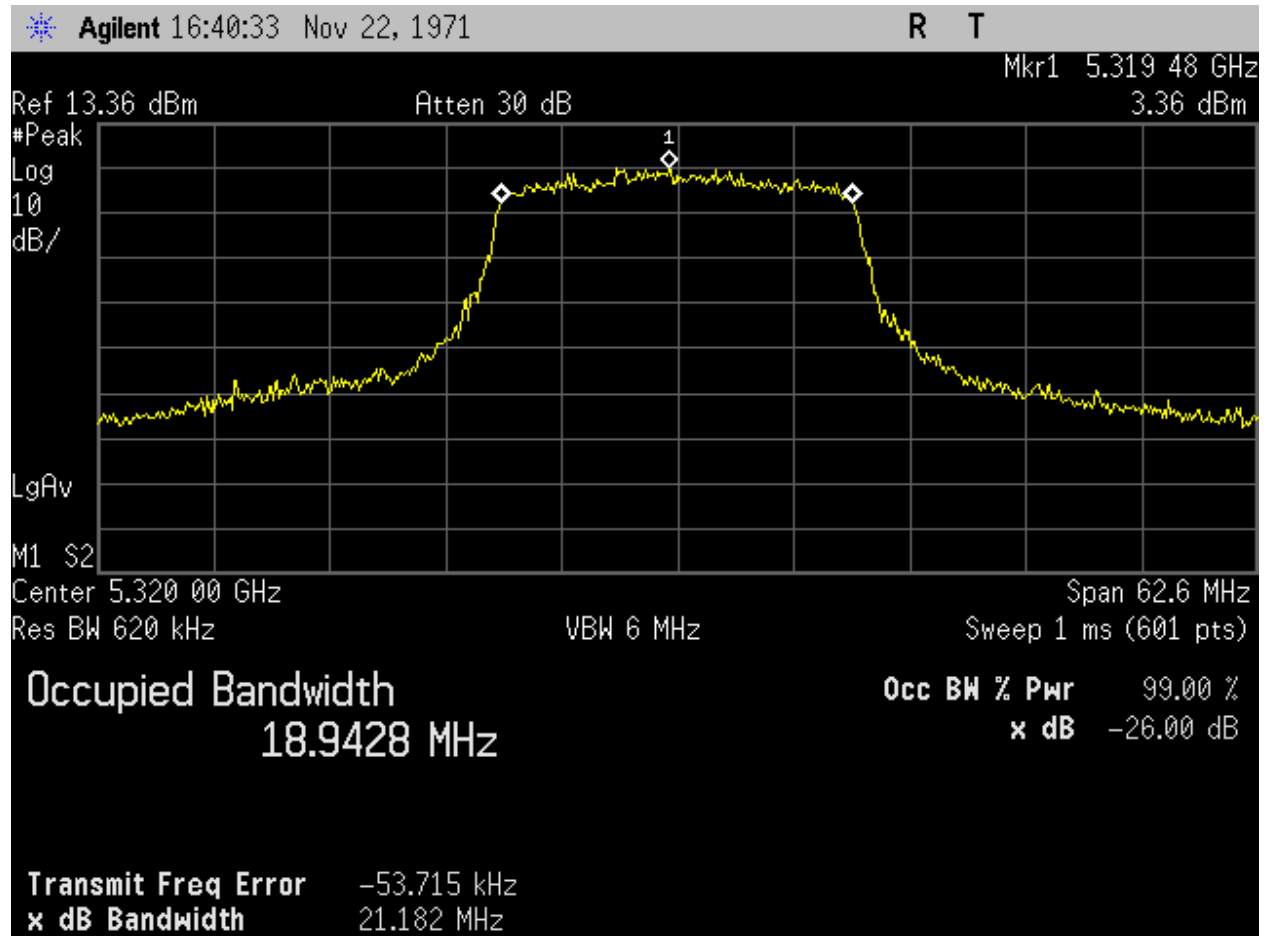


Figure 41: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

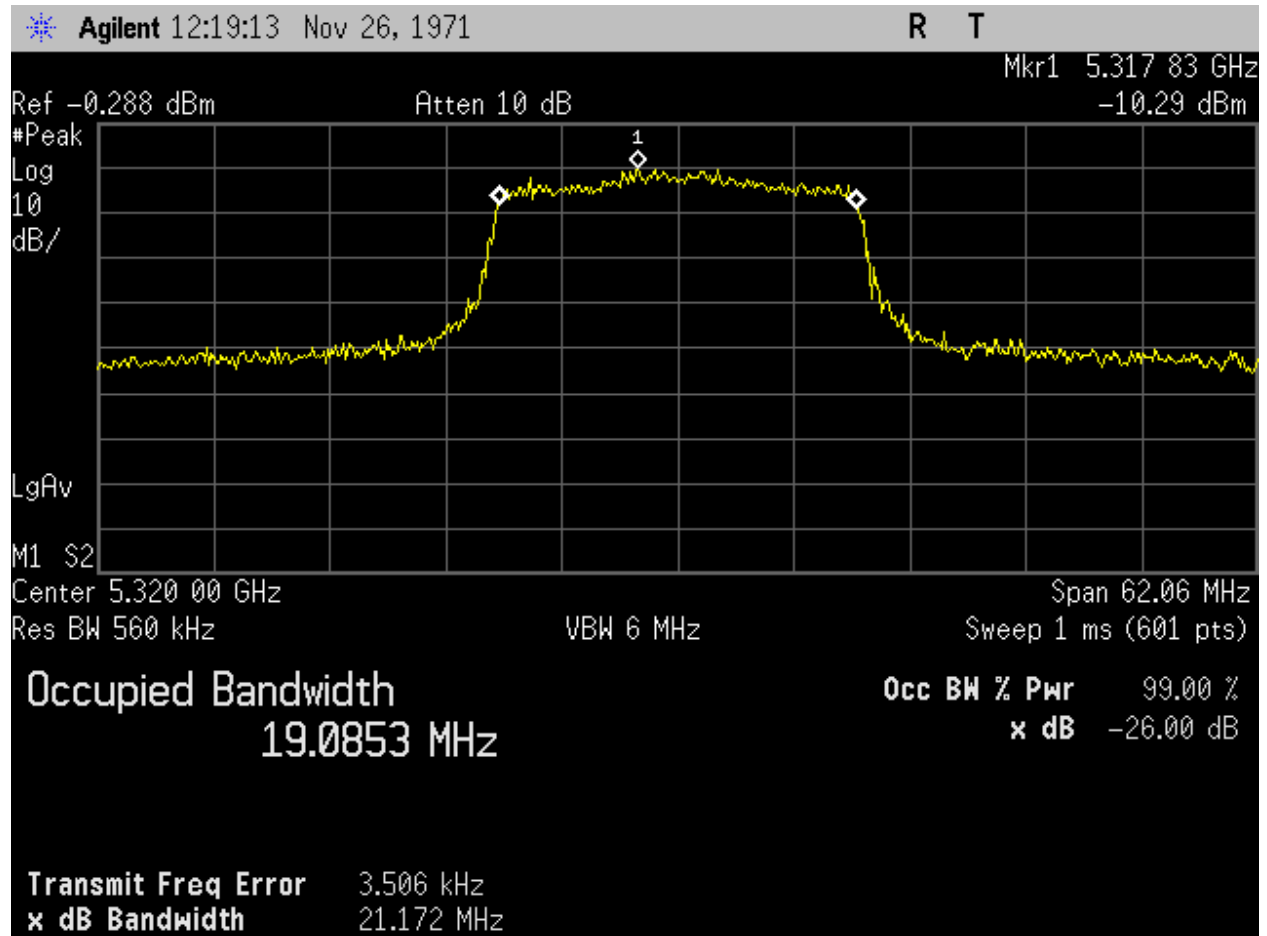


Figure 42: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

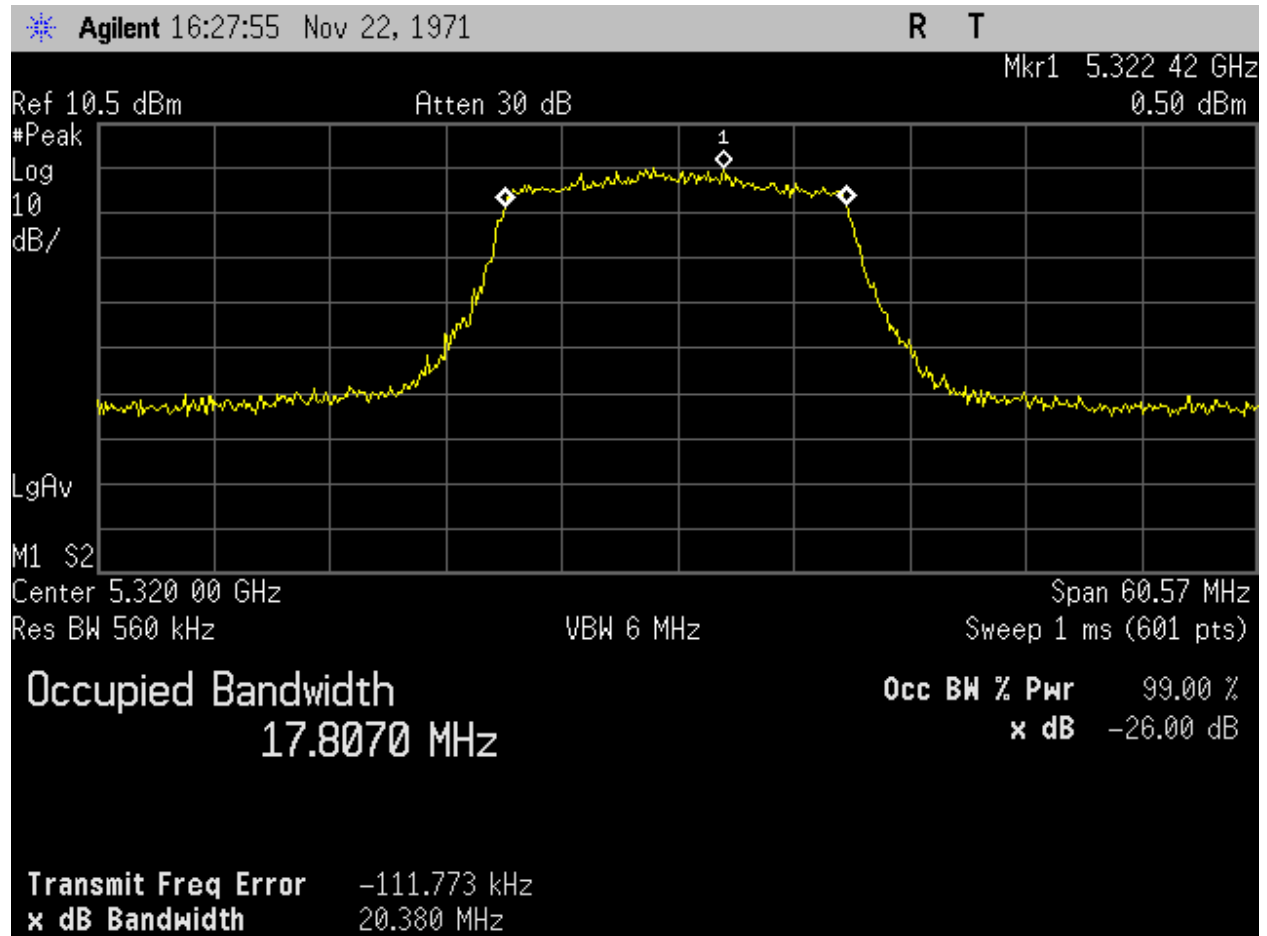


Figure 43: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

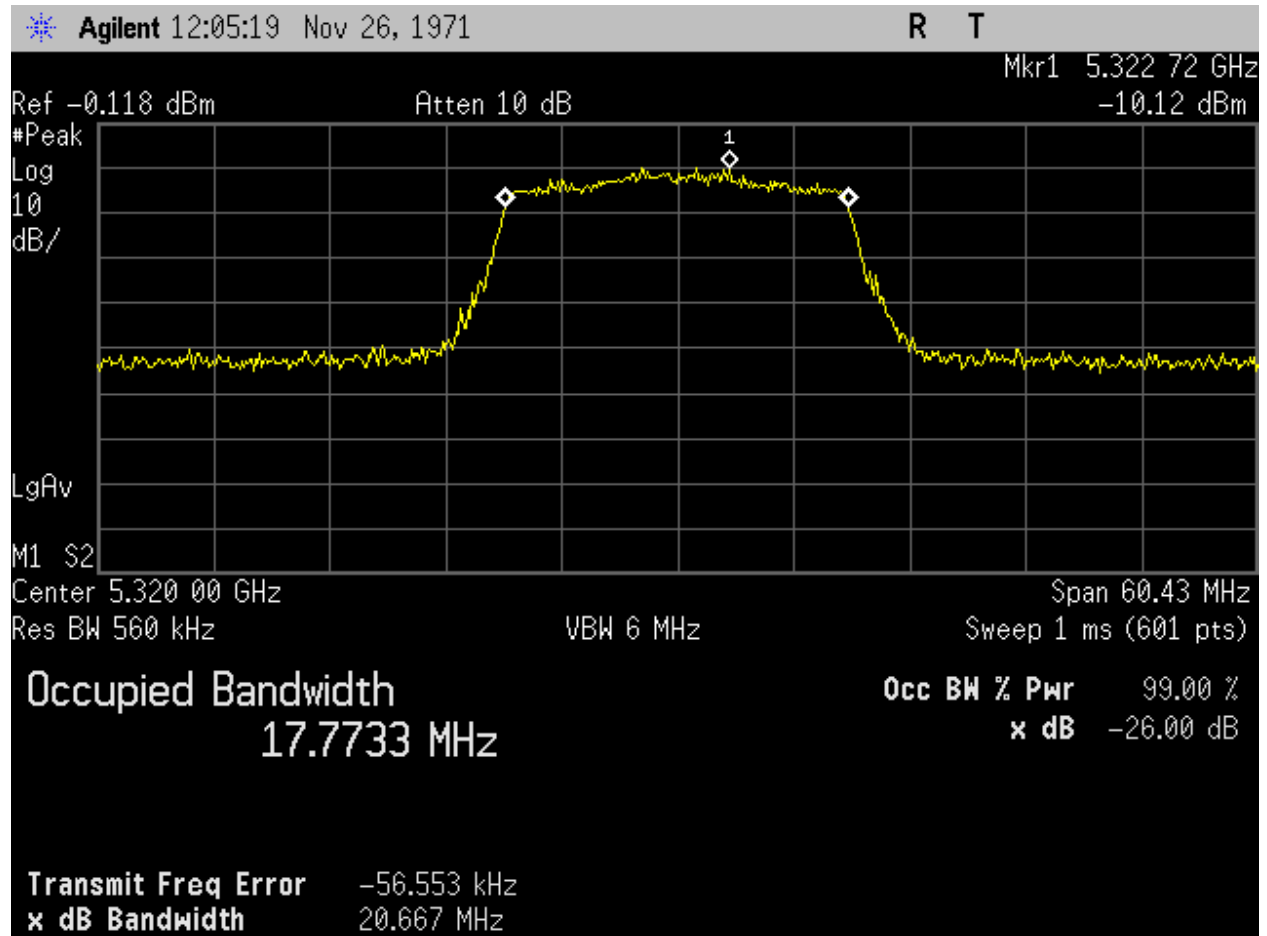


Figure 44: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

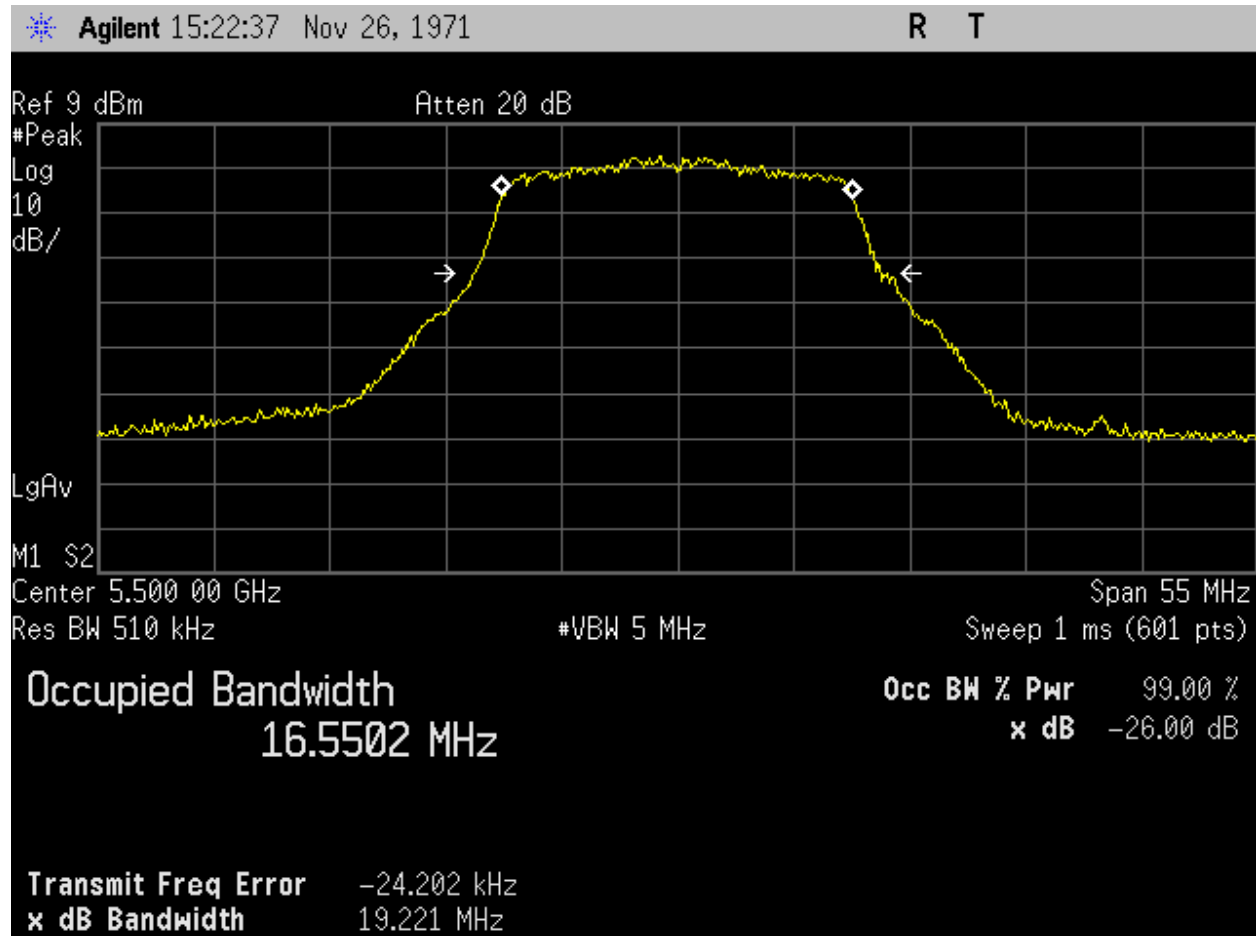


Figure 45: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

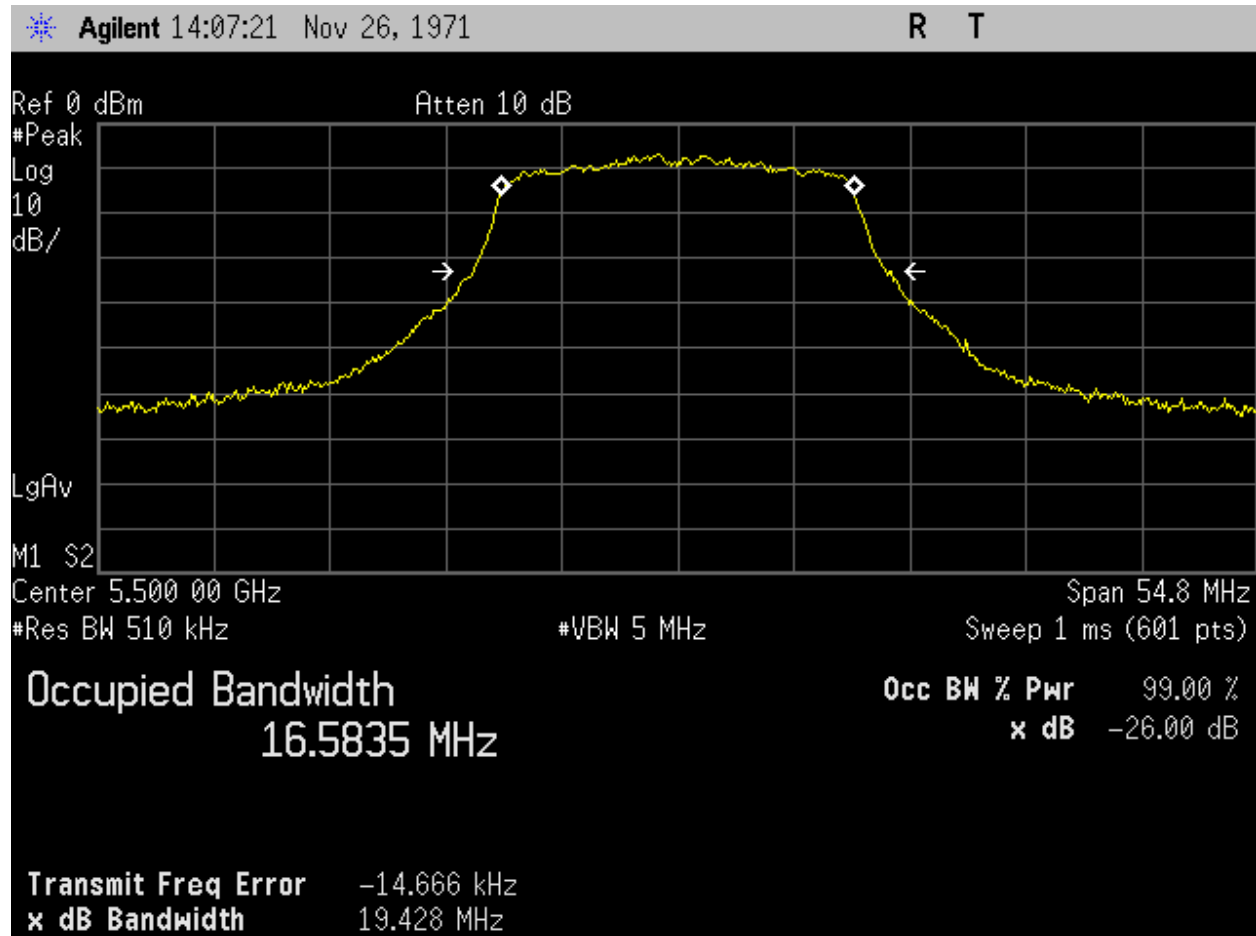


Figure 46: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

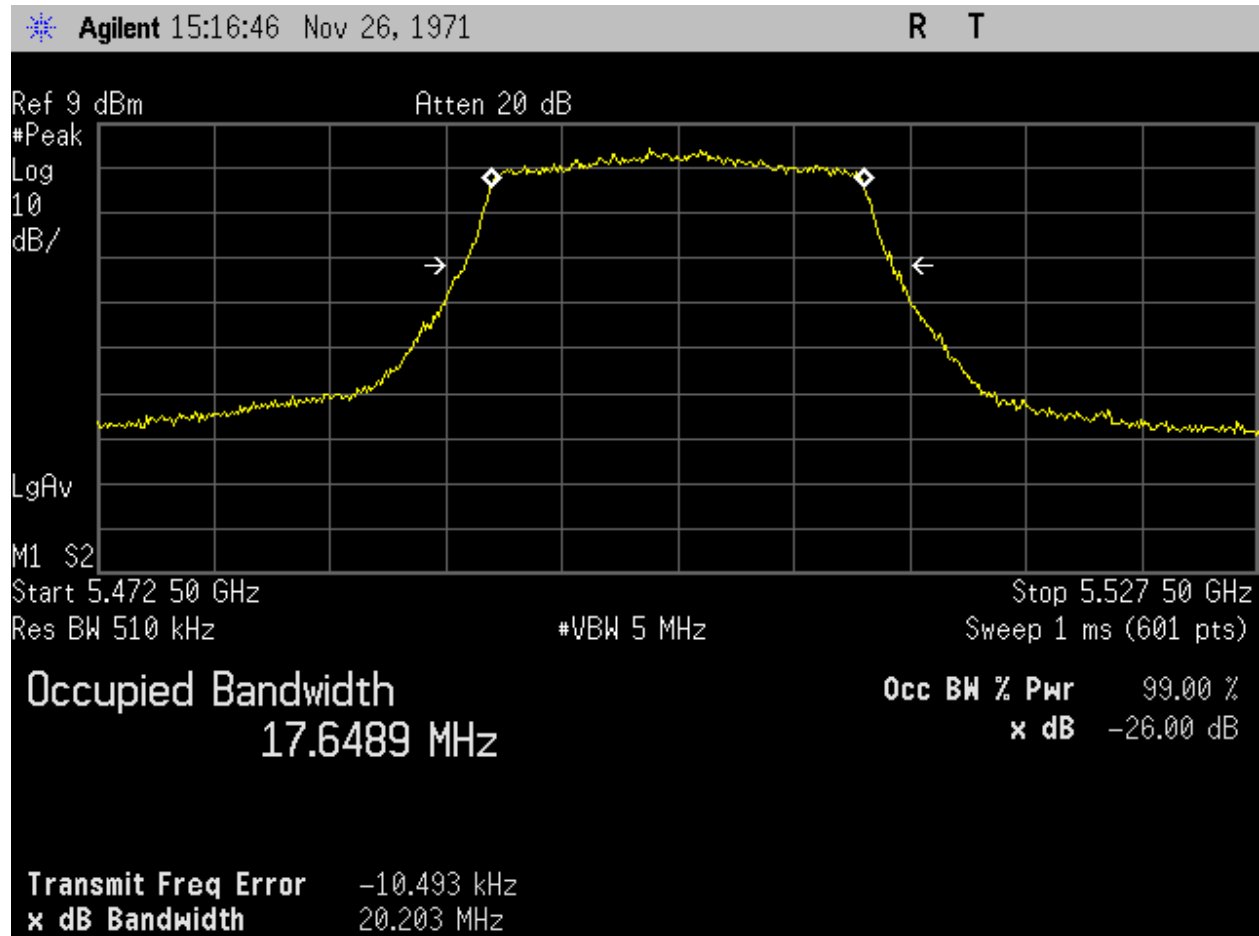


Figure 47: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

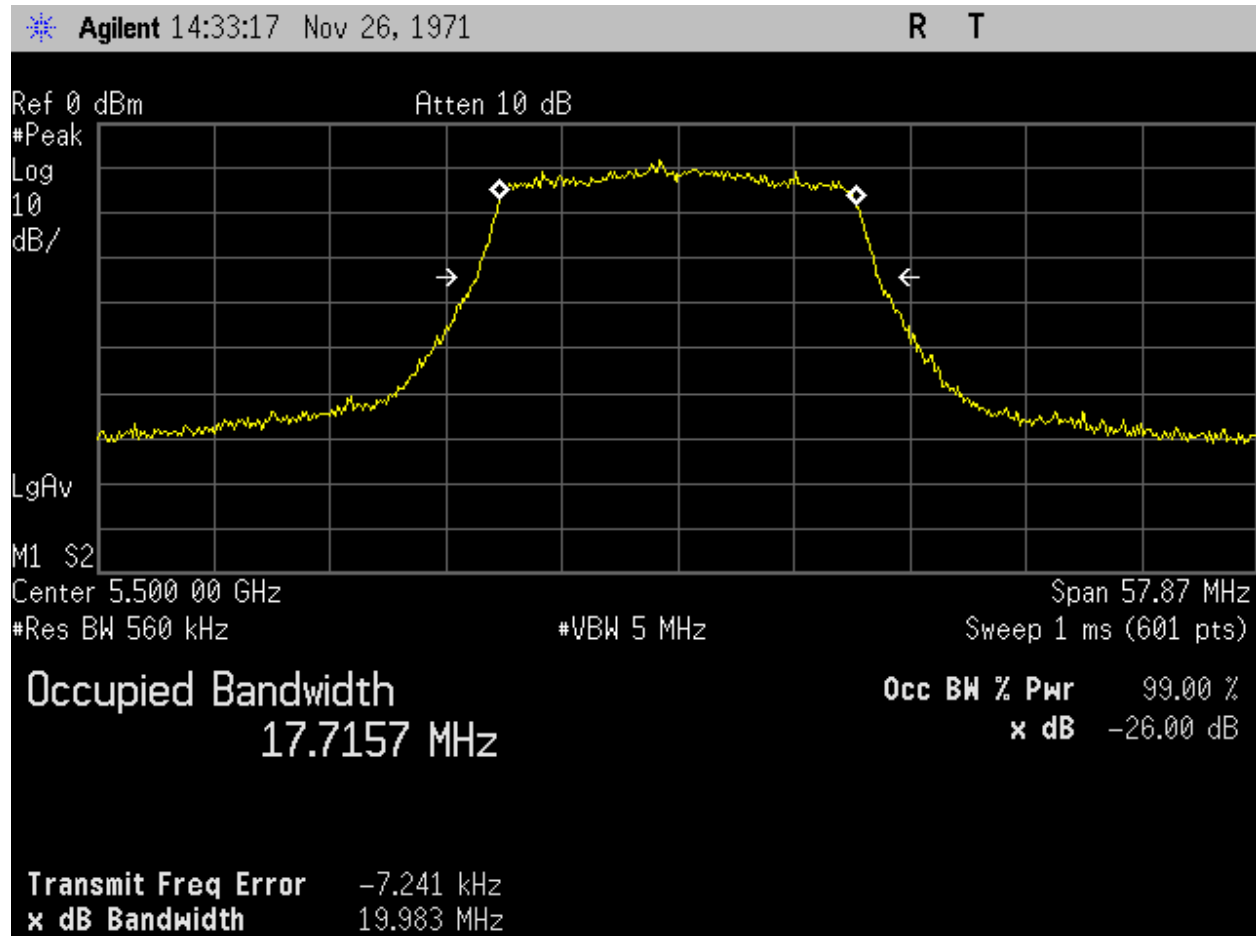


Figure 48: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

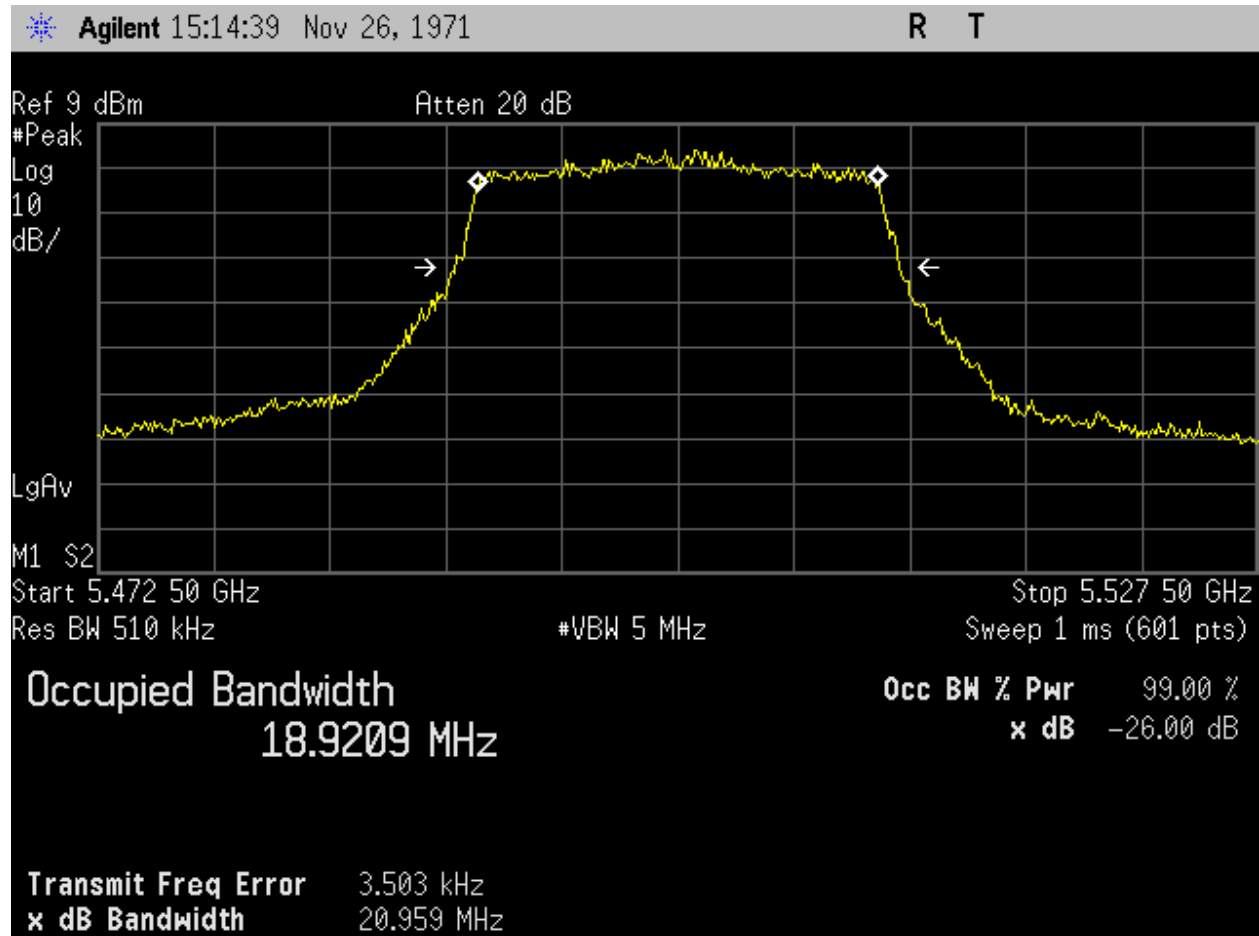


Figure 49: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

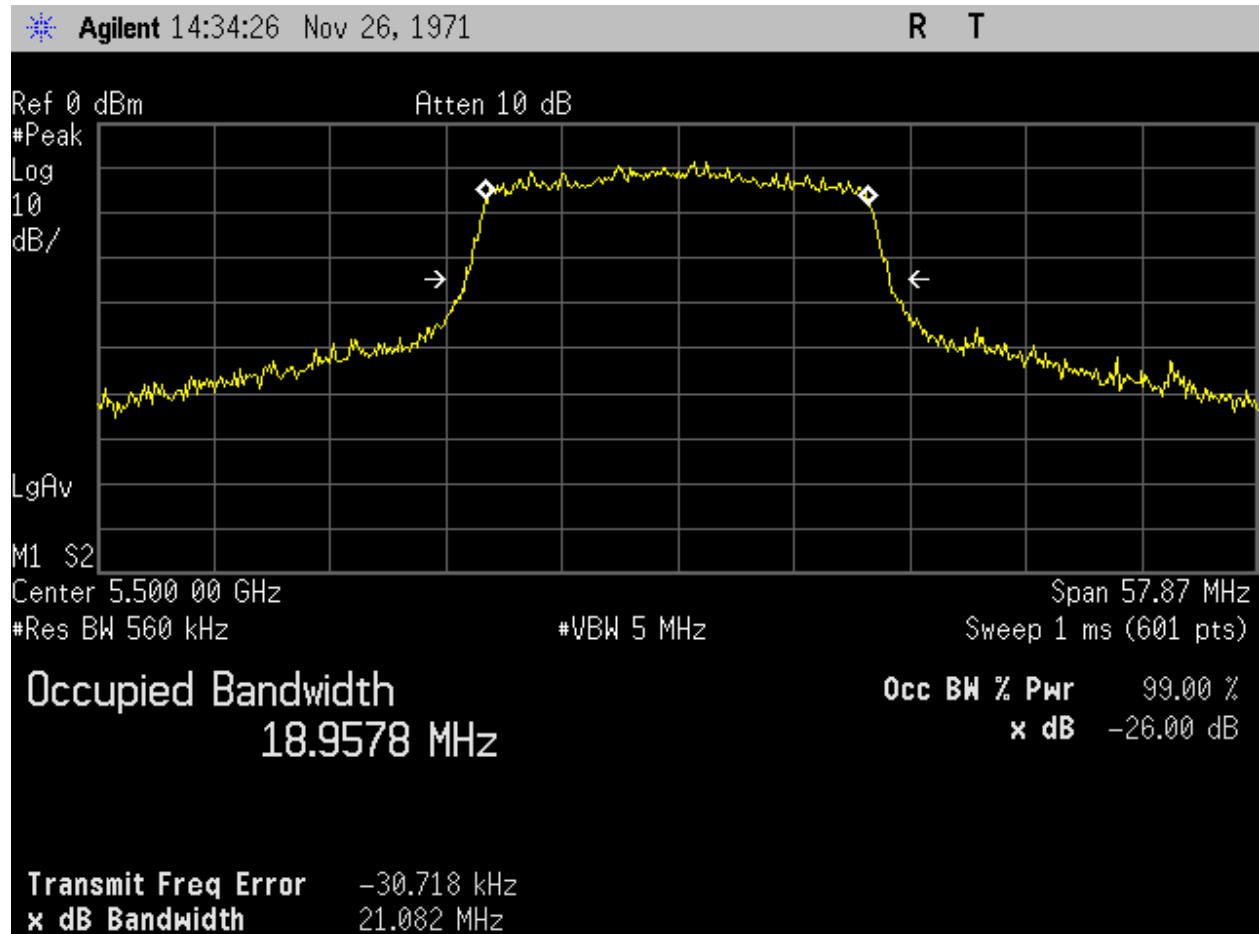


Figure 50: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

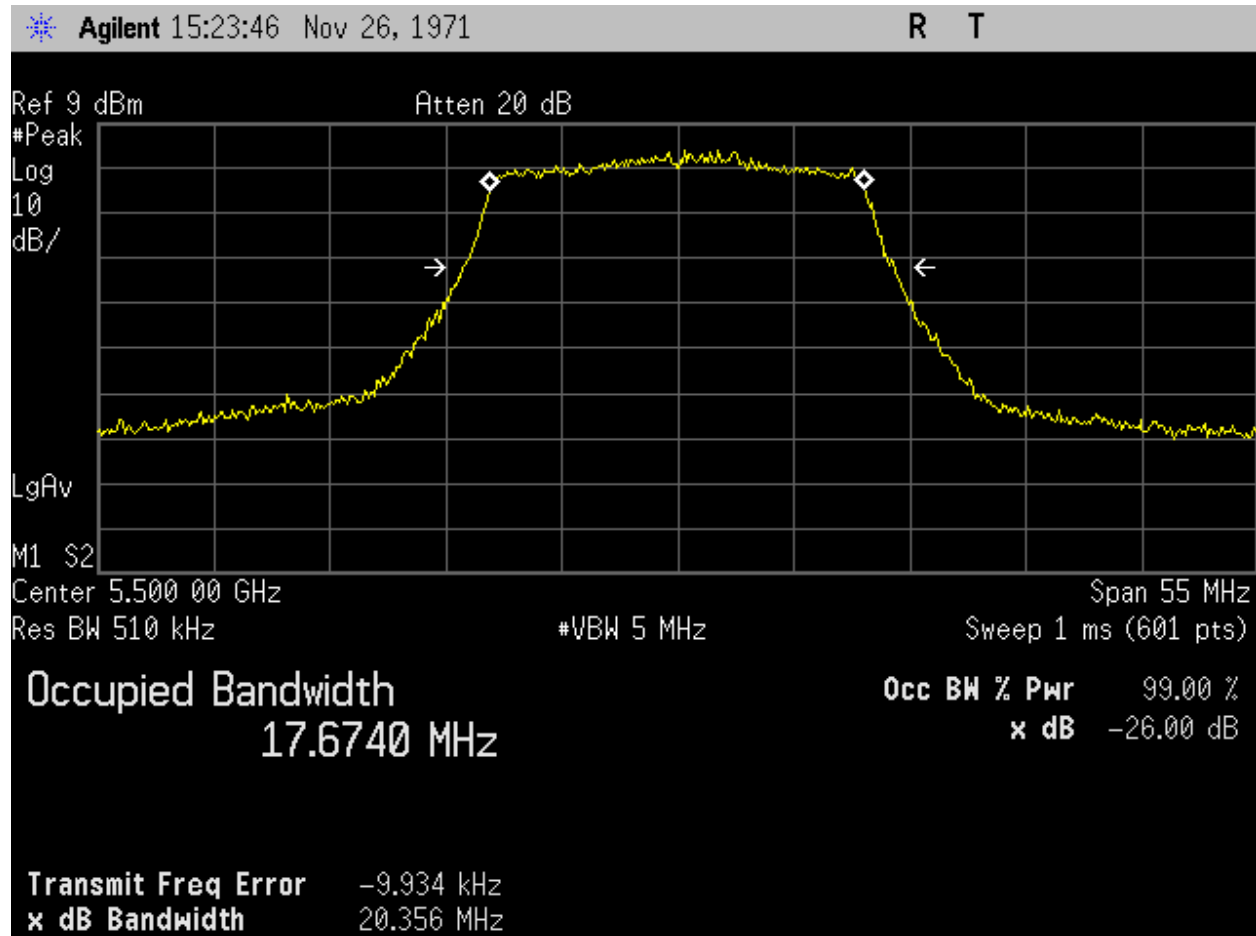


Figure 51: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

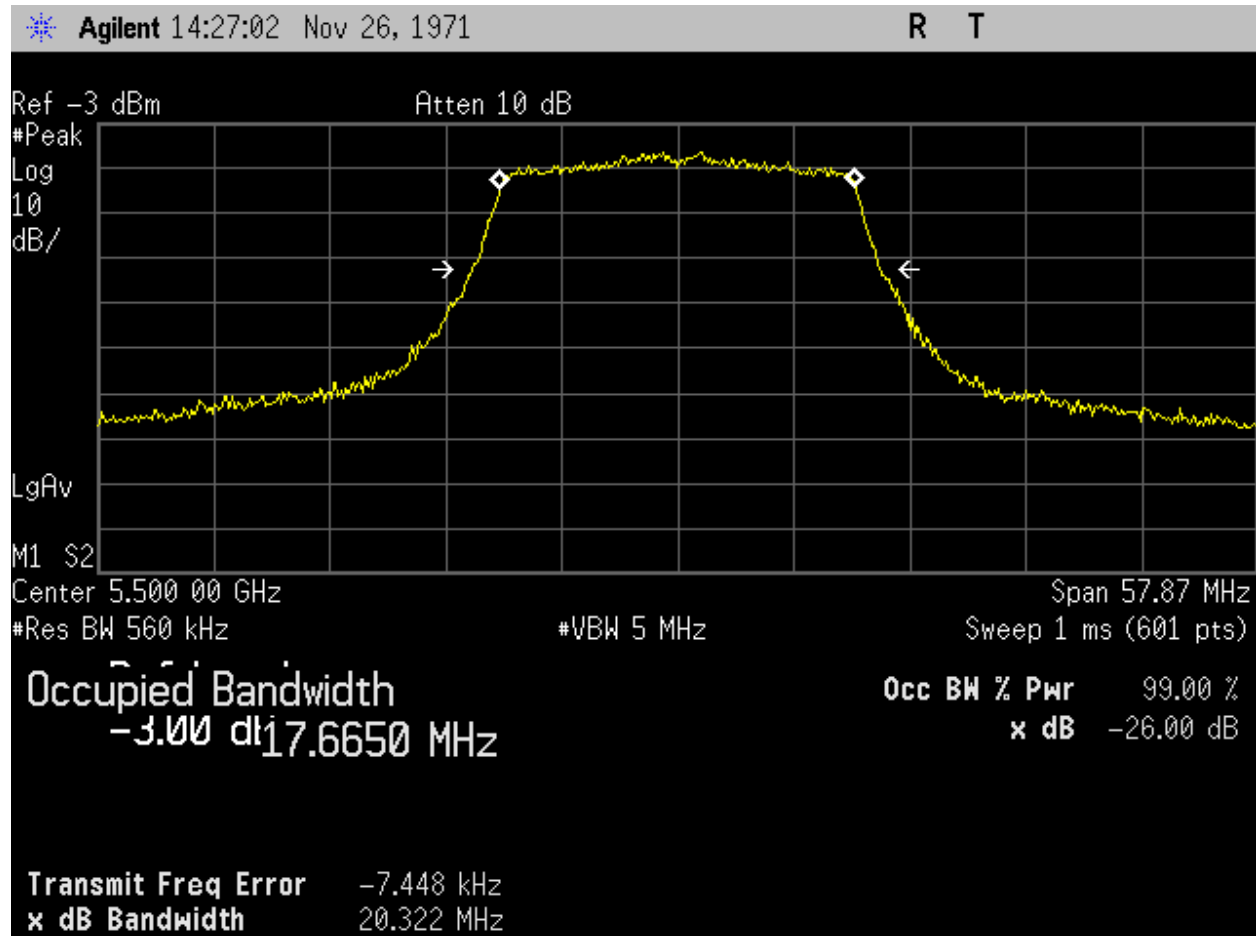


Figure 52: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

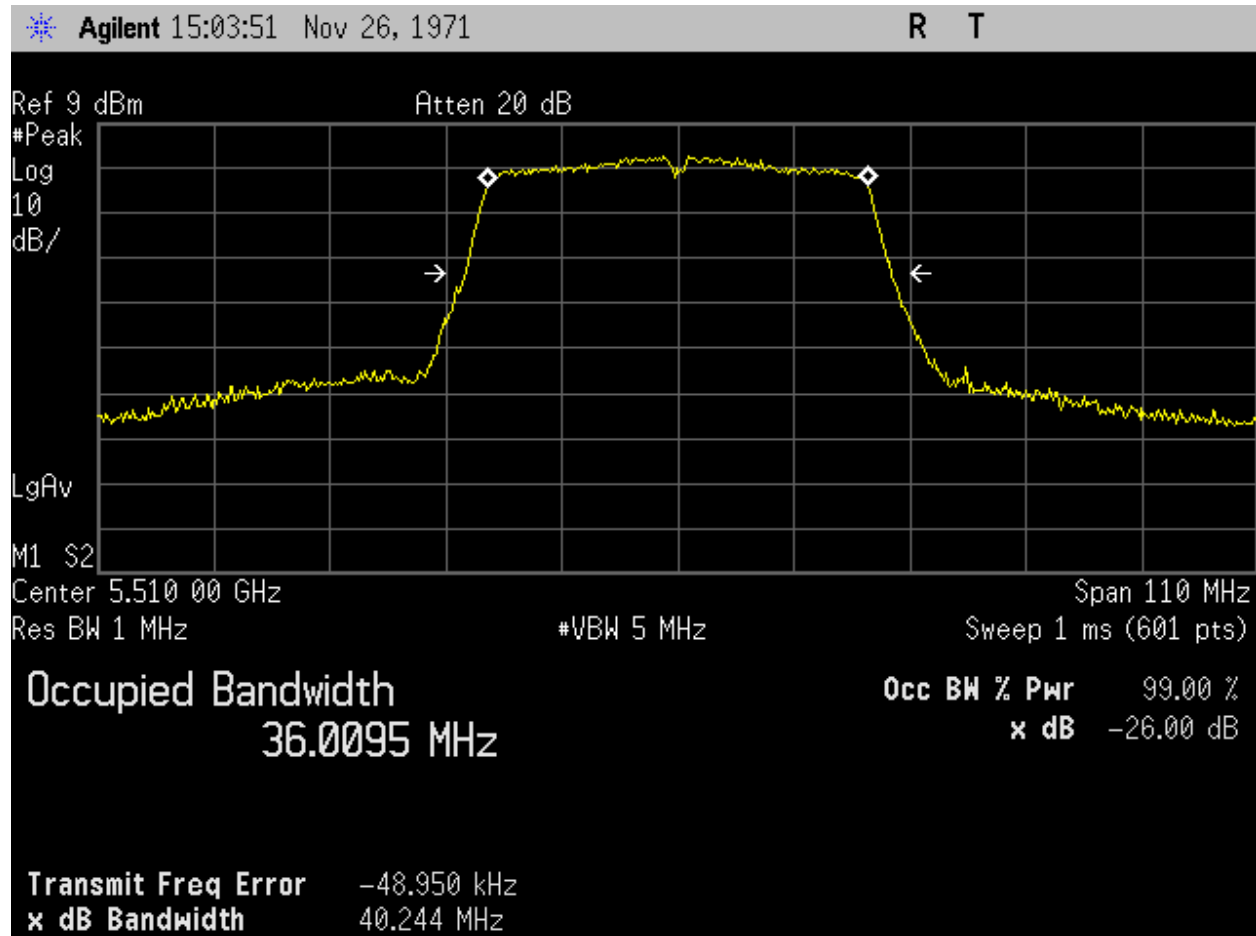


Figure 53: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

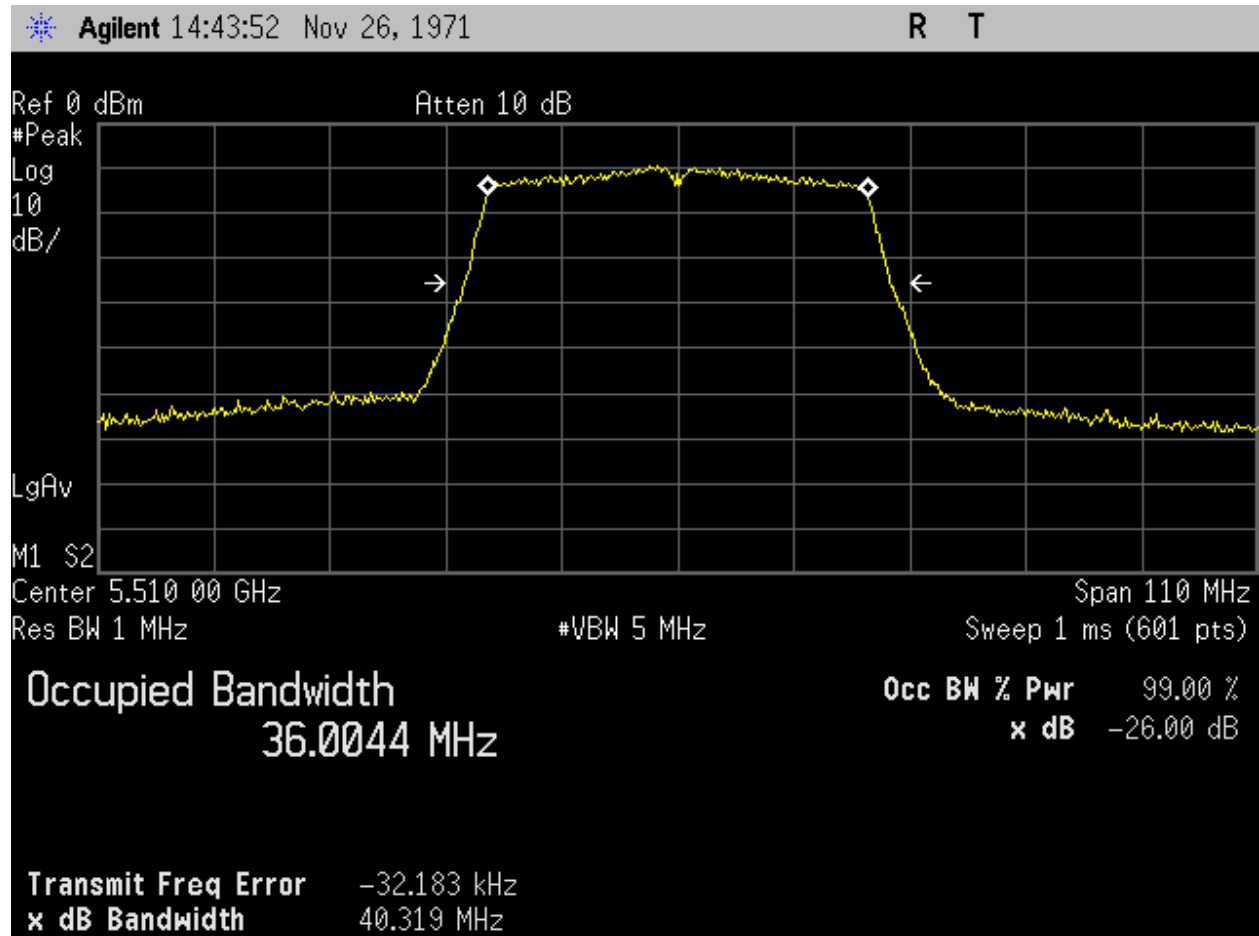


Figure 54: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

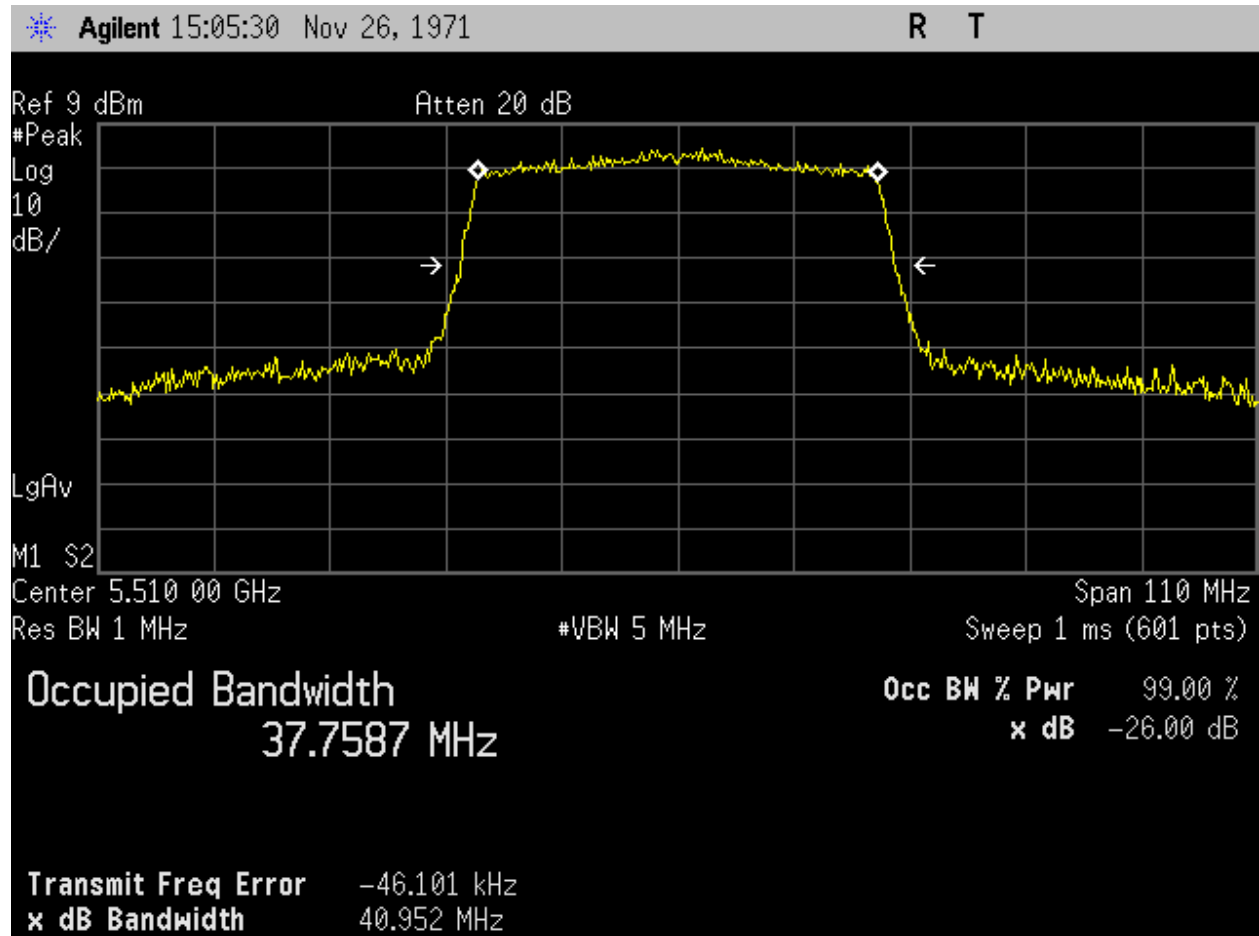


Figure 55: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

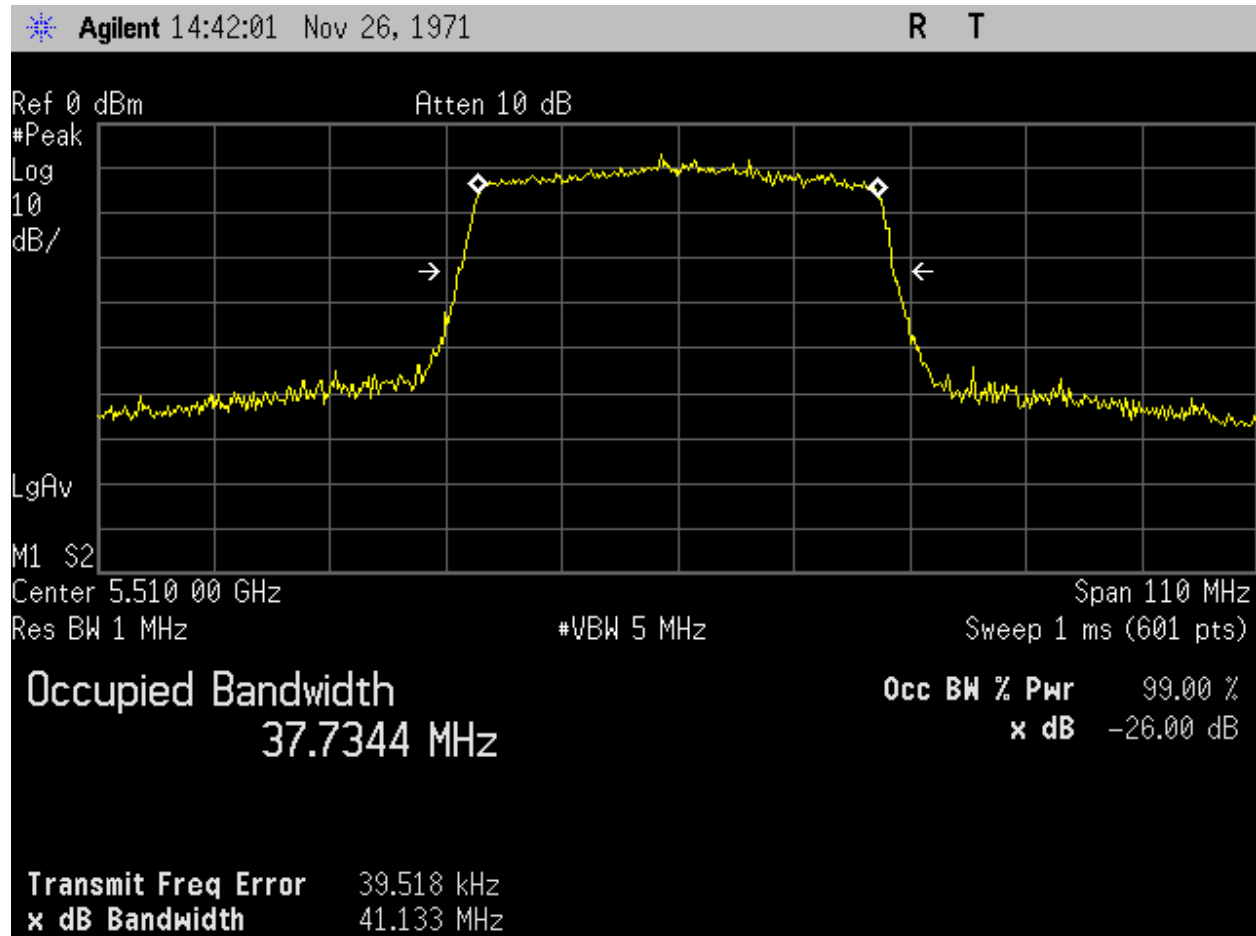


Figure 56: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

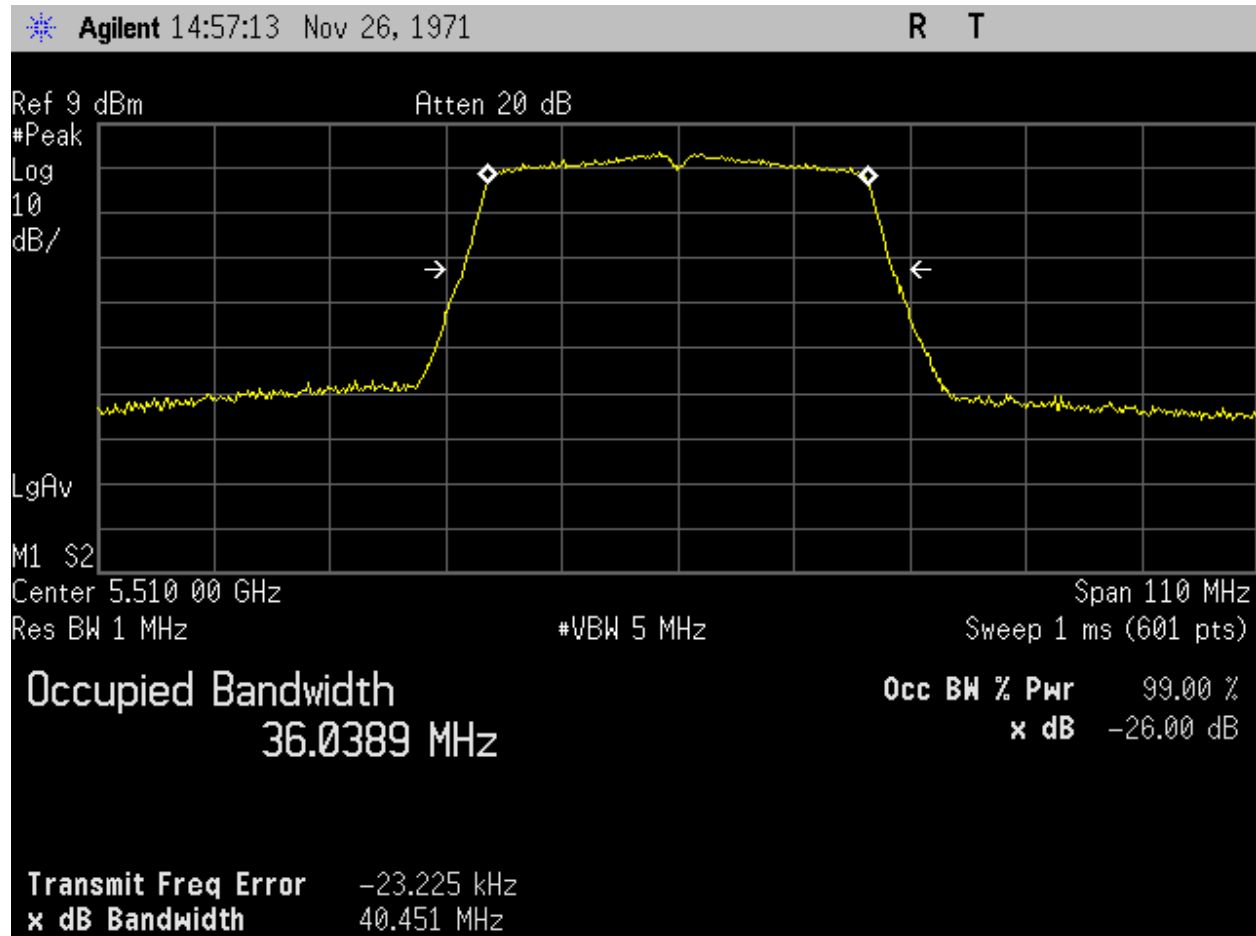


Figure 57: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

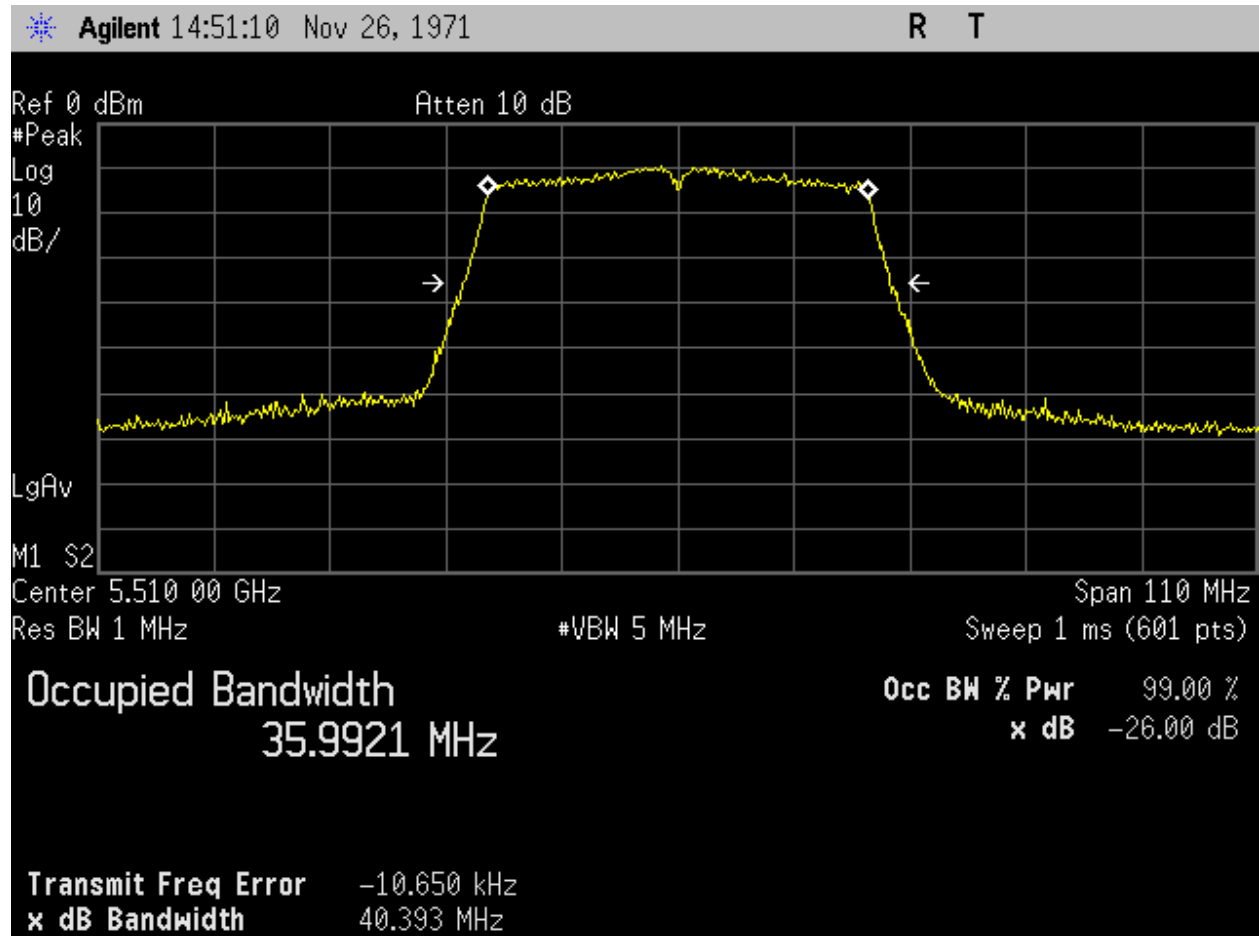


Figure 58: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

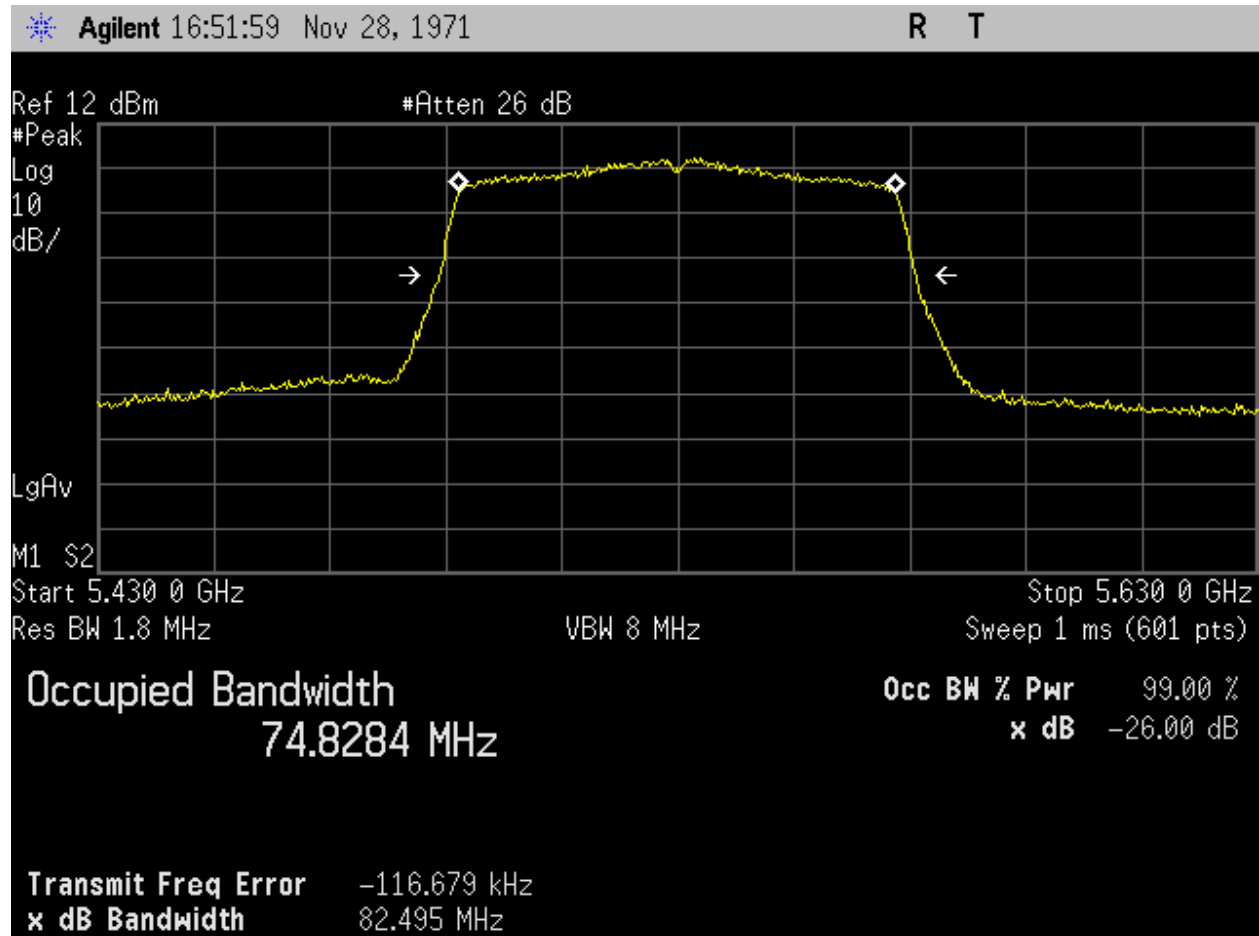


Figure 59: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

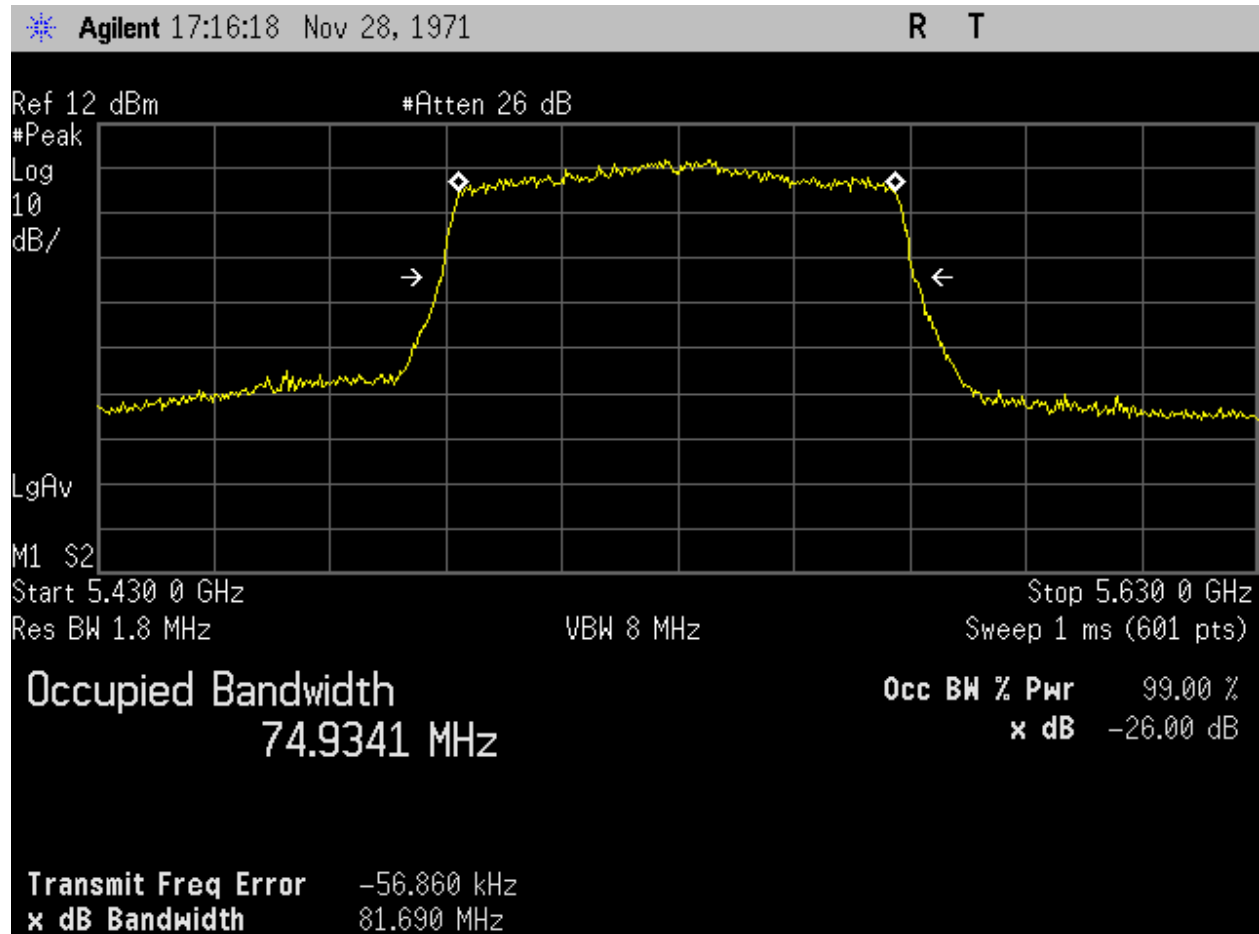


Figure 60: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

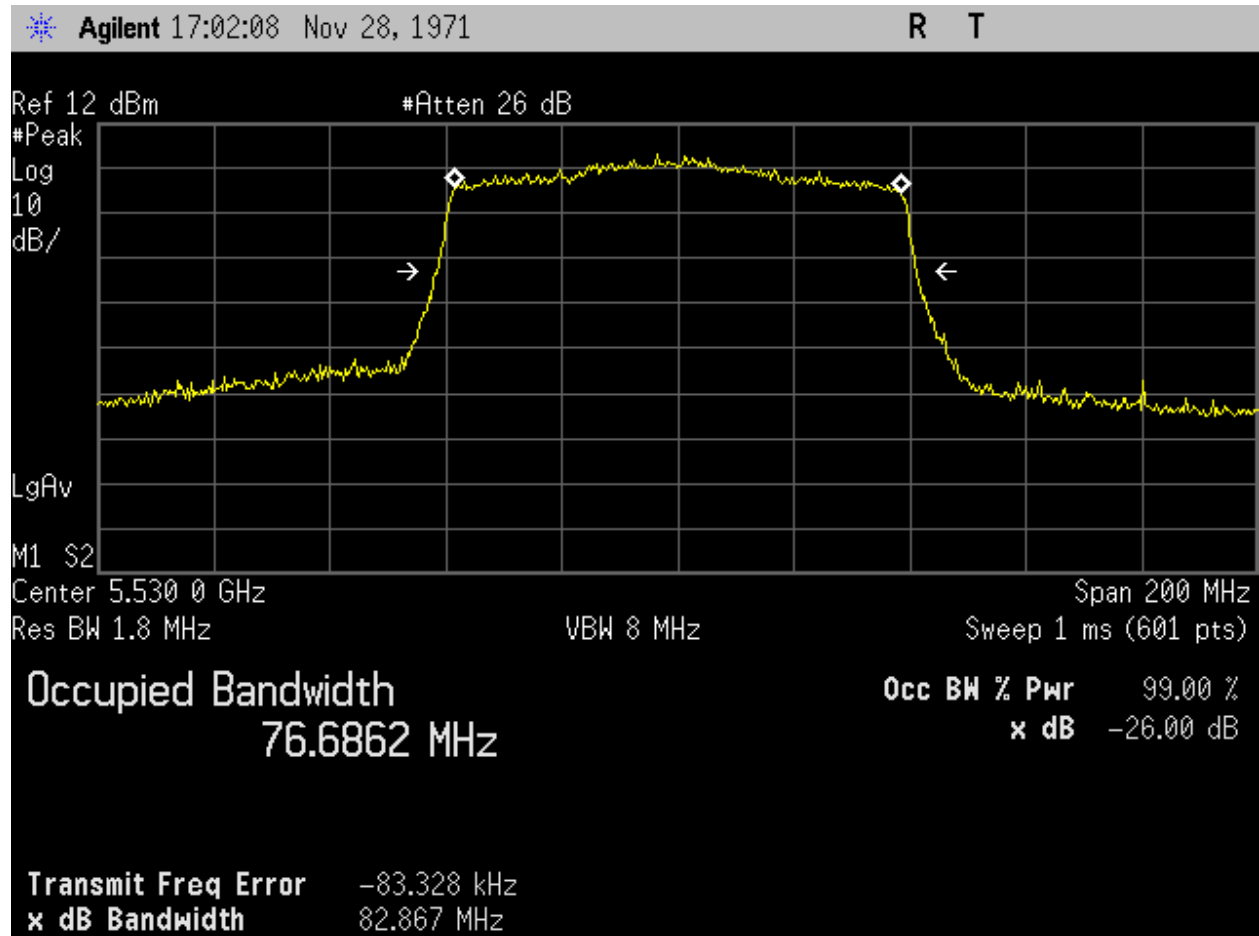


Figure 61: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

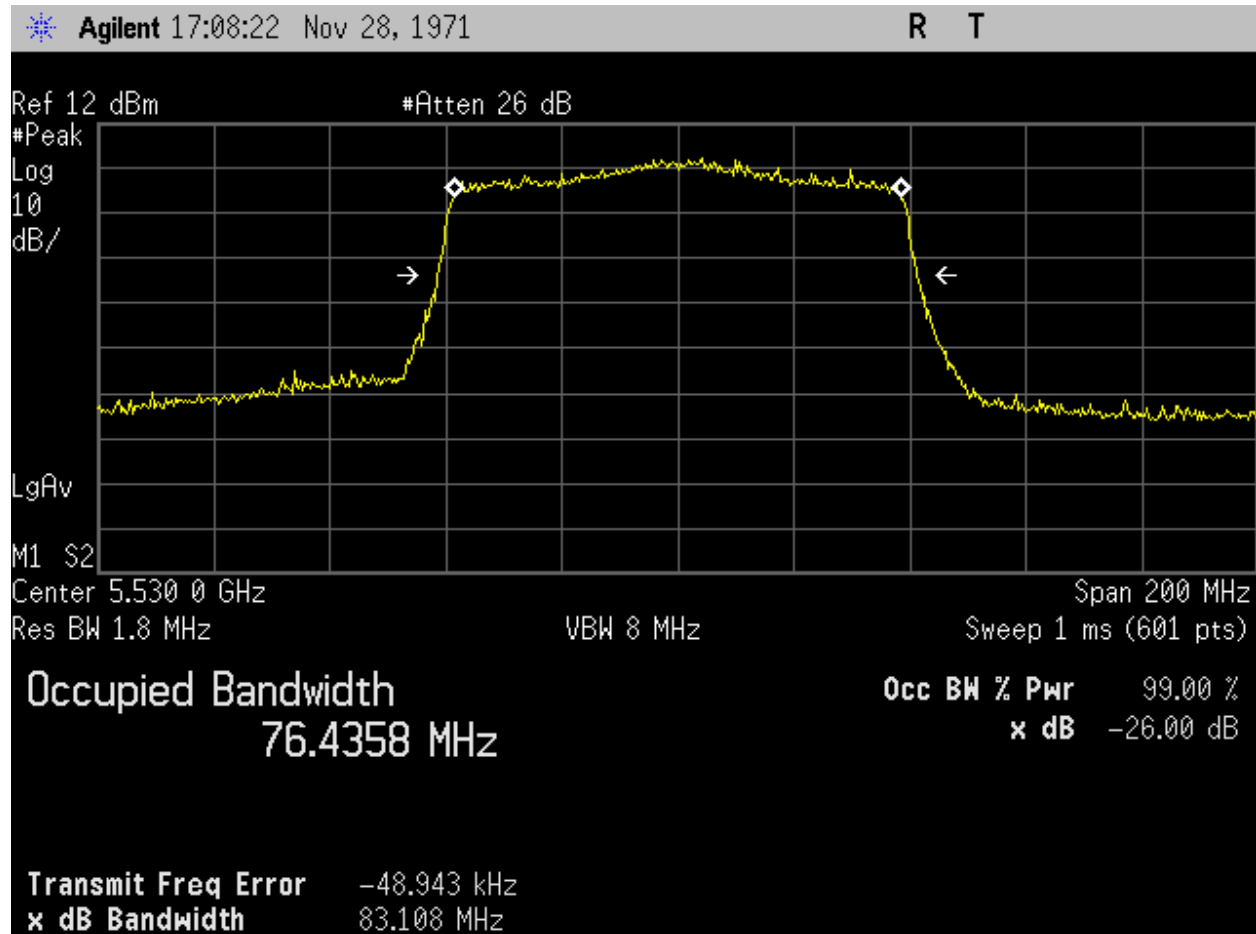


Figure 62: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

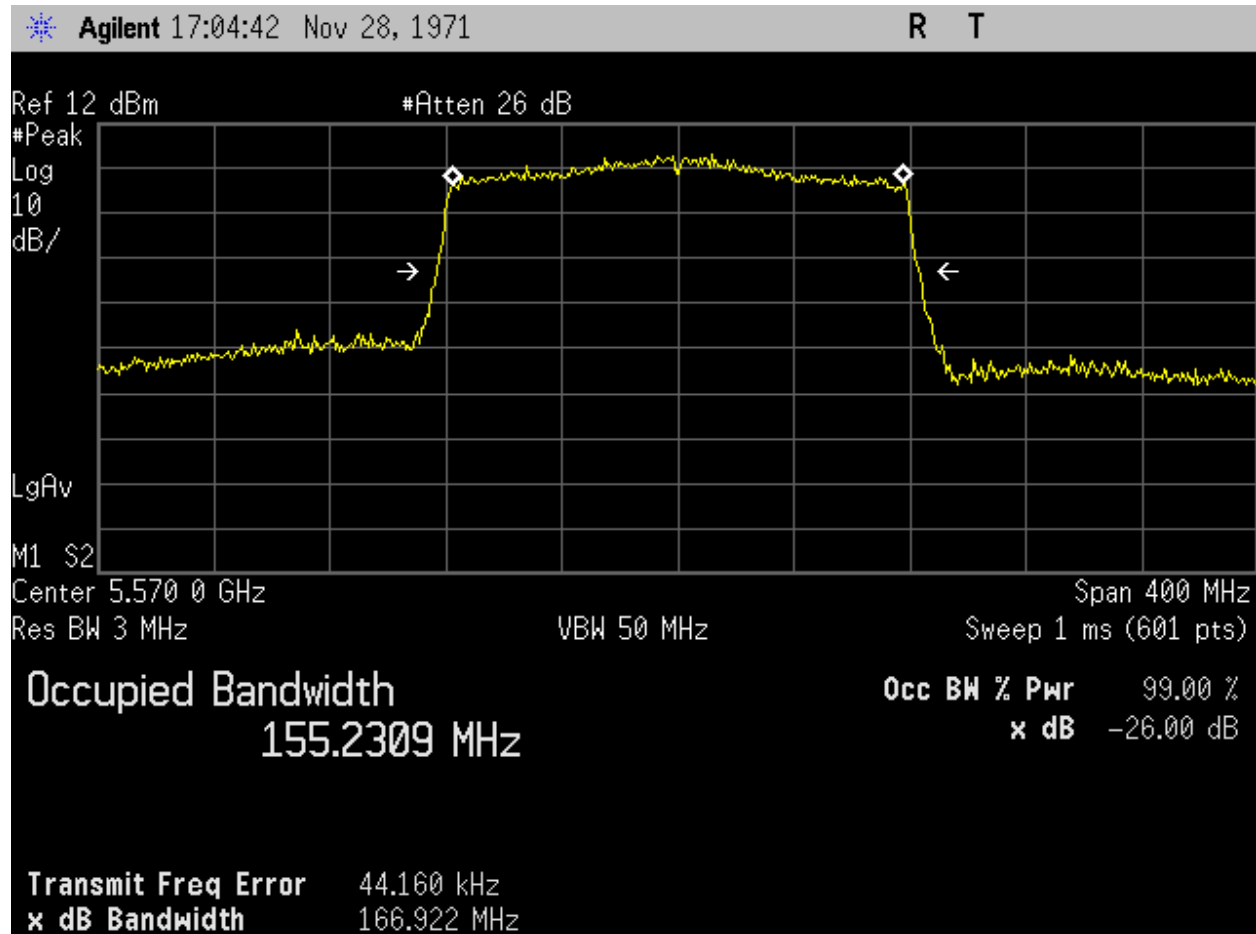


Figure 63: U-NII-2C_5570MHz_Mid Ch_114_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

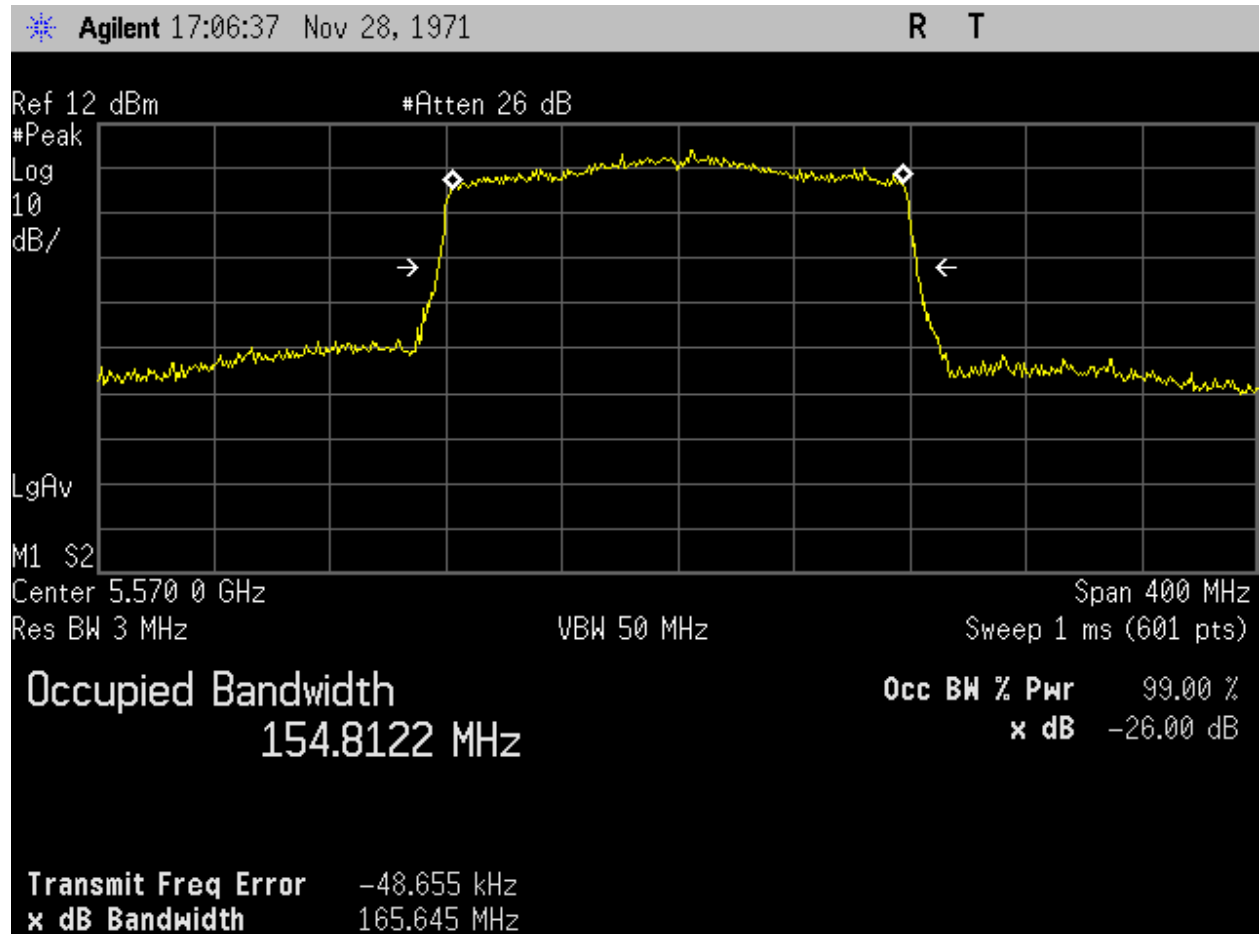


Figure 64: U-NII-2C_5570MHz_Mid Ch_114_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

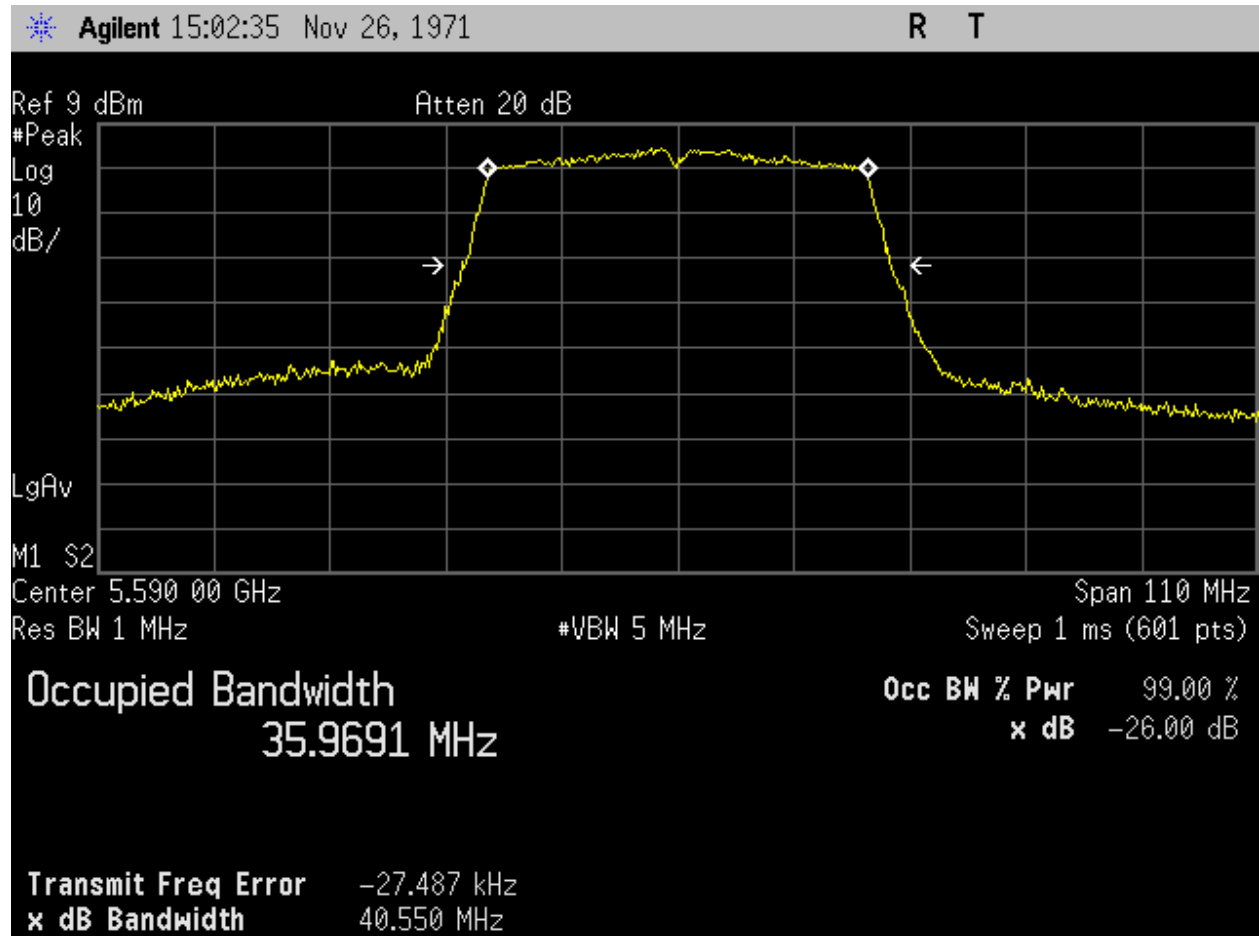


Figure 65: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

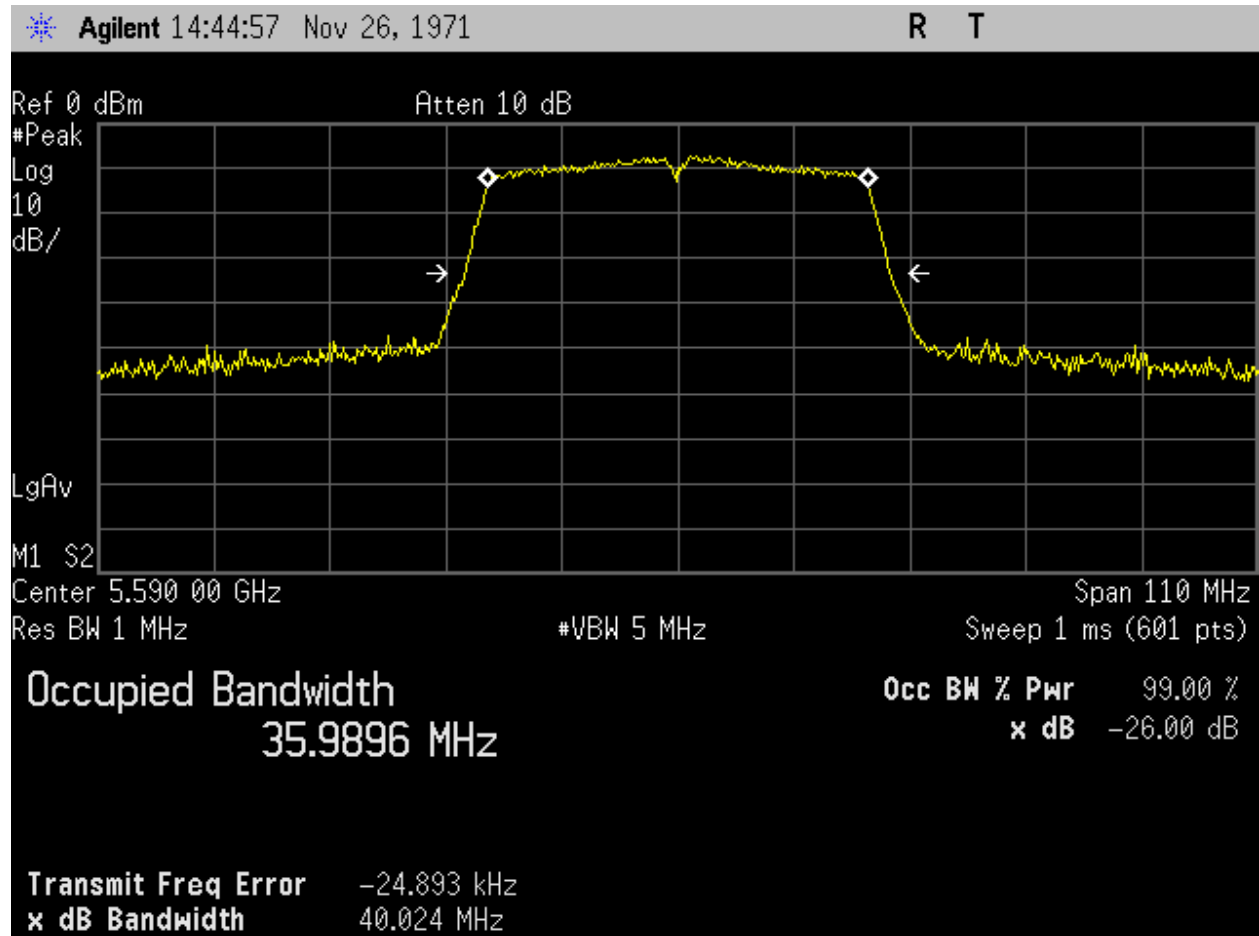


Figure 66: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

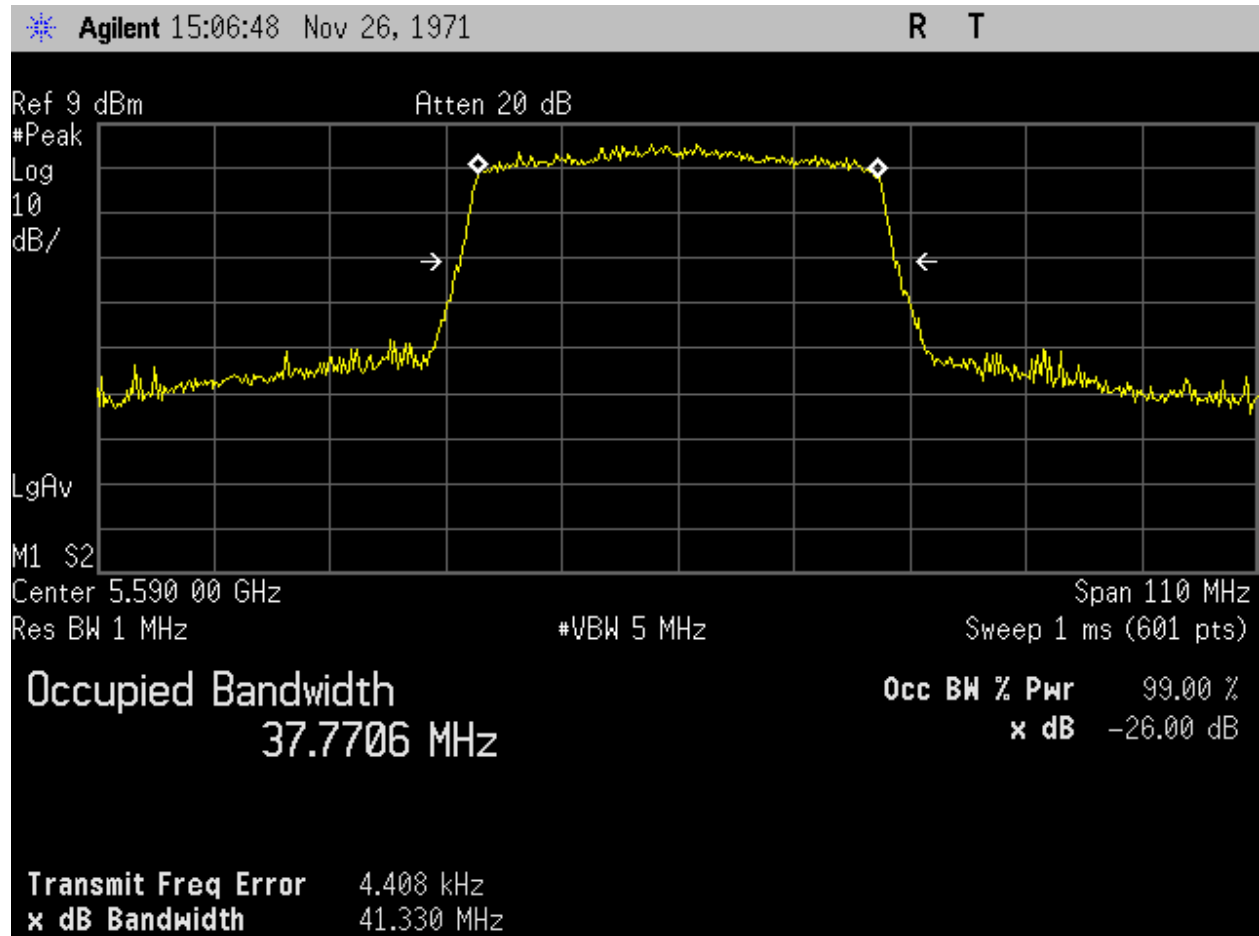


Figure 67: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

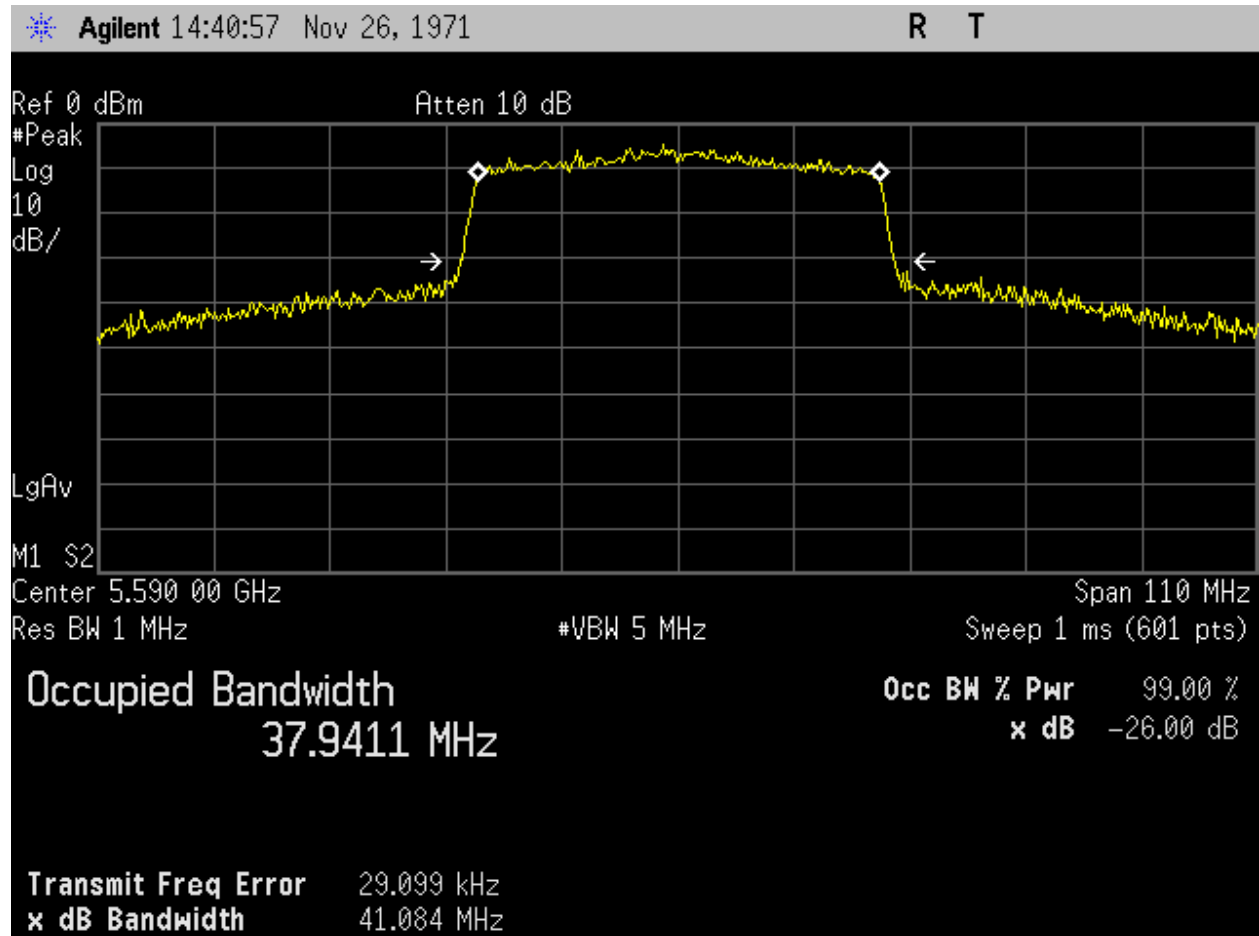


Figure 68: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

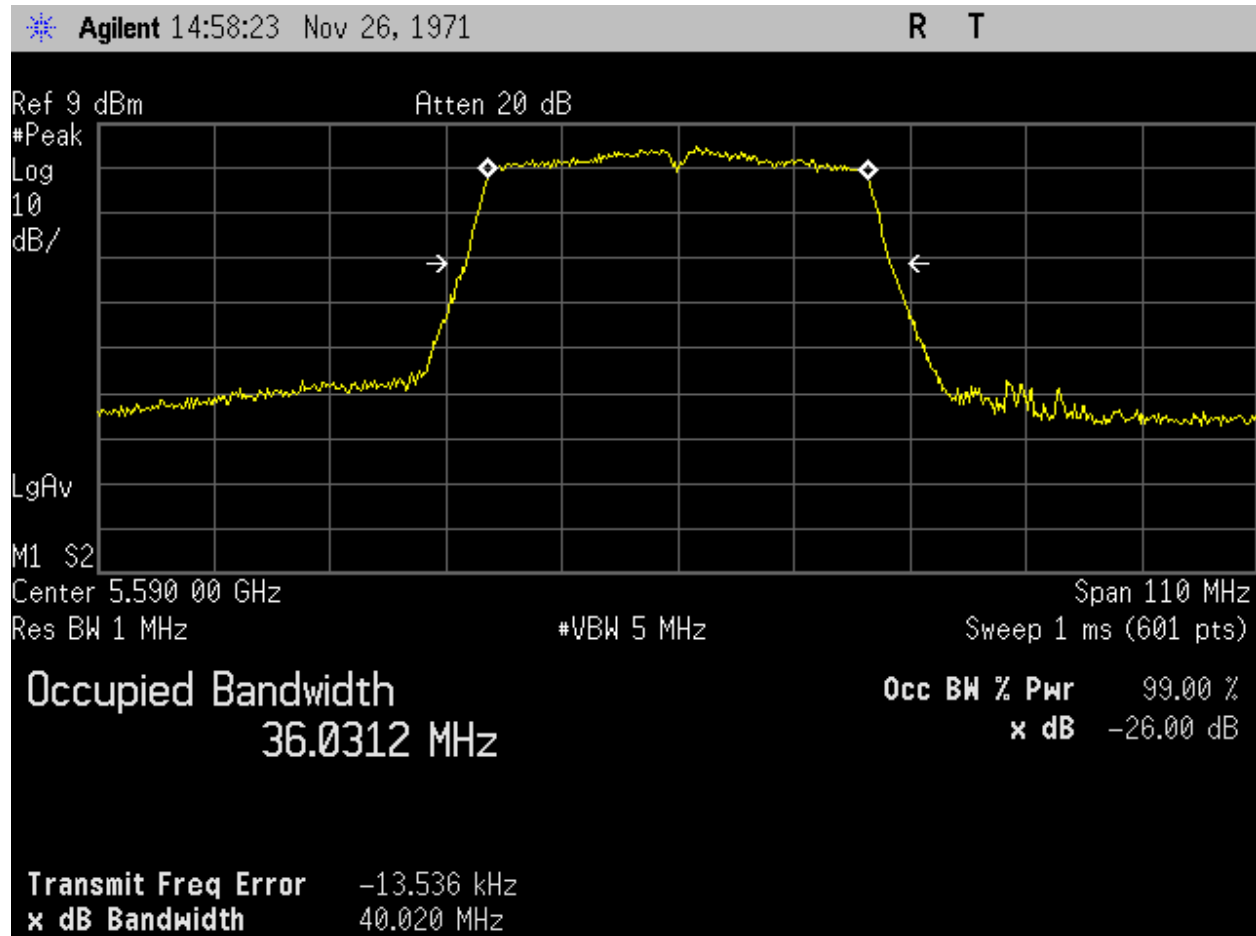


Figure 69: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

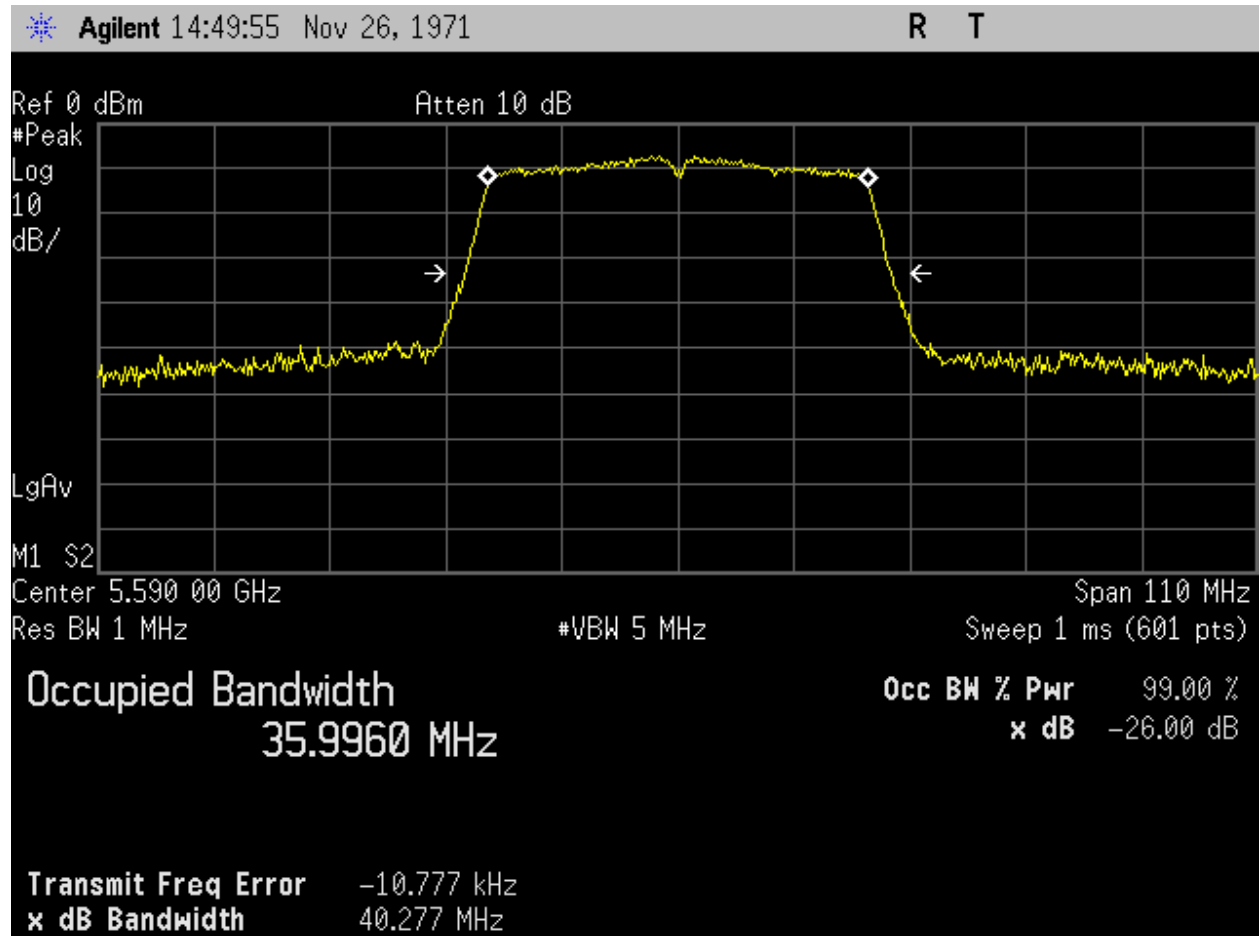


Figure 70: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

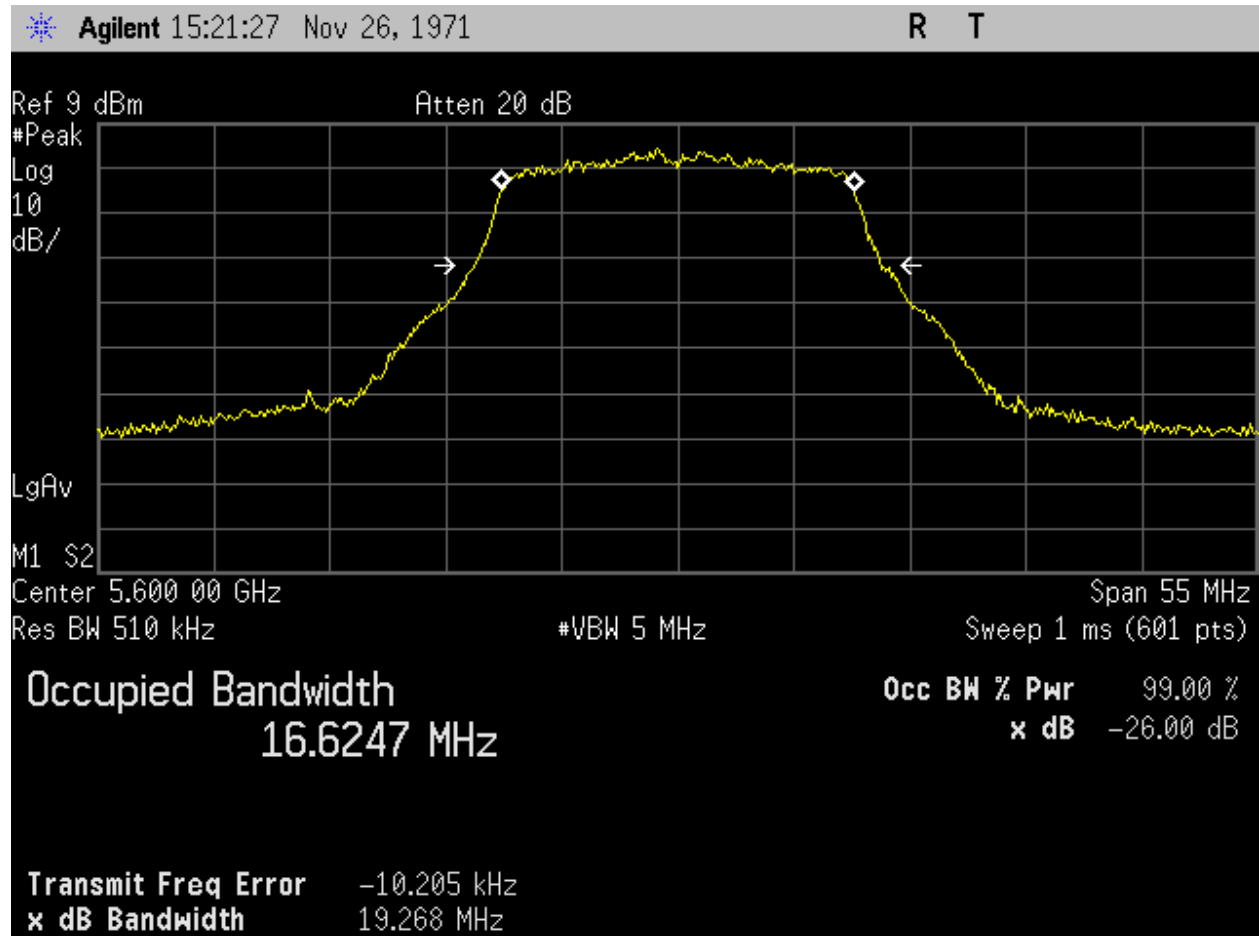


Figure 71: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

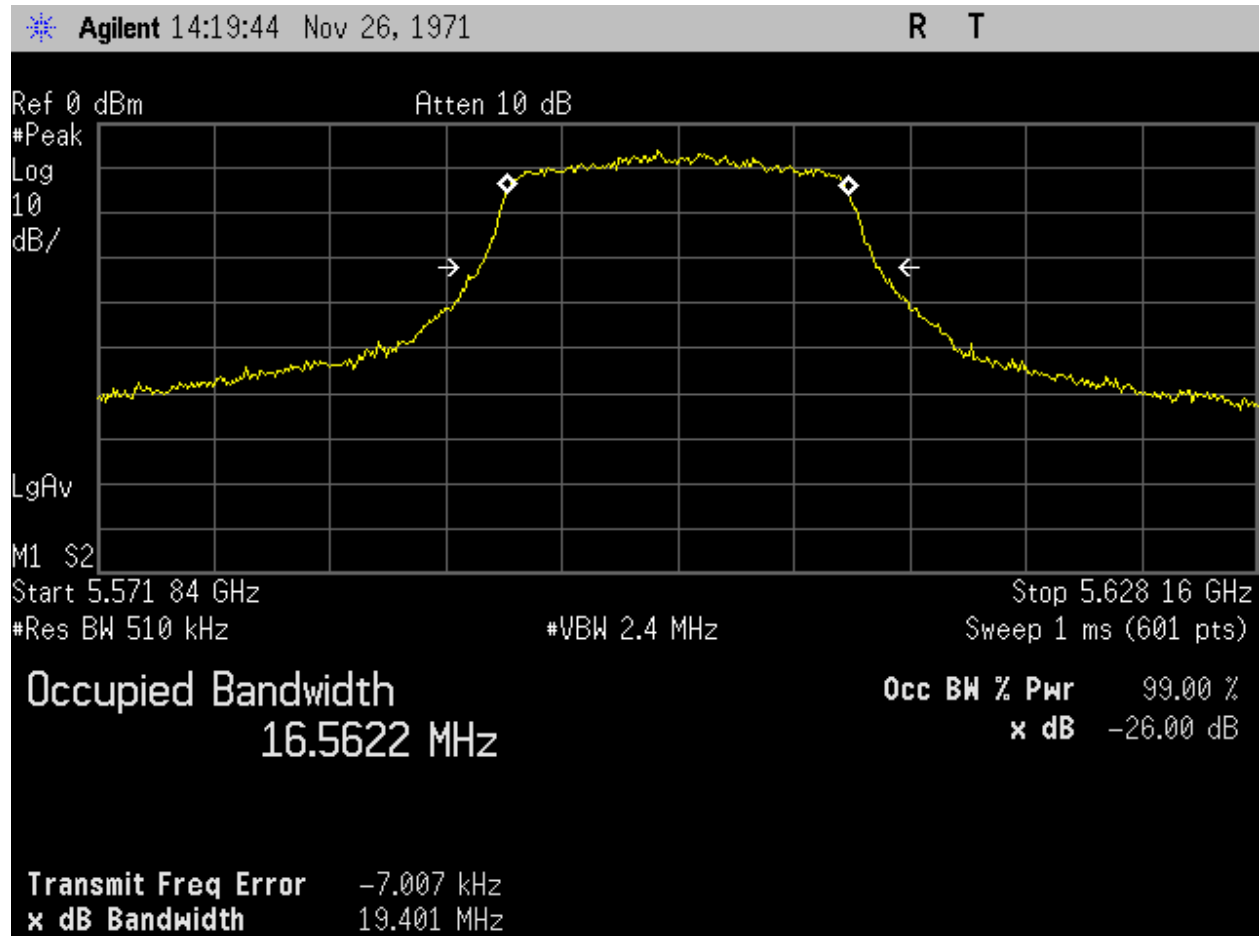


Figure 72: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

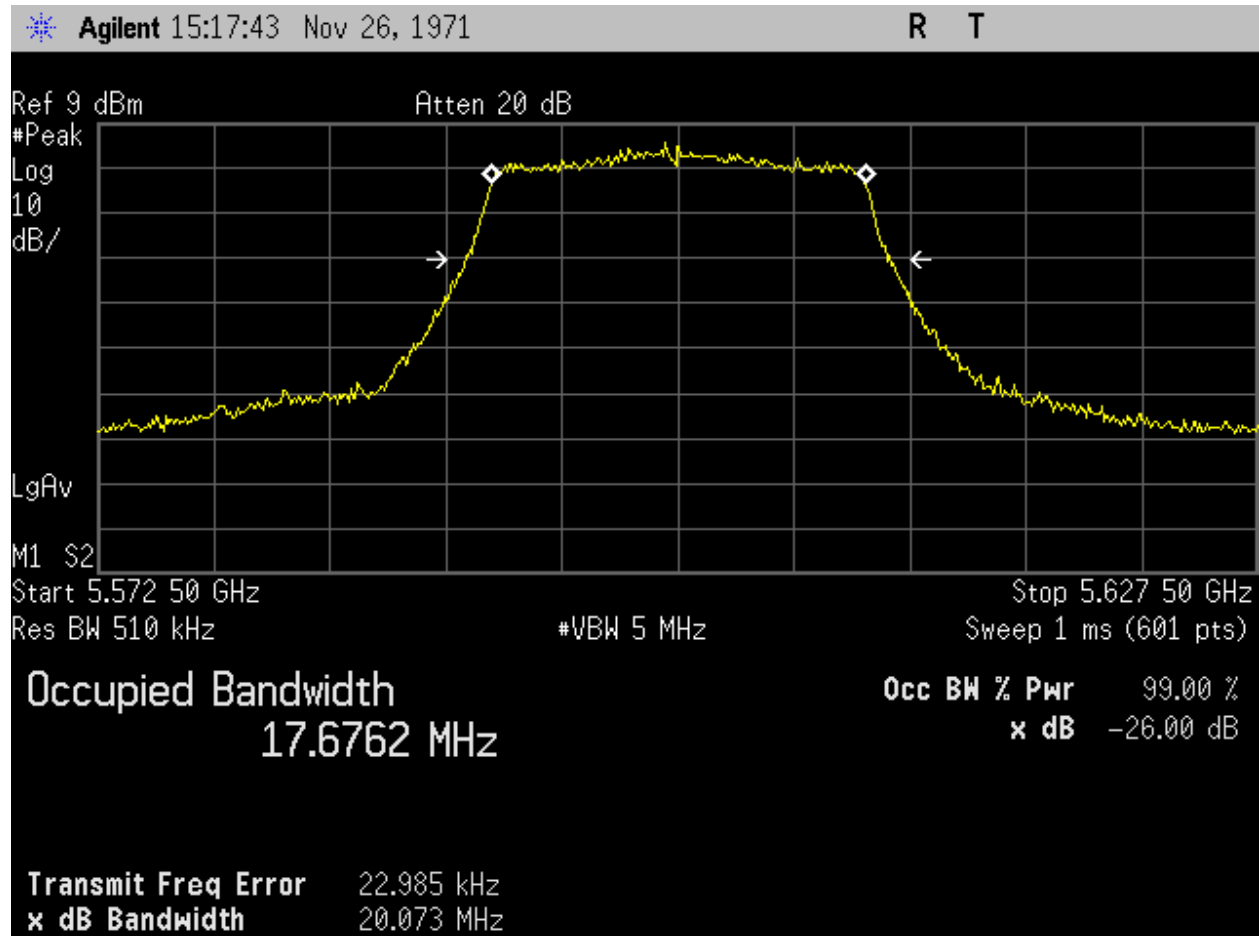


Figure 73: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

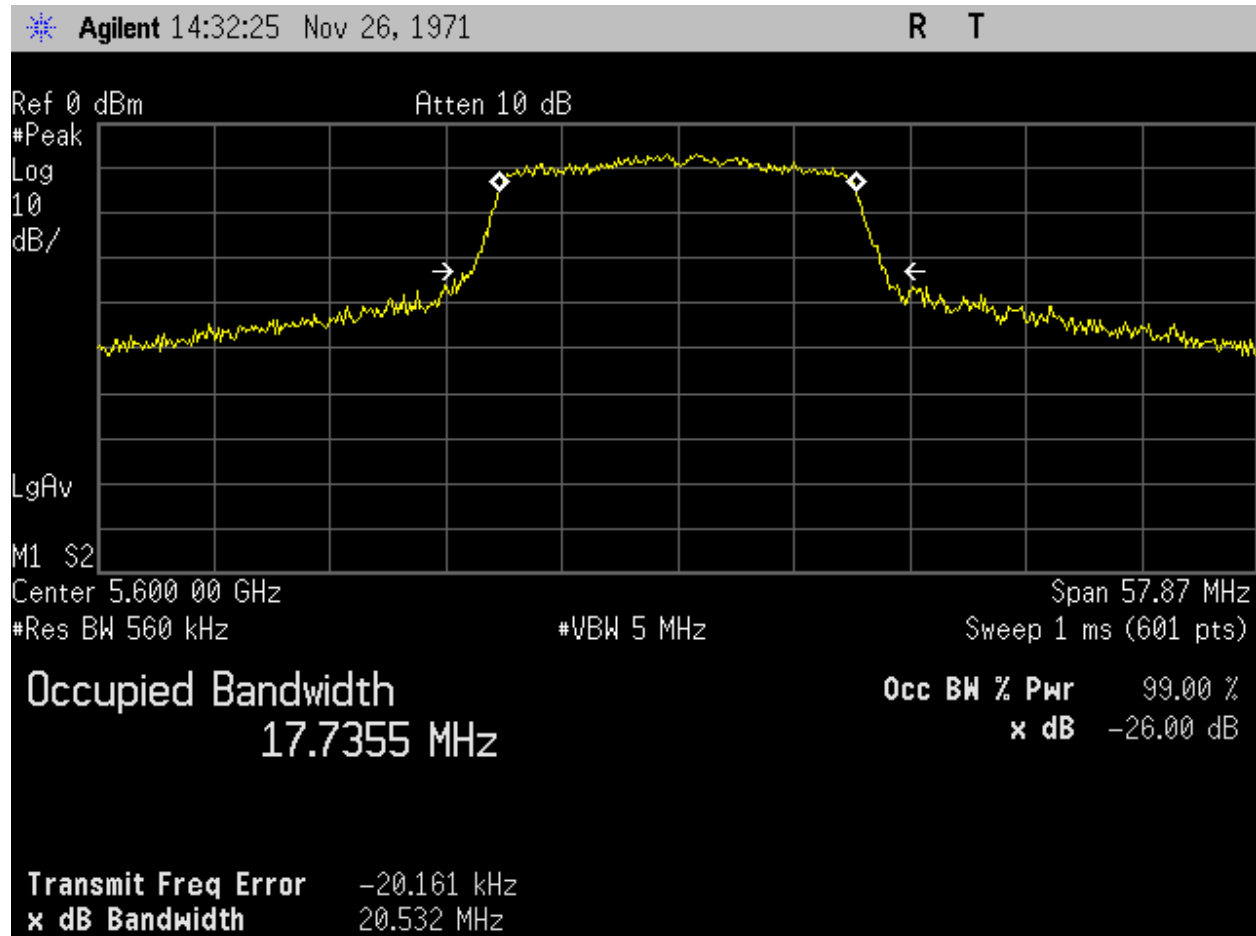


Figure 74: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

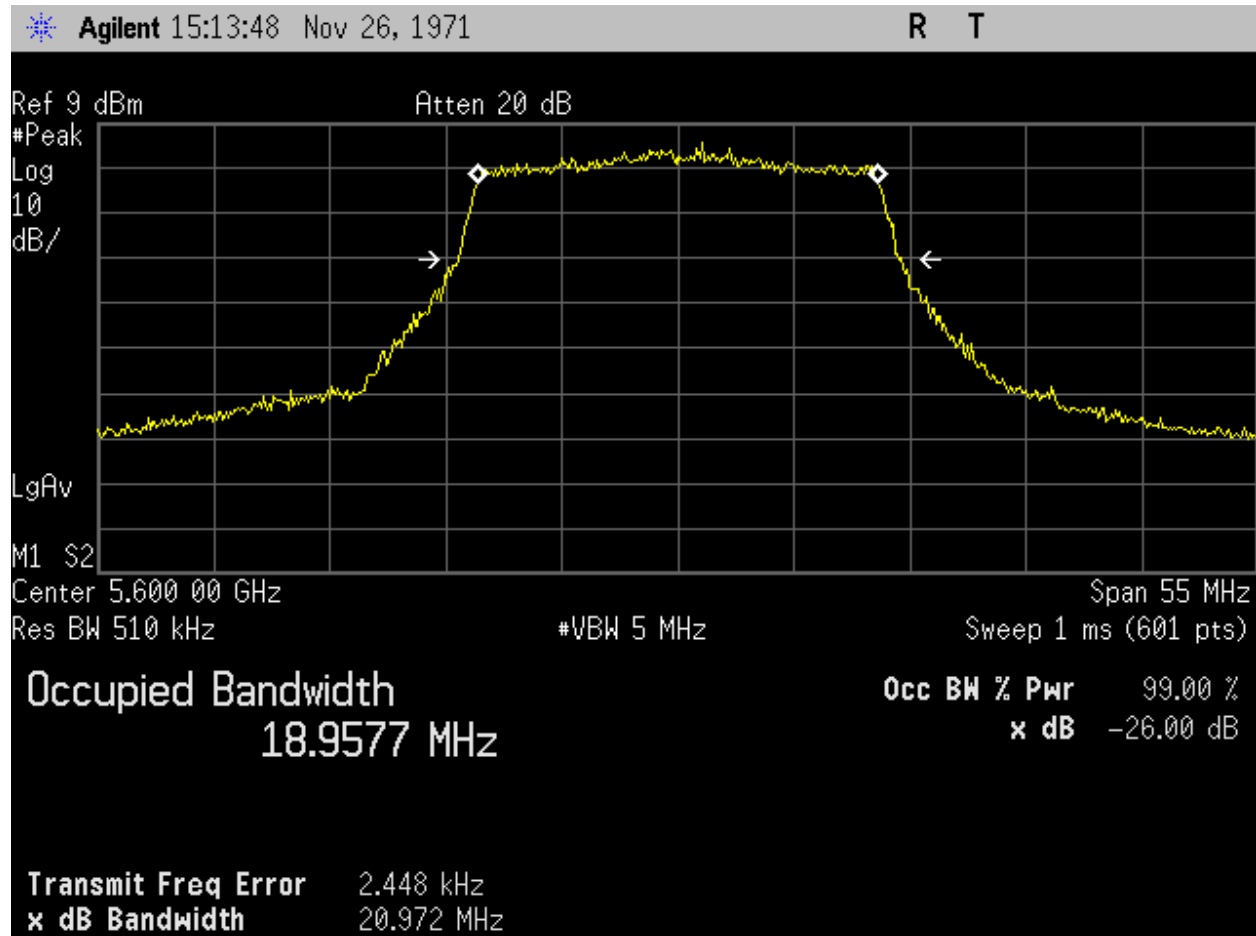


Figure 75: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

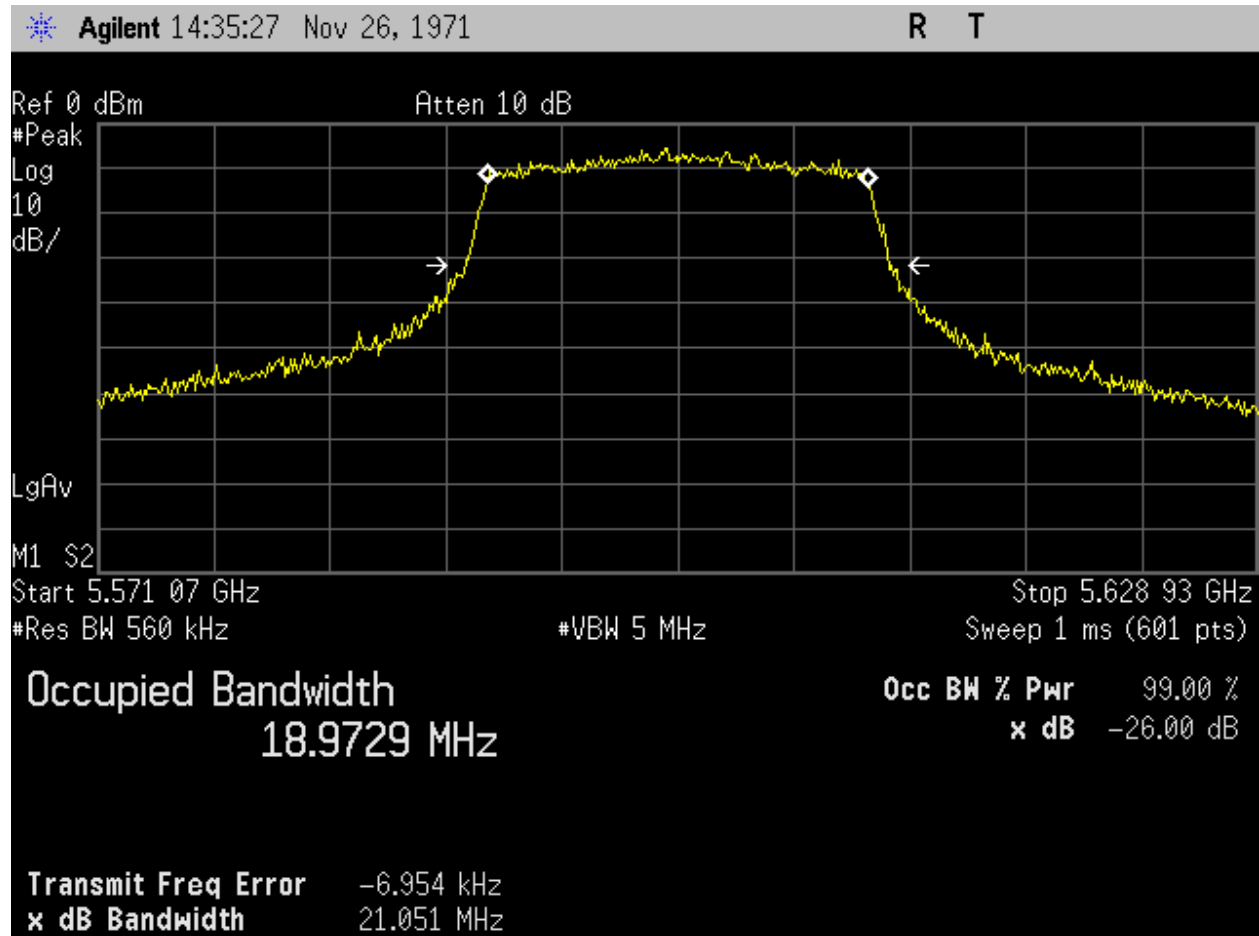


Figure 76: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

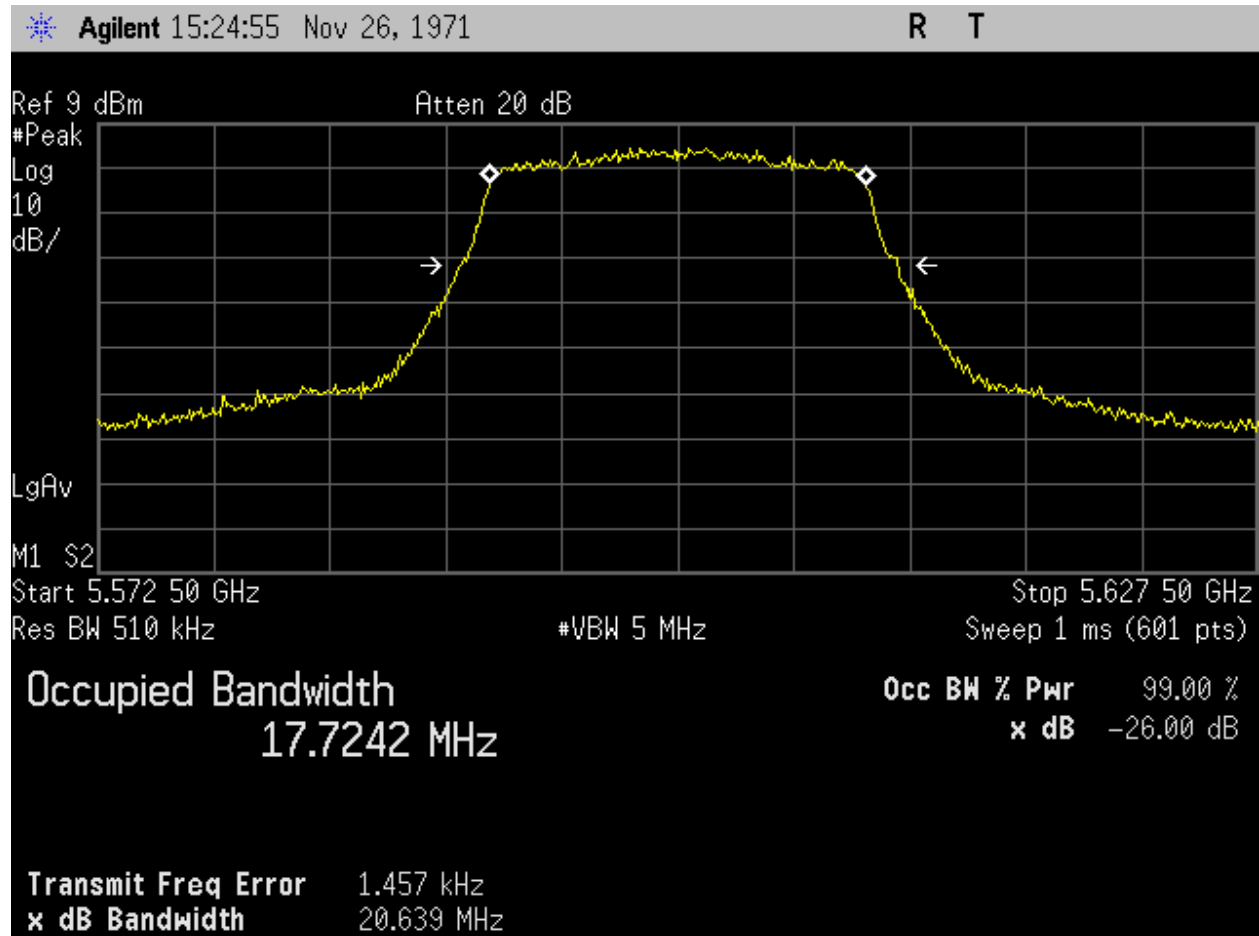


Figure 77: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

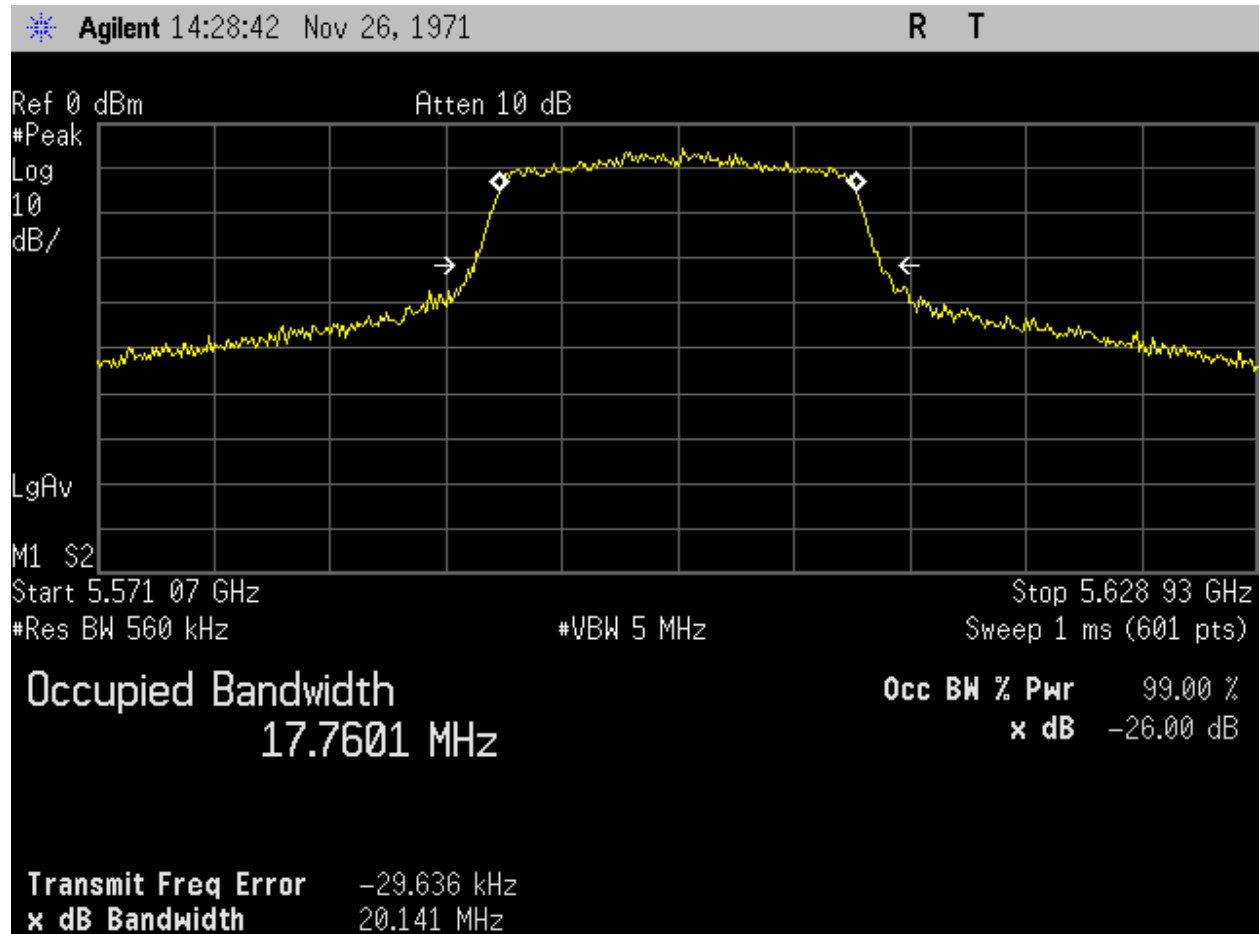


Figure 78: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

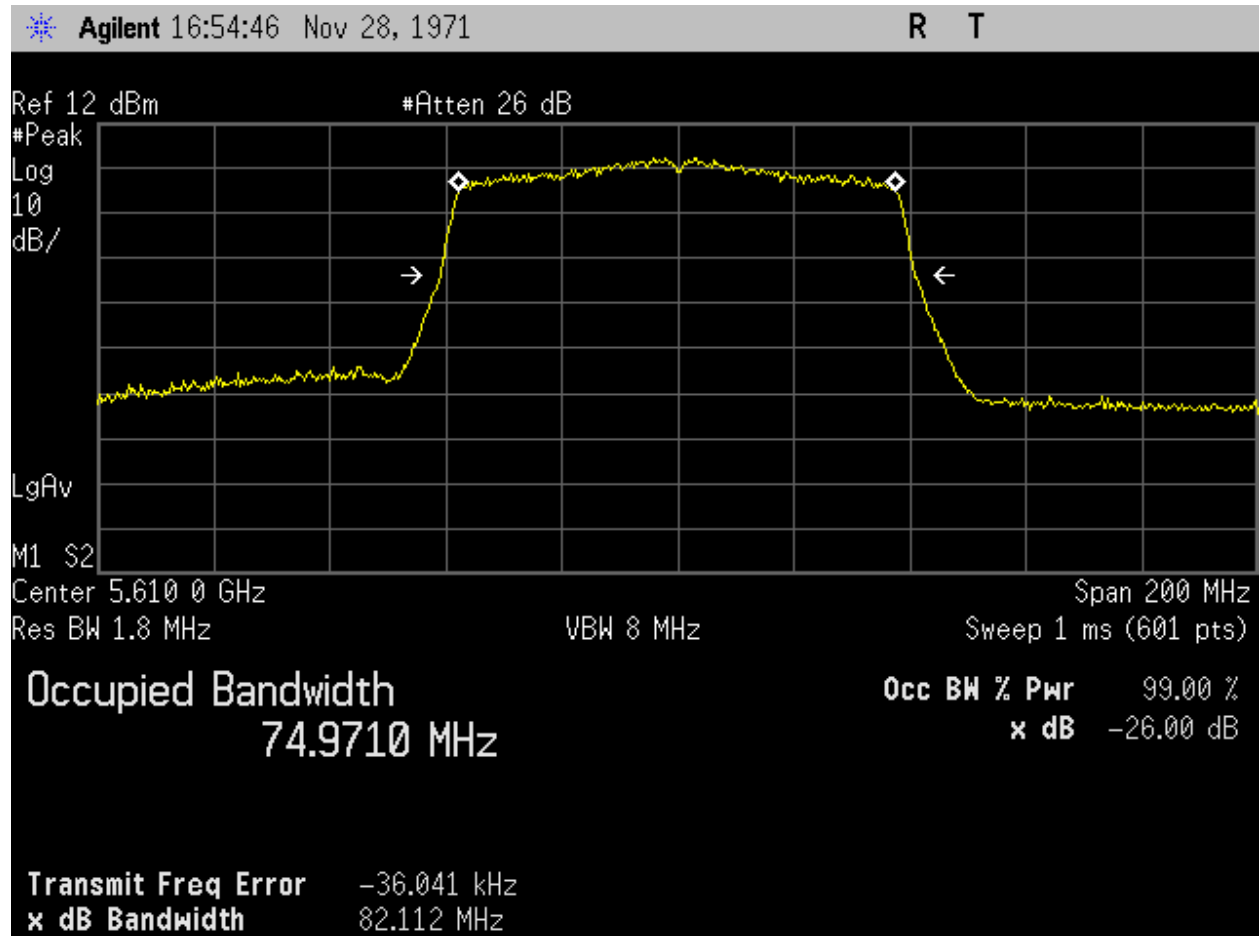


Figure 79: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

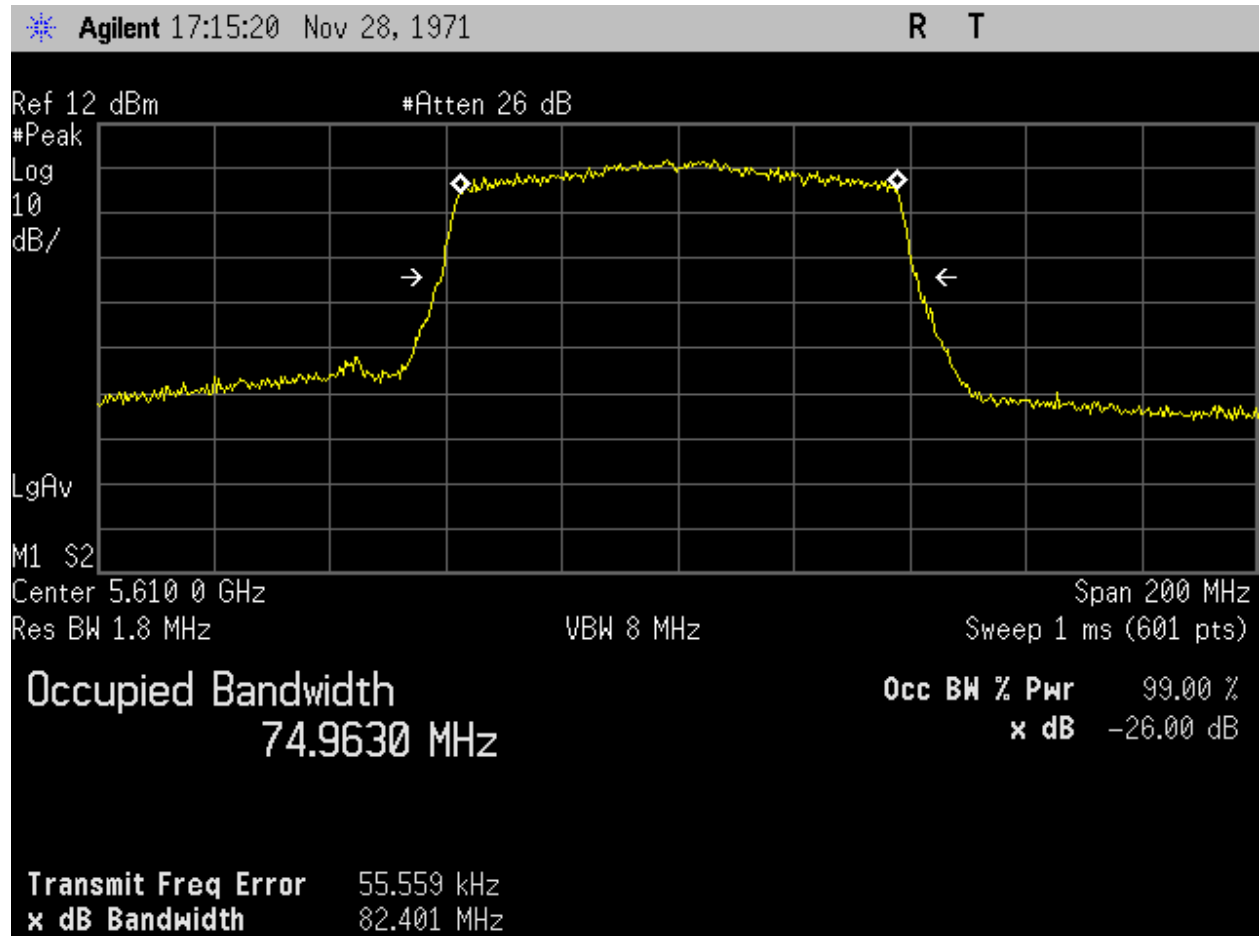


Figure 80: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

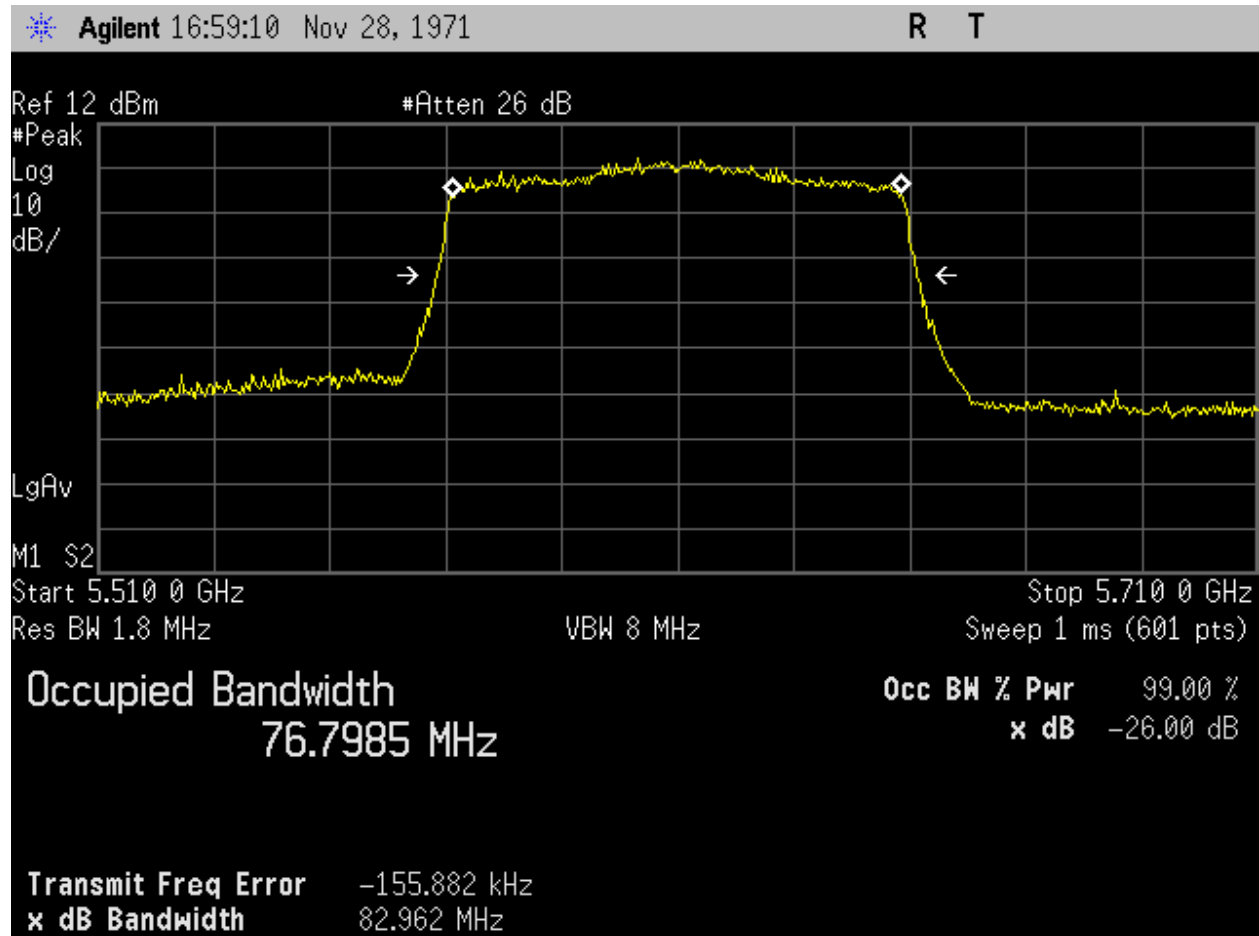


Figure 81: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

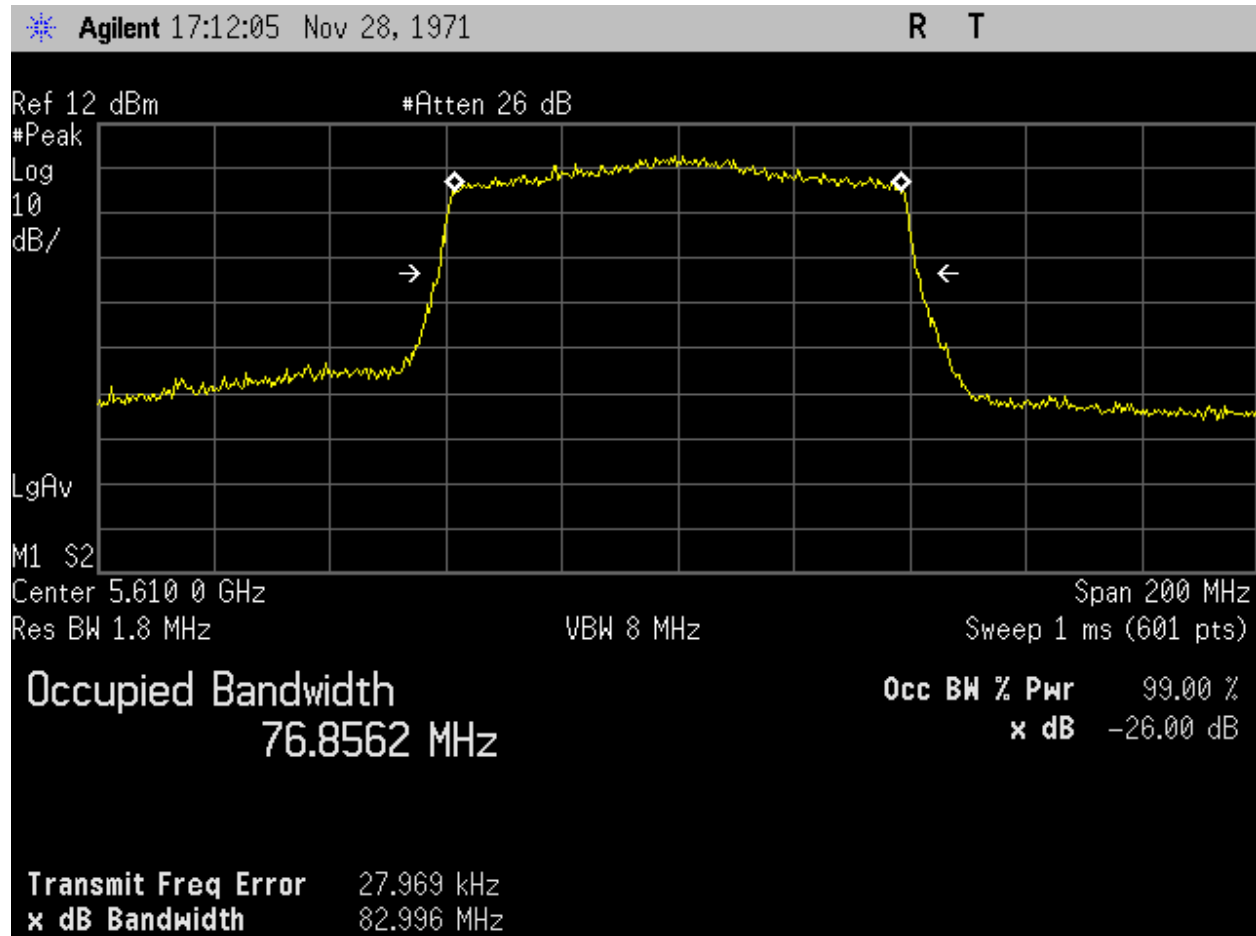


Figure 82: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

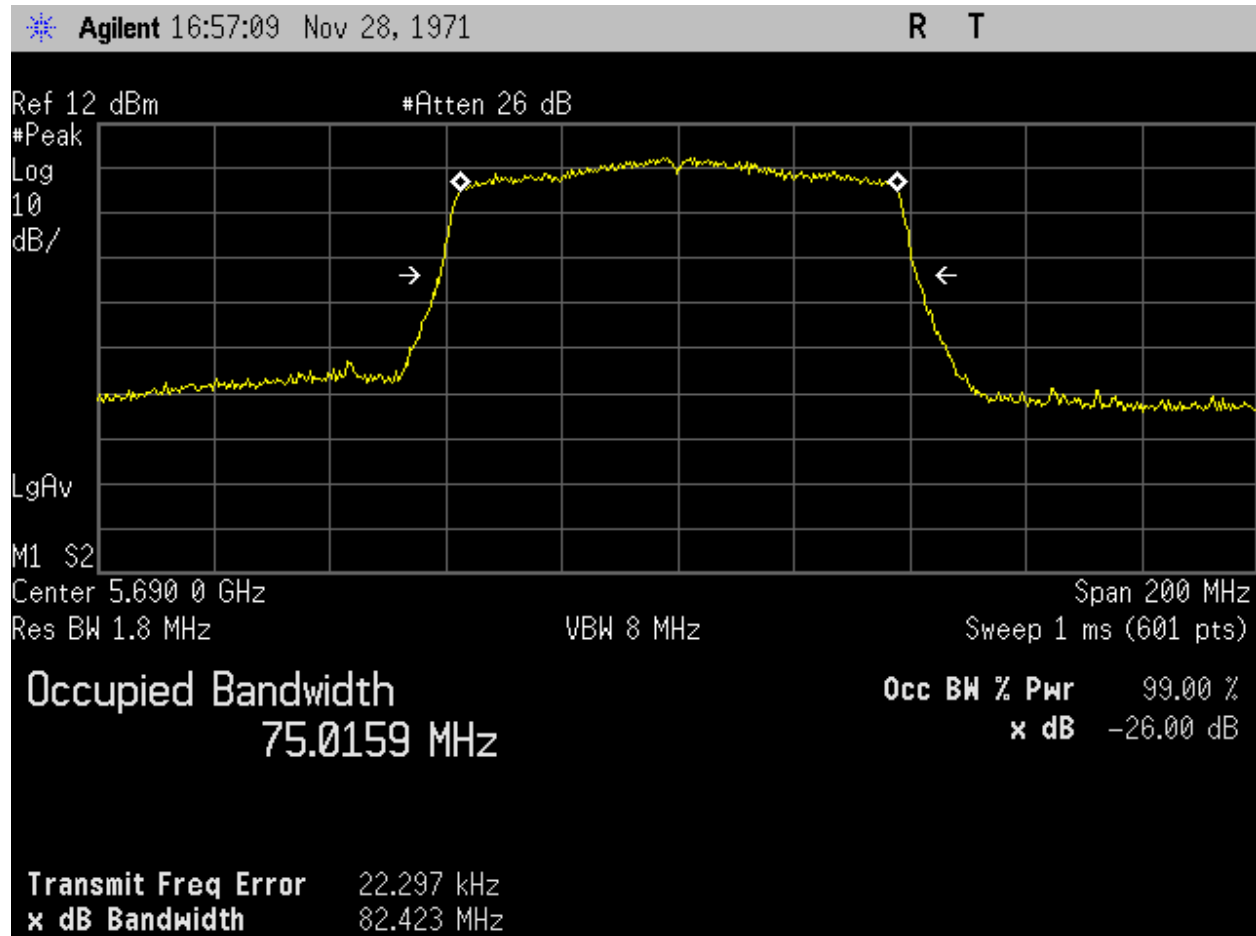


Figure 83: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

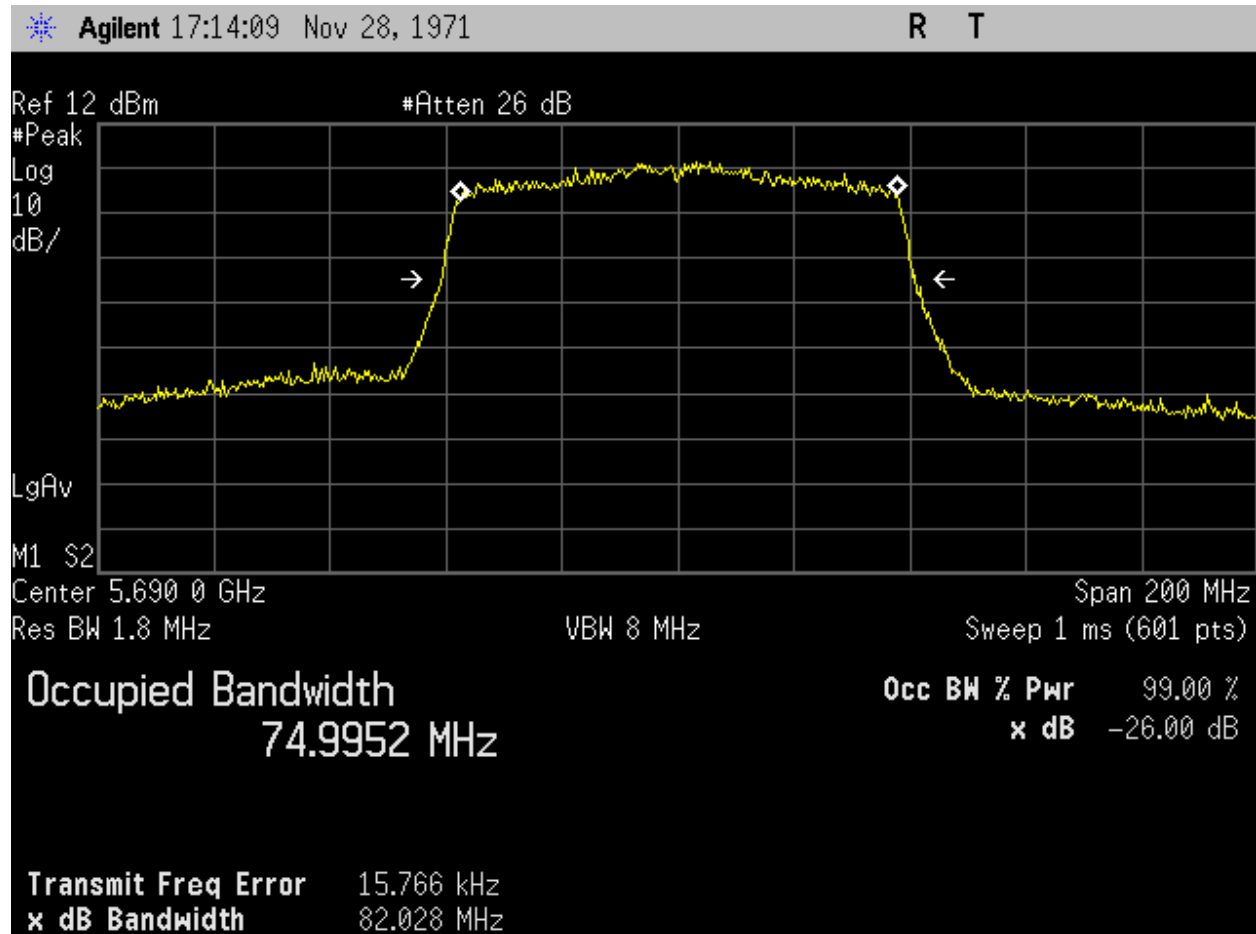


Figure 84: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

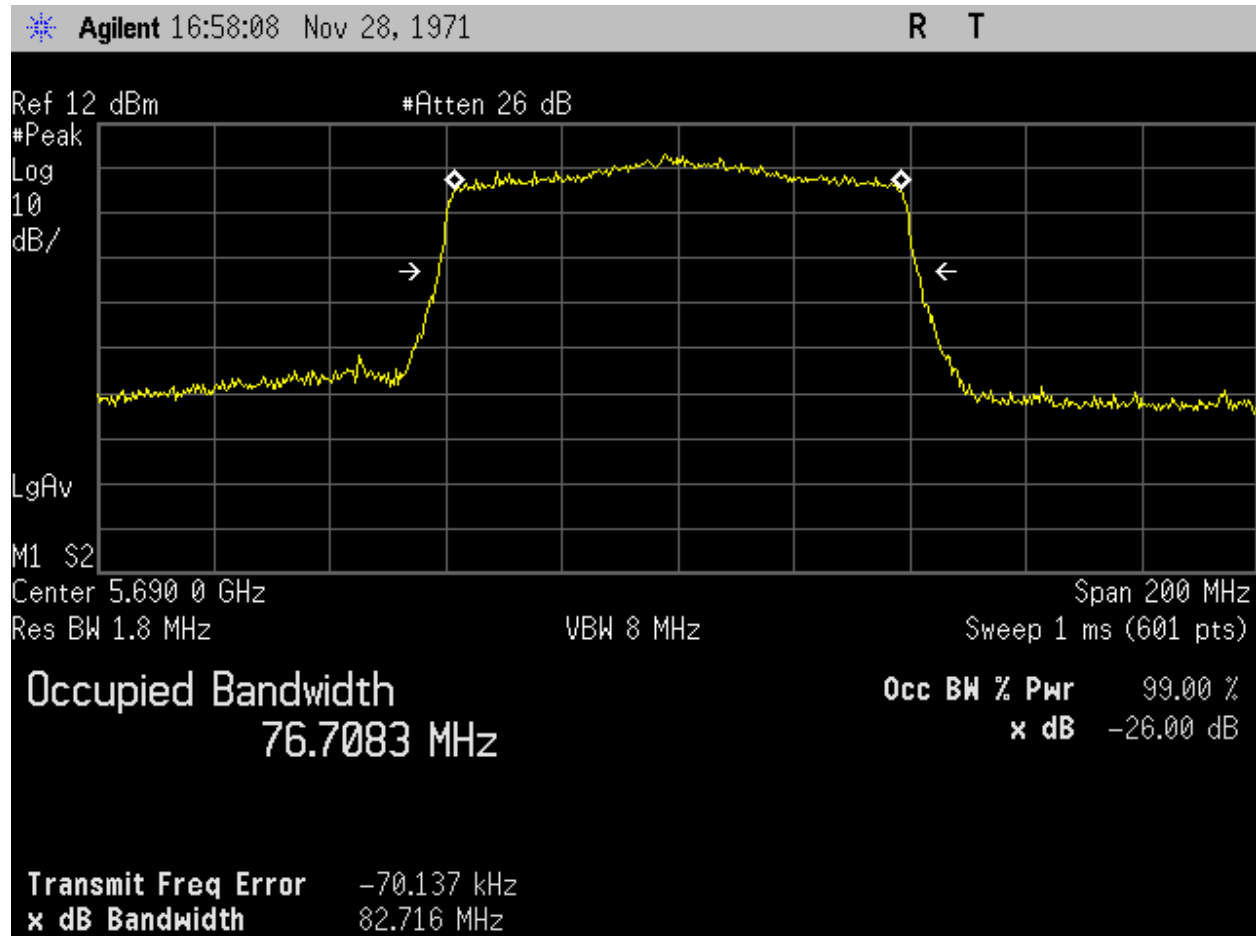


Figure 85: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

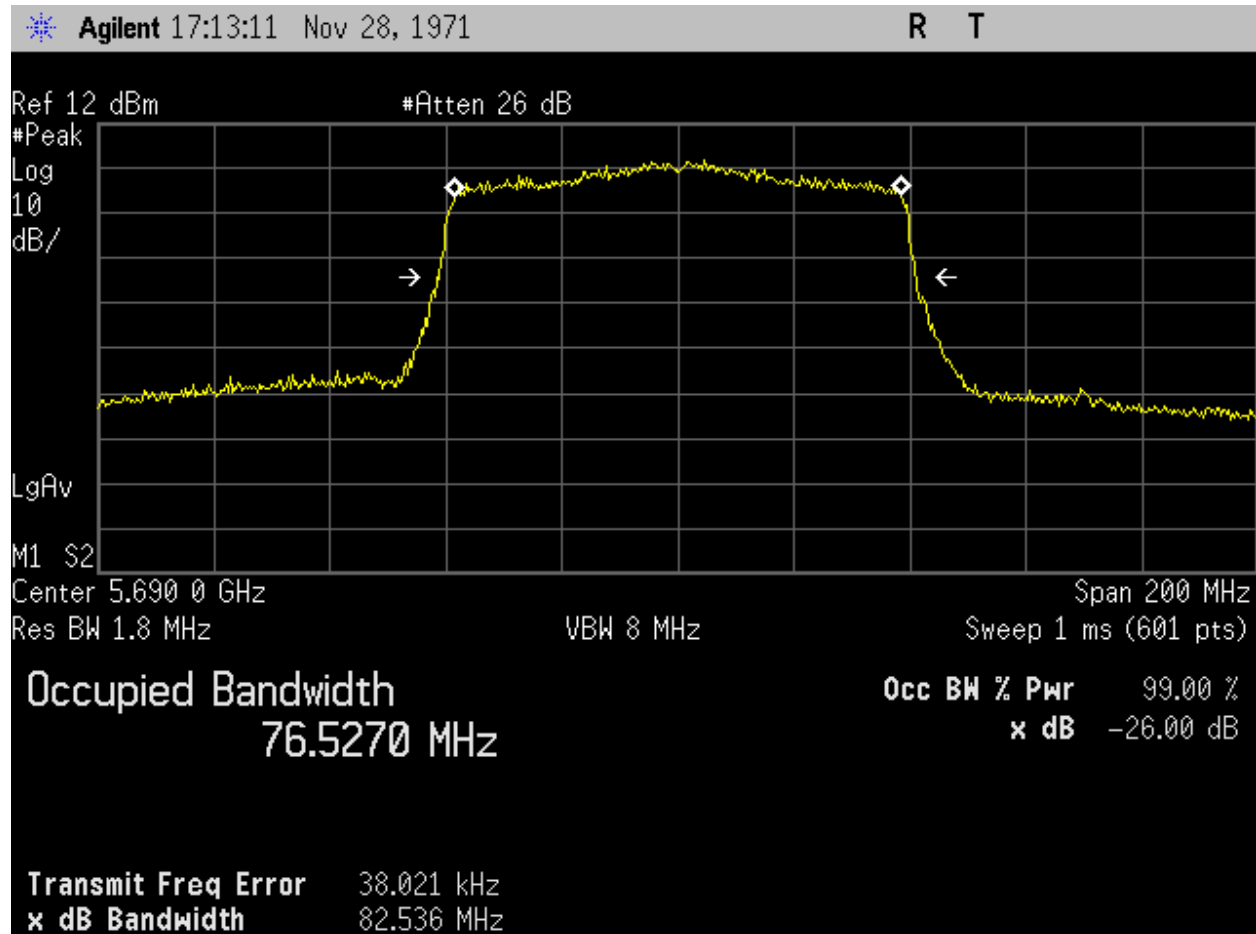


Figure 86: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

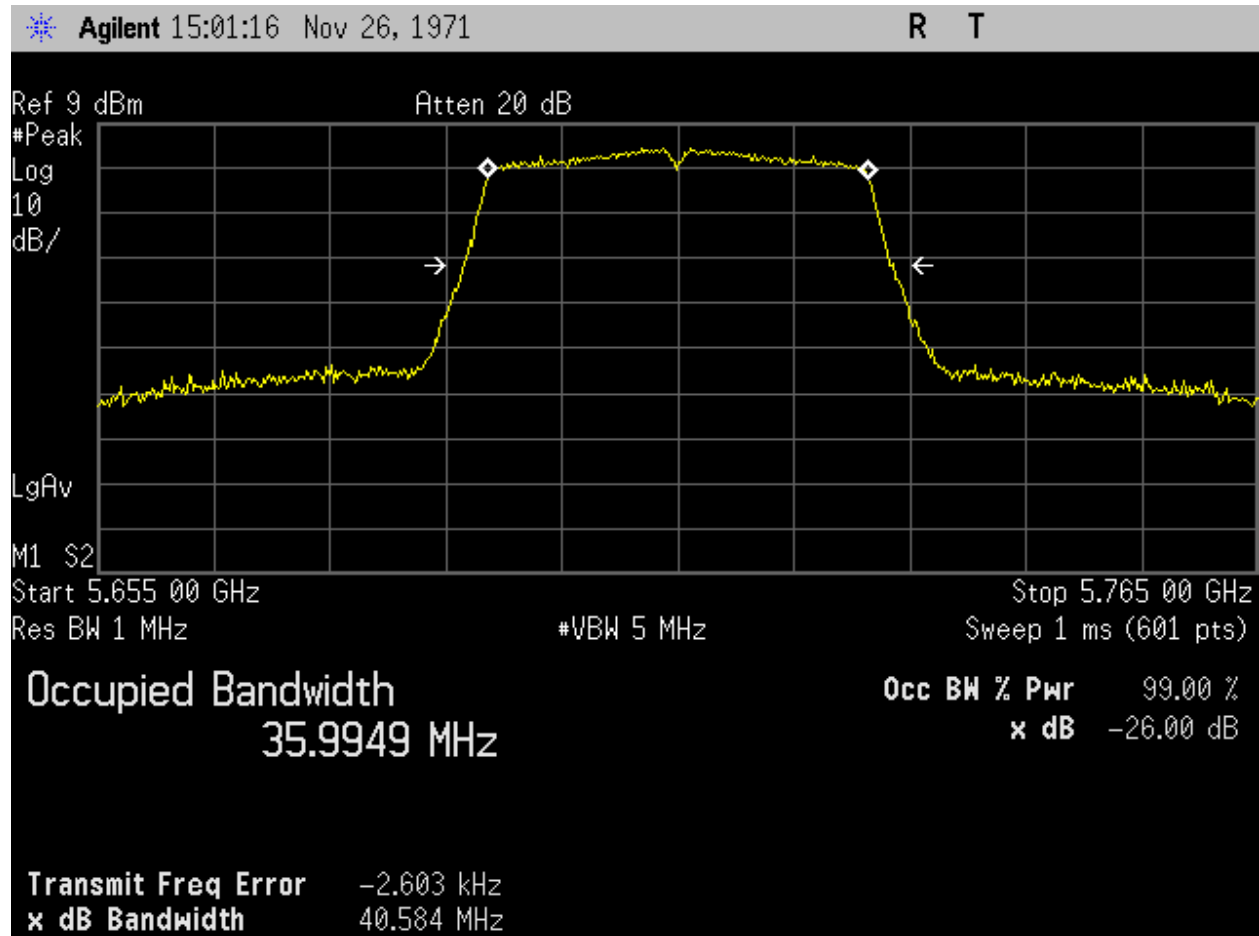


Figure 87: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

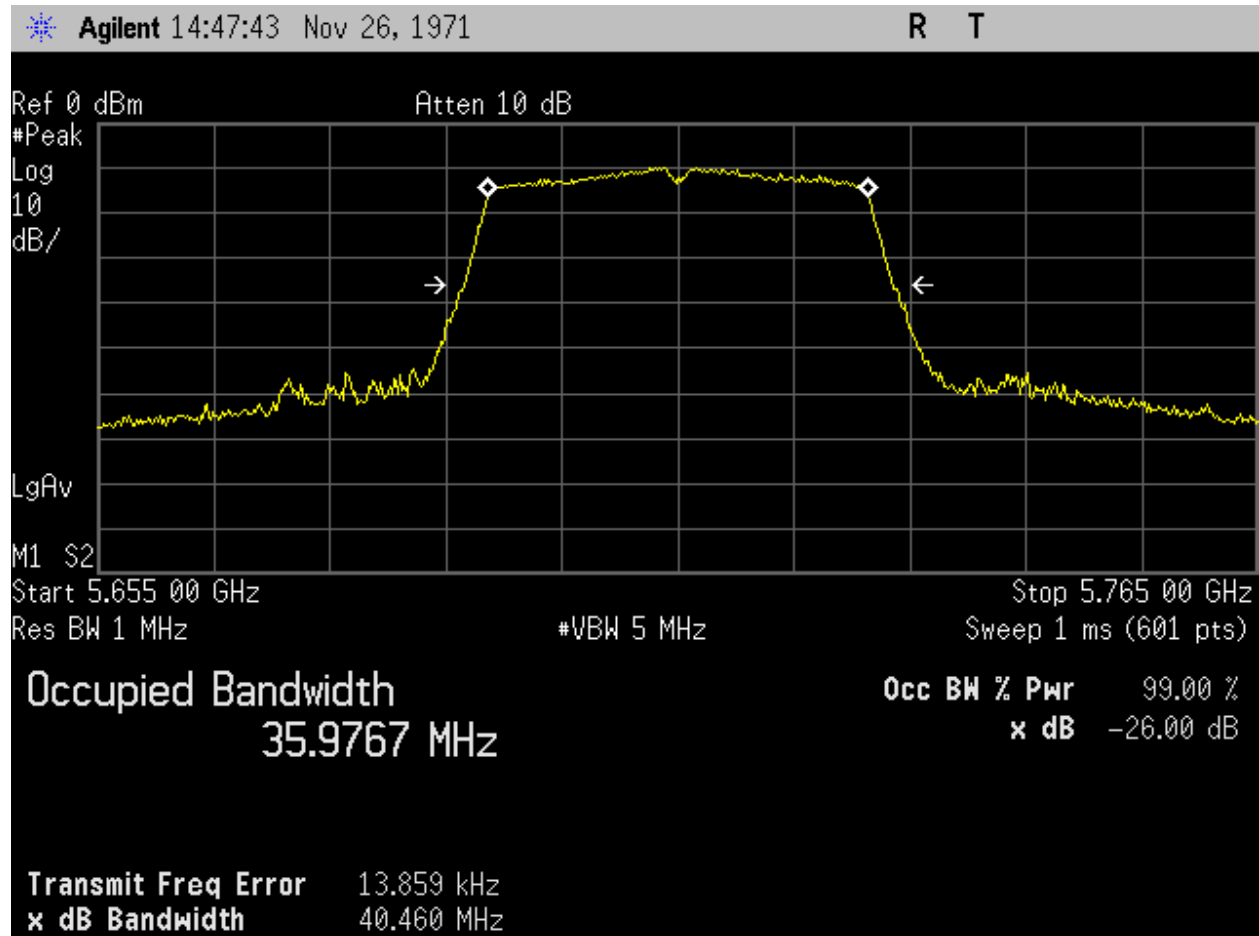


Figure 88: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

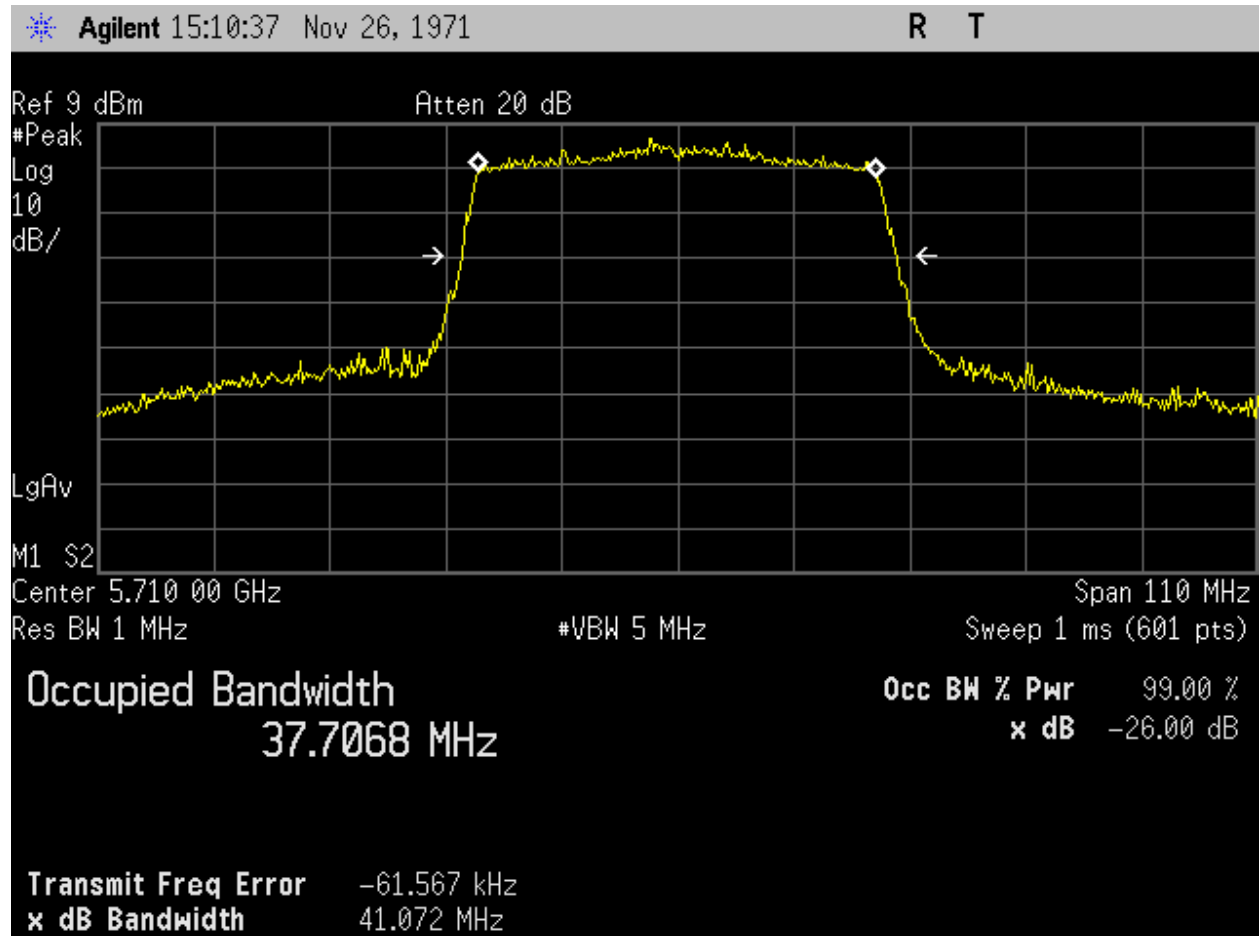


Figure 89: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

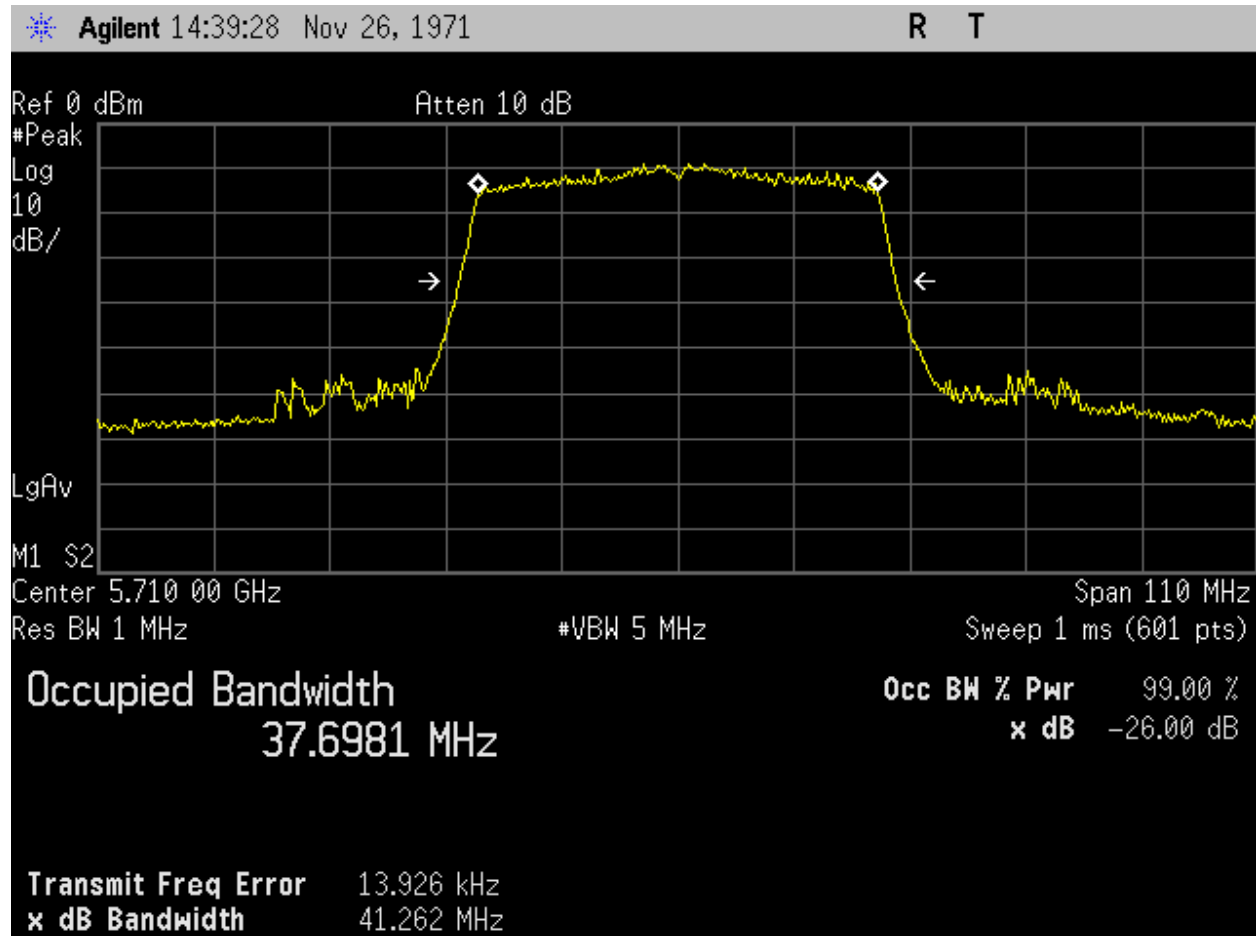


Figure 90: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

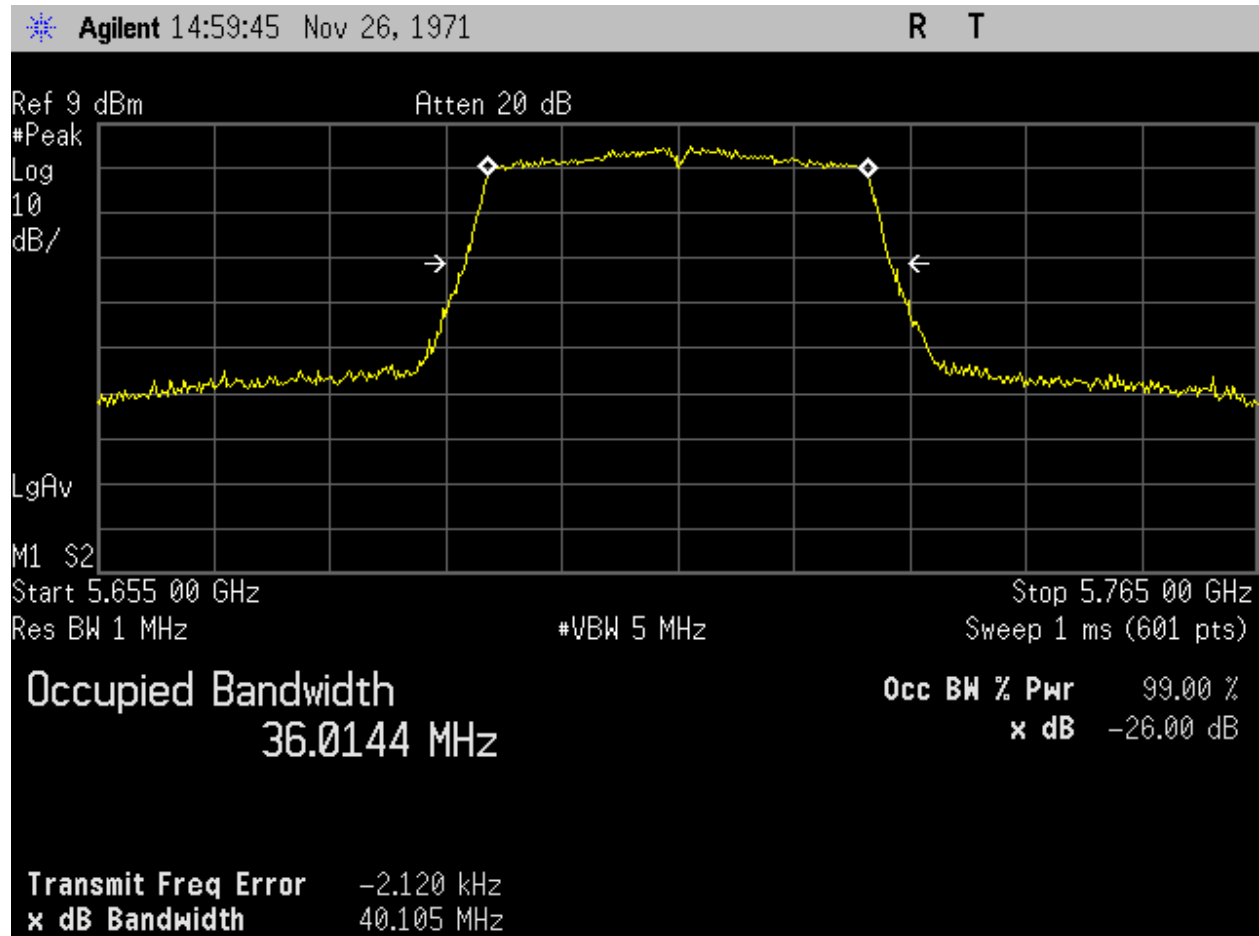


Figure 91: U-NII-2C_5710MHz_High Ch_142_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

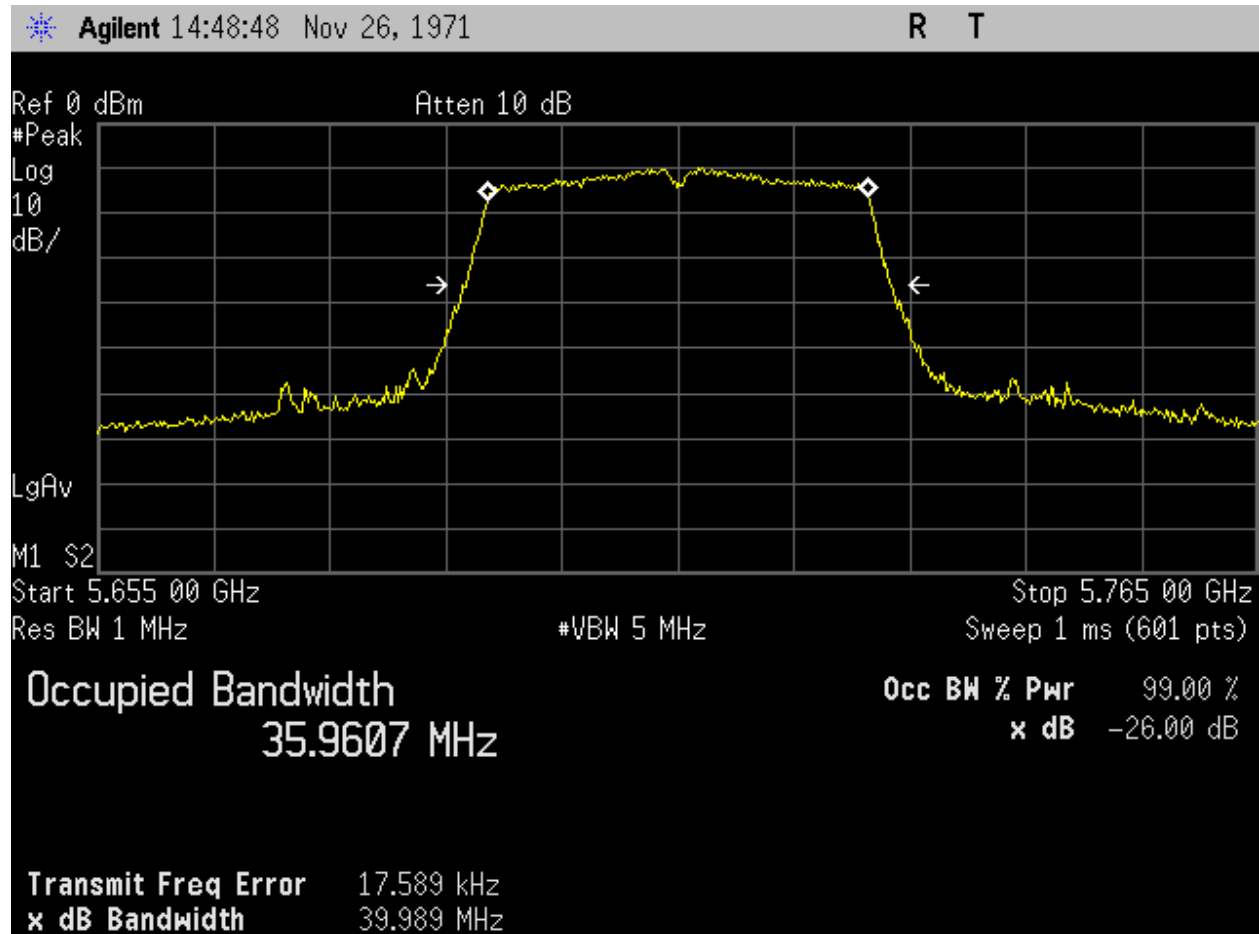


Figure 92: U-NII-2C_5710MHz_High Ch_142_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

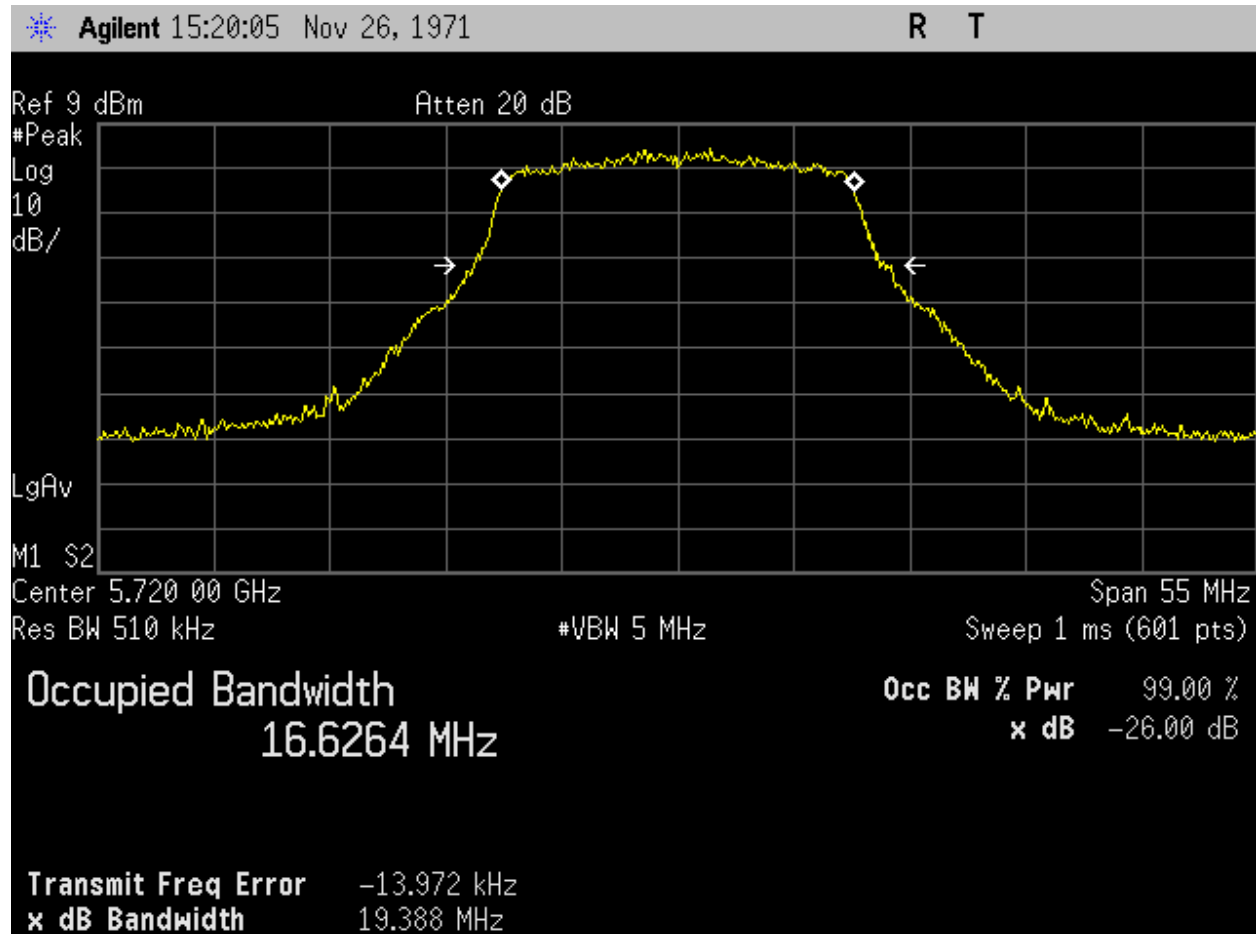


Figure 93: U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

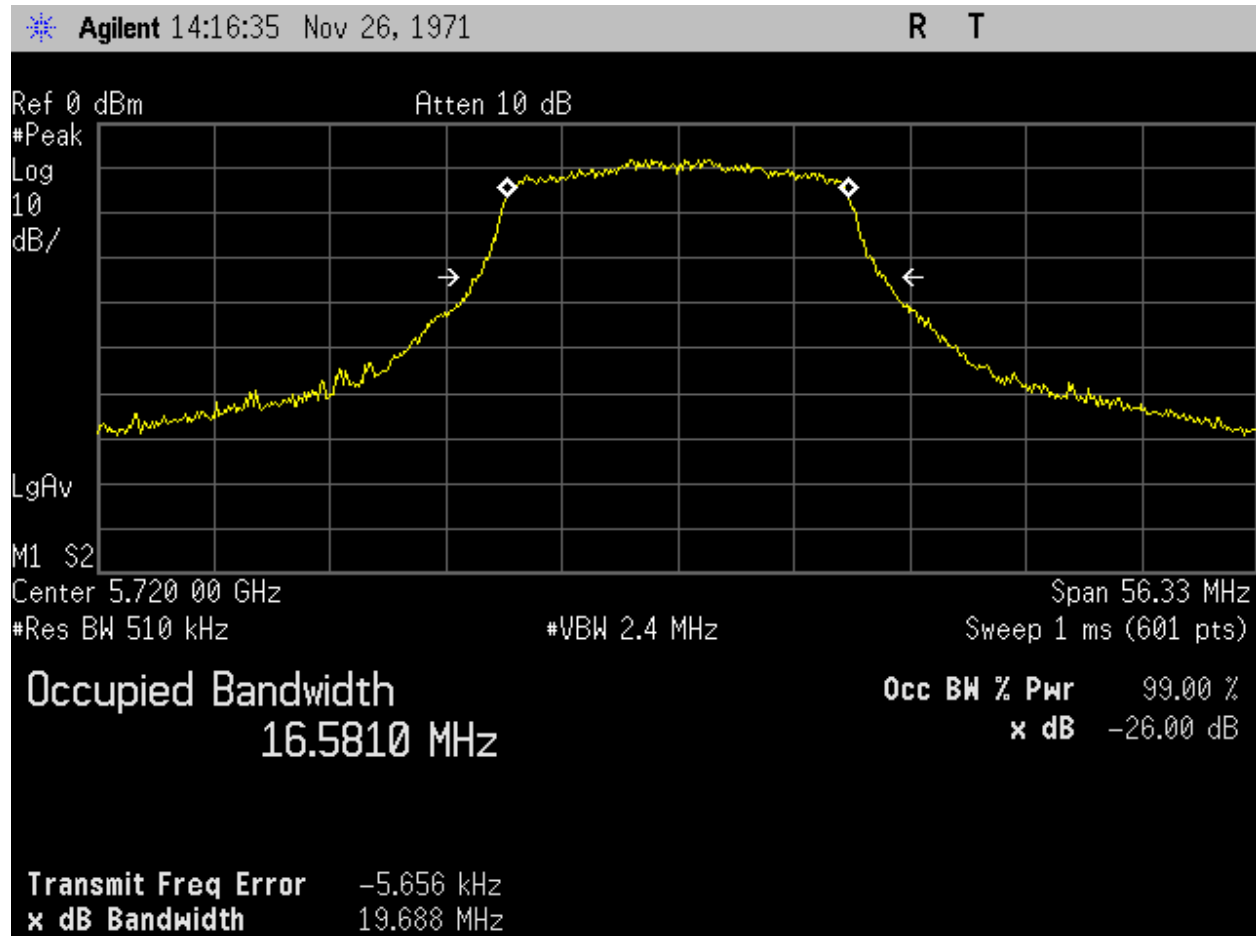


Figure 94: U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

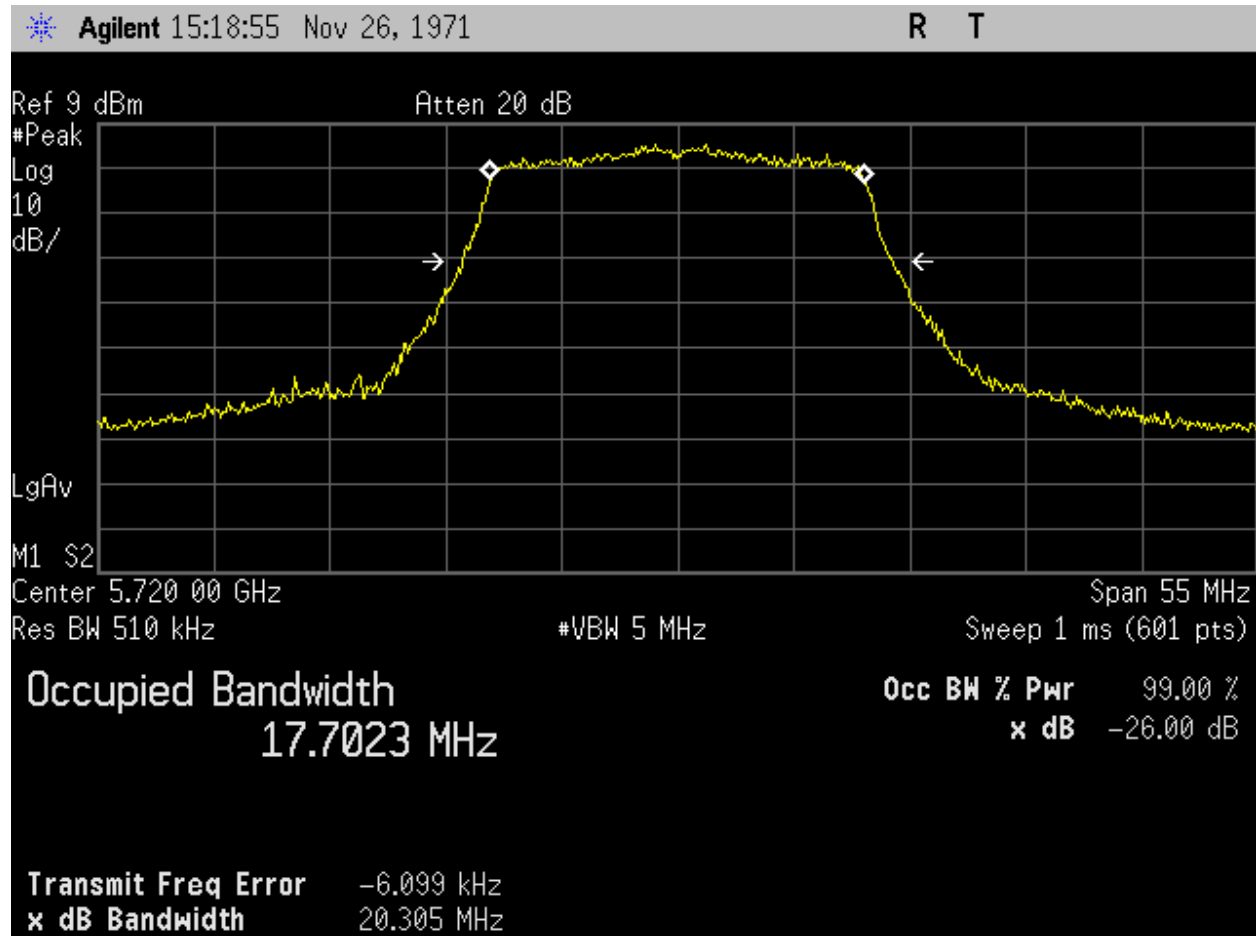


Figure 95: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

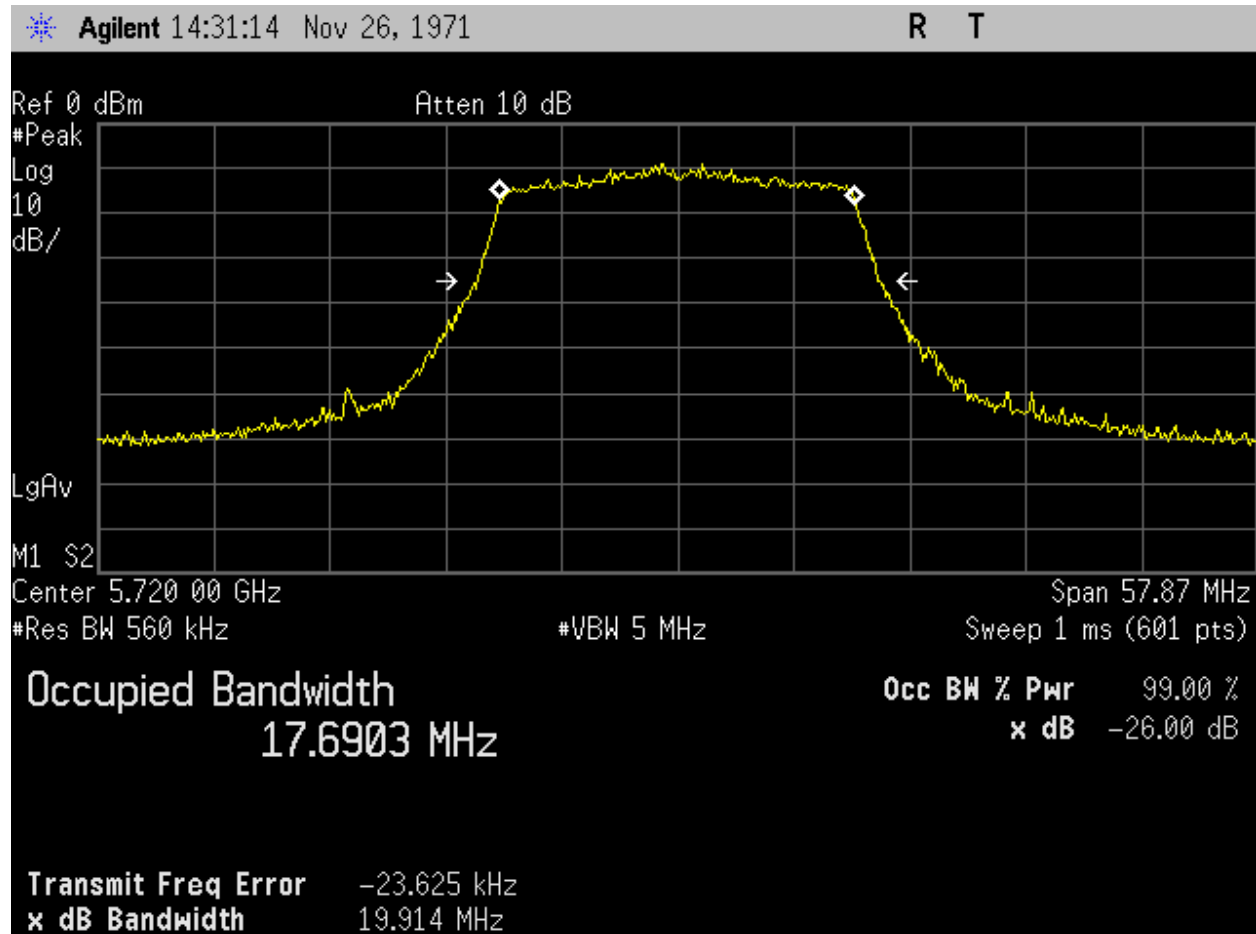


Figure 96: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

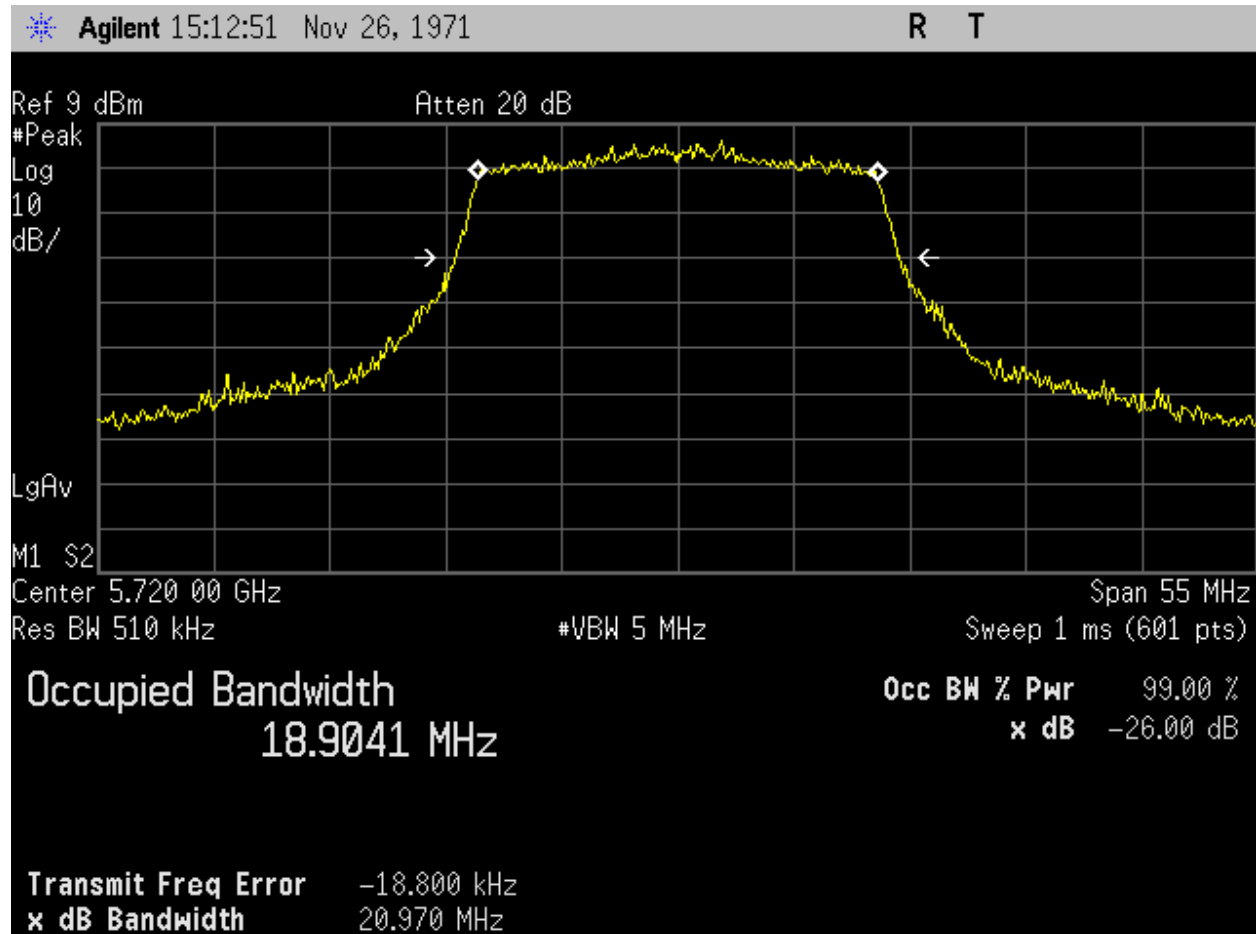


Figure 97: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

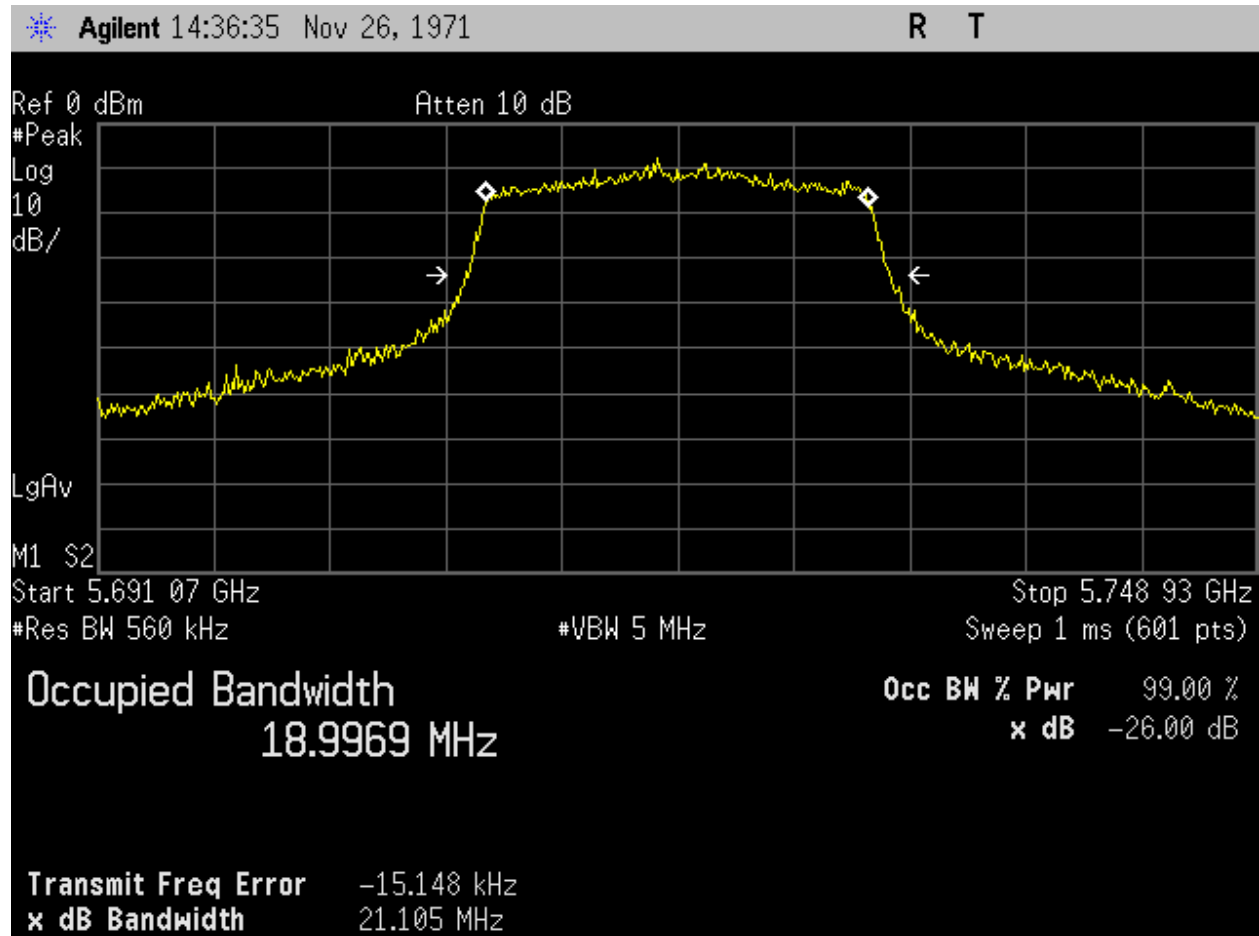


Figure 98: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

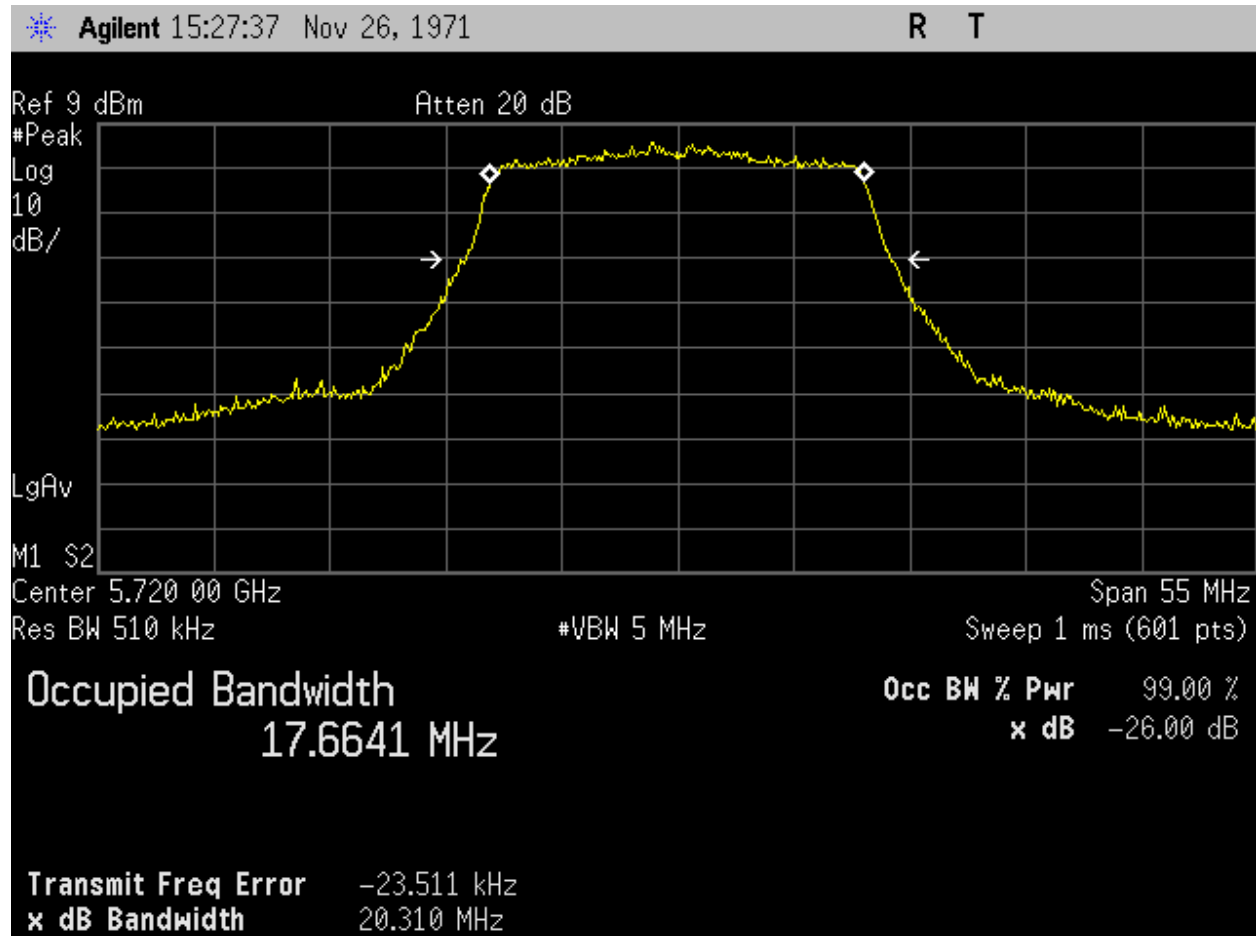


Figure 99: U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

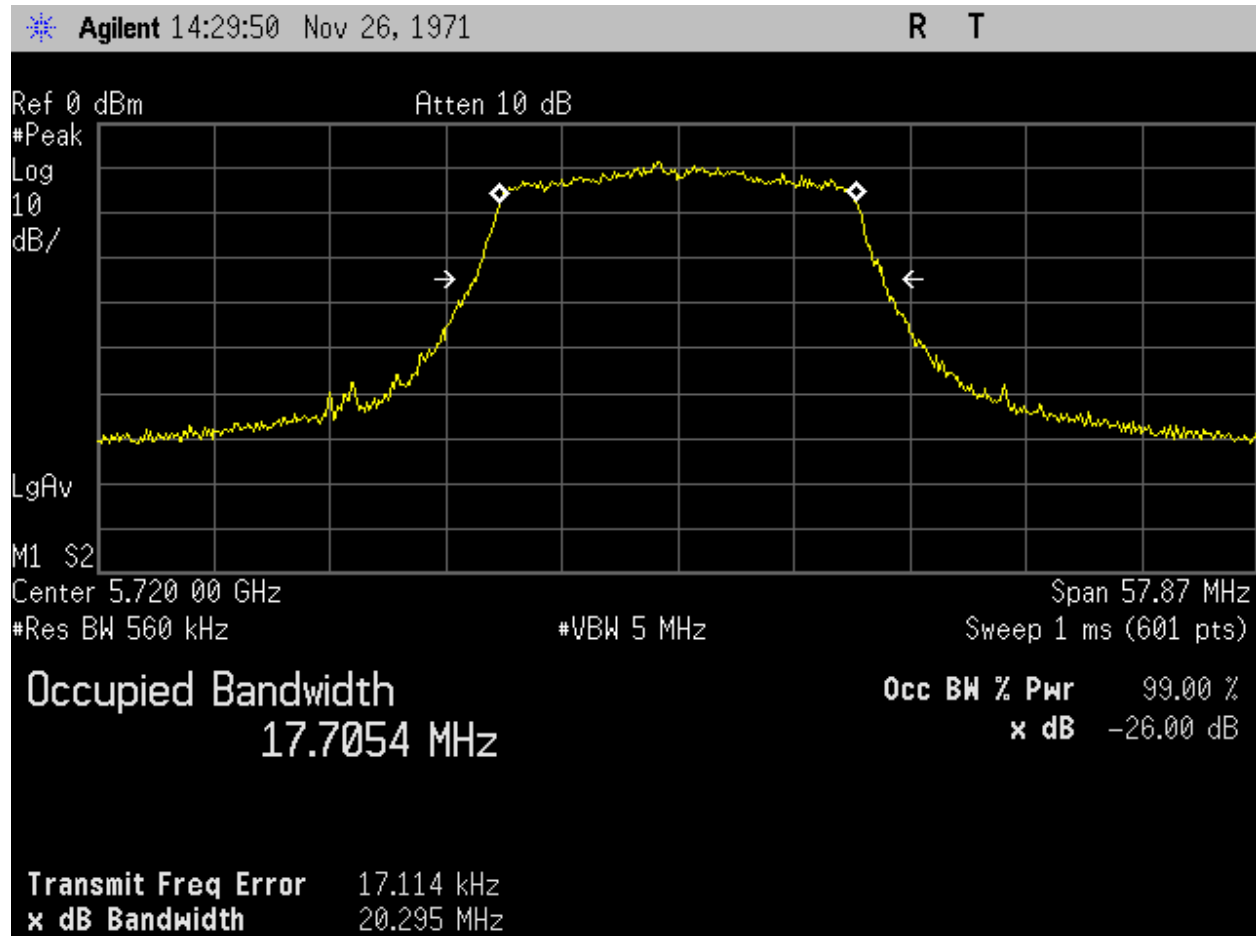


Figure 100: U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

Duty Cycle

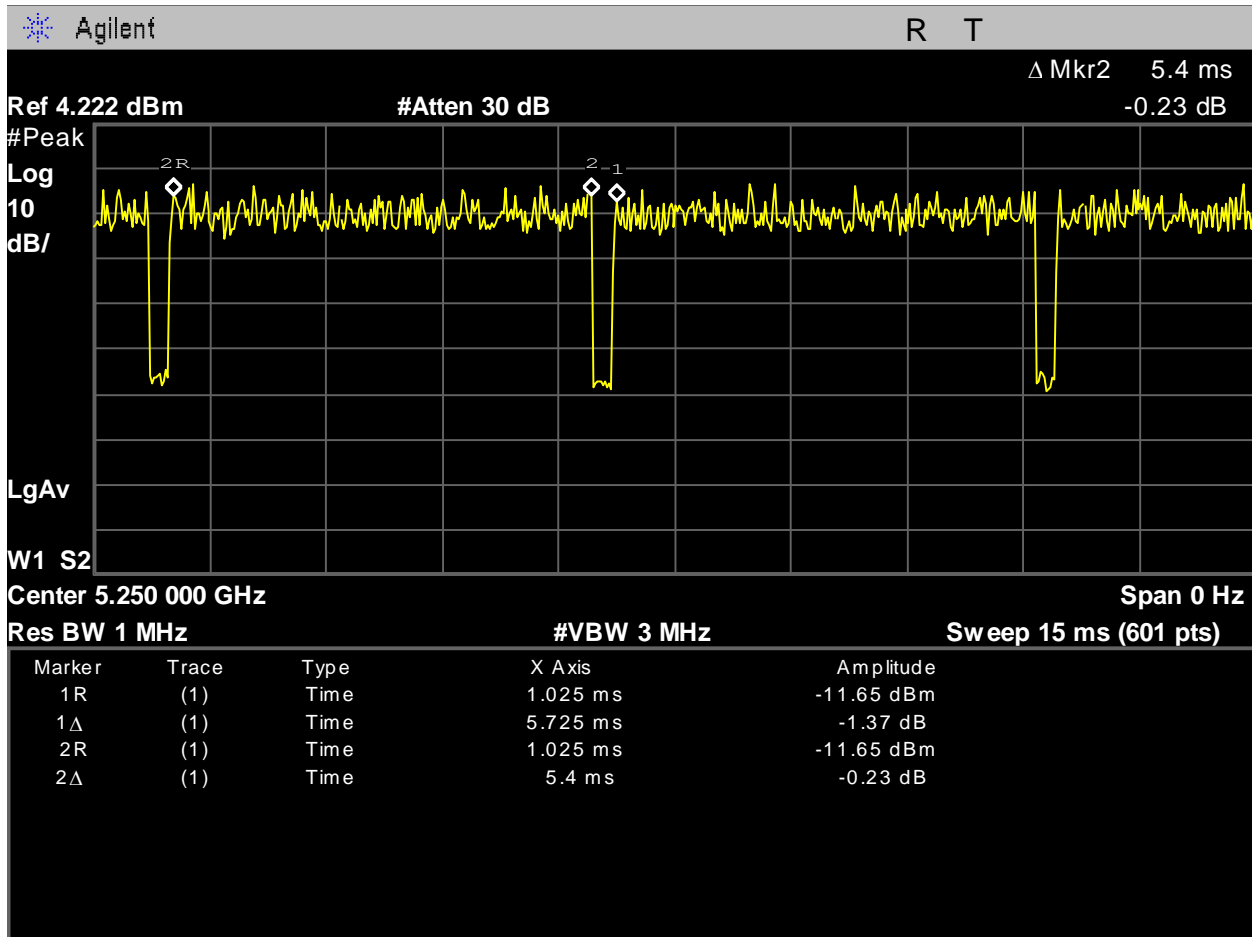


Figure 101: U-NII-1,2A_5250MHz_Mid Ch_50_160MHz BW_ax-mode_Duty Cycle_Port 1.

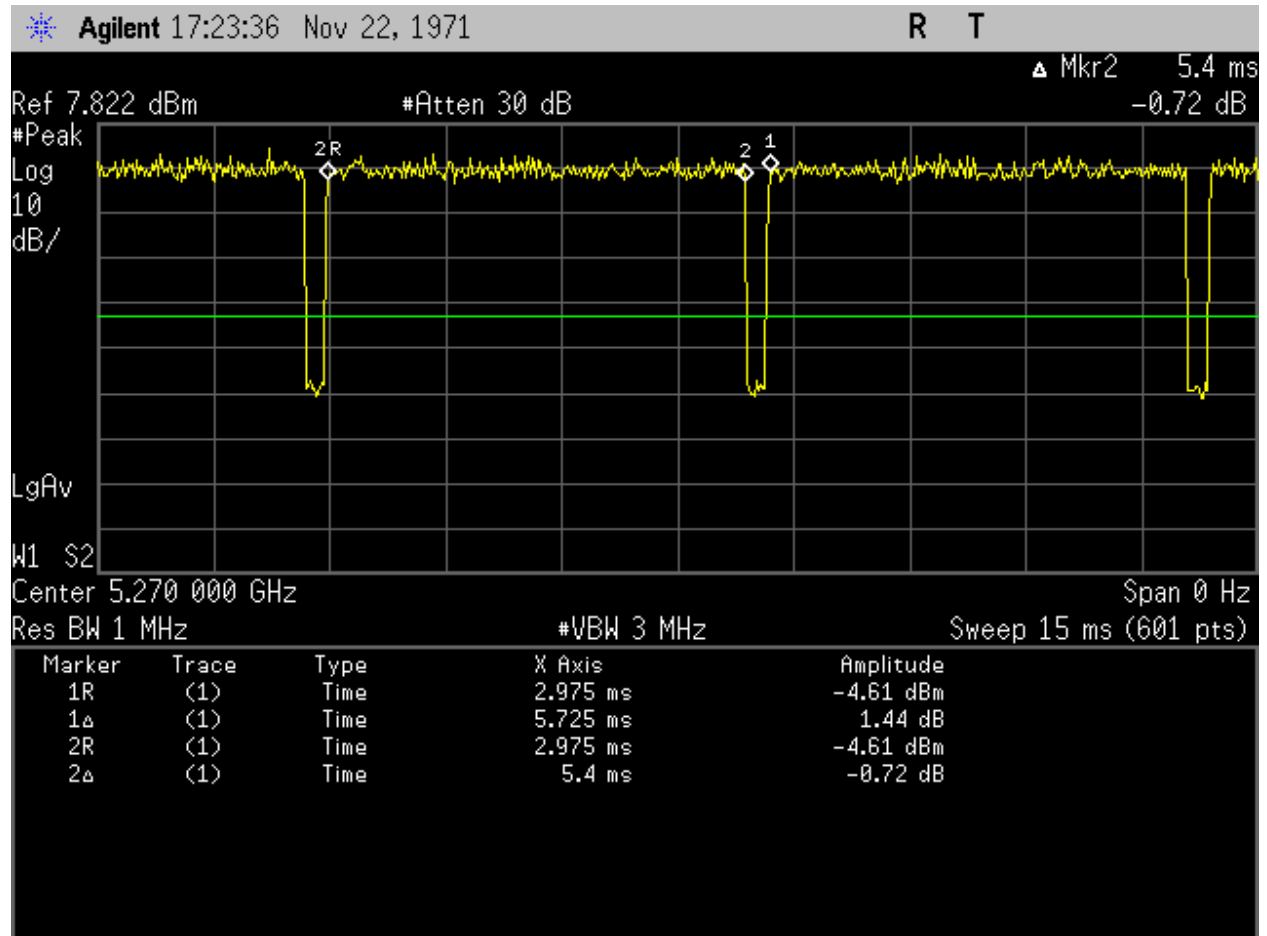


Figure 102: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Duty Cycle_Port 1.

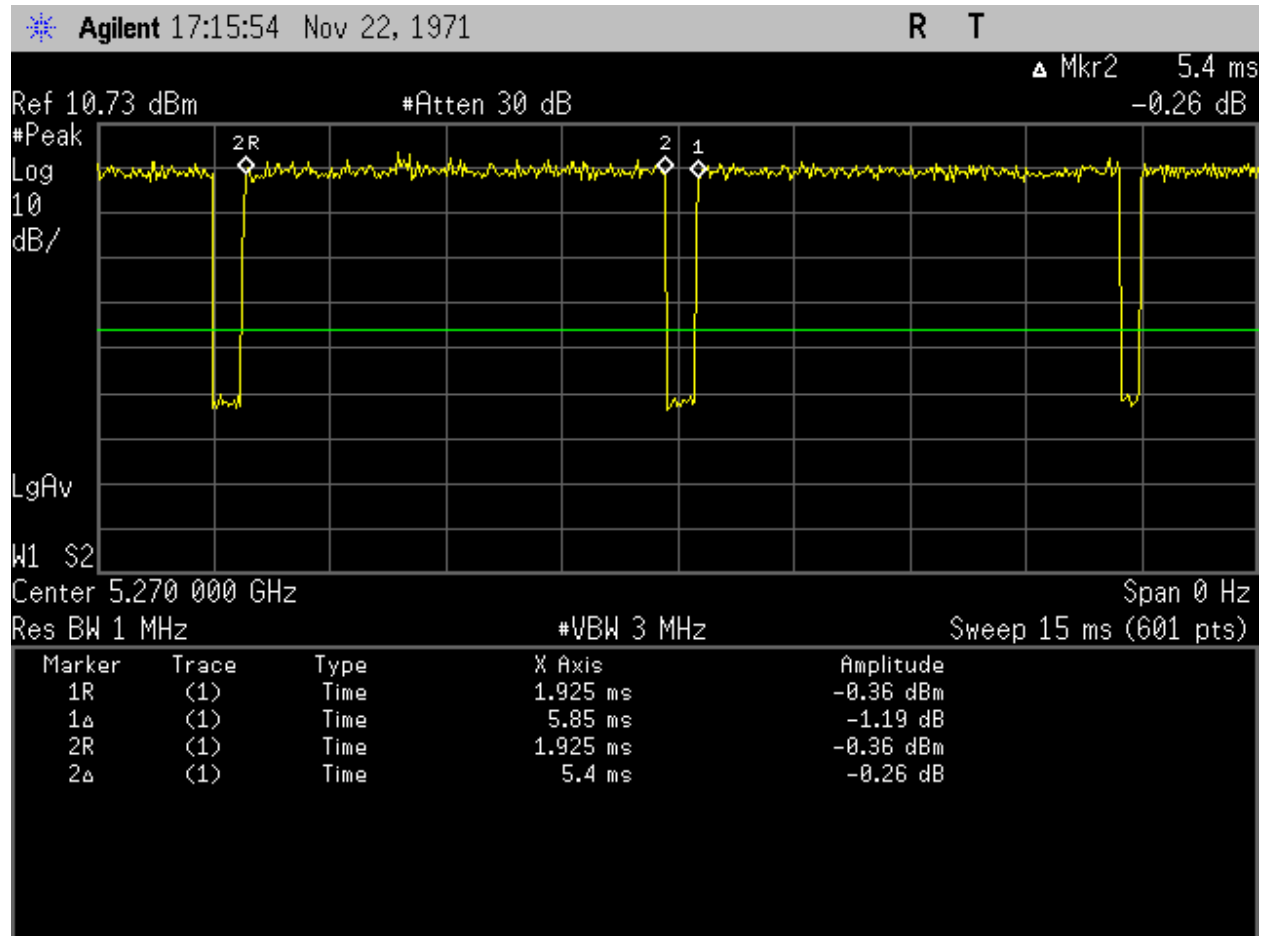


Figure 103: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Duty Cycle_Port 1.

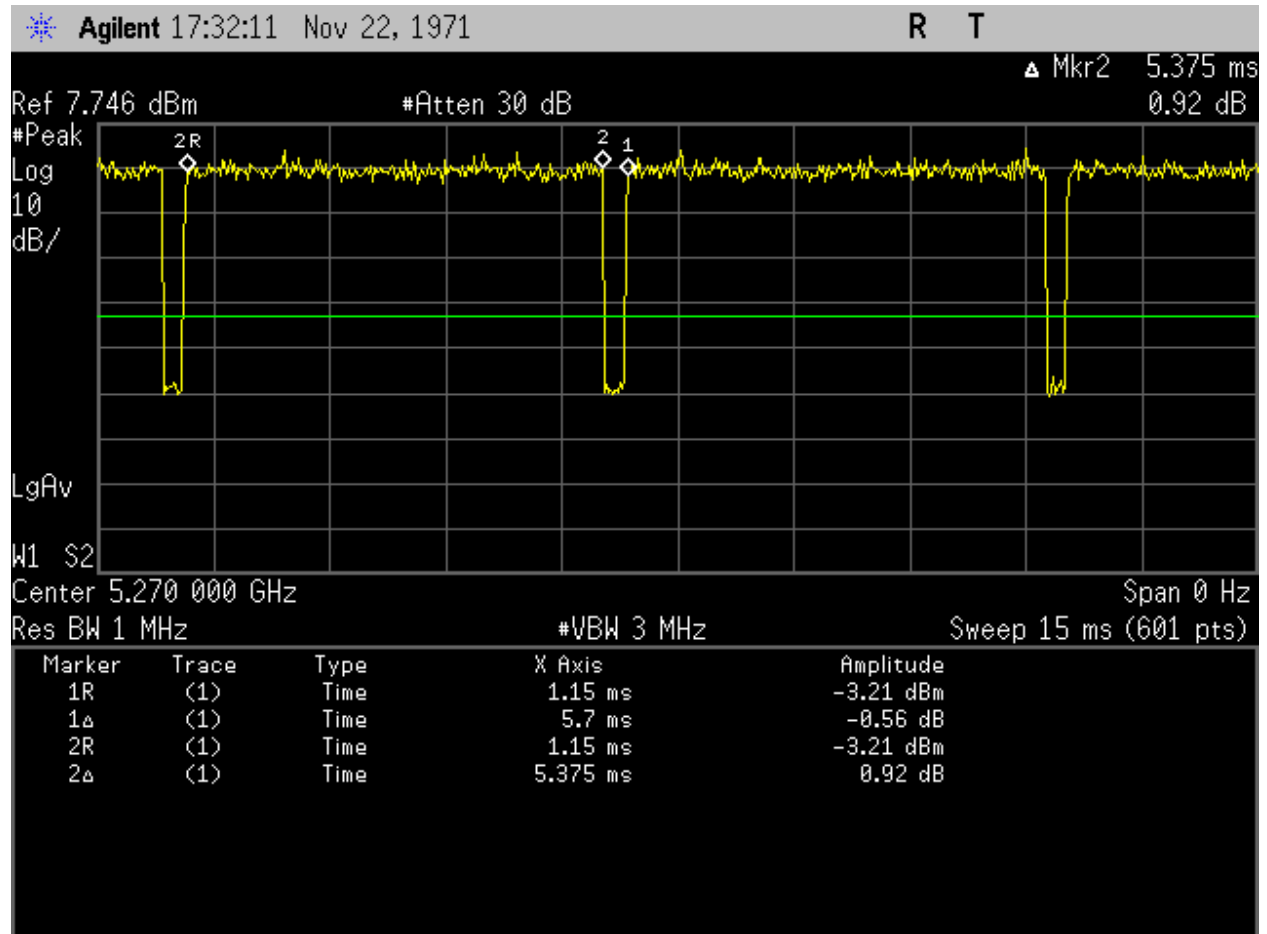


Figure 104: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Duty Cycle_Port 1.

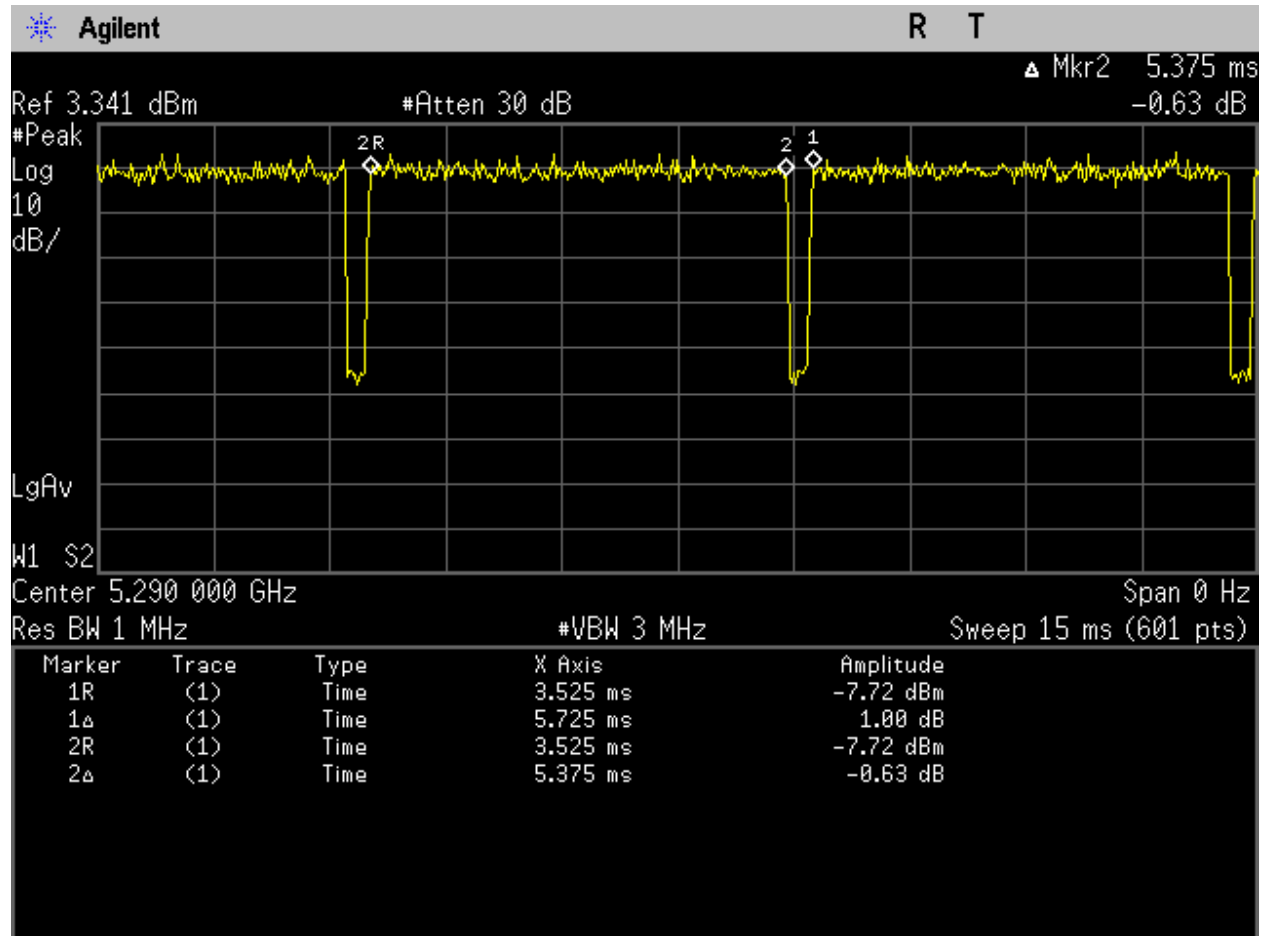


Figure 105: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Duty Cycle_Port 1.

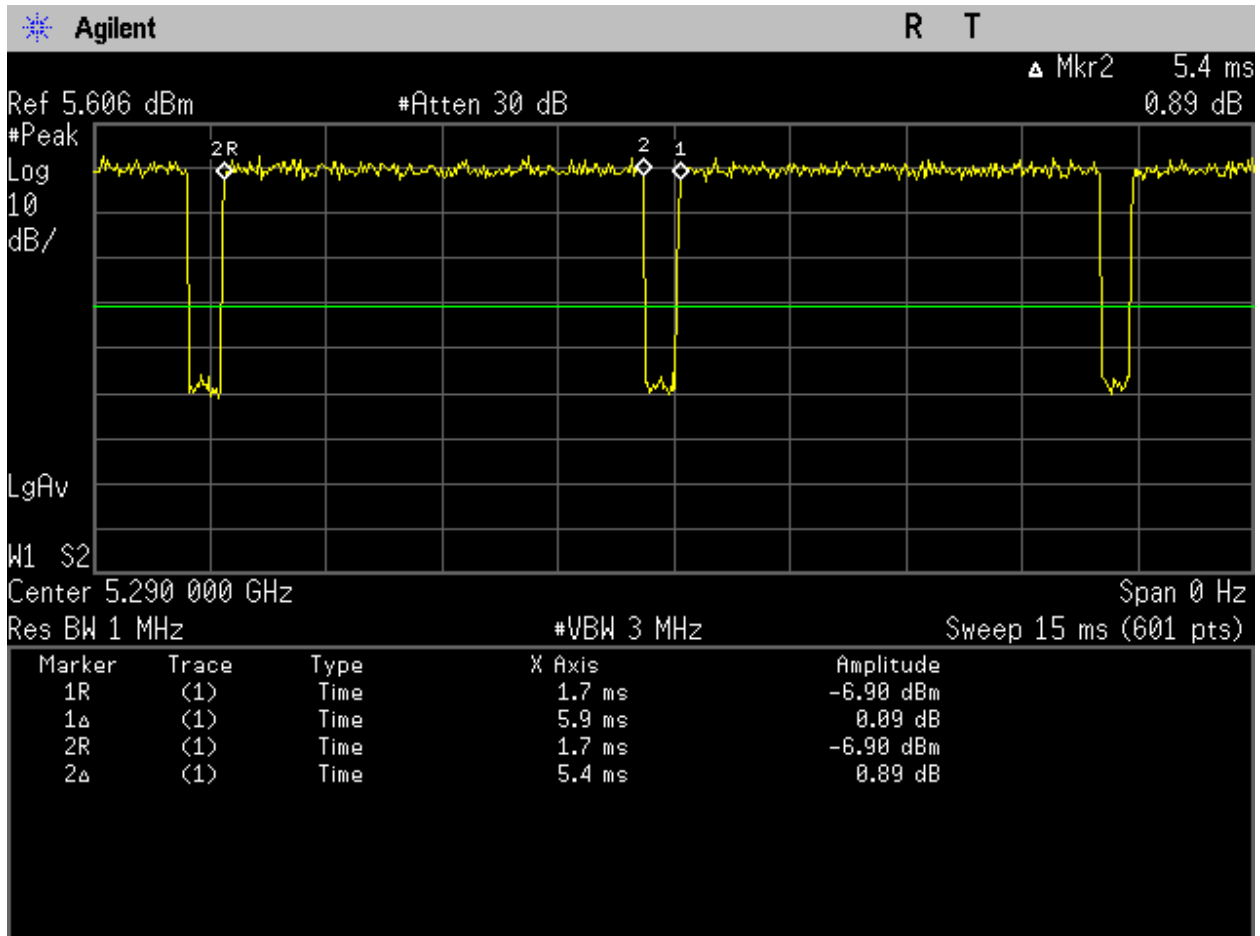


Figure 106: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Duty Cycle_Port 1.

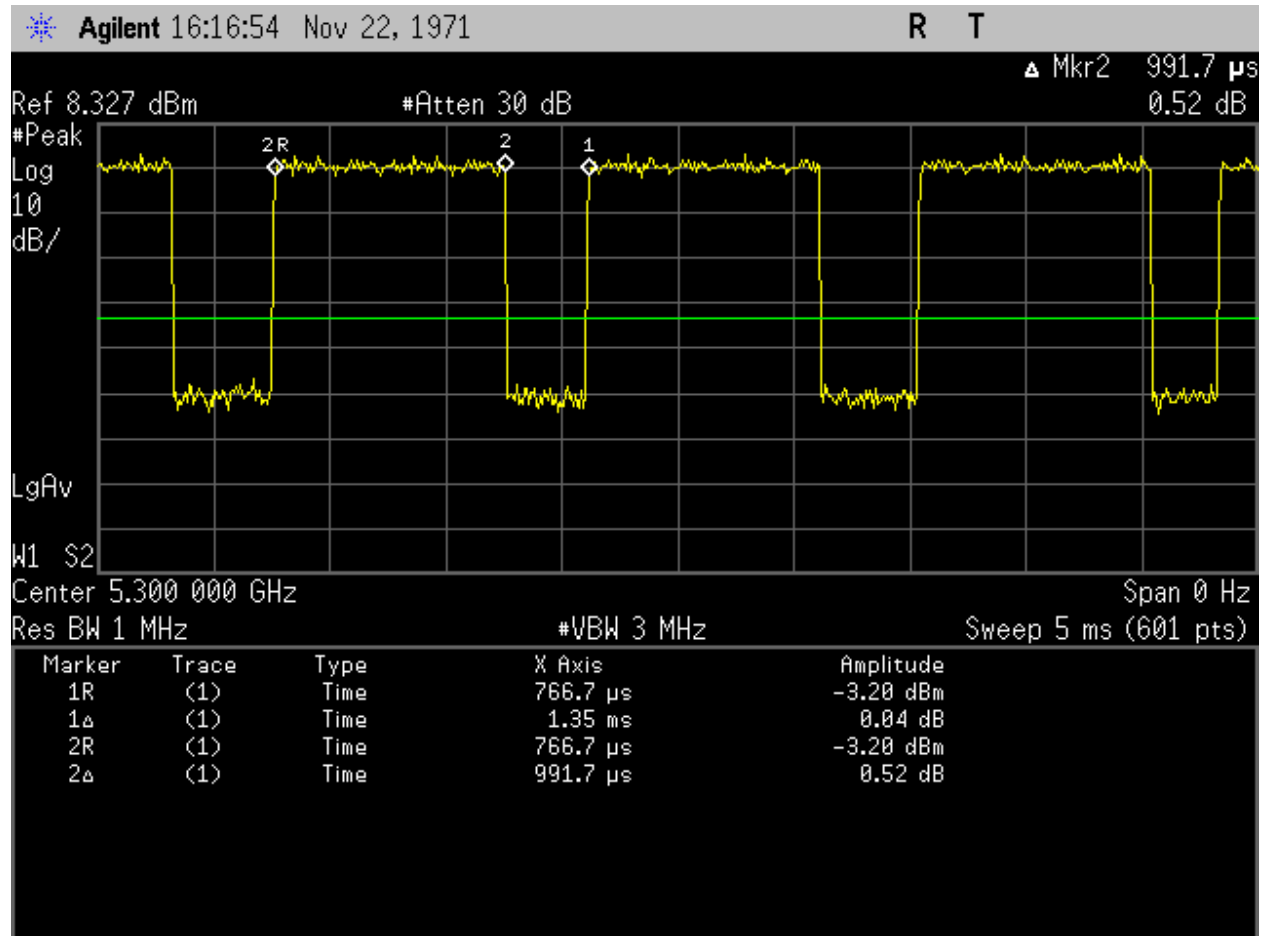


Figure 107: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Duty Cycle_Port 1.

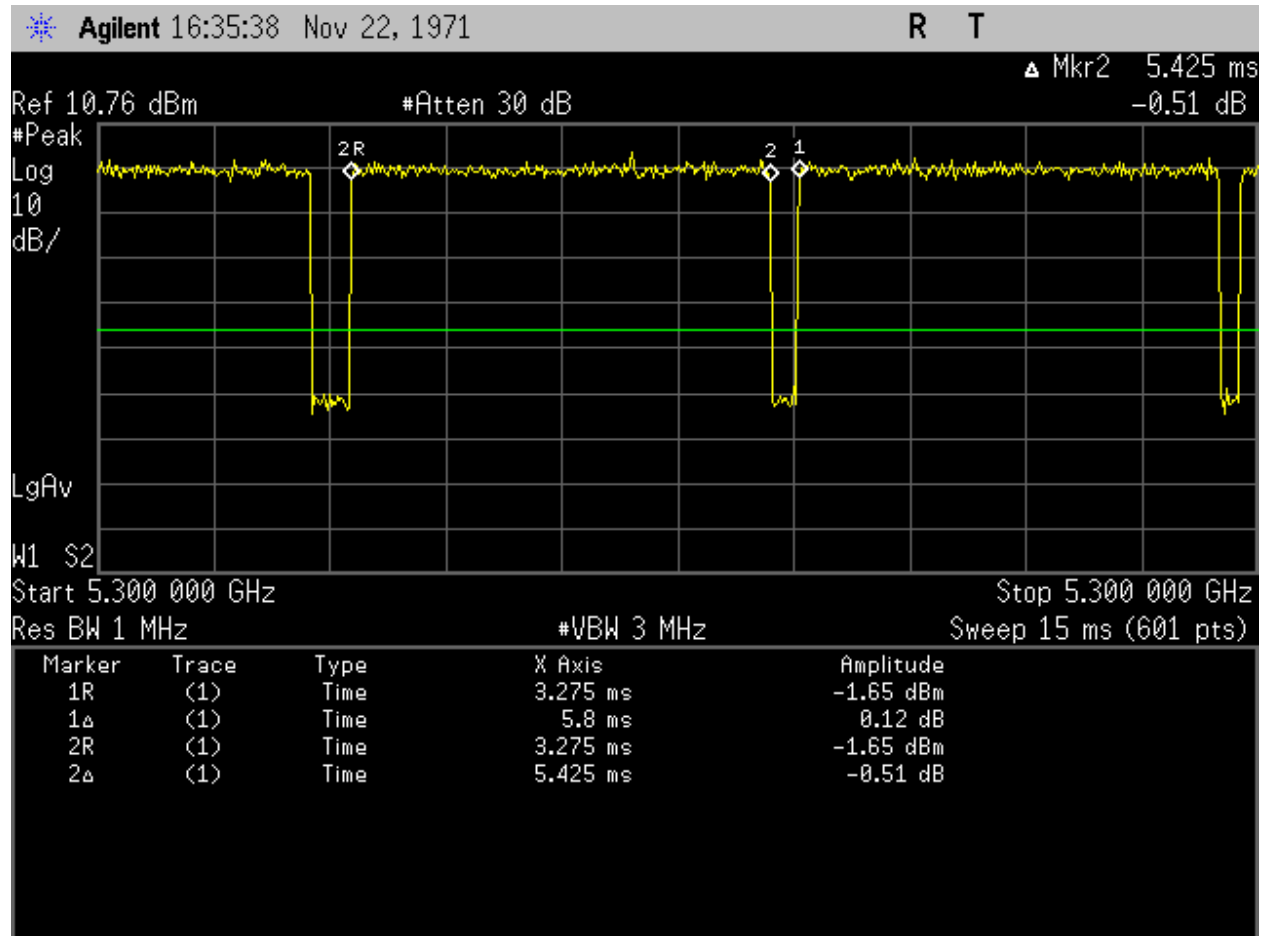


Figure 108: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Duty Cycle_Port 1.

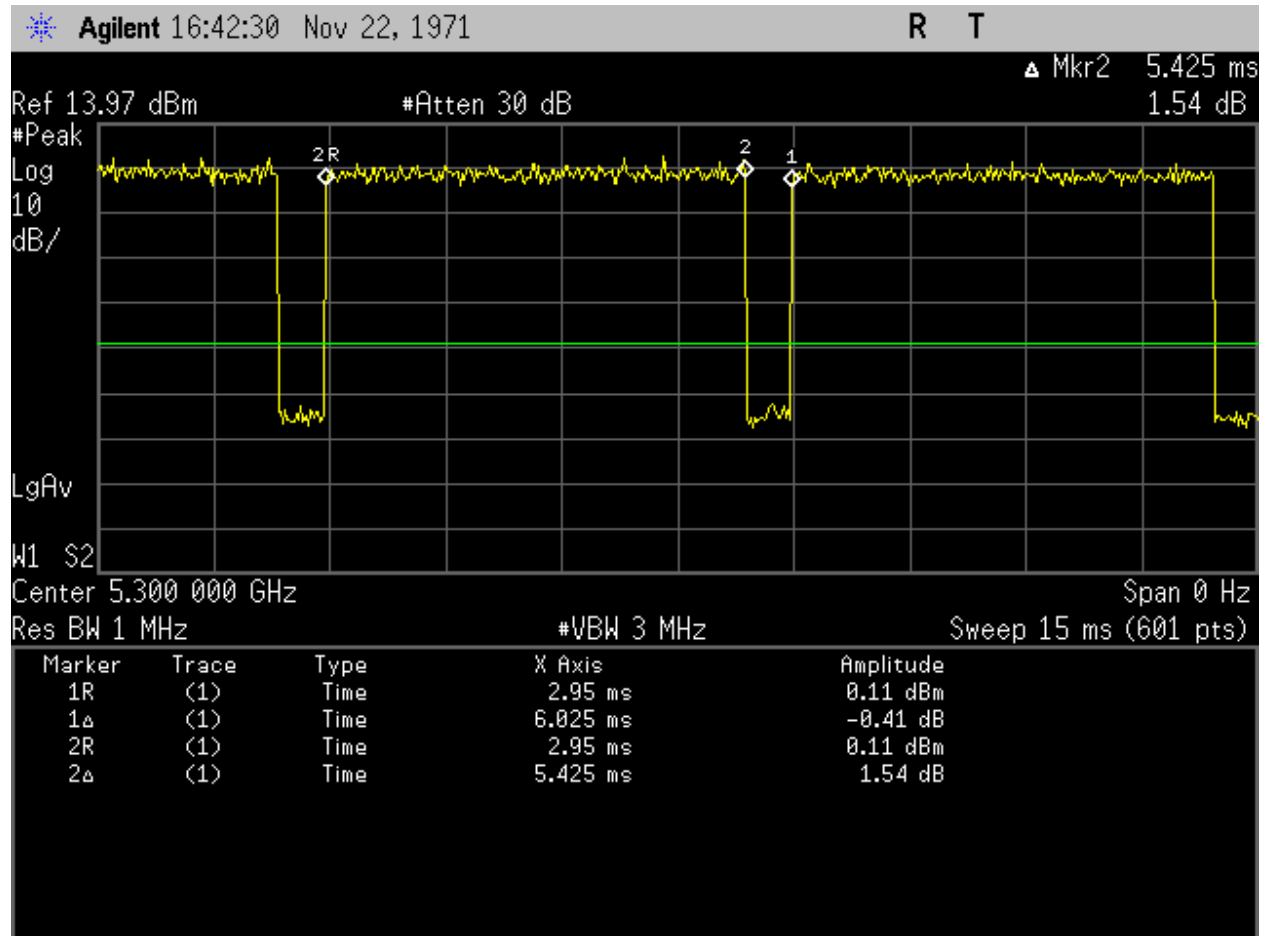


Figure 109: U-NII-2A_5300MHz_high_Mid_Ch_60_20MHz BW_ax-mode_Duty Cycle_Port 1.

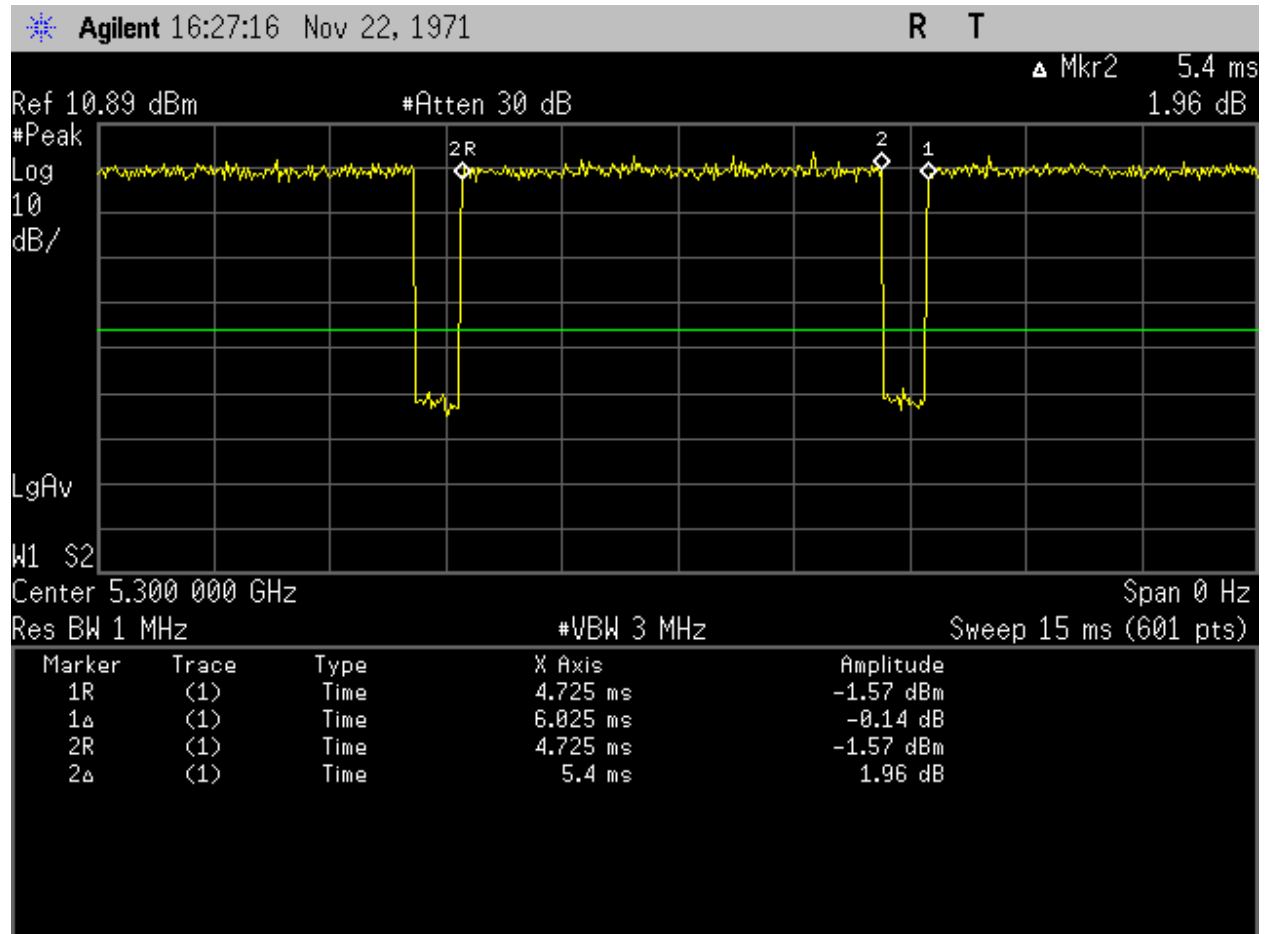


Figure 110: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Duty Cycle_Port 1.

§15. 407(a)(2) Maximum Conducted Output Power

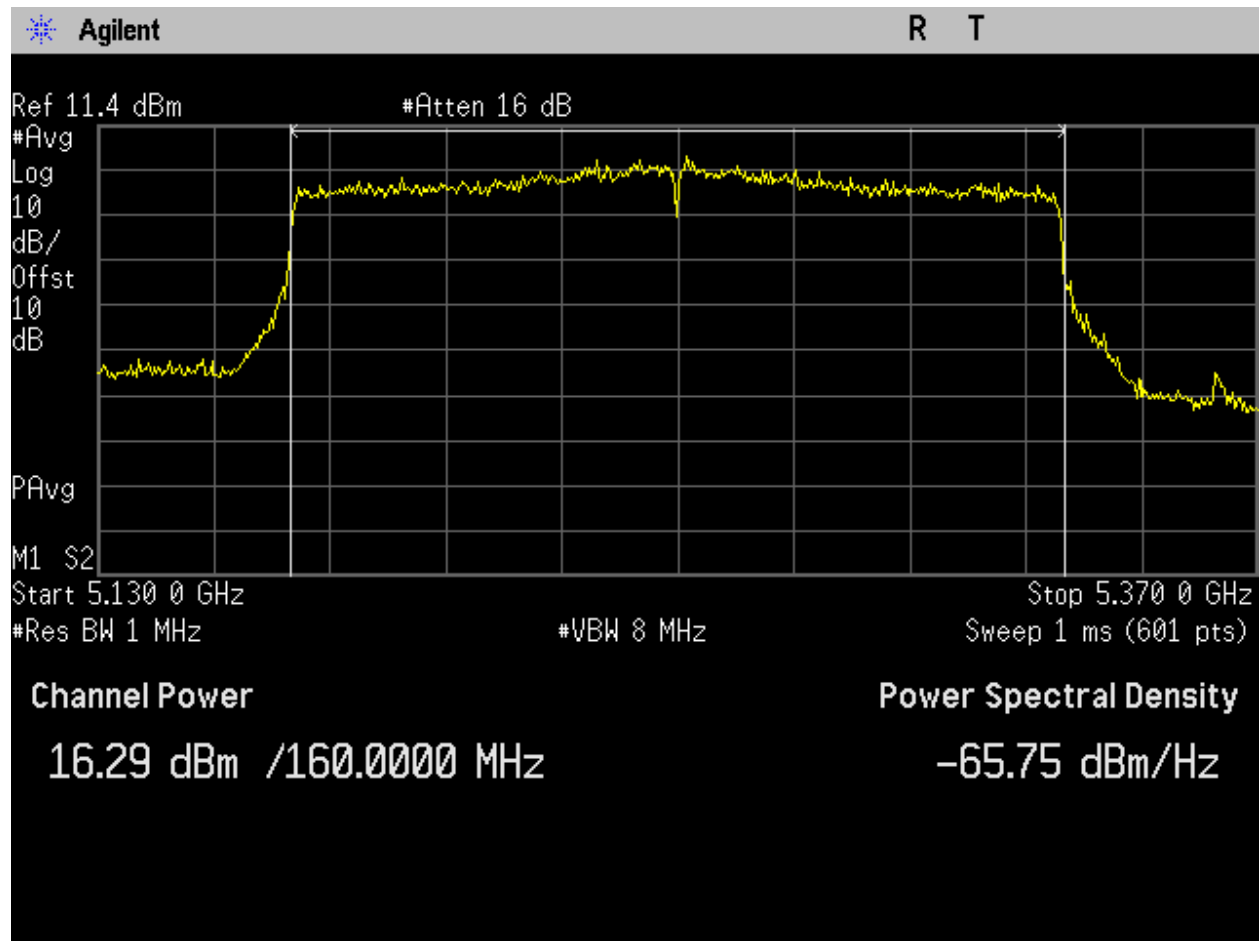


Figure 111: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Output Power_Port 1.

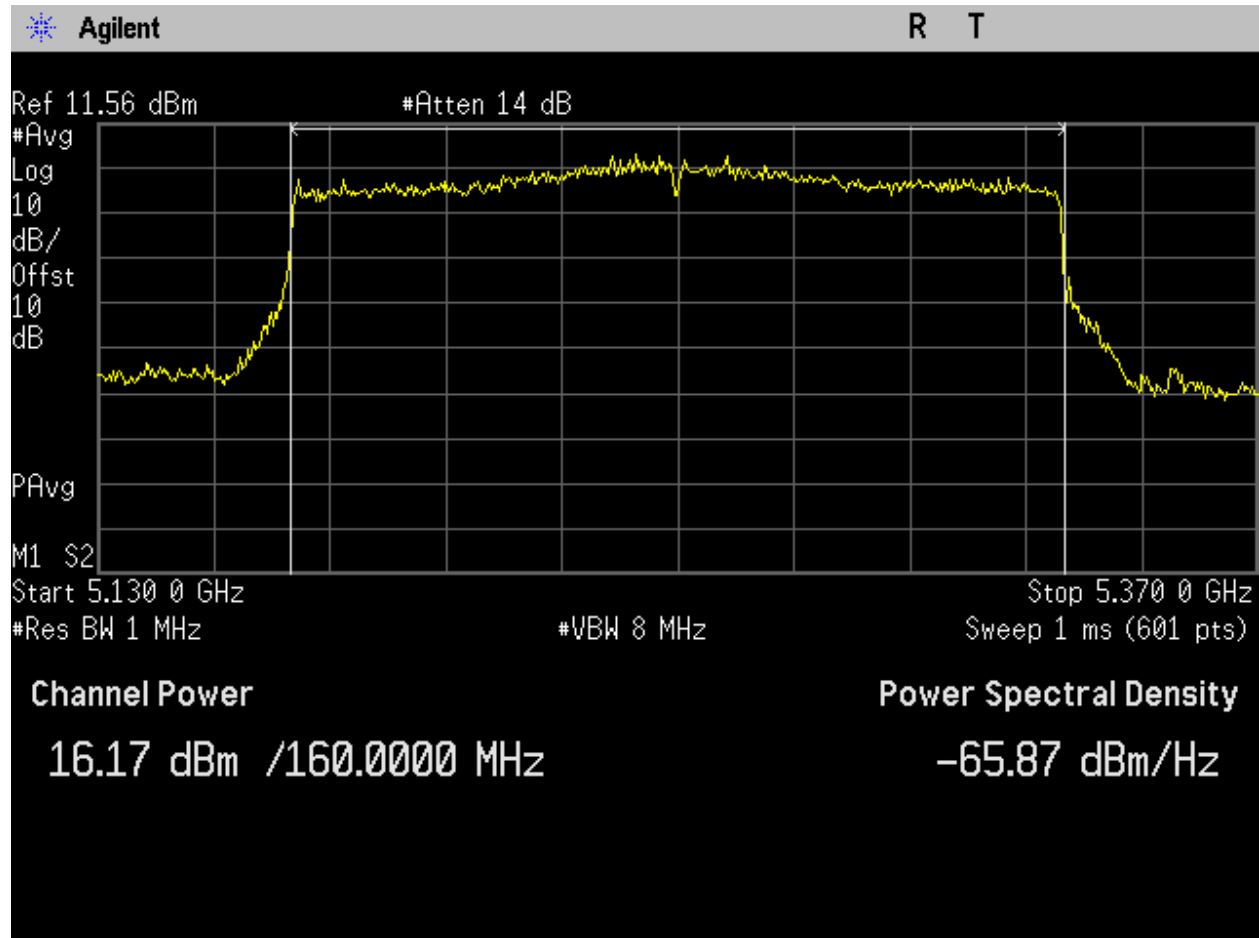


Figure 112: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Output Power_Port 2.

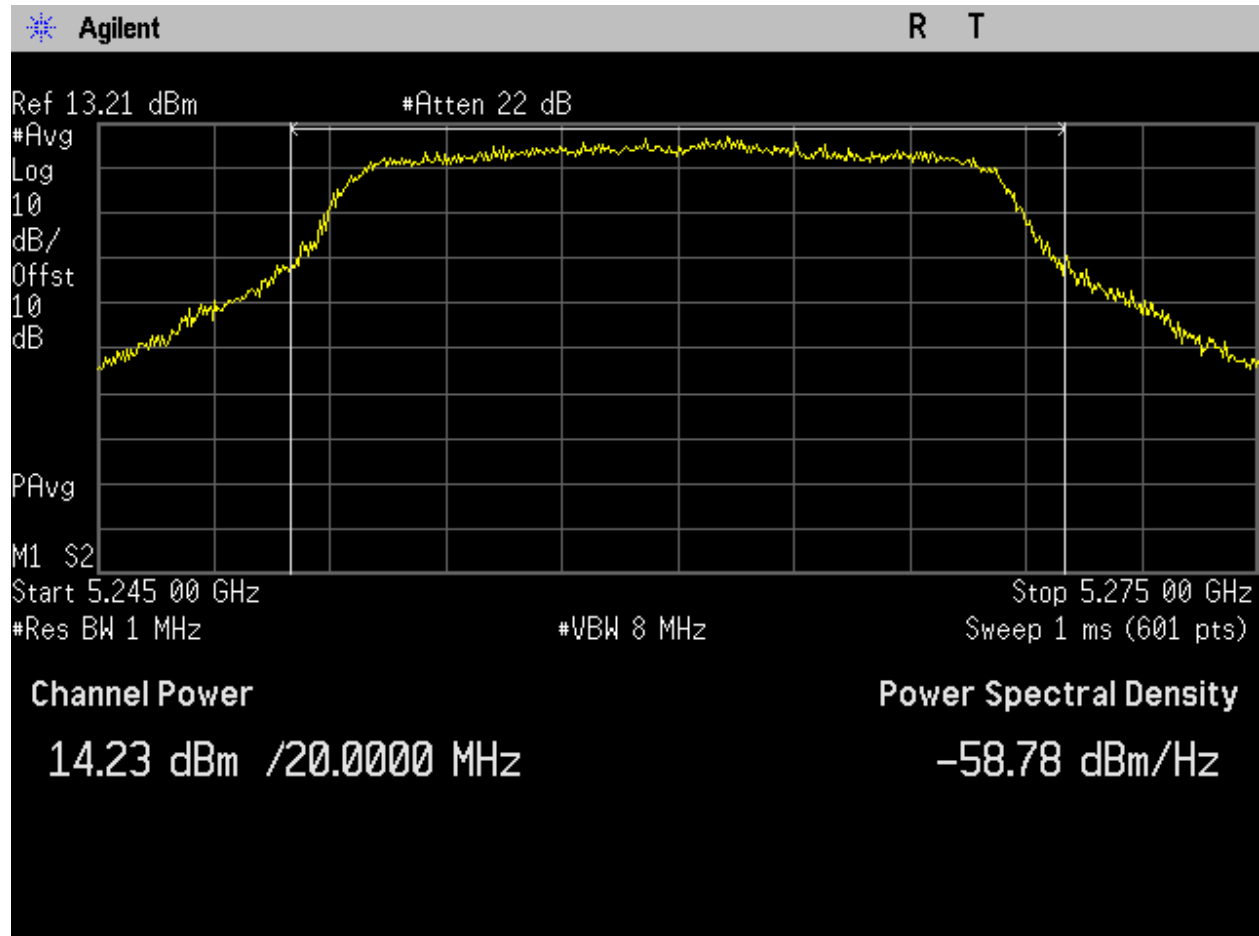


Figure 113: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_Output Power_Port 1.

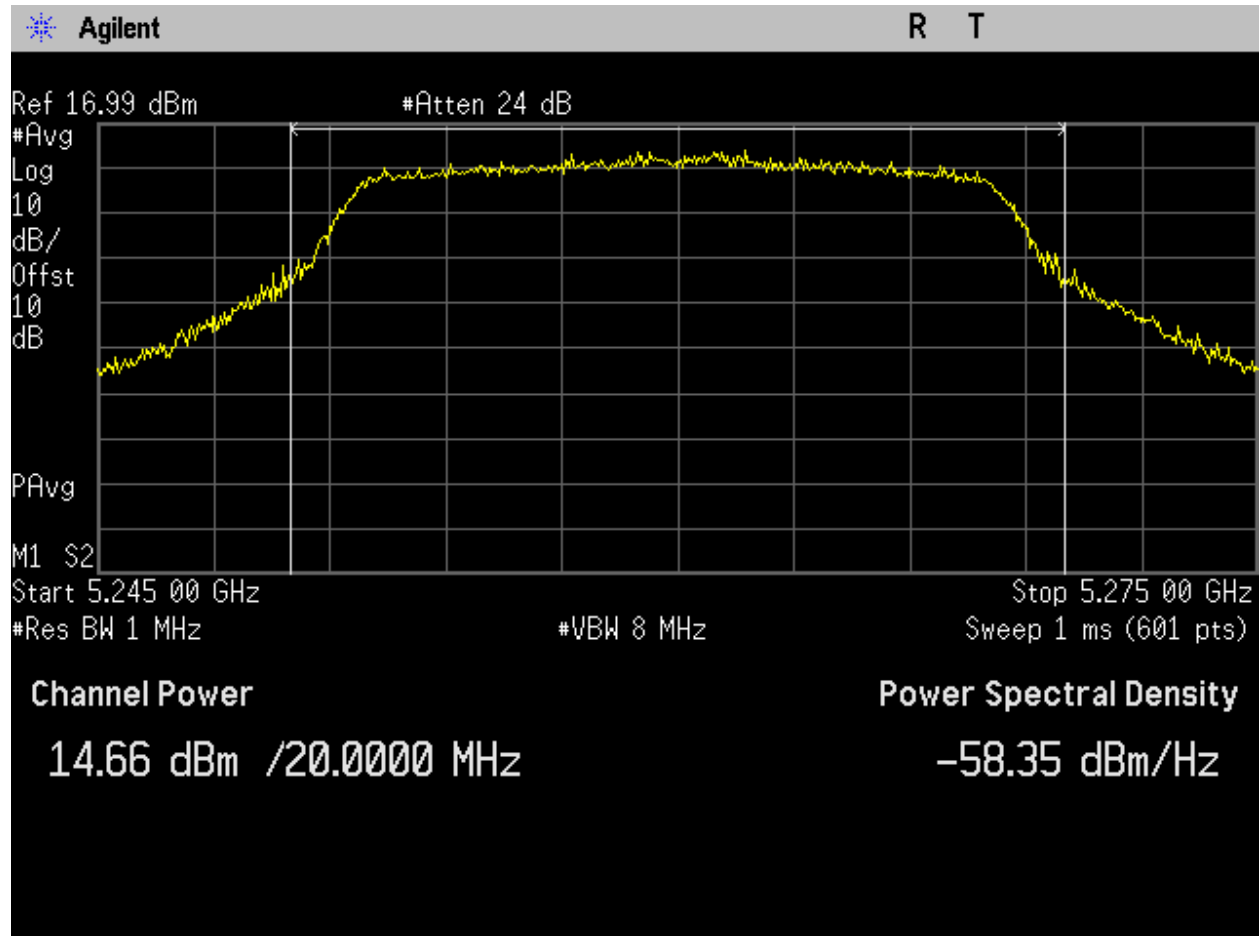


Figure 114: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_Output Power_Port 2.

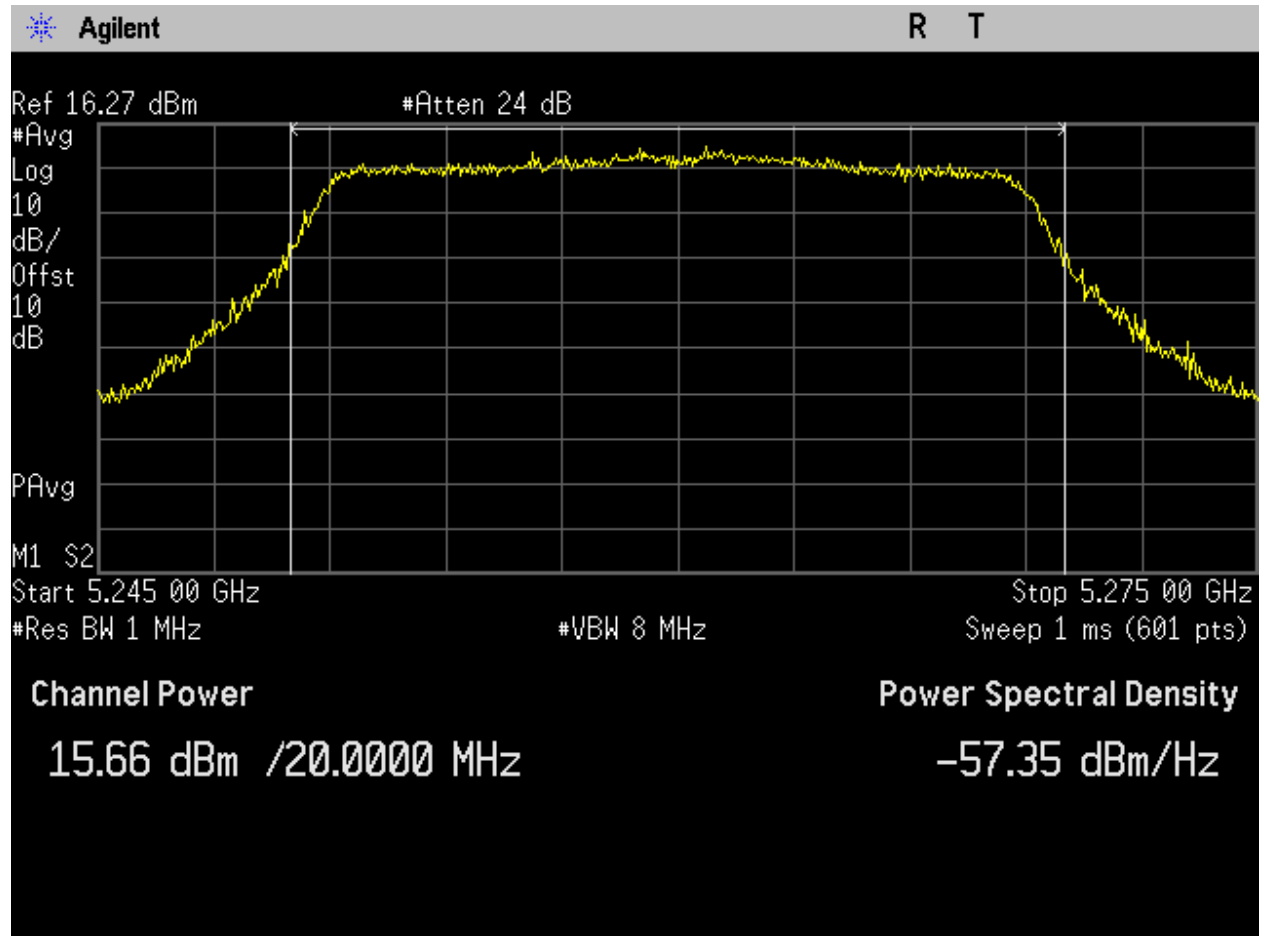


Figure 115: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_Output Power_Port 1.

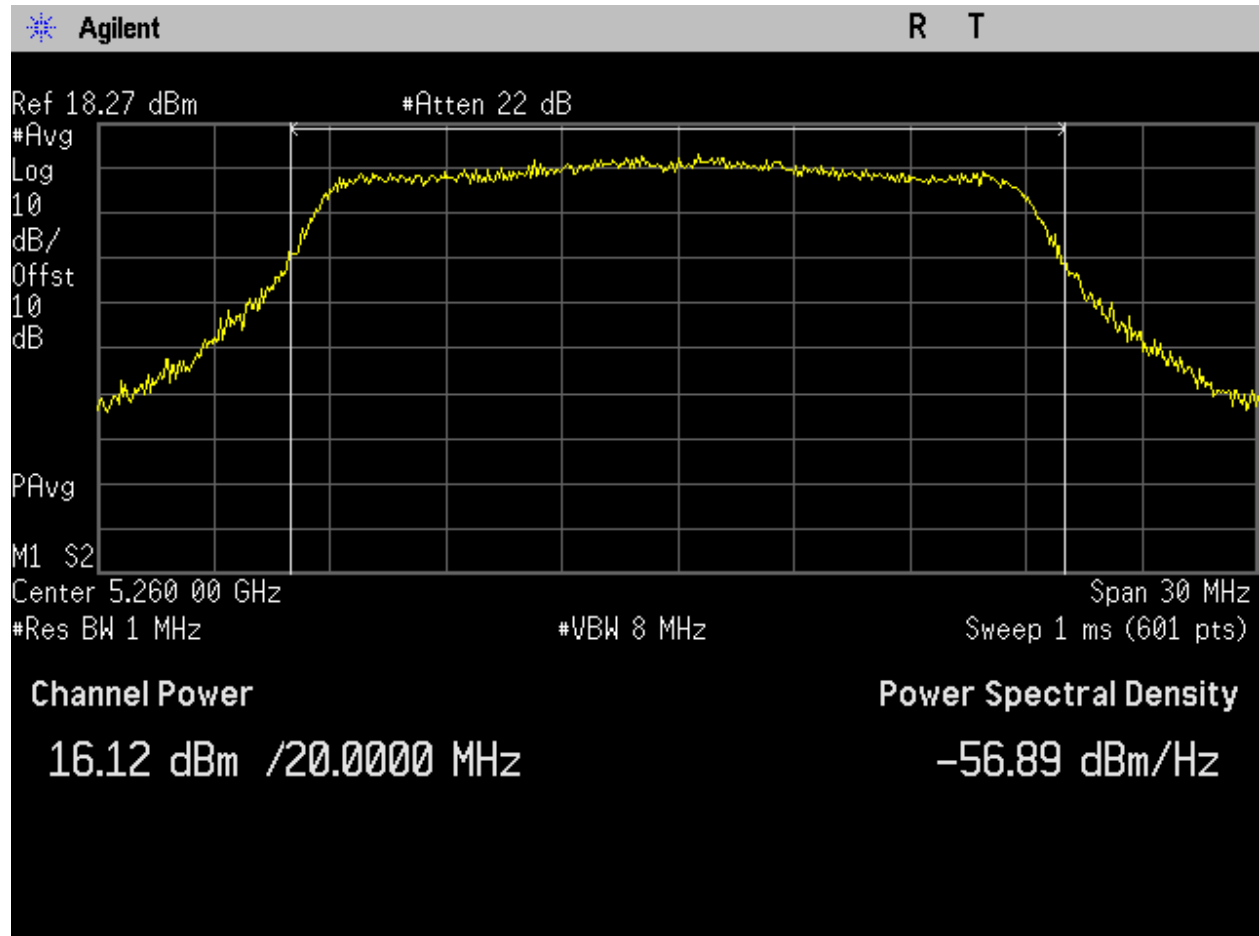


Figure 116: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_Output Power_Port 2.

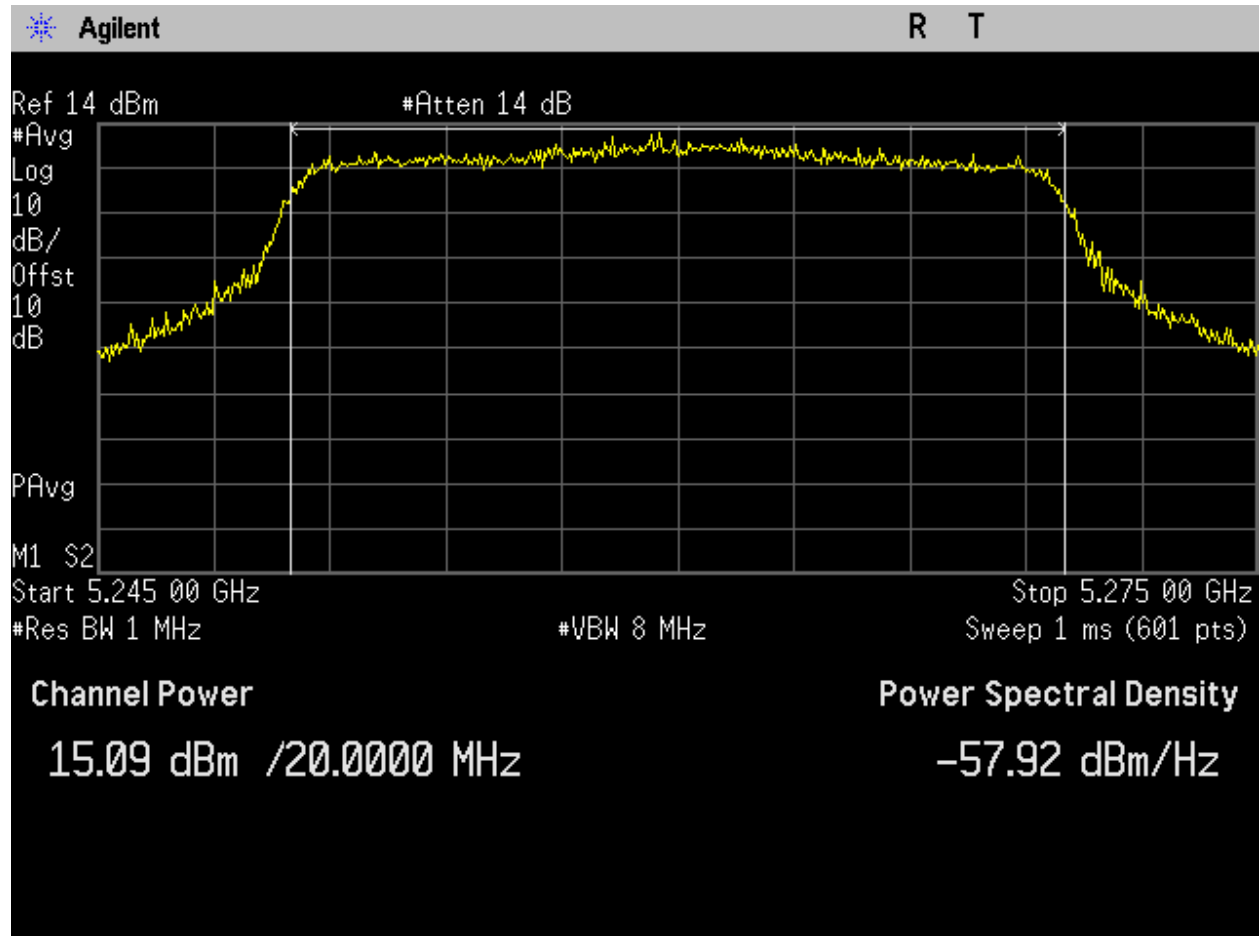


Figure 117: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_Output Power_Port 1.

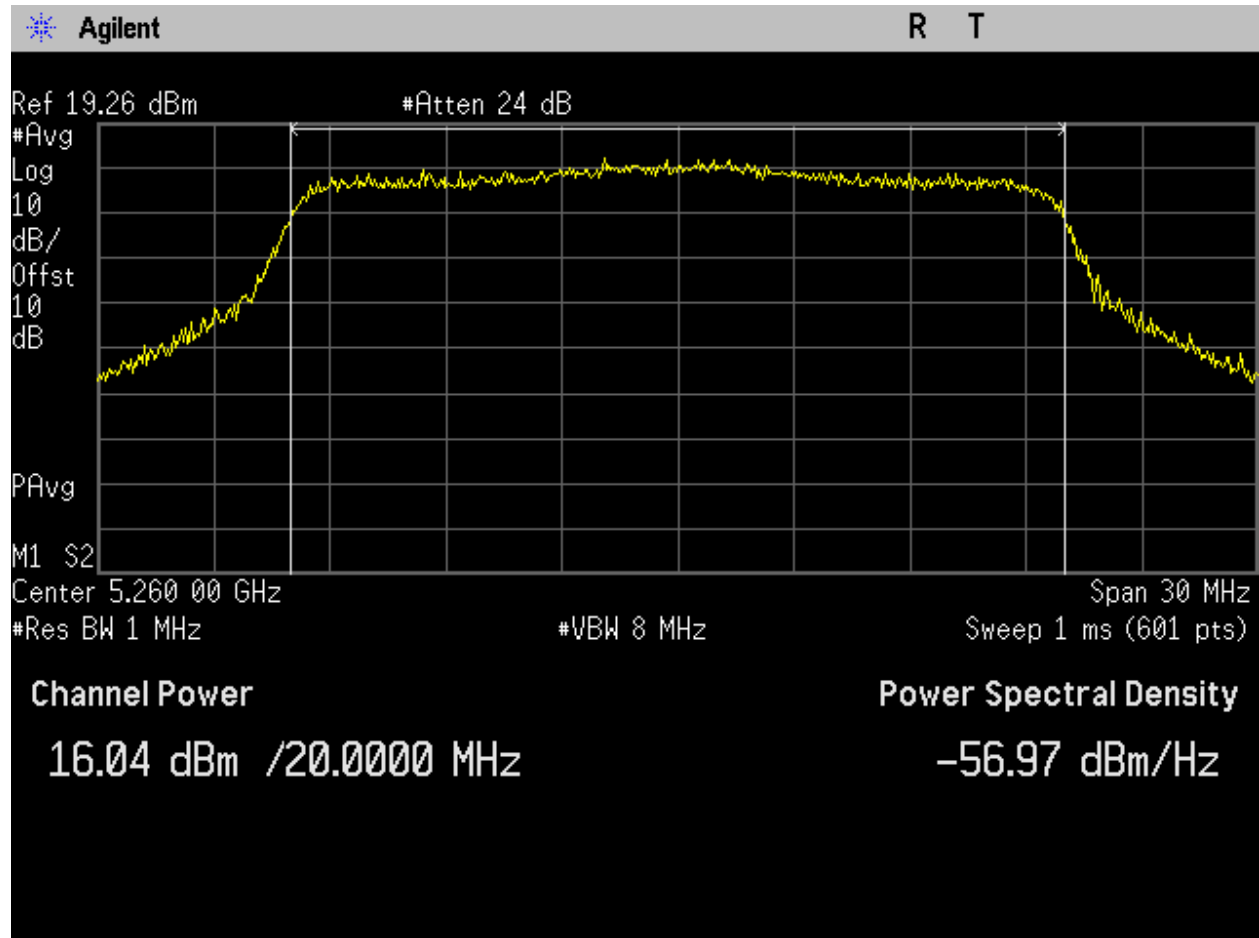


Figure 118: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_Output Power_Port 2.

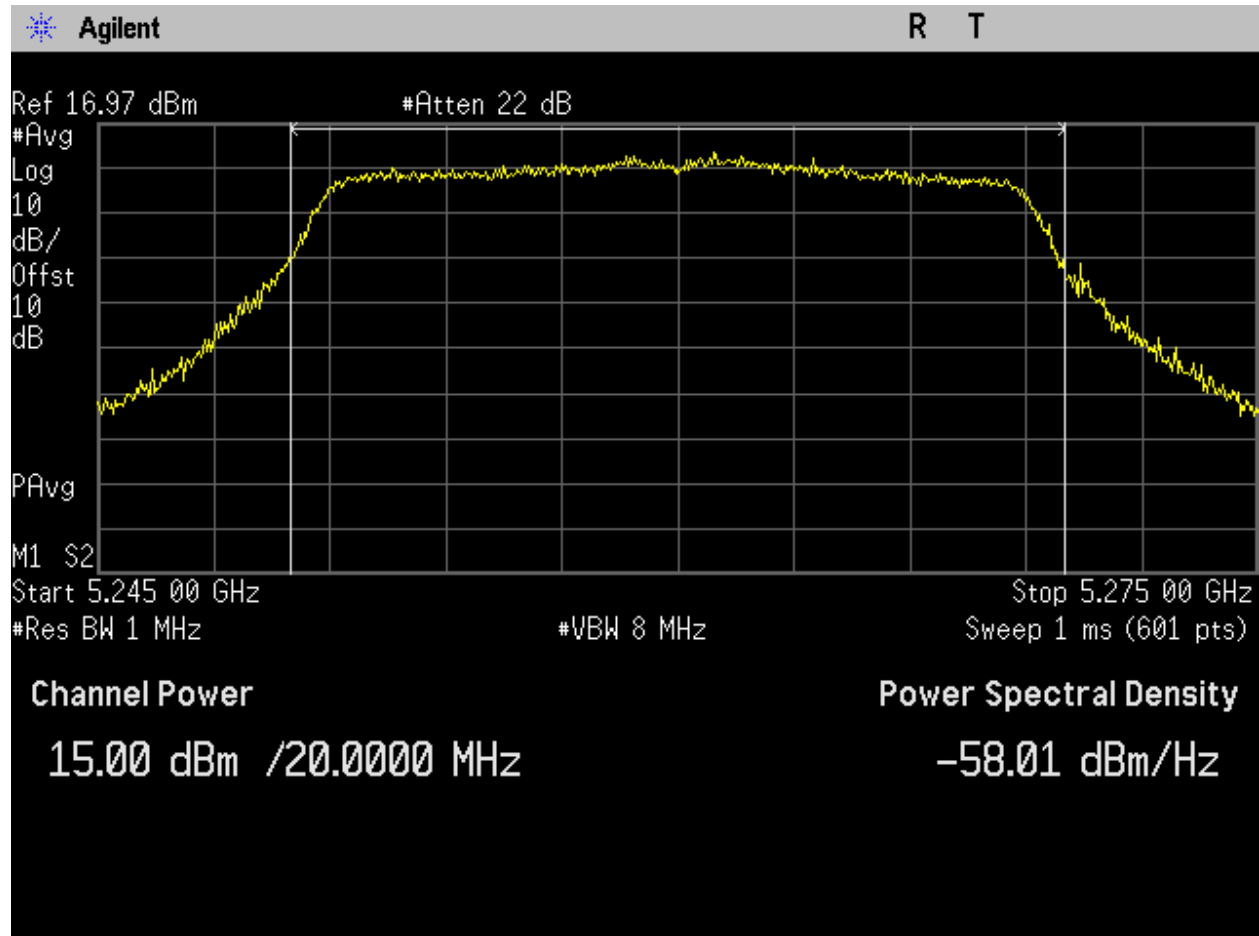


Figure 119: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_Output Power_Port 1.

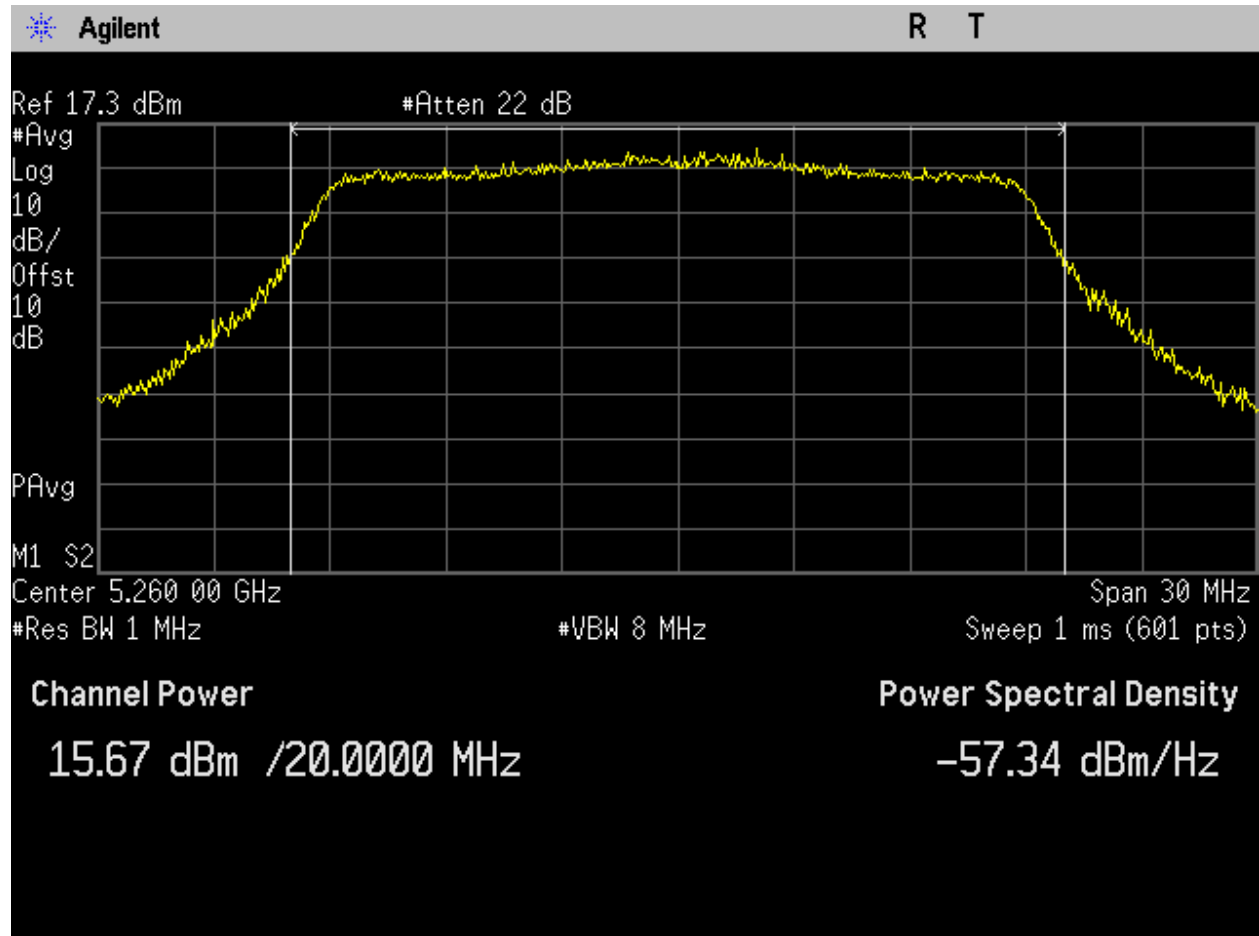


Figure 120: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_Output Power_Port 2.

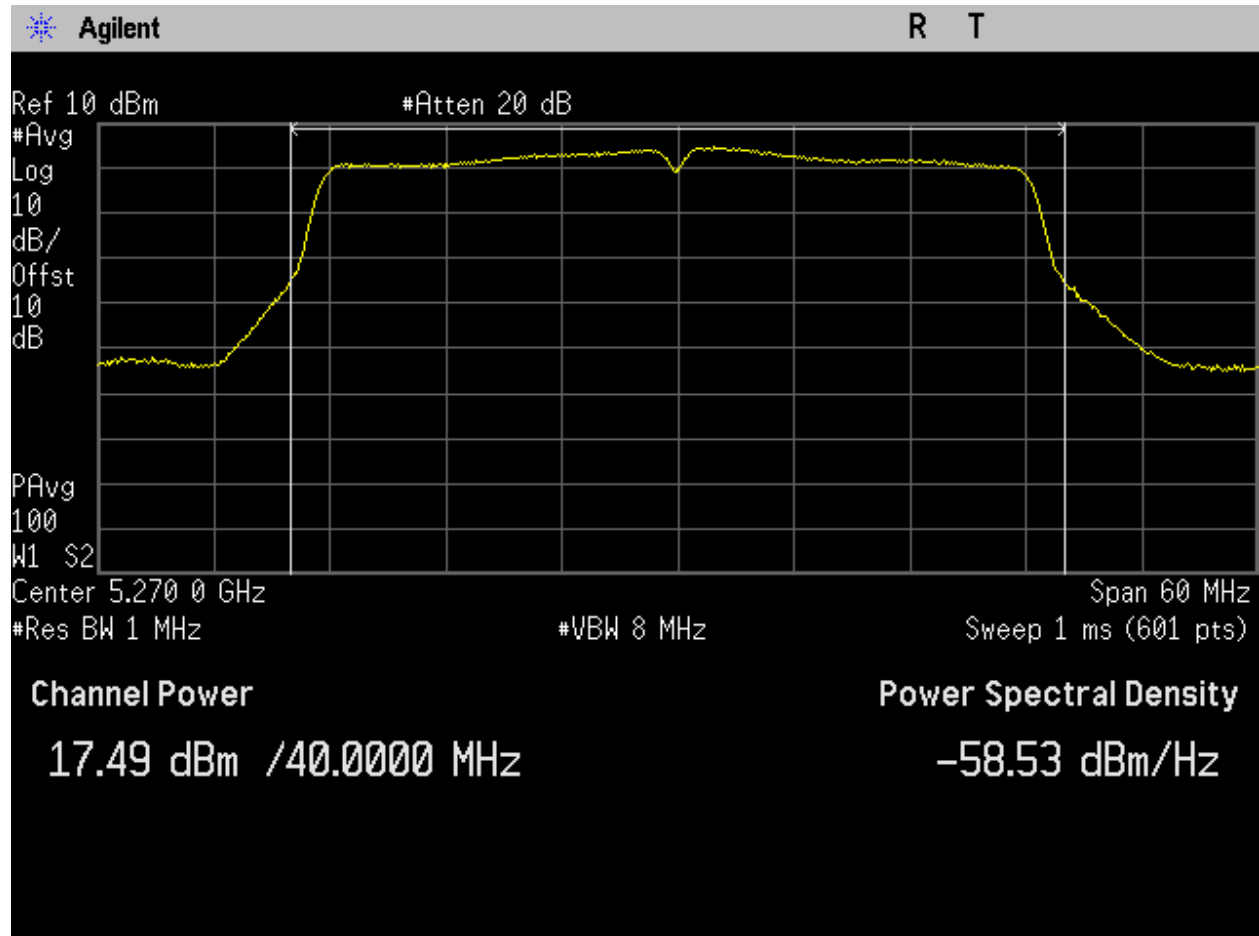


Figure 121: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Output Power_Port 1.

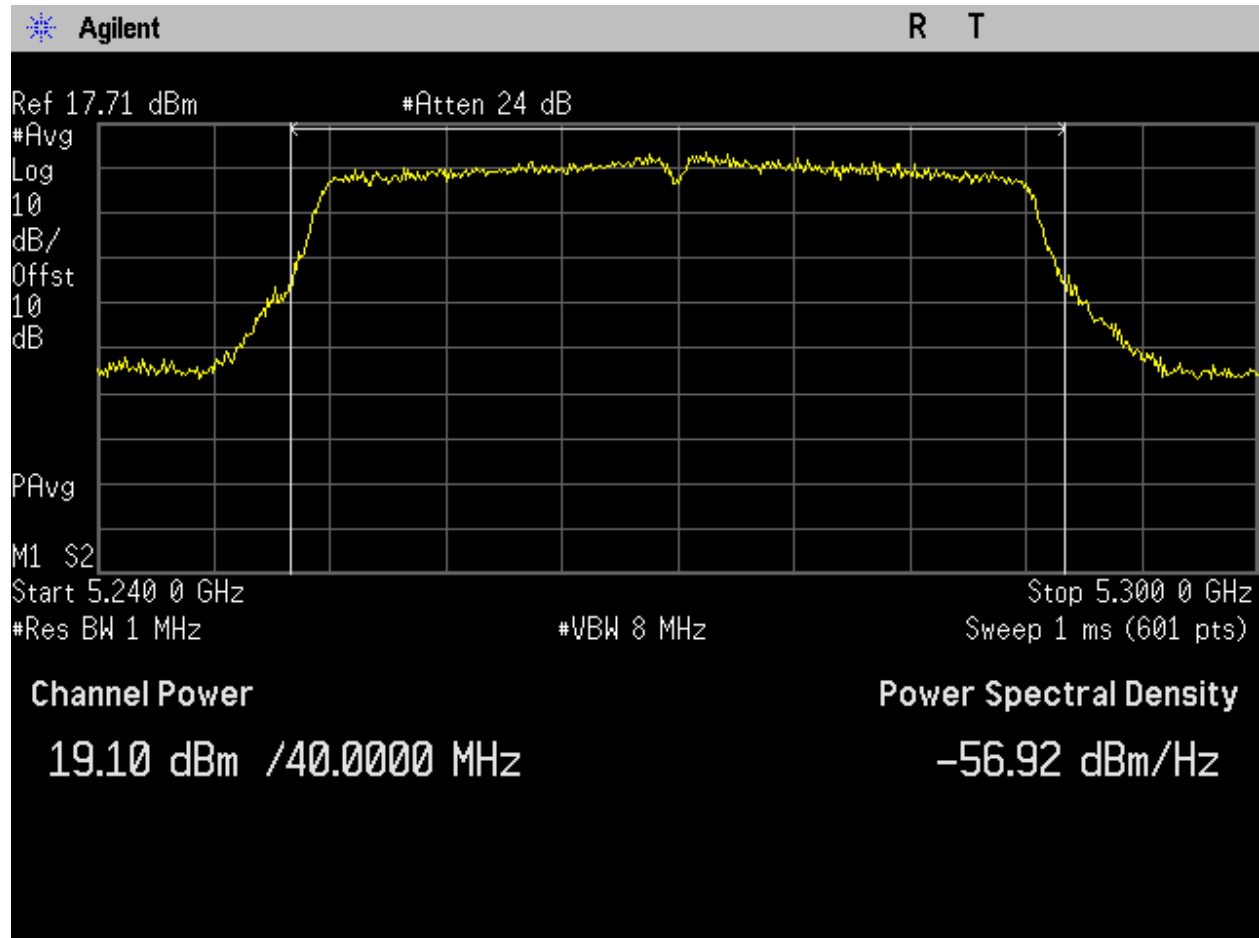


Figure 122: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Output Power_Port 2.

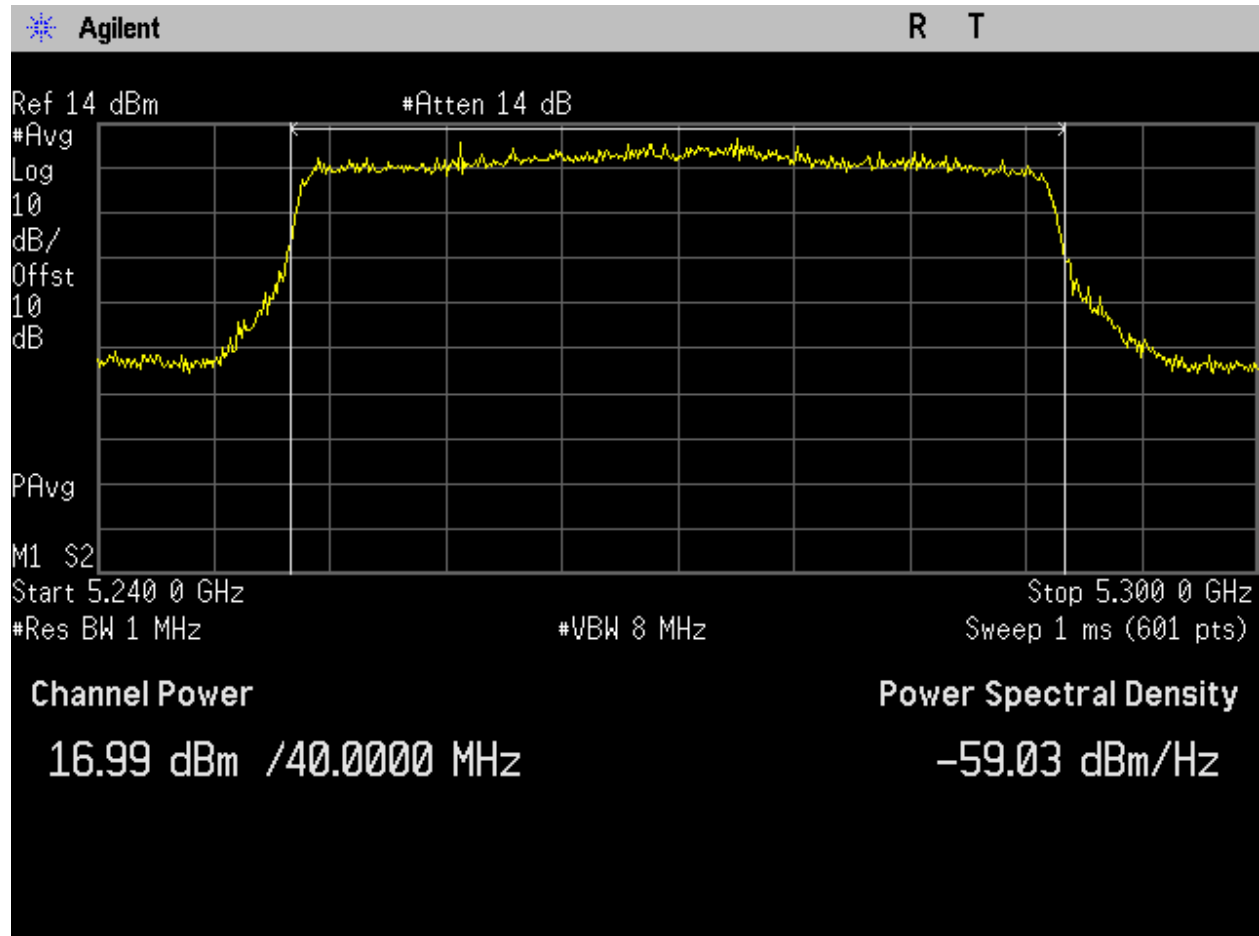


Figure 123: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Output Power_Port 1.

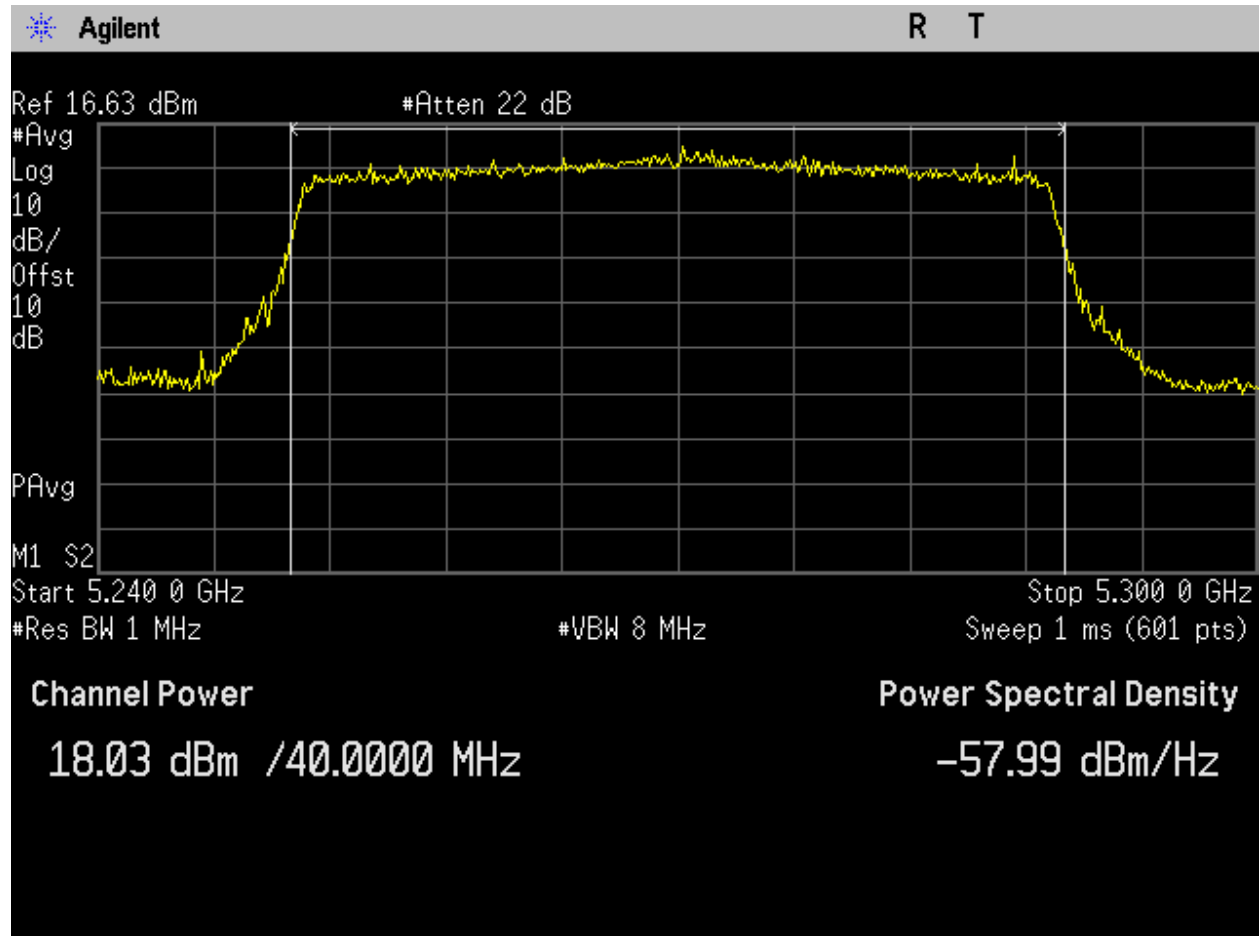


Figure 124: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Output Power_Port 2.

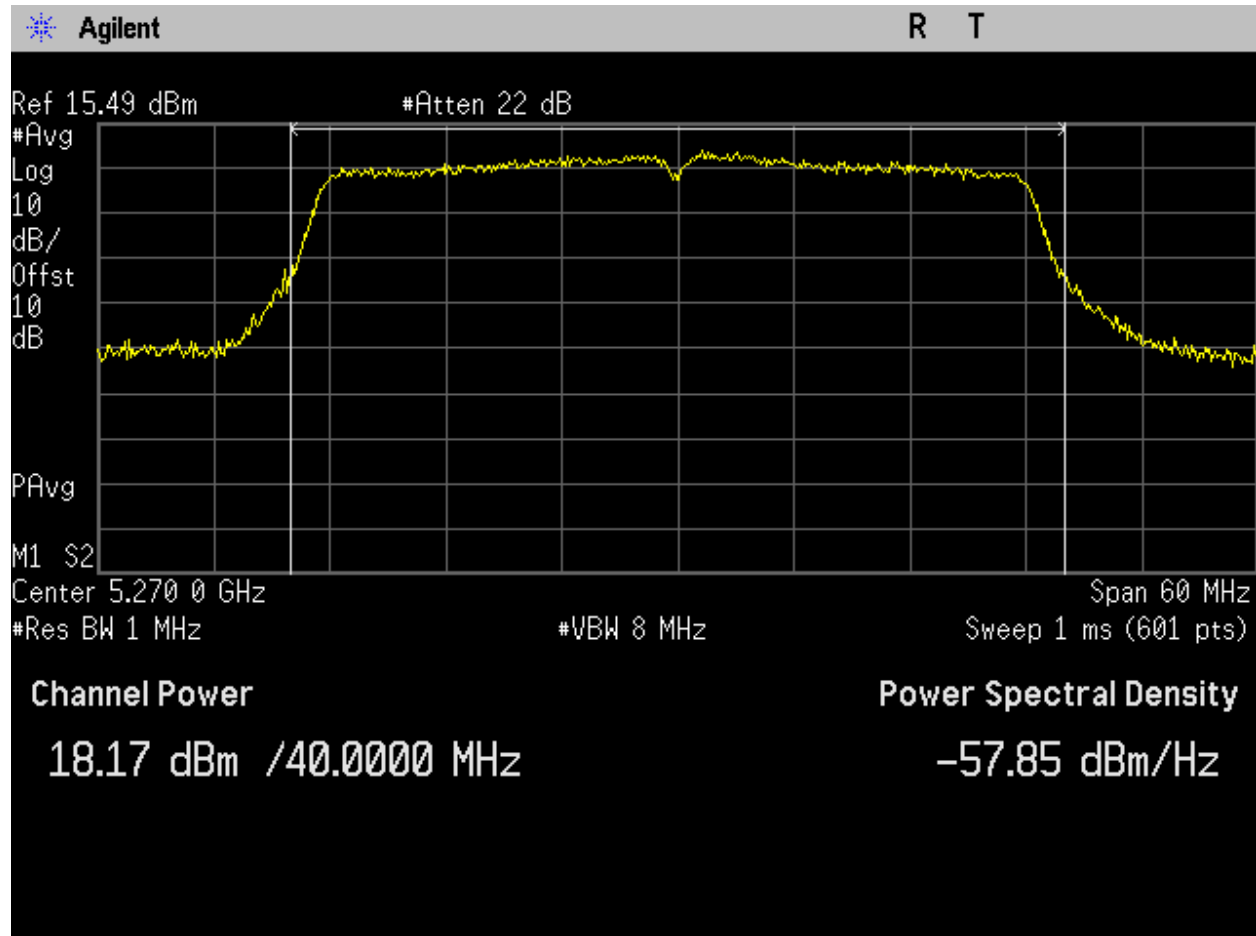


Figure 125: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Output Power_Port 1.

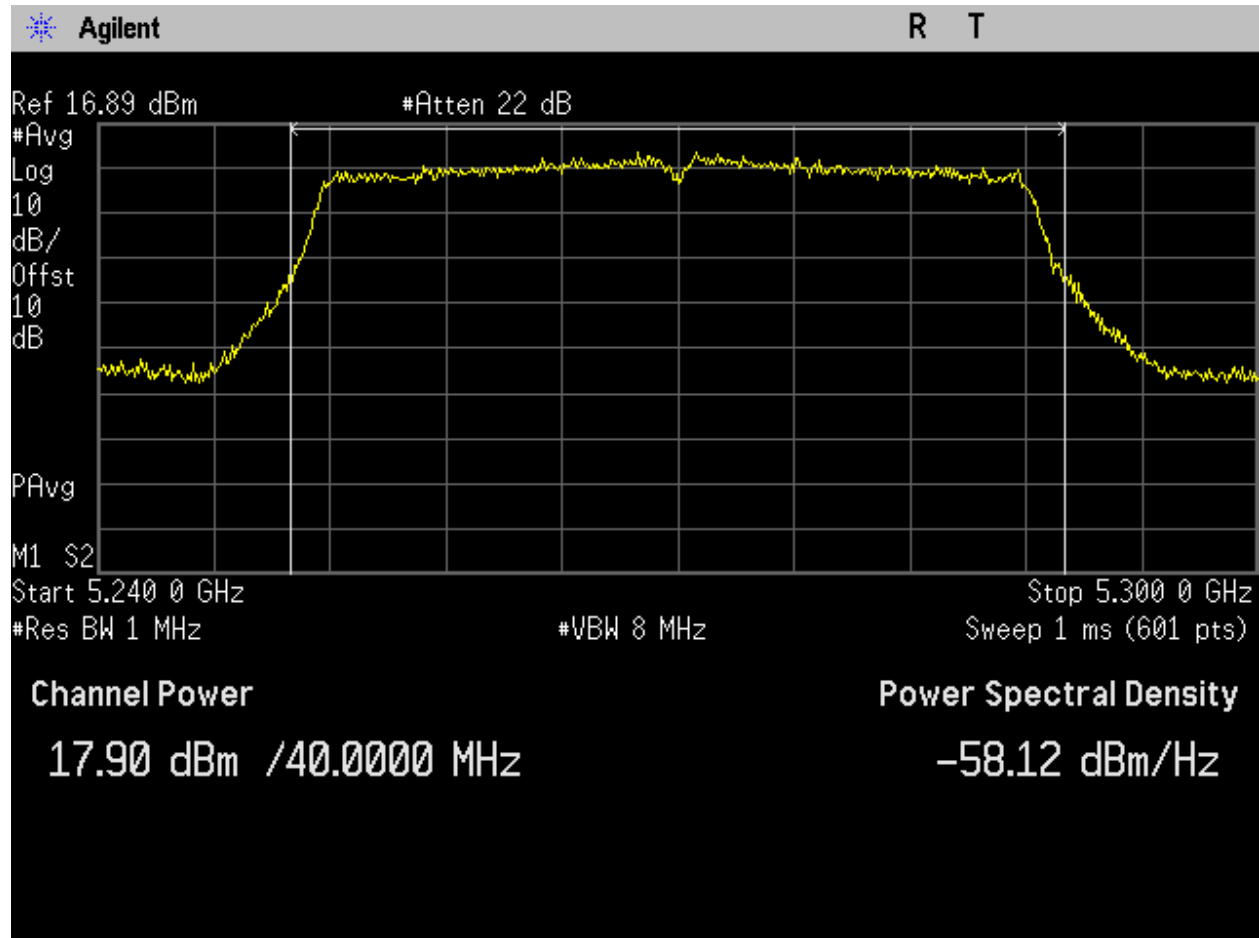


Figure 126: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Output Power_Port 2.

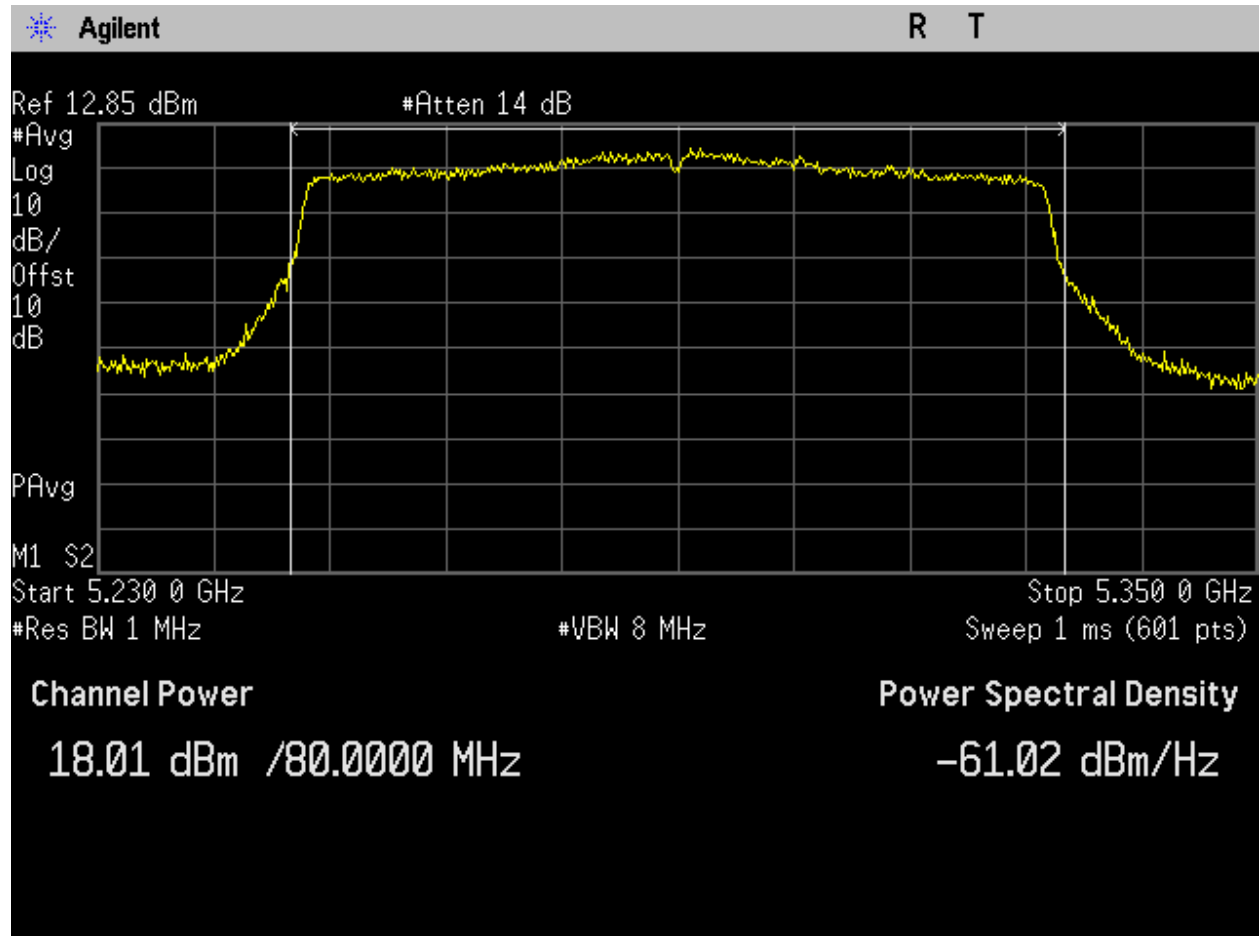


Figure 127: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Output Power_Port 1.

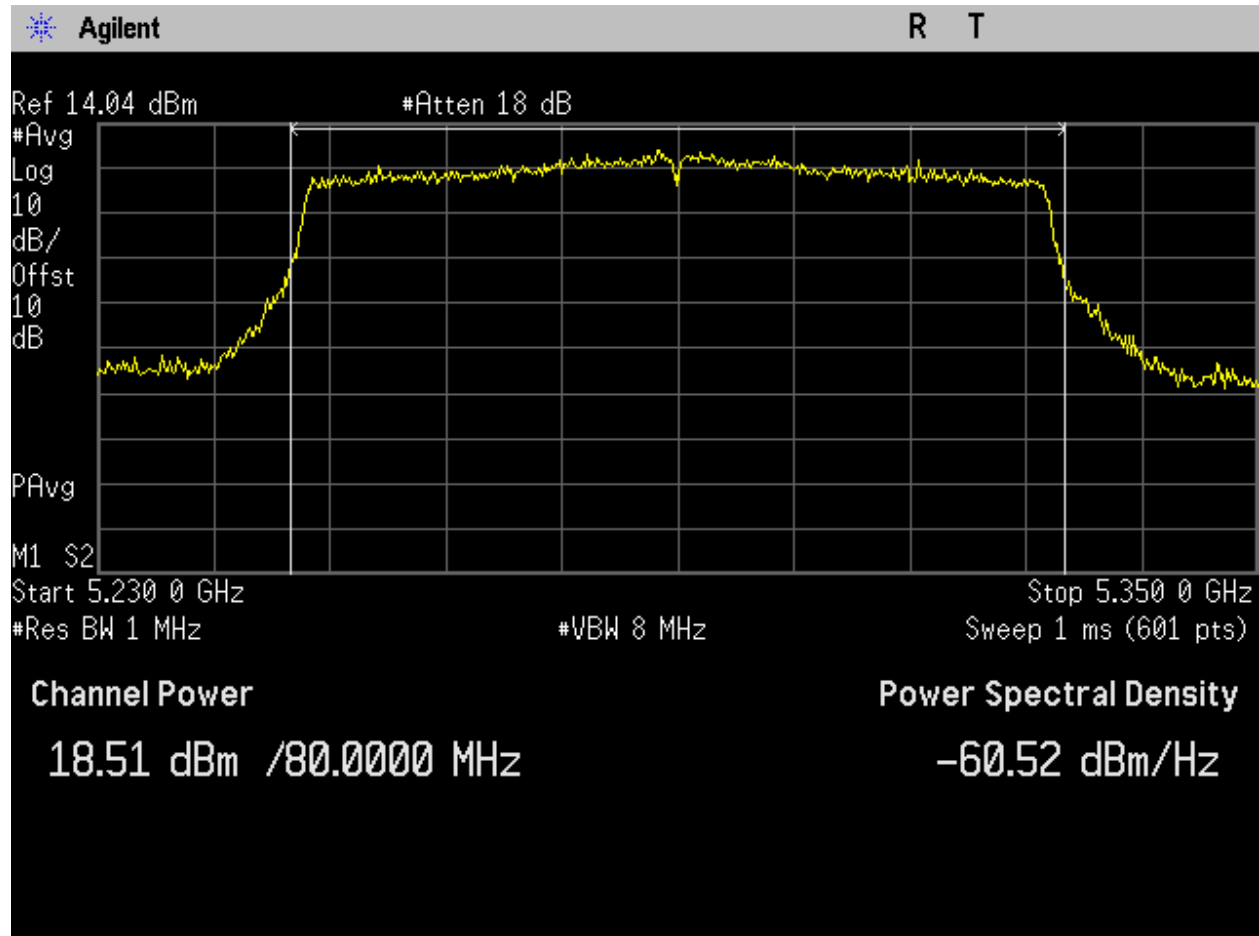


Figure 128: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Output Power_Port 2.

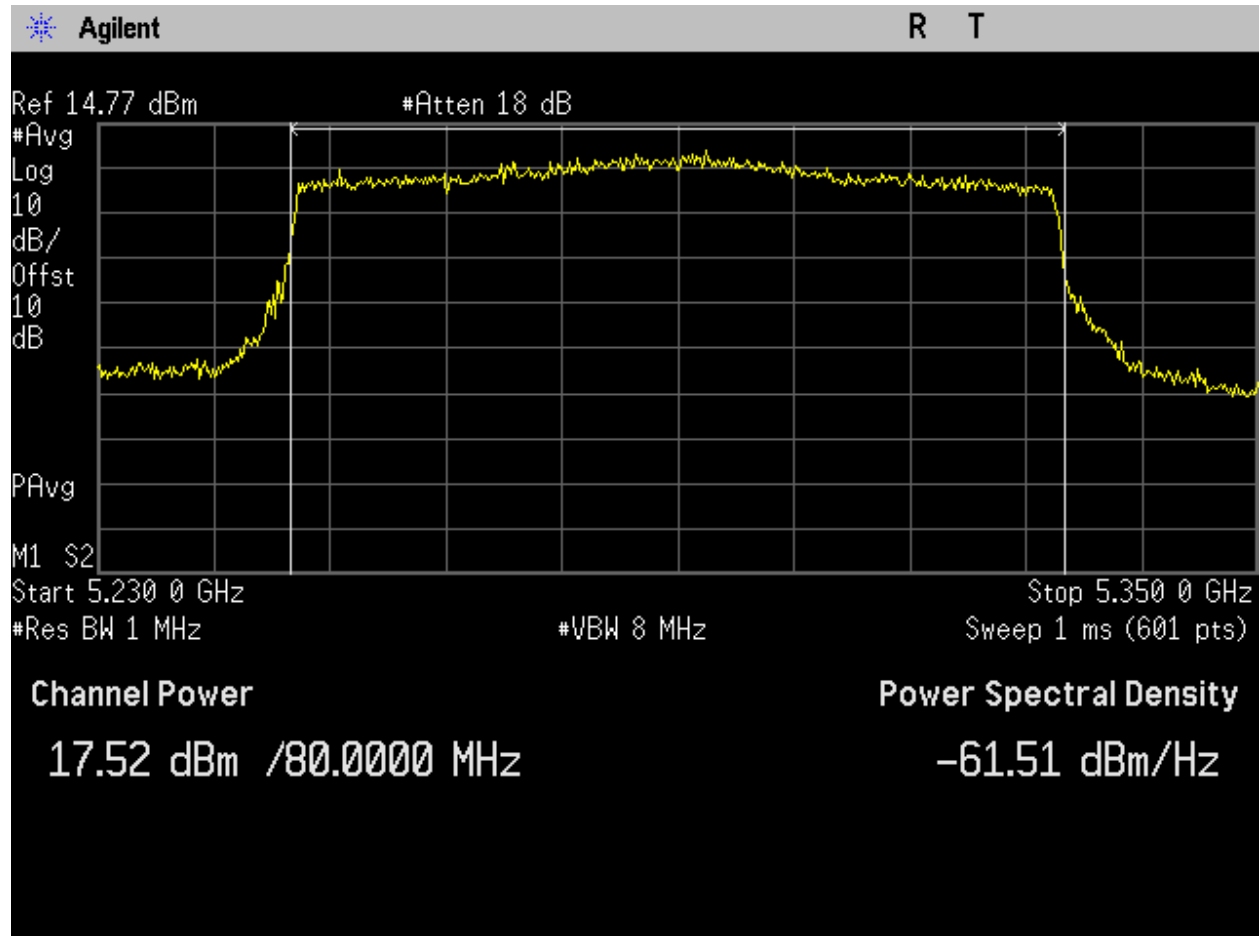


Figure 129: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Output Power_Port 1.

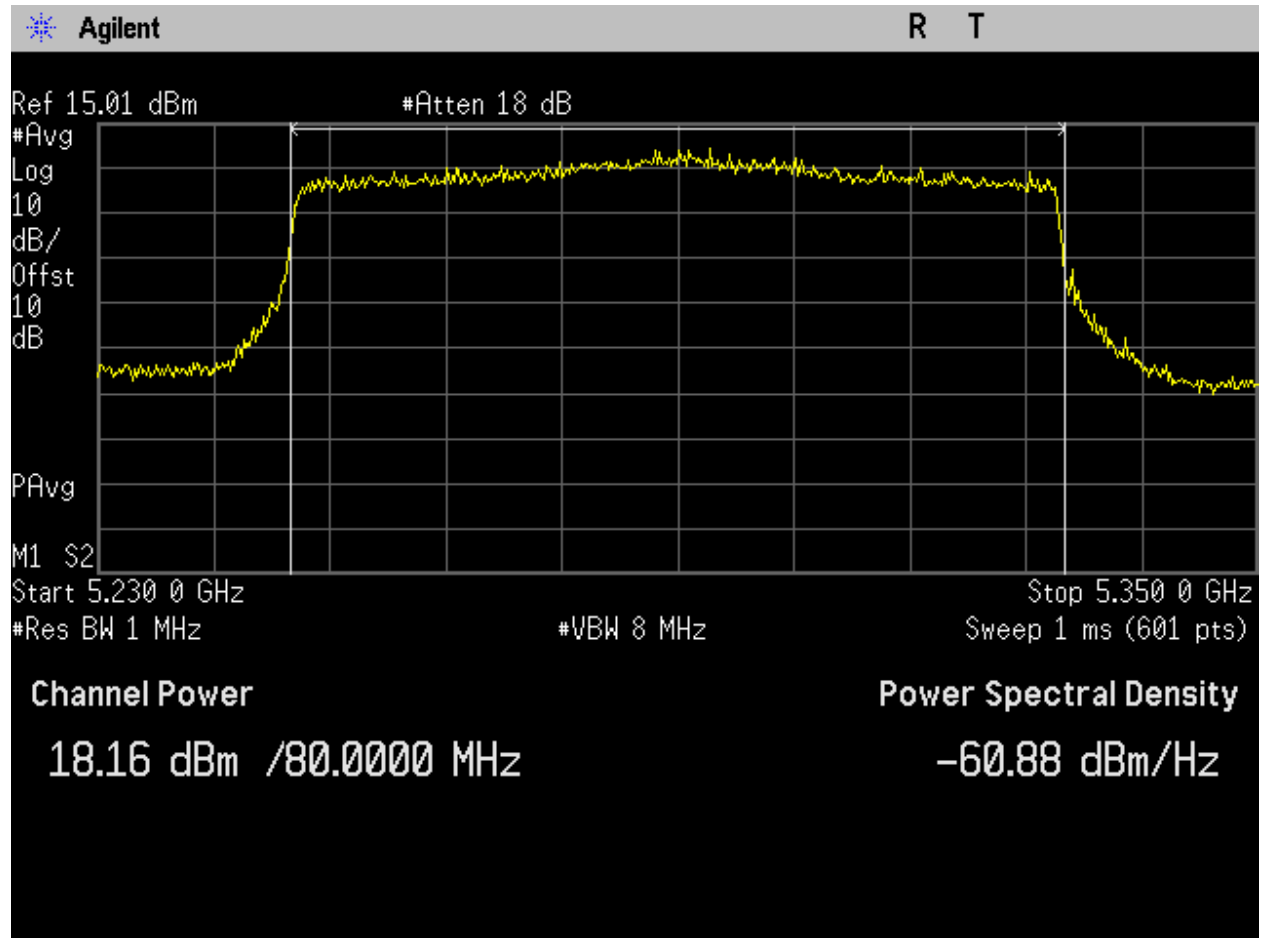


Figure 130: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Output Power_Port 2.

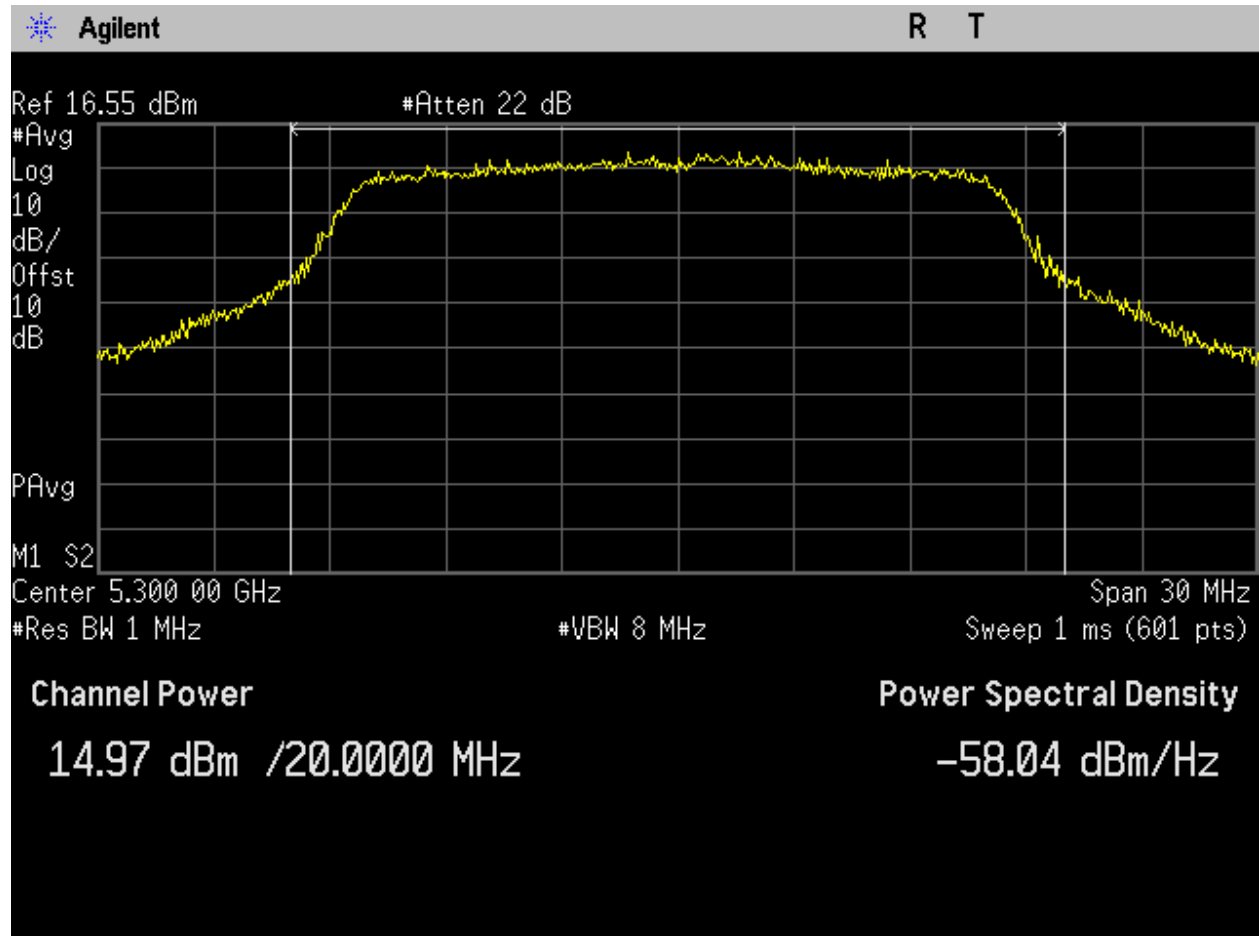


Figure 131: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Output Power_Port 1.

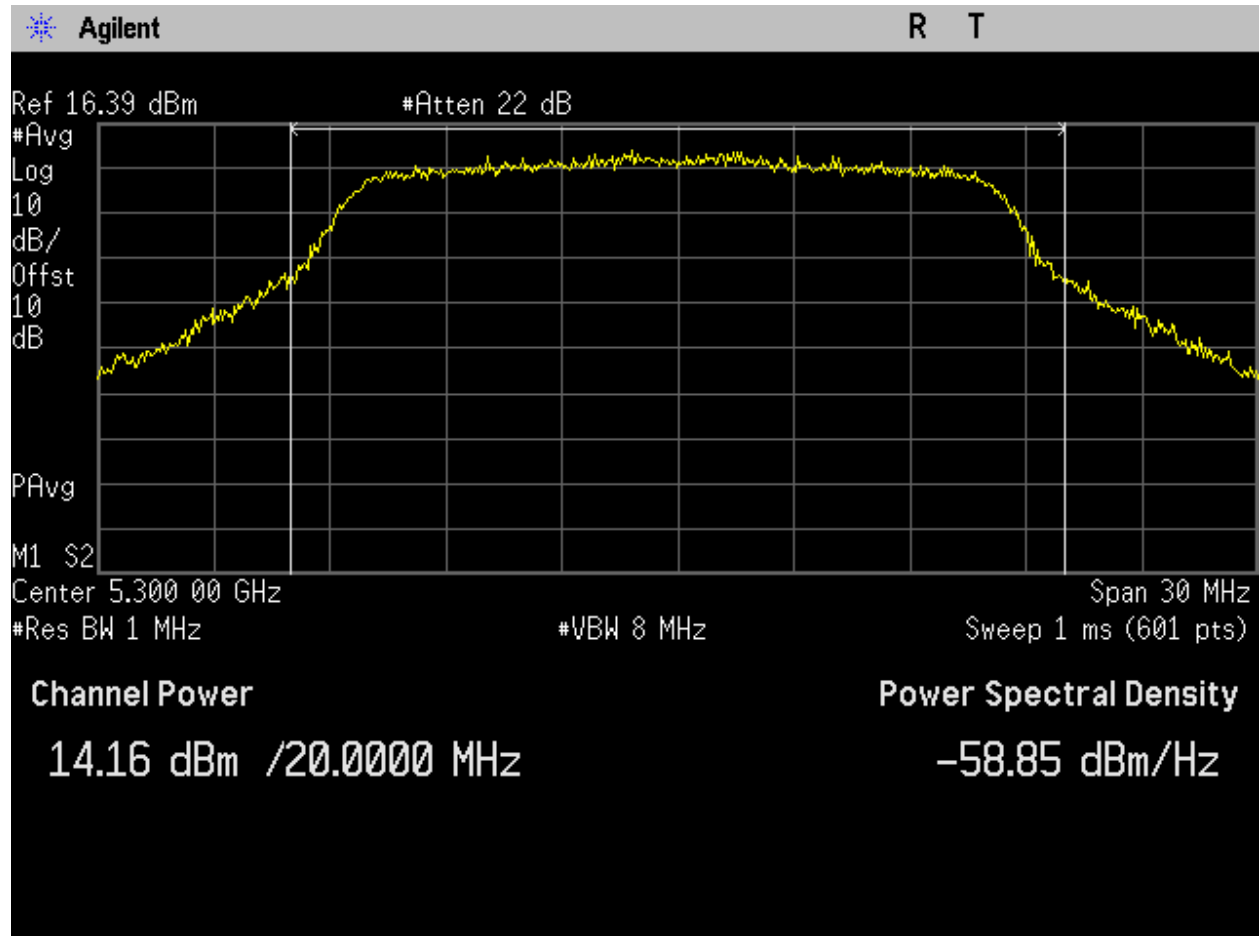


Figure 132: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Output Power_Port 2.

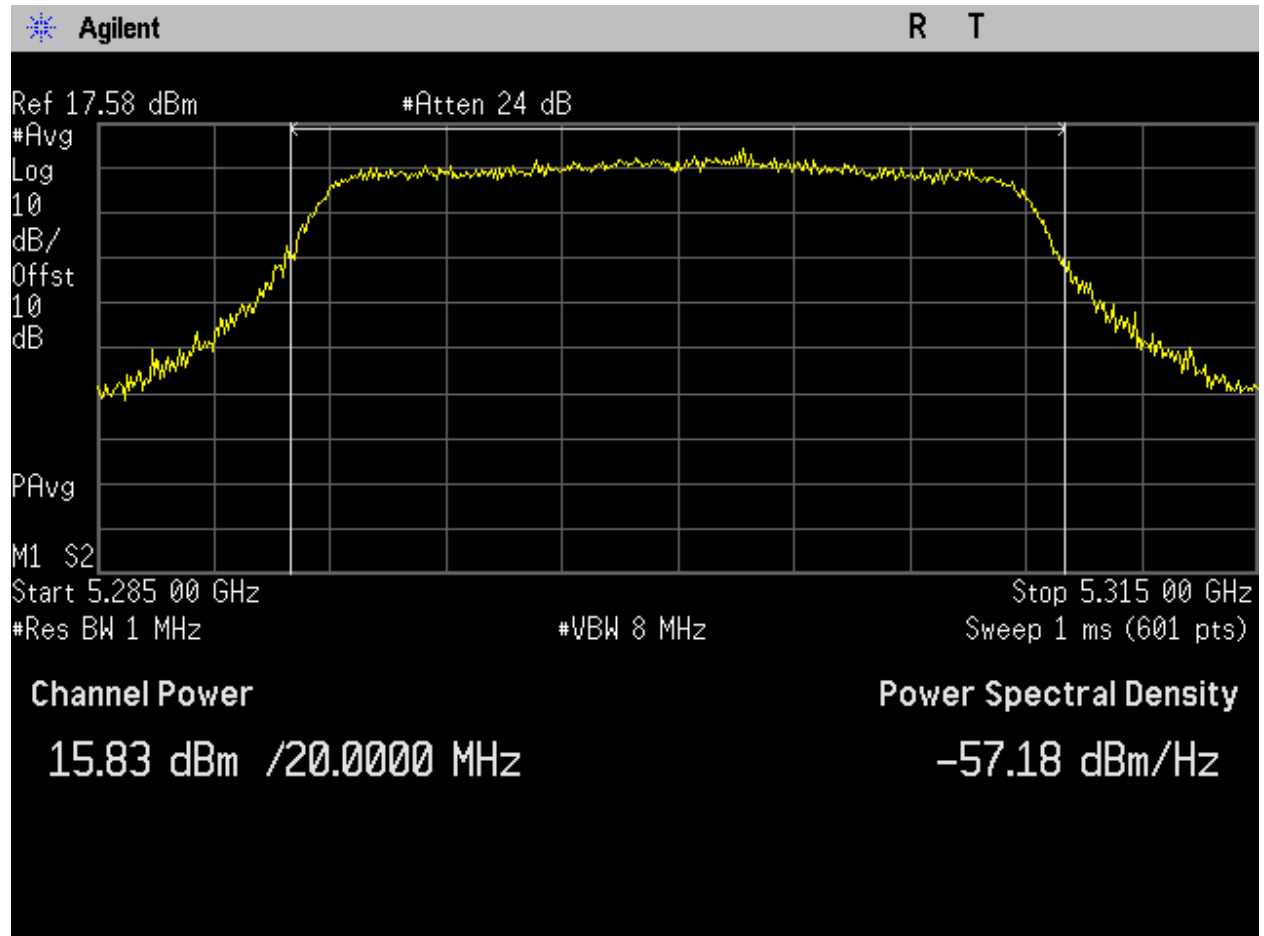


Figure 133: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Output Power_Port 1.

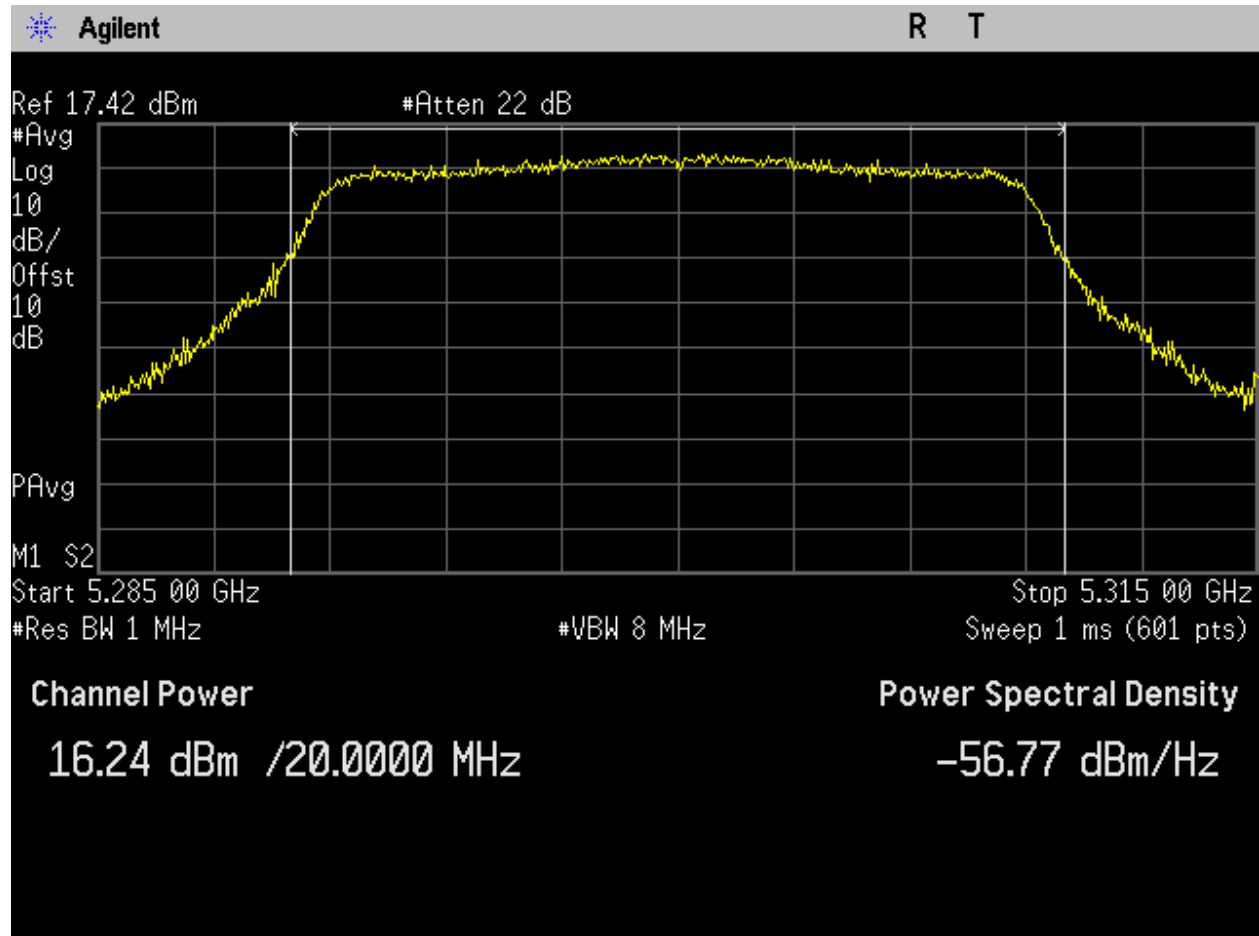


Figure 134: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Output Power_Port 2.

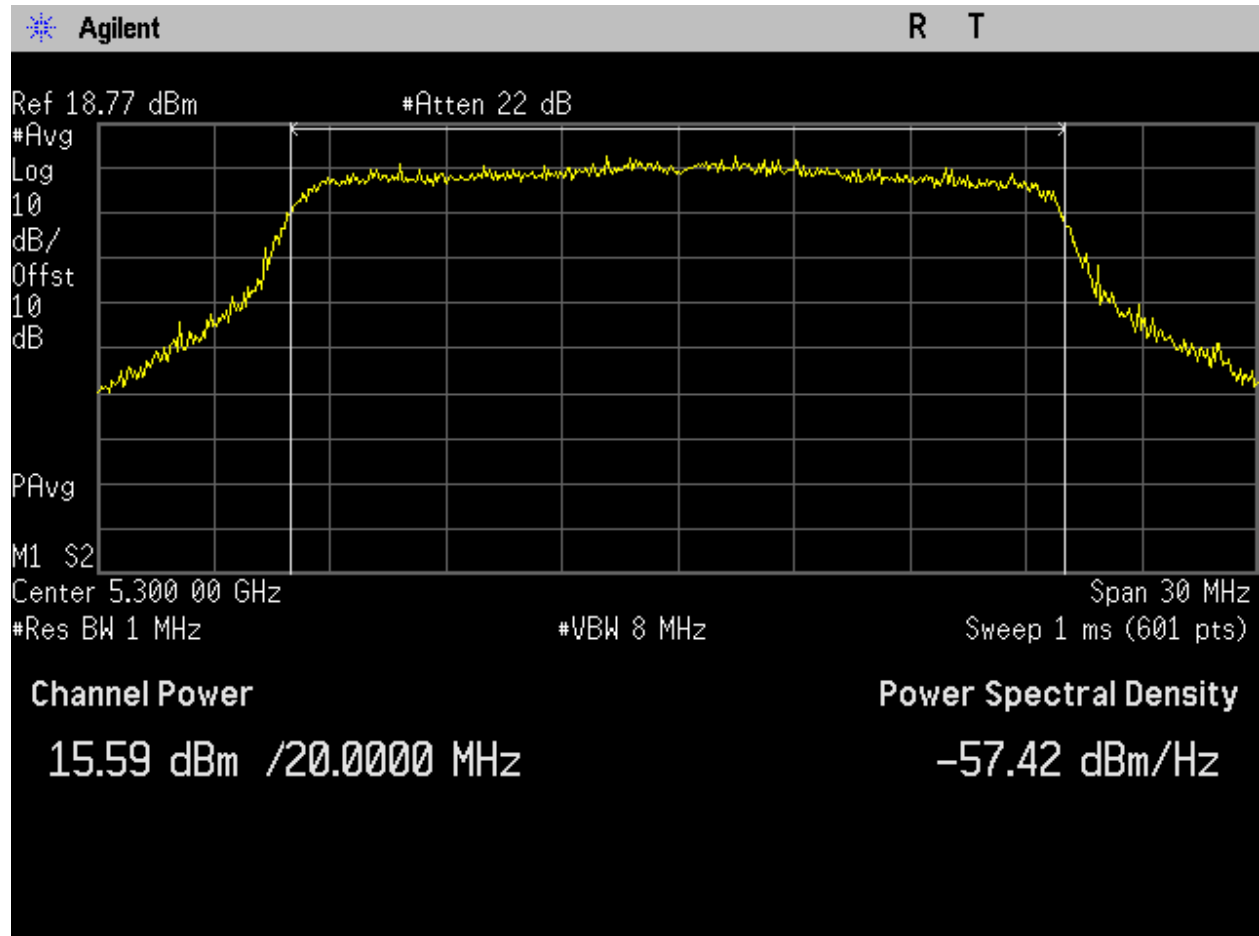


Figure 135: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_Output Power_Port 1.

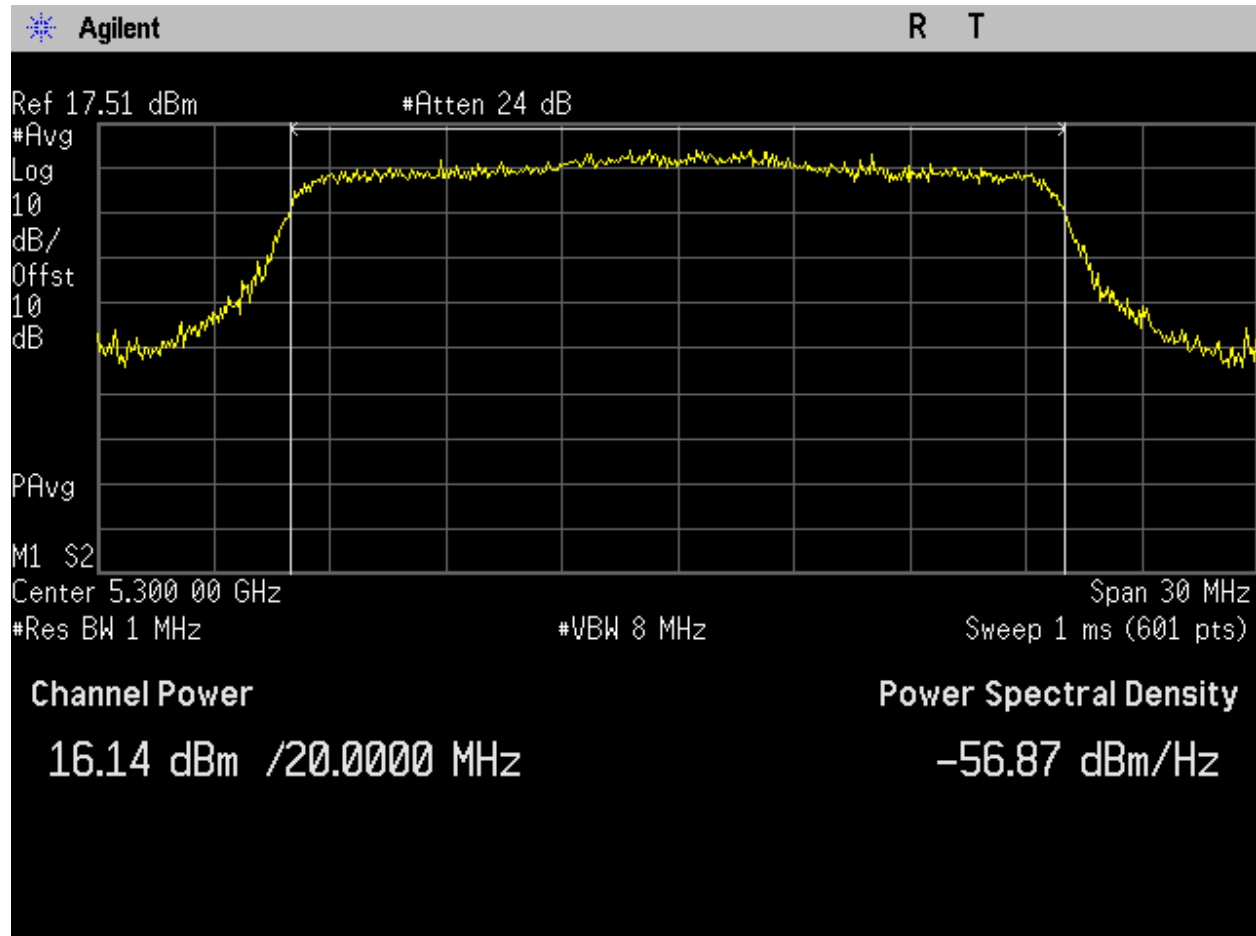


Figure 136: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_Output Power_Port 2.

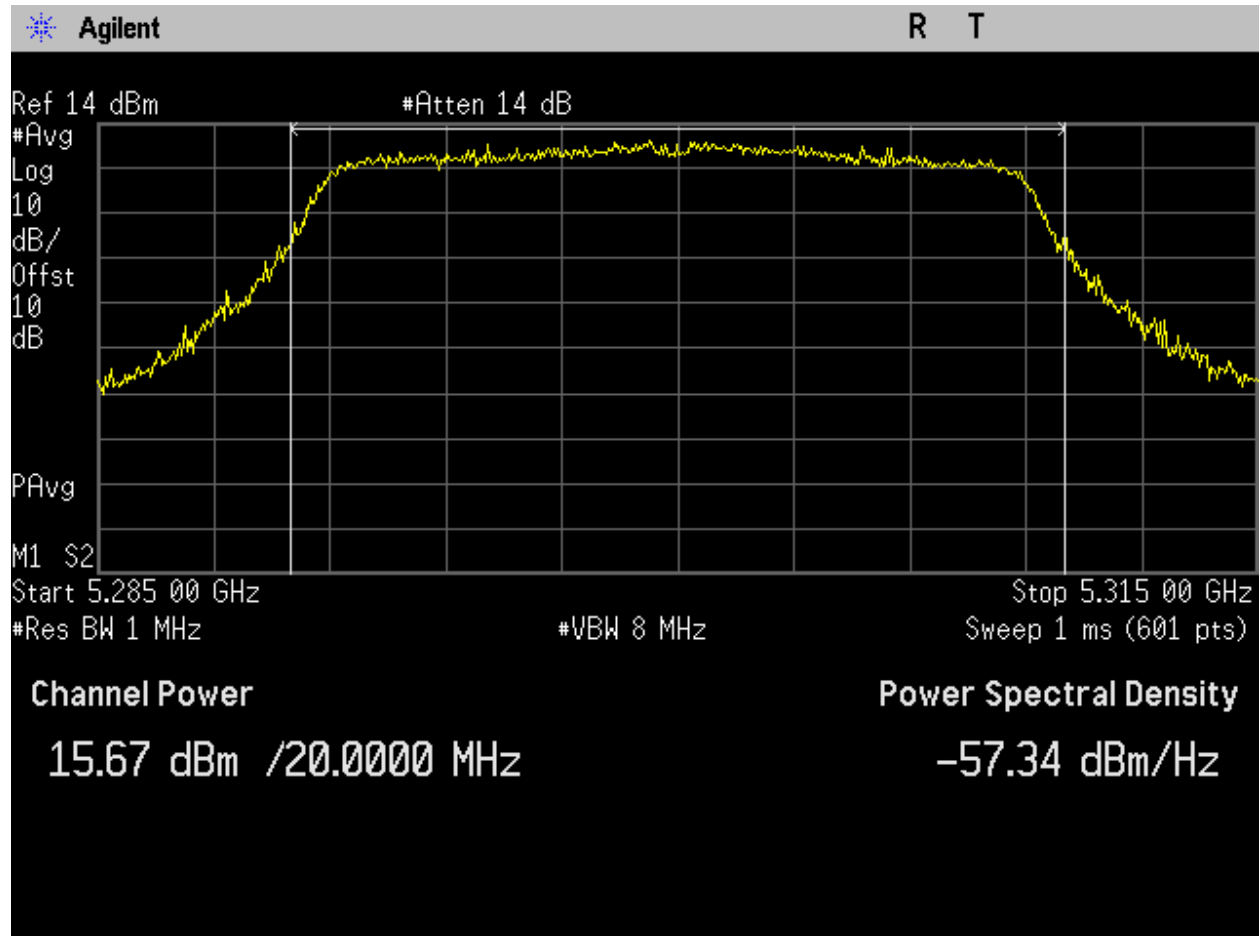


Figure 137: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Output Power_Port 1.

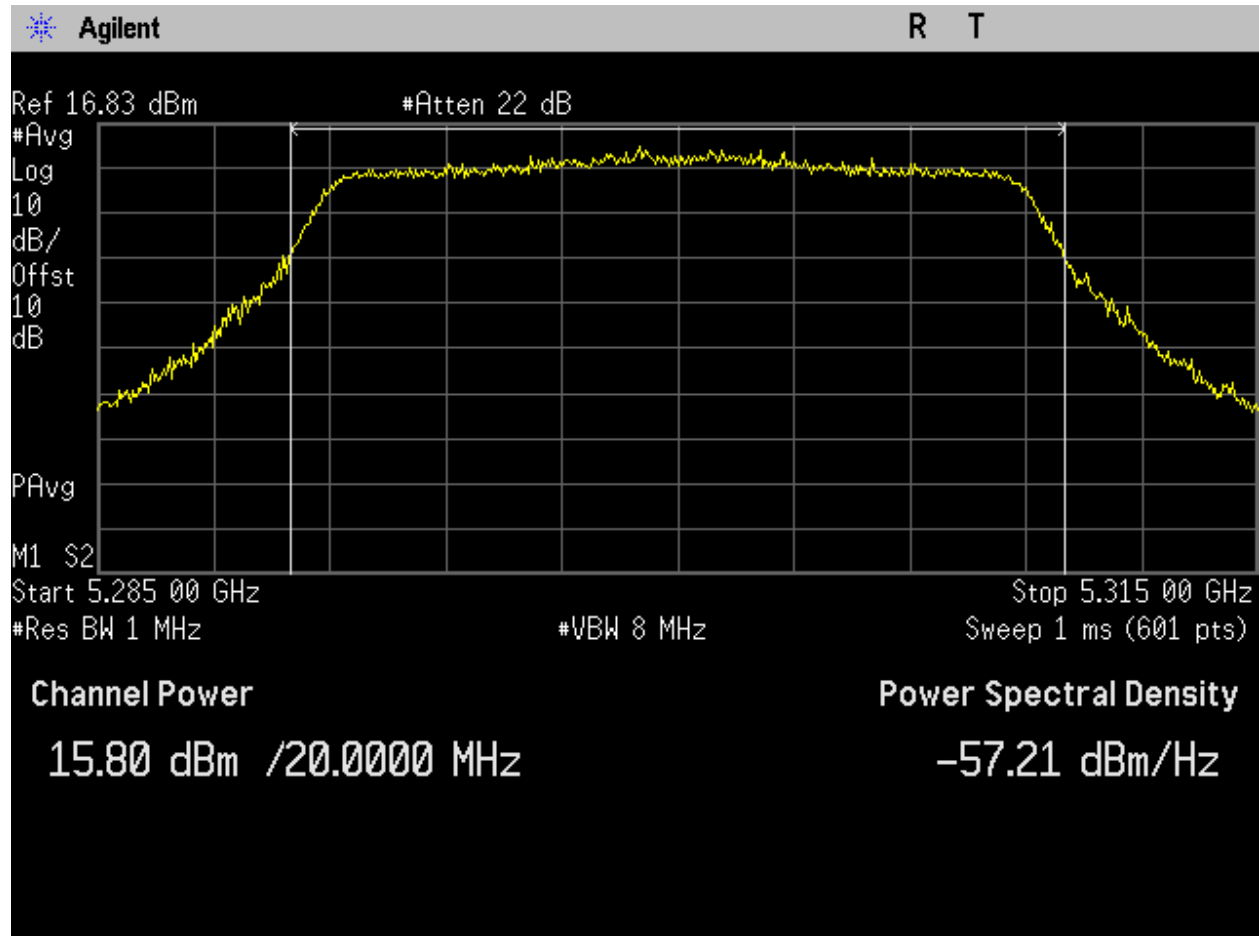


Figure 138: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Output Power_Port 2.

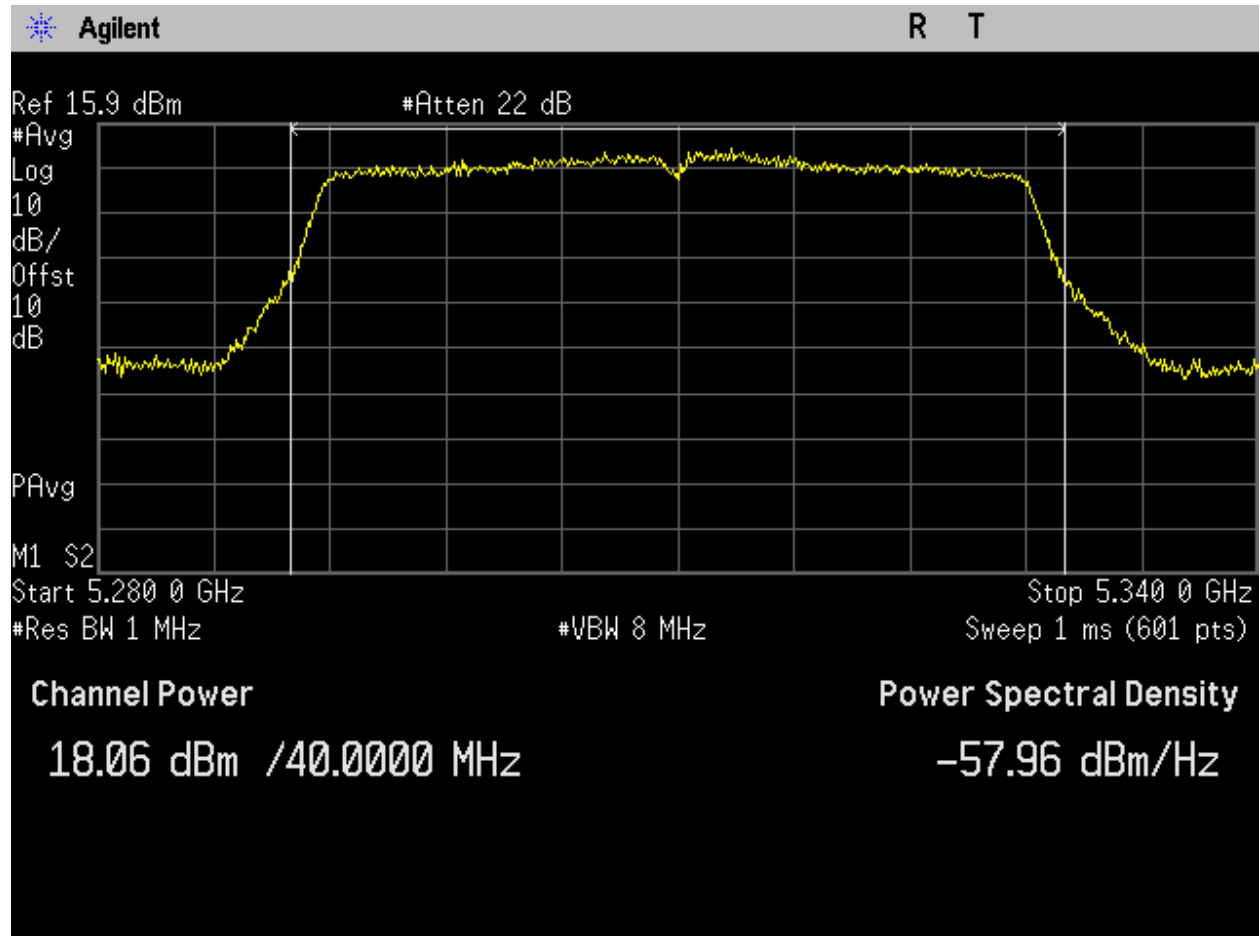


Figure 139: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_Output Power_Port 1.

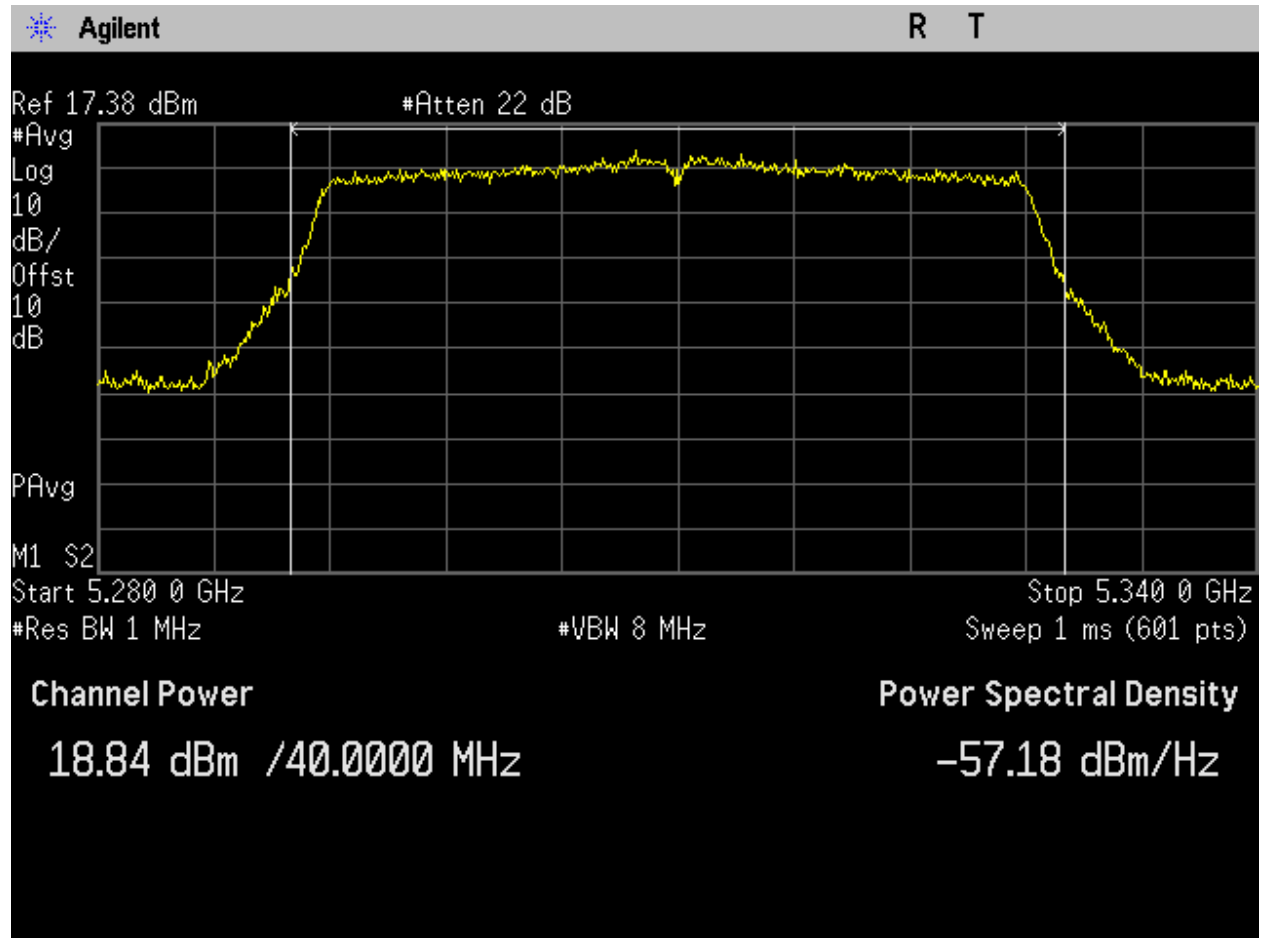


Figure 140: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_Output Power_Port 2.

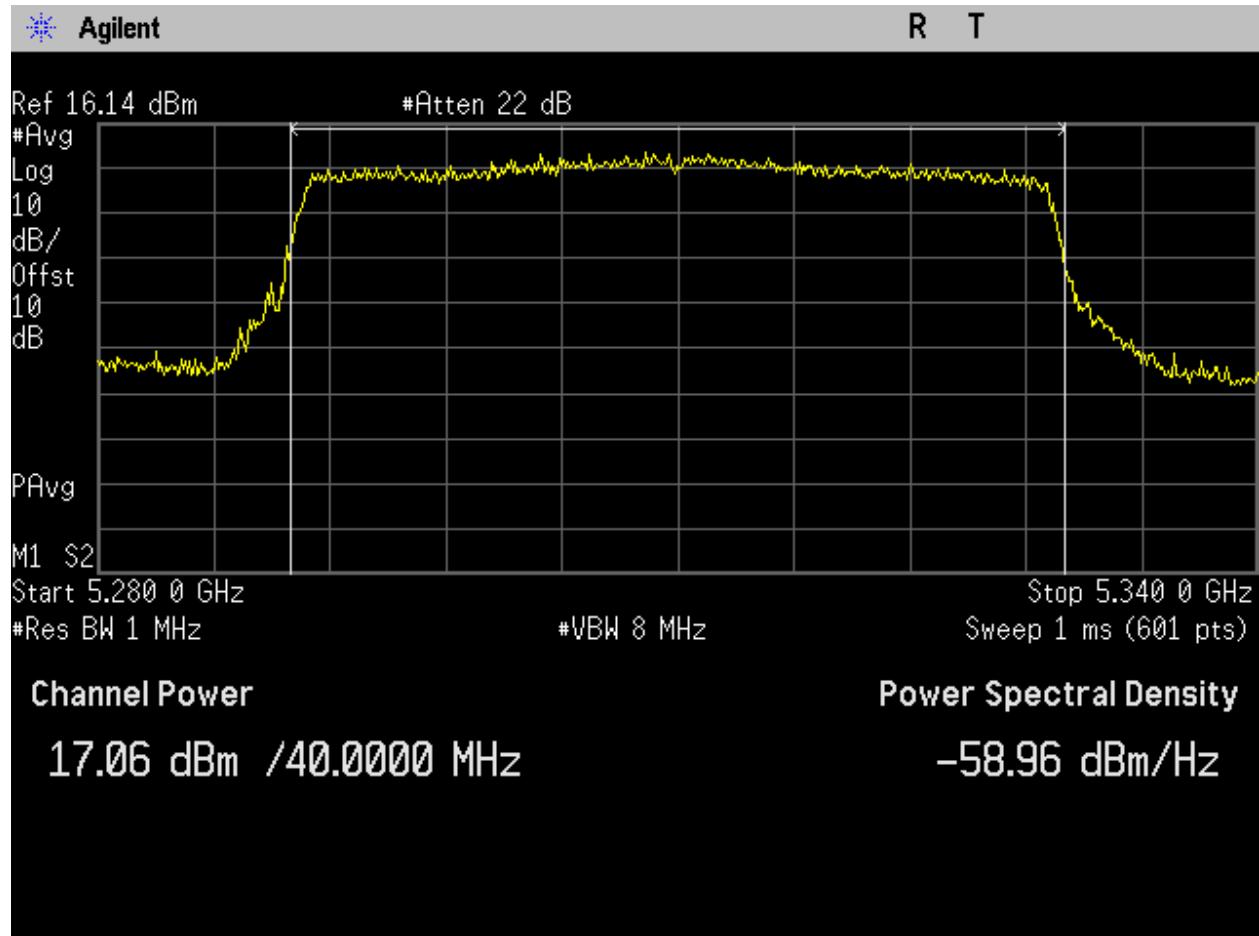


Figure 141: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_Output Power_Port 1.

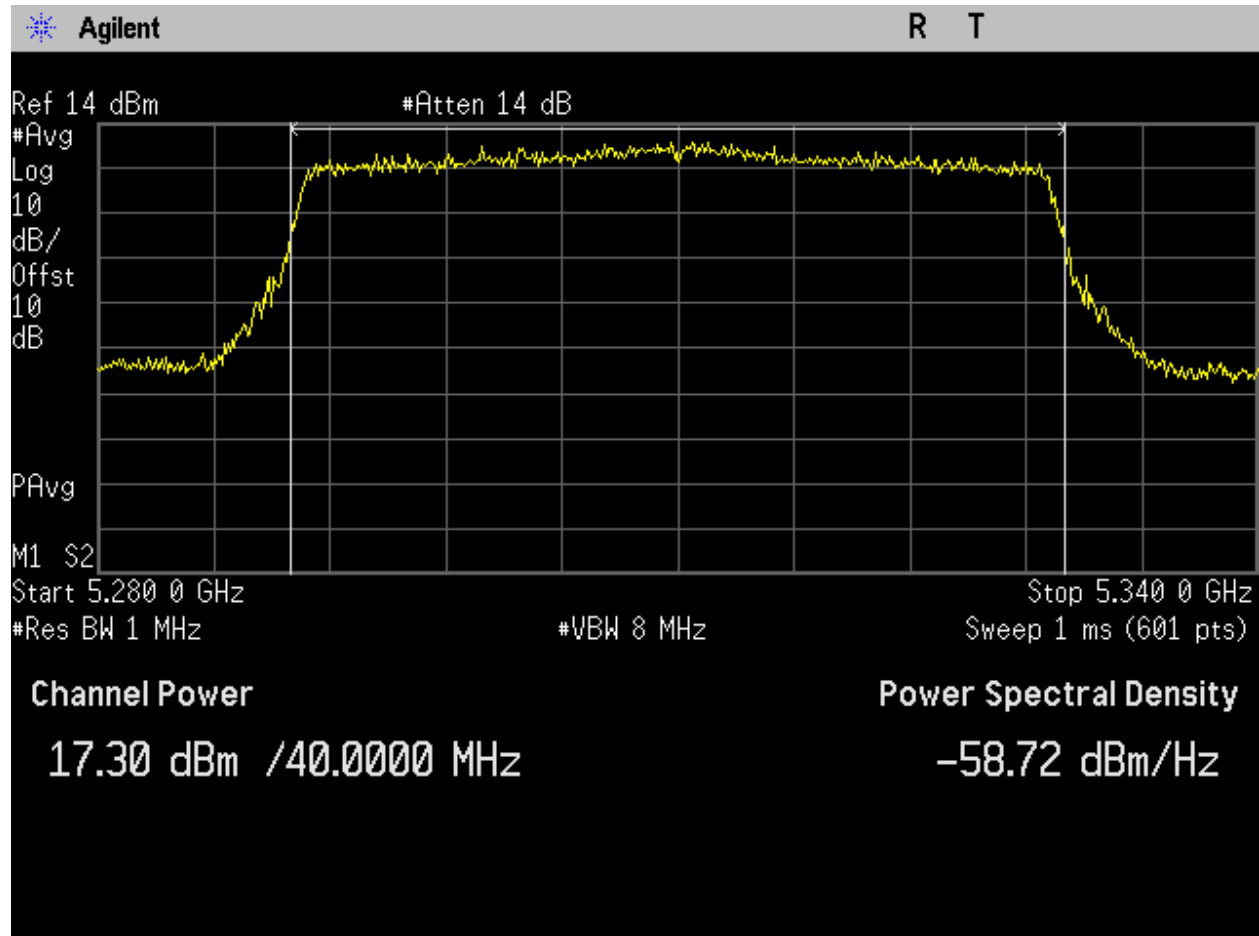


Figure 142: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_Output Power_Port 2.

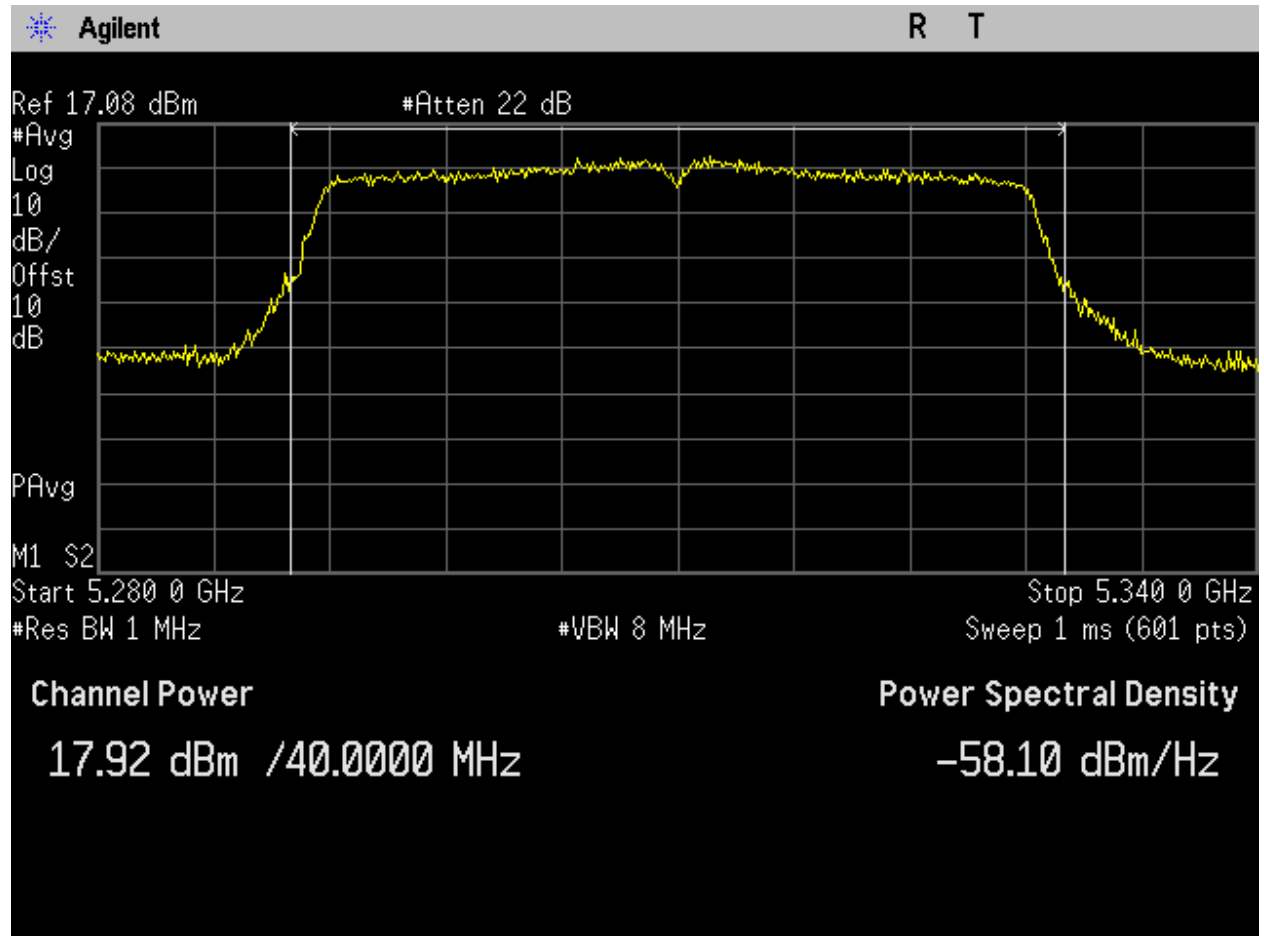


Figure 143: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Output Power_Port 1.

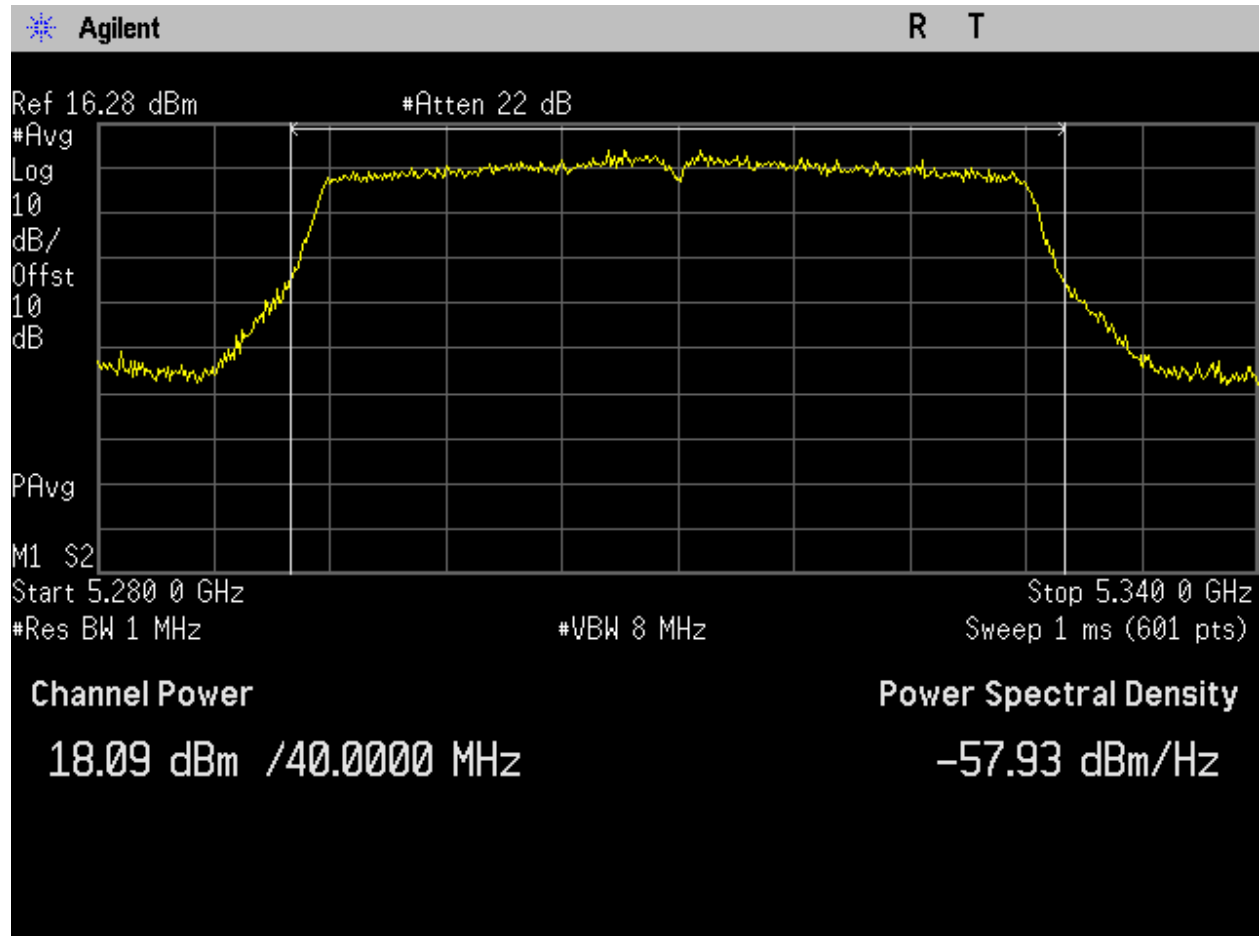


Figure 144: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Output Power_Port 2.

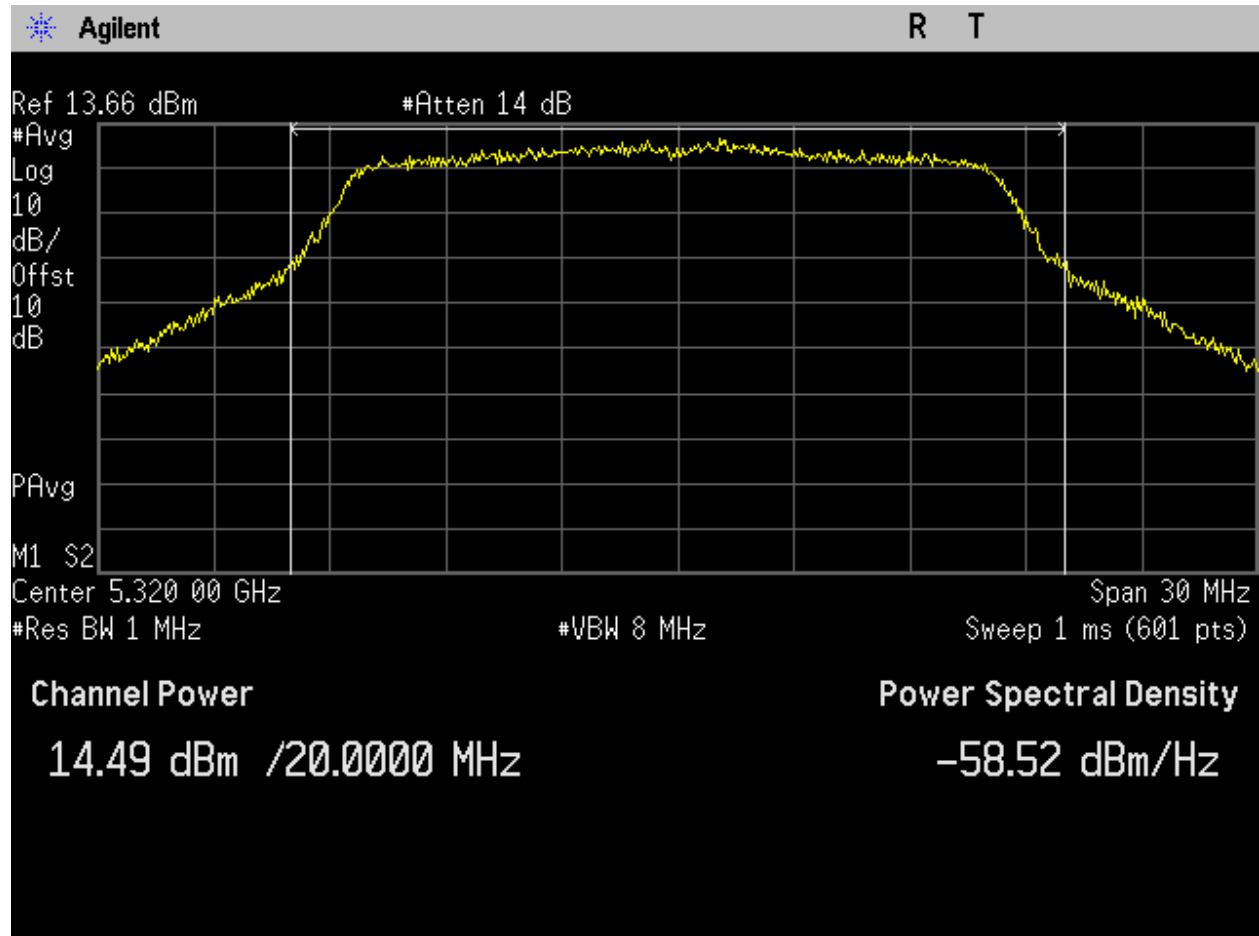


Figure 145: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_Output Power_Port 1.

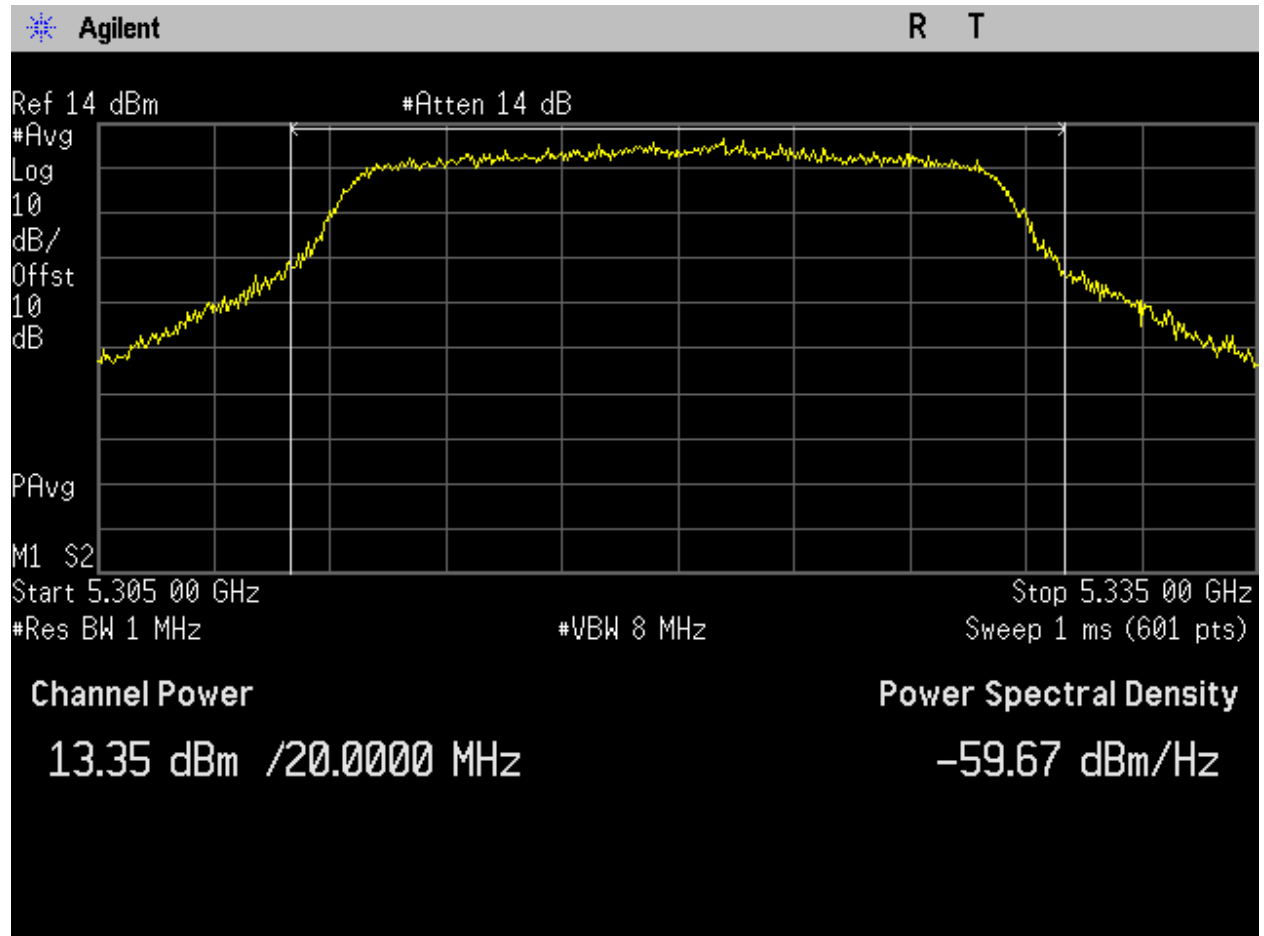


Figure 146: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_Output Power_Port 2.

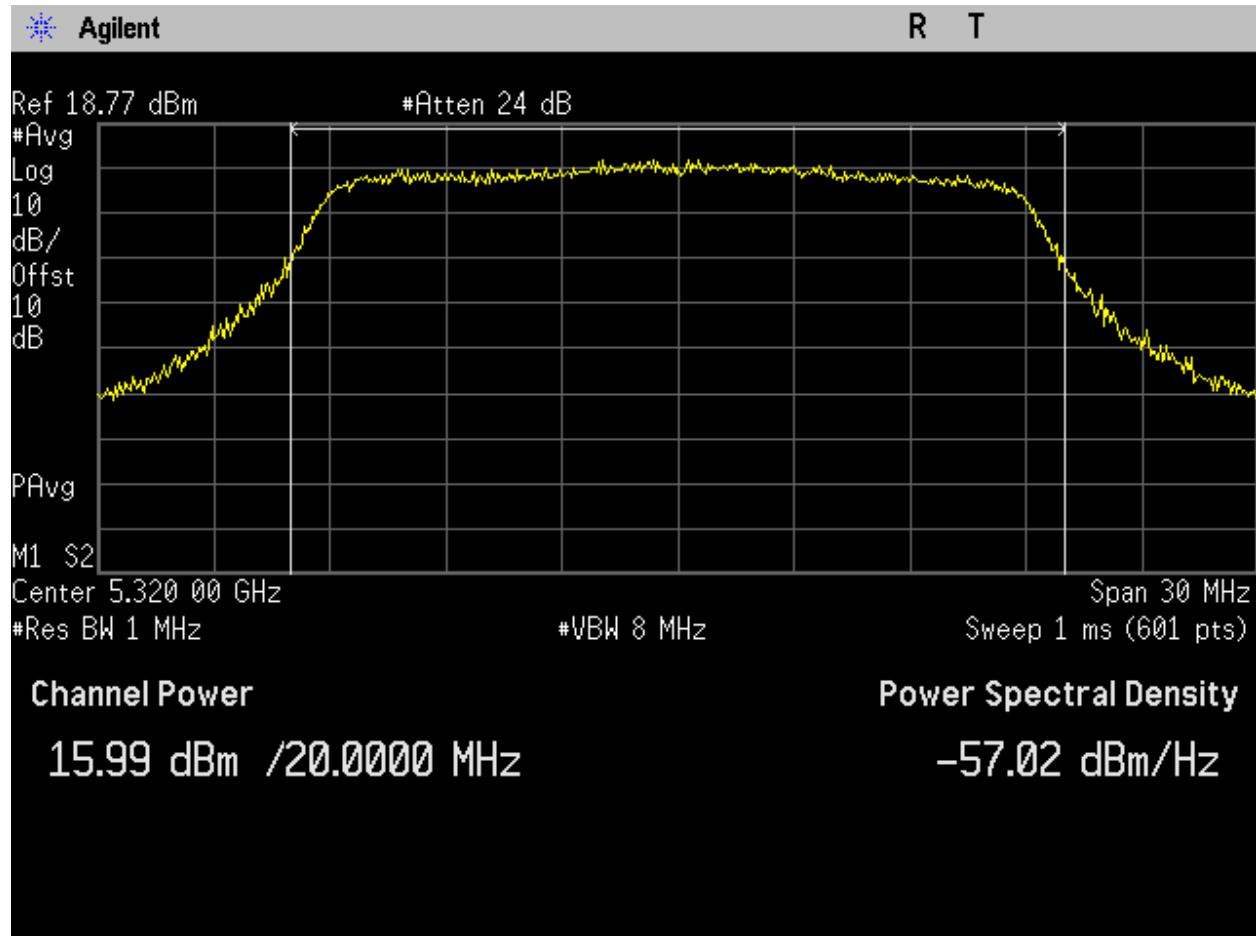


Figure 147: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_Output Power_Port 1.

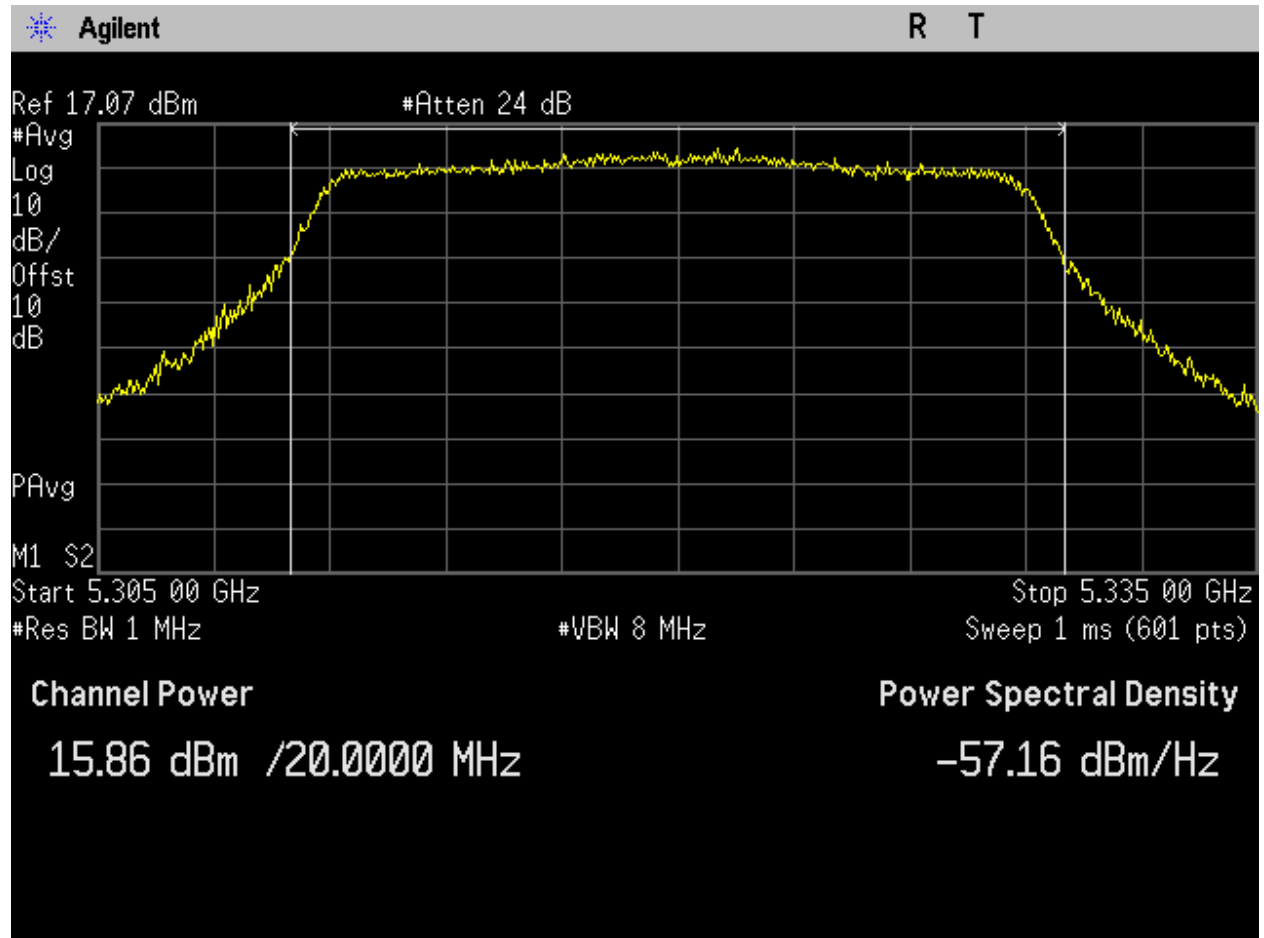


Figure 148: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_Output Power_Port 2.

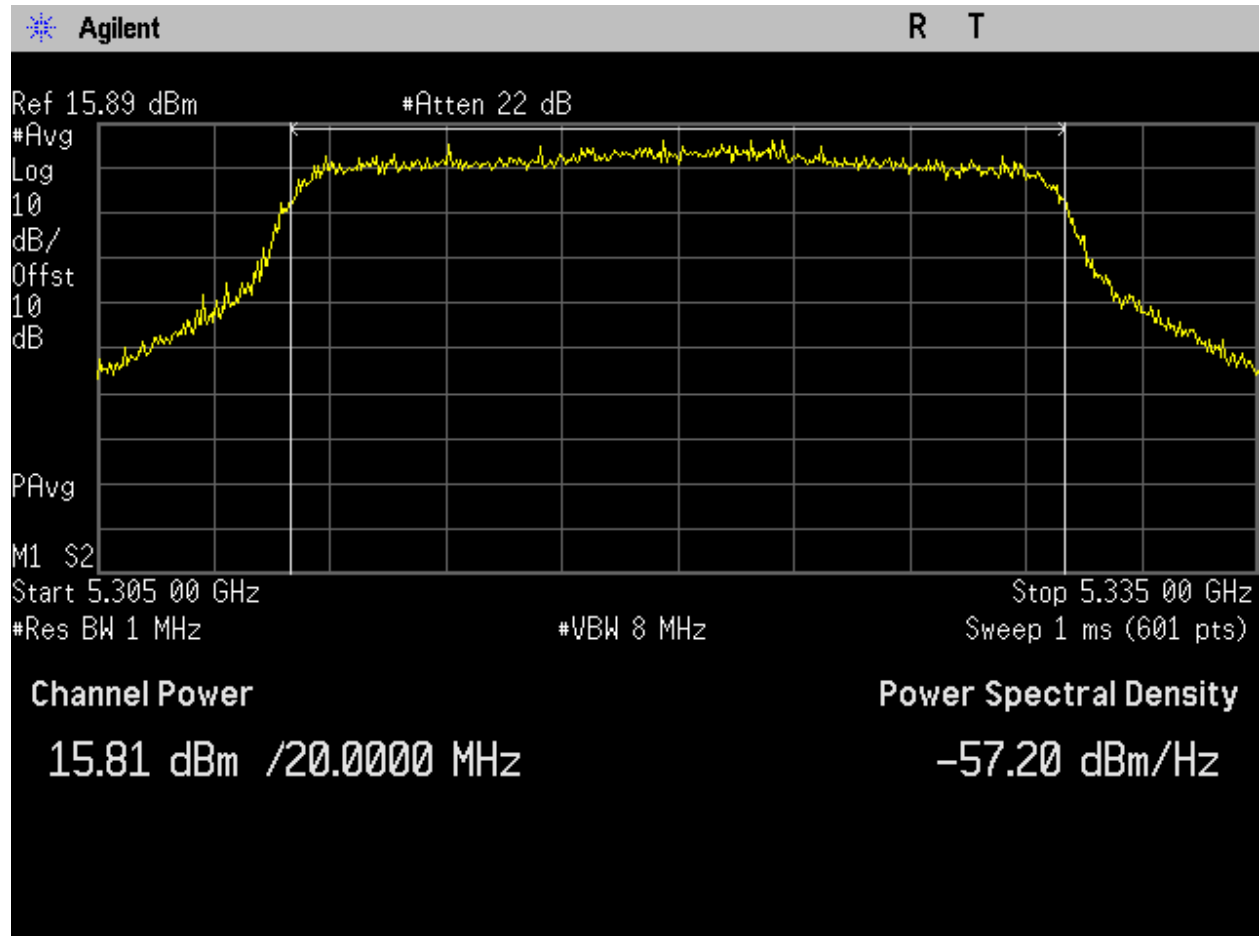


Figure 149: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_Output Power_Port 1.

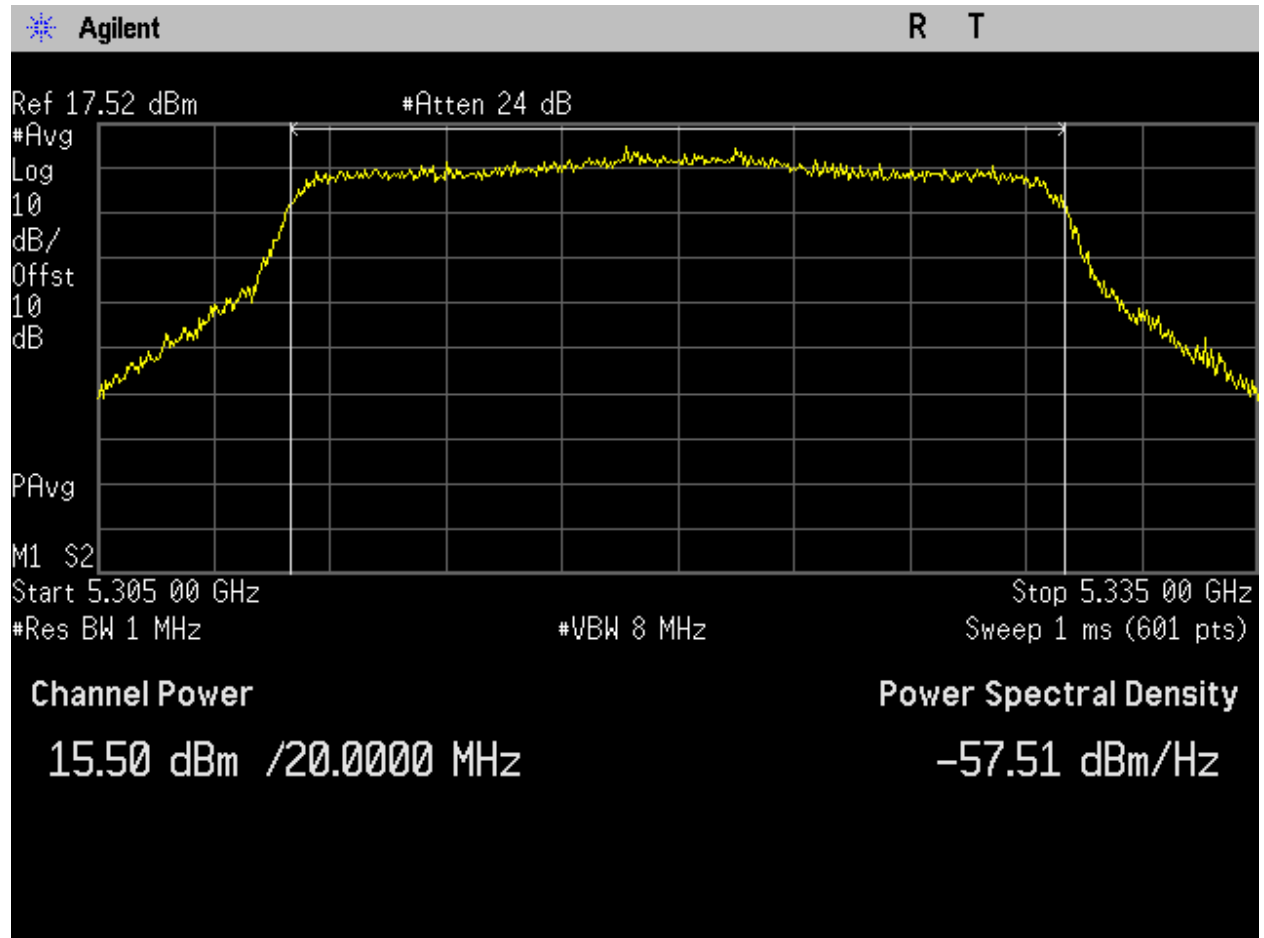


Figure 150: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_Output Power_Port 2.

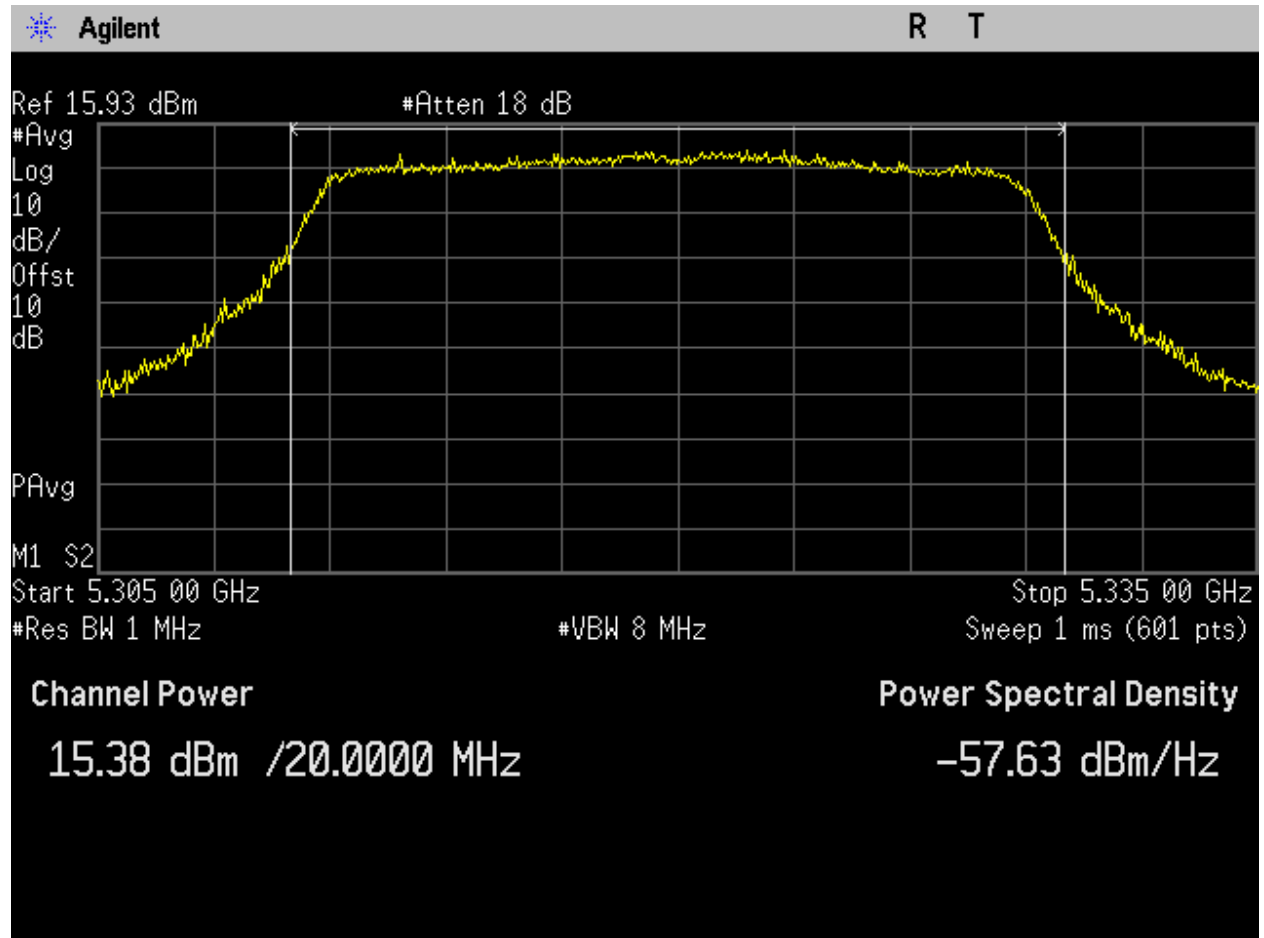


Figure 151: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Output Power_Port 1.

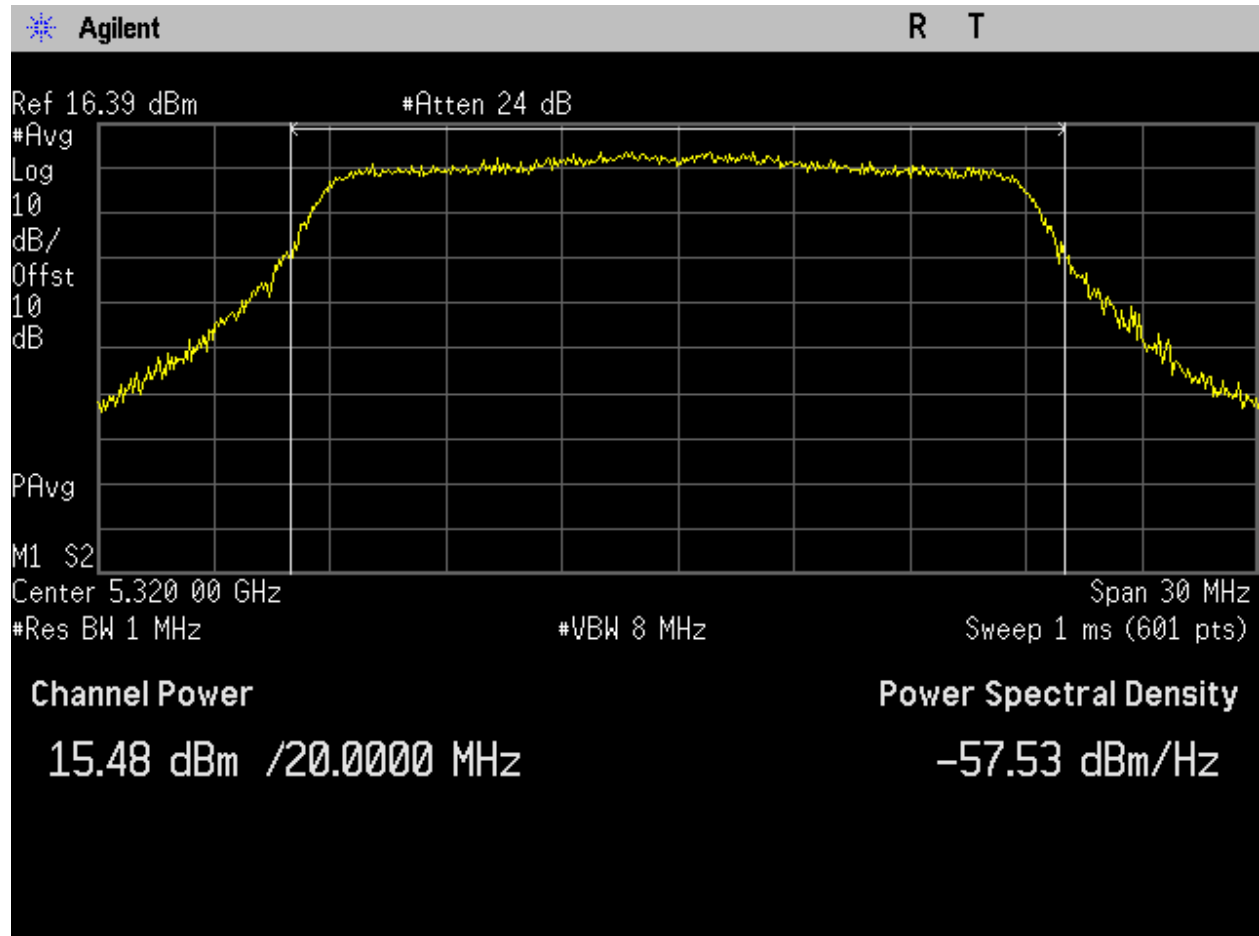


Figure 152: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Output Power_Port 2.

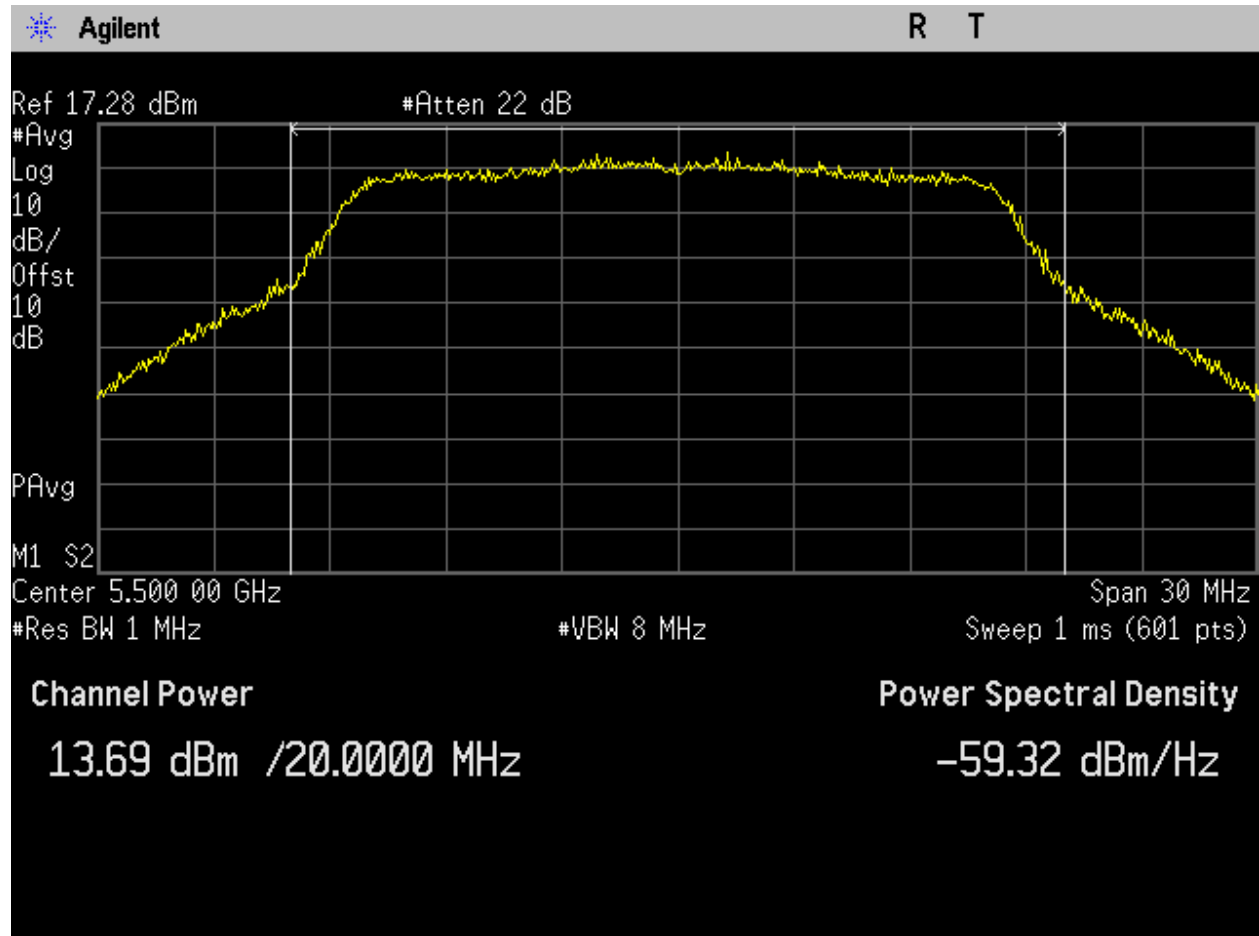


Figure 153: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_Output Power_Port 1.

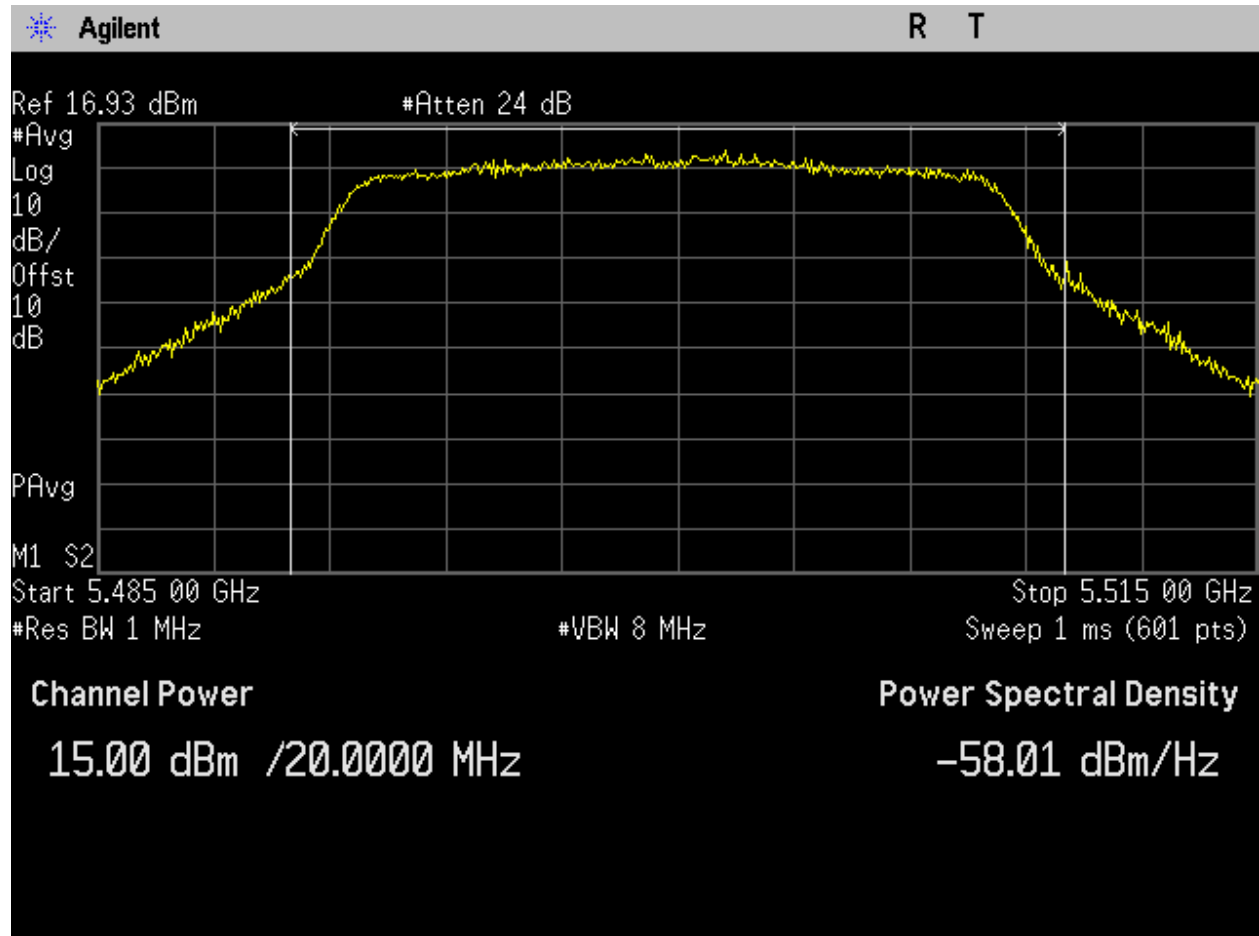


Figure 154: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_Output Power_Port 2.

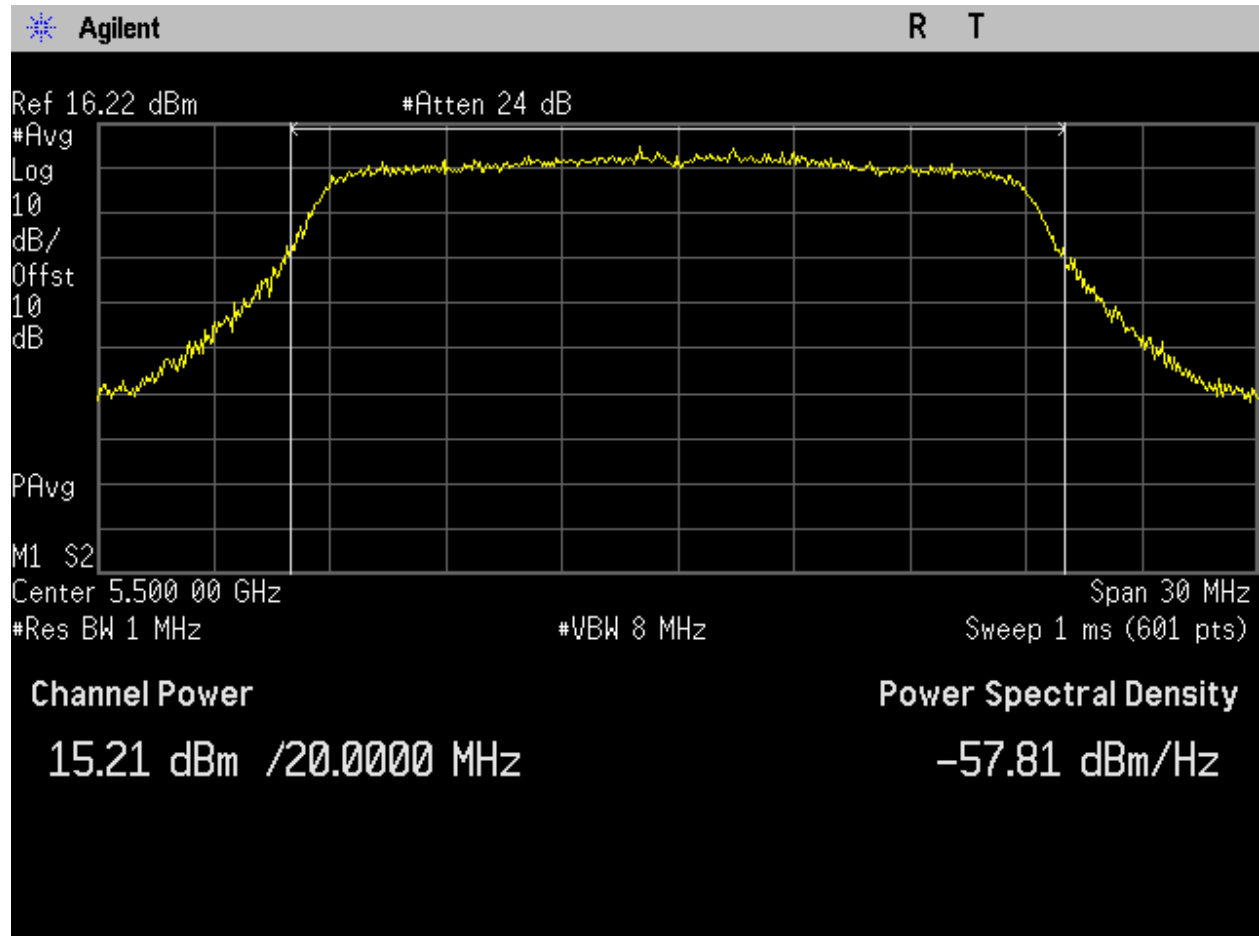


Figure 155: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_Output Power_Port 1.

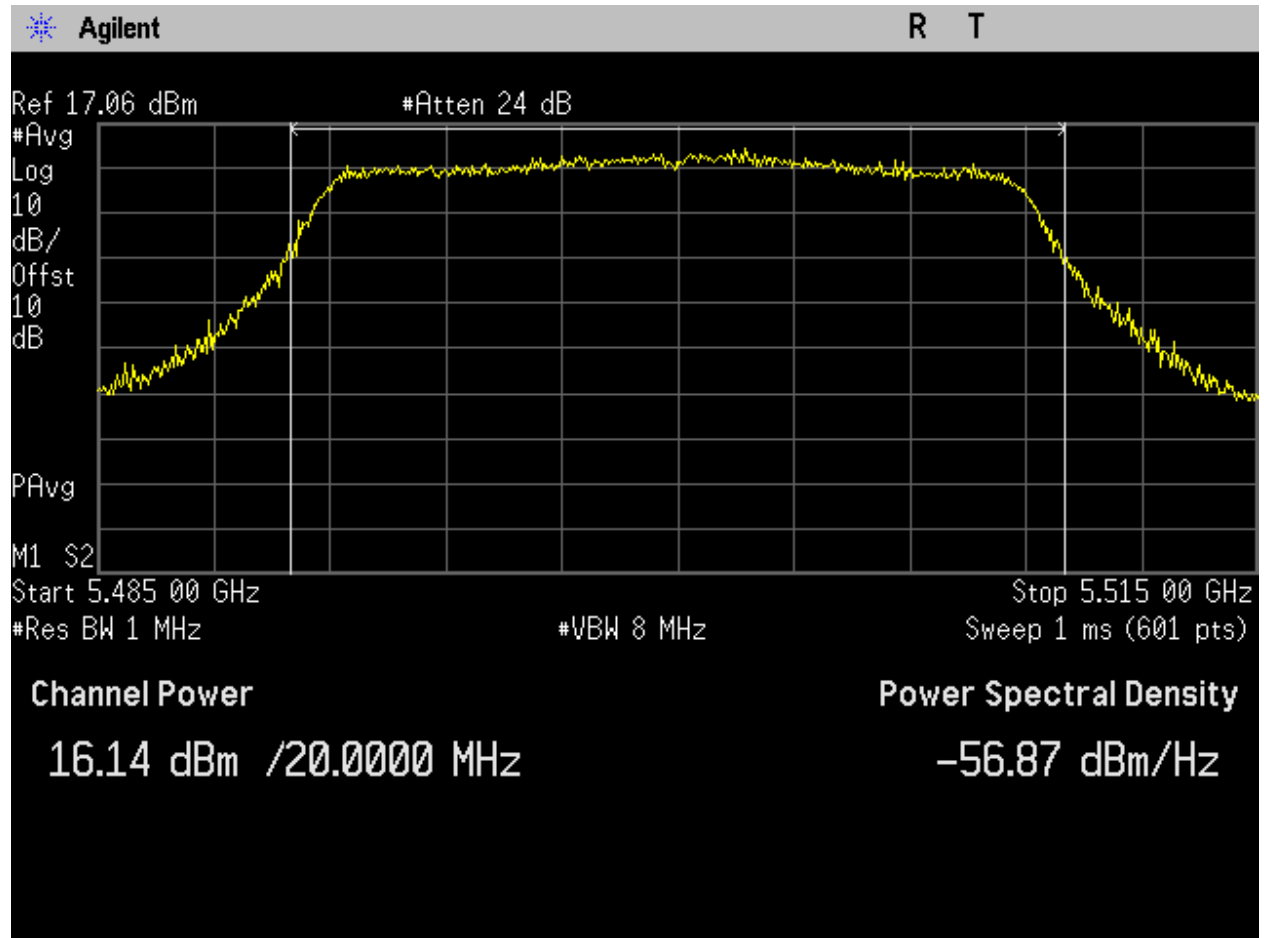


Figure 156: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_Output Power_Port 2.

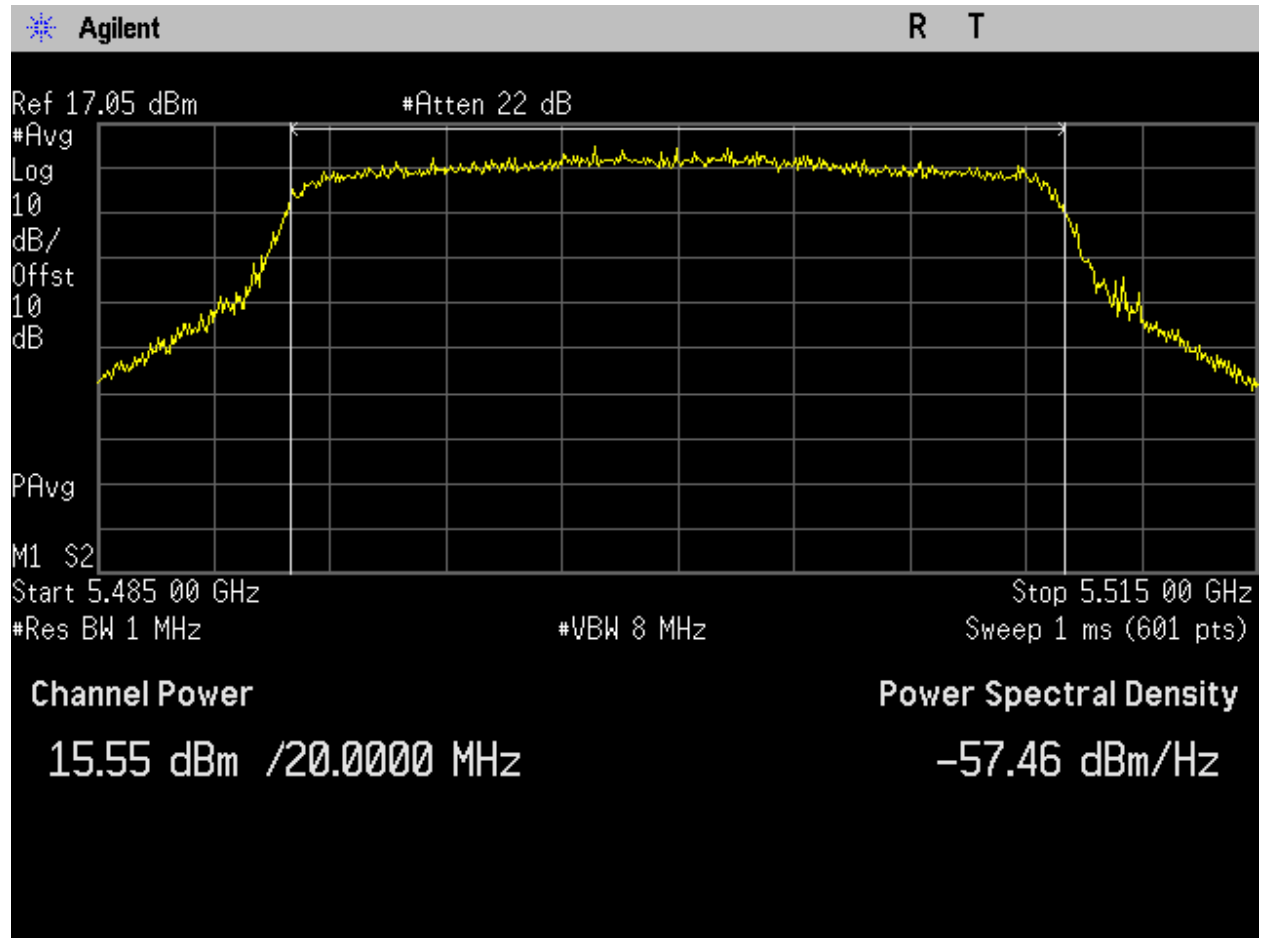


Figure 157: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_Output Power_Port 1.

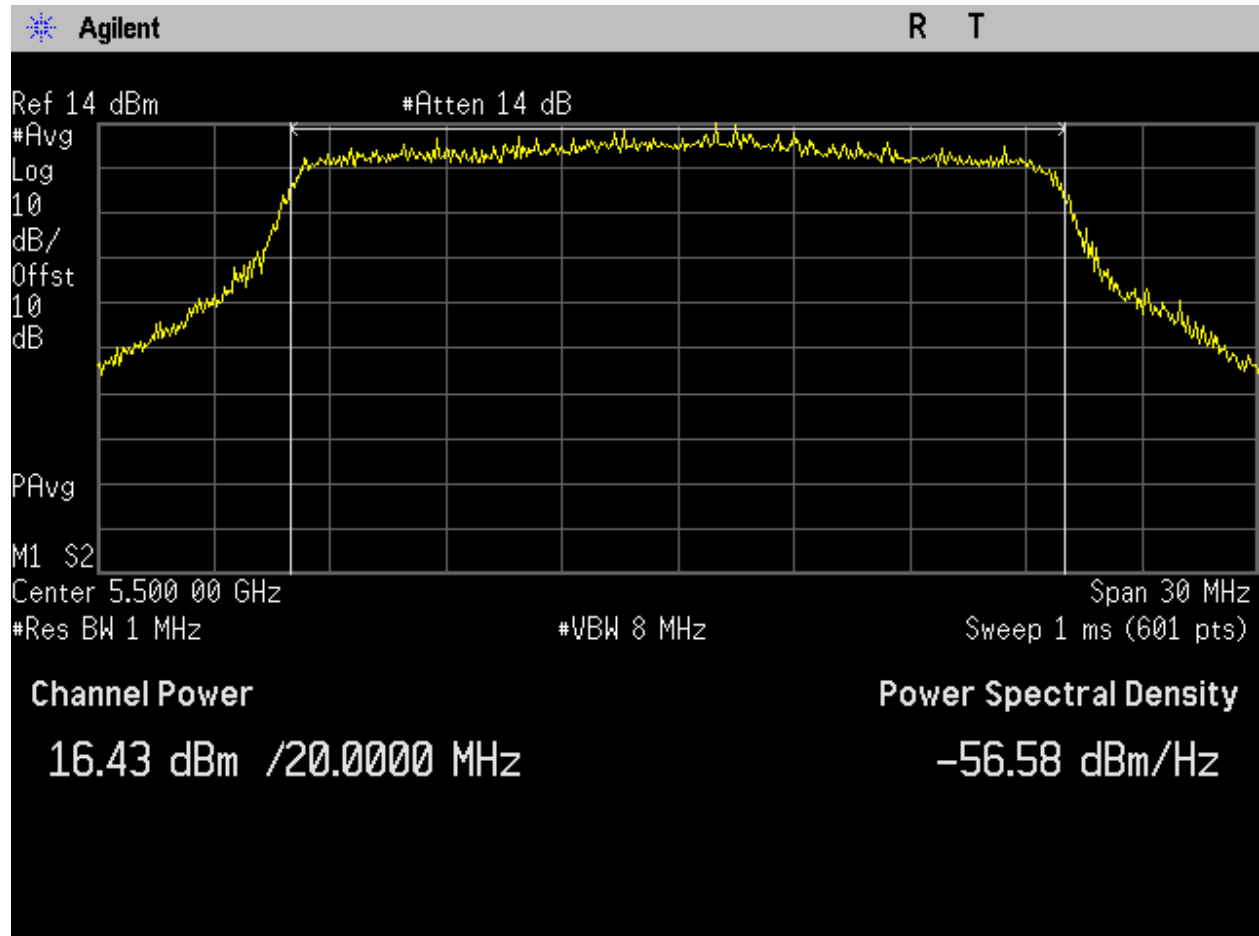


Figure 158: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_Output Power_Port 2.

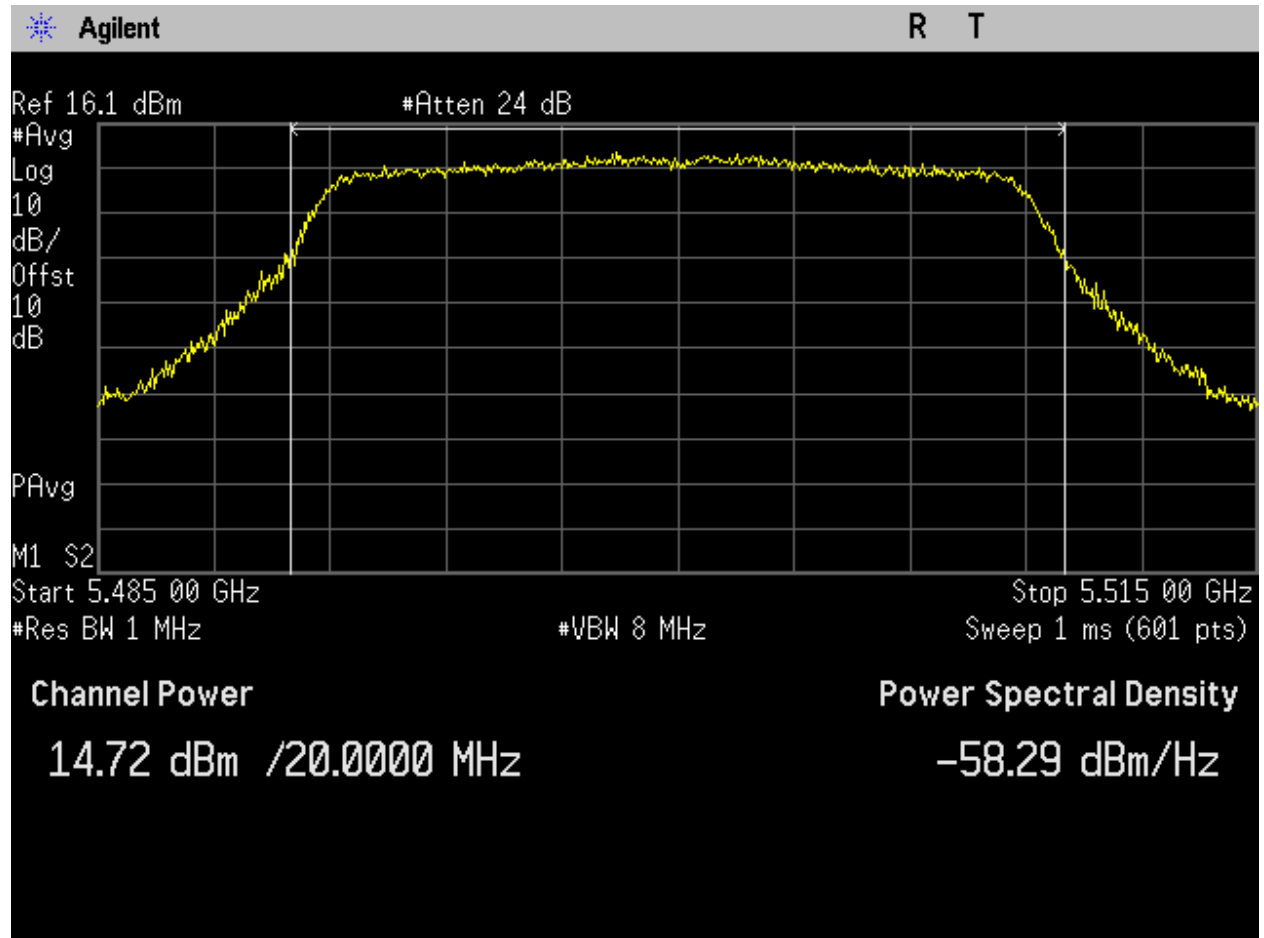


Figure 159: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_Output Power_Port 1.

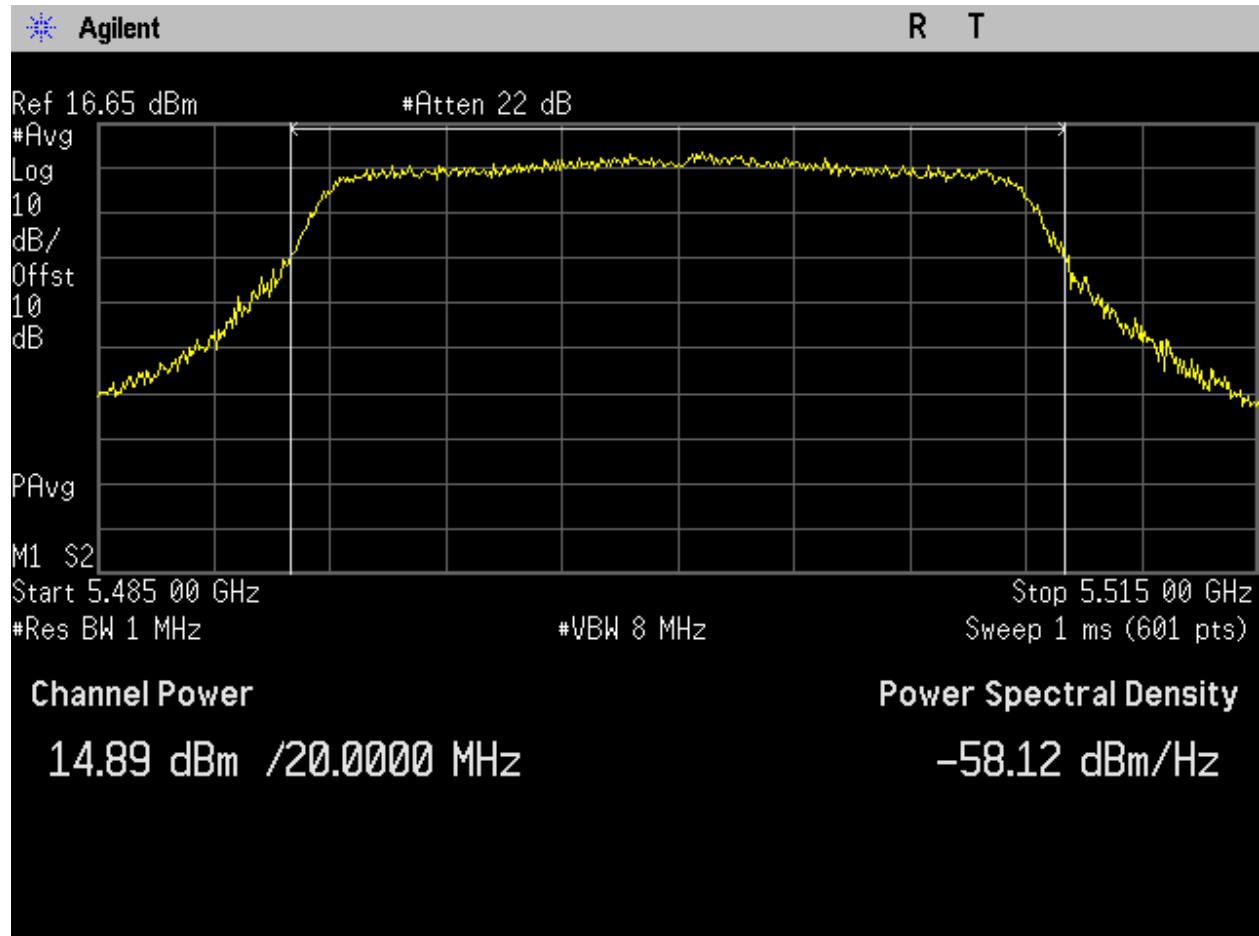


Figure 160: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_Output Power_Port 2.

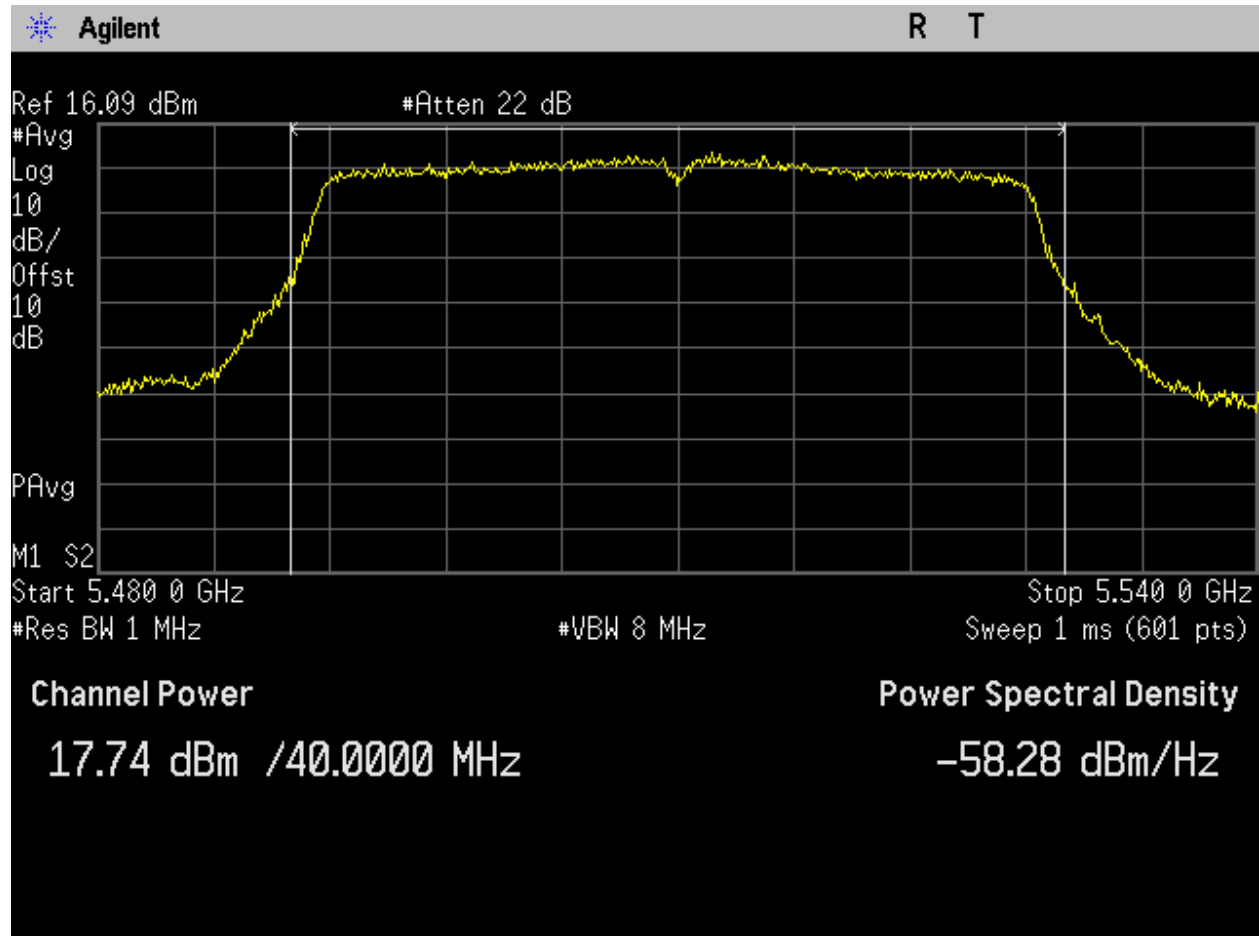


Figure 161: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_Output Power_Port 1.

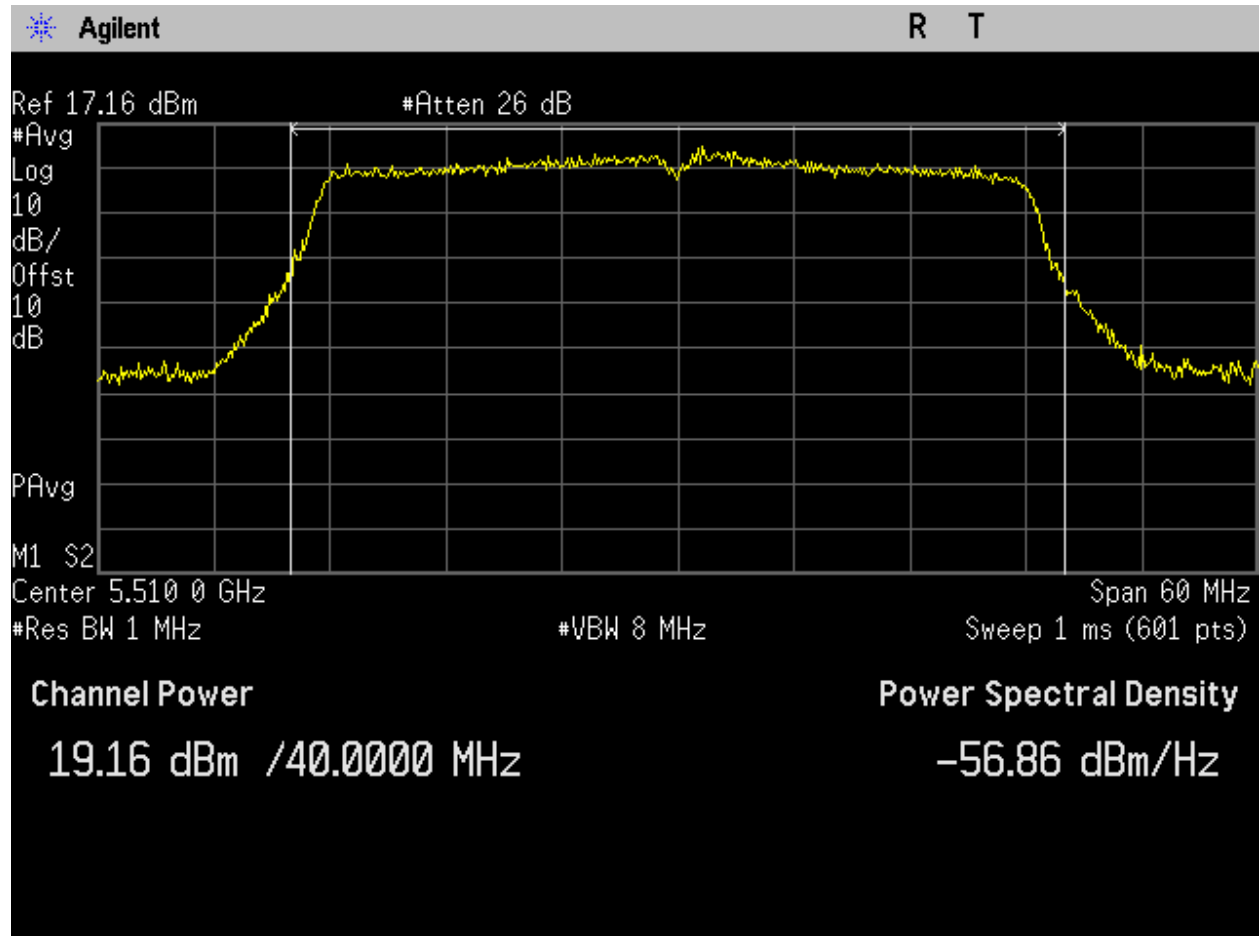


Figure 162: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_Output Power_Port 2.

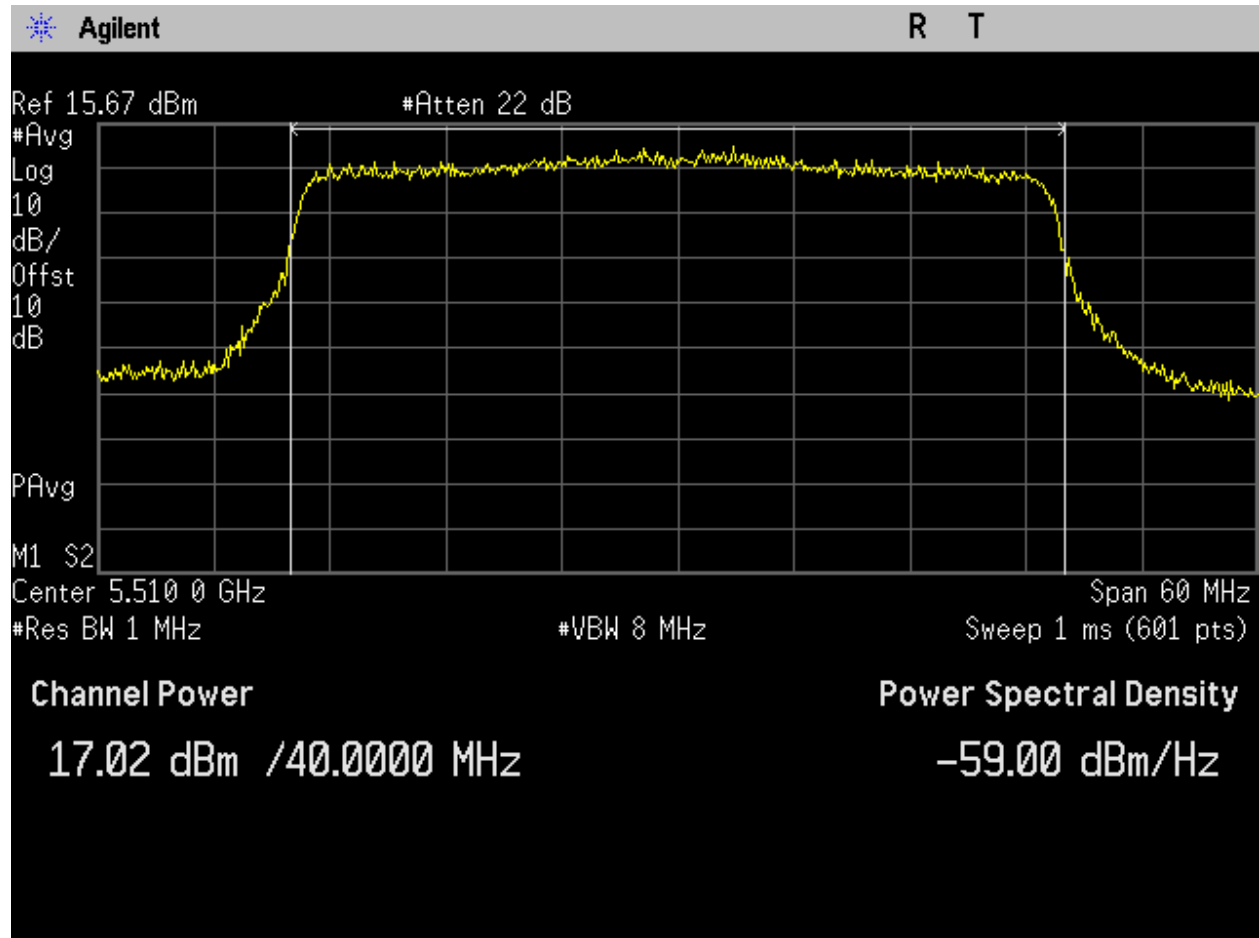


Figure 163: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_Output Power_Port 1.

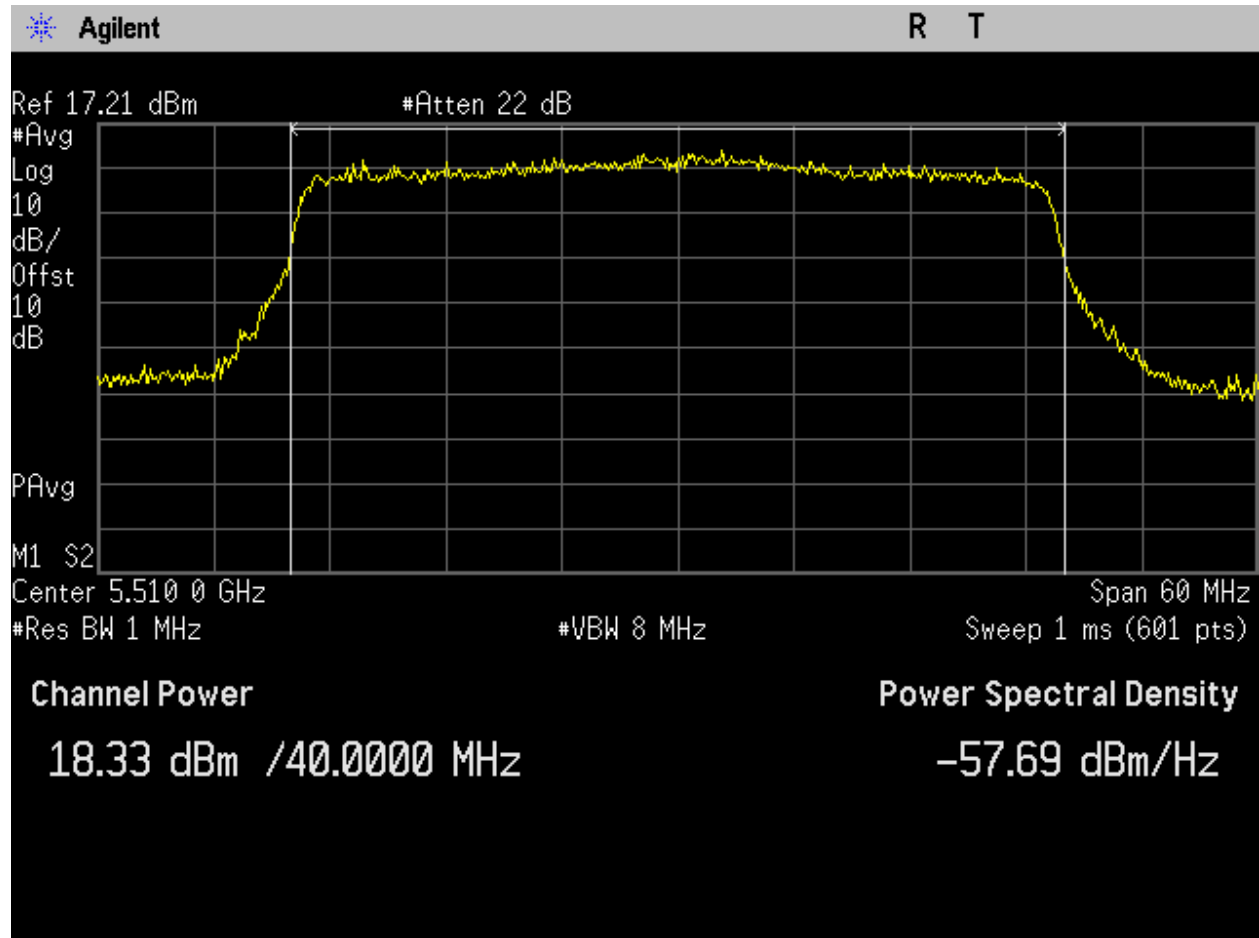


Figure 164: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_Output Power_Port 2.

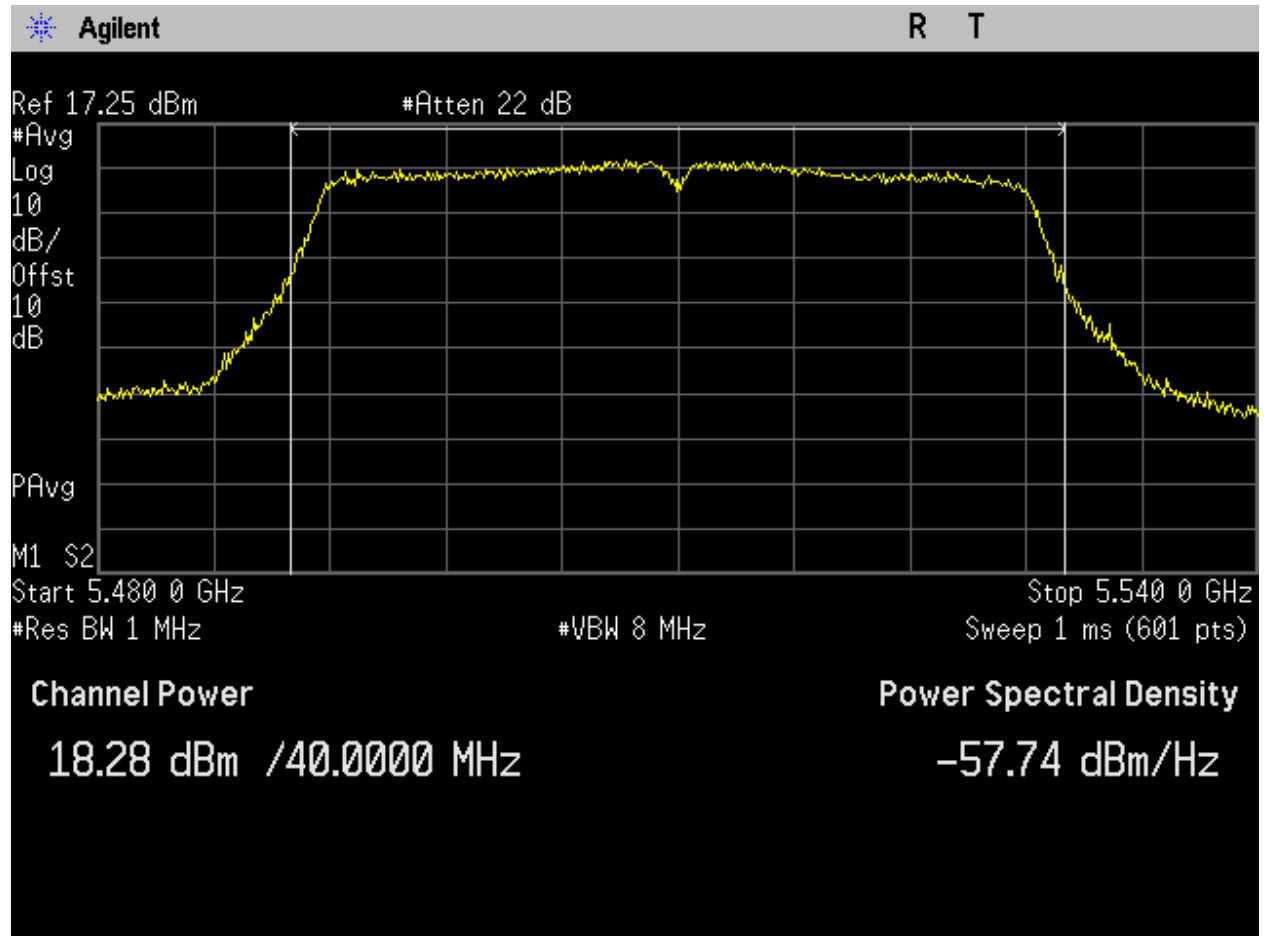


Figure 165: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_Output Power_Port 1.

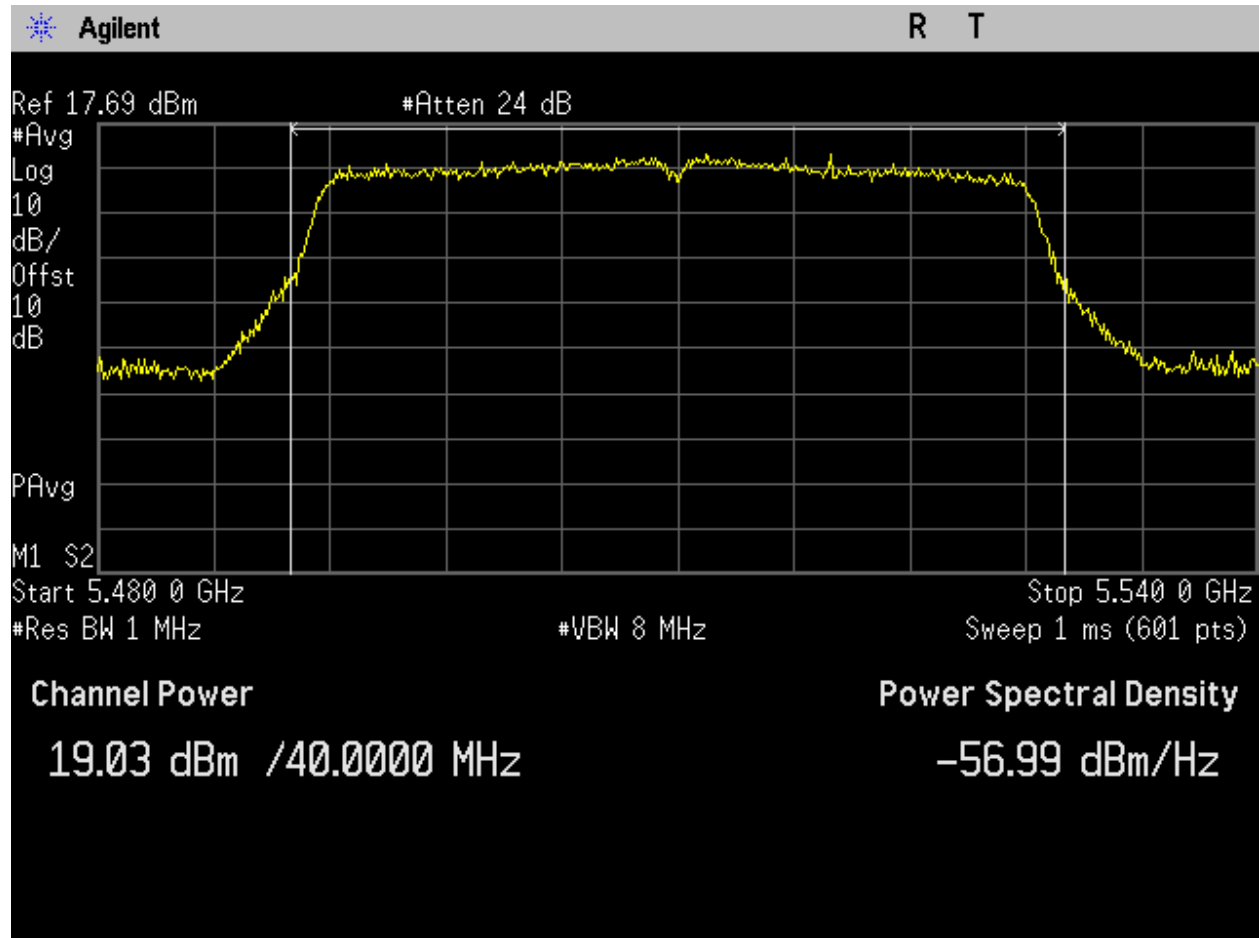


Figure 166: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_Output Power_Port 2.

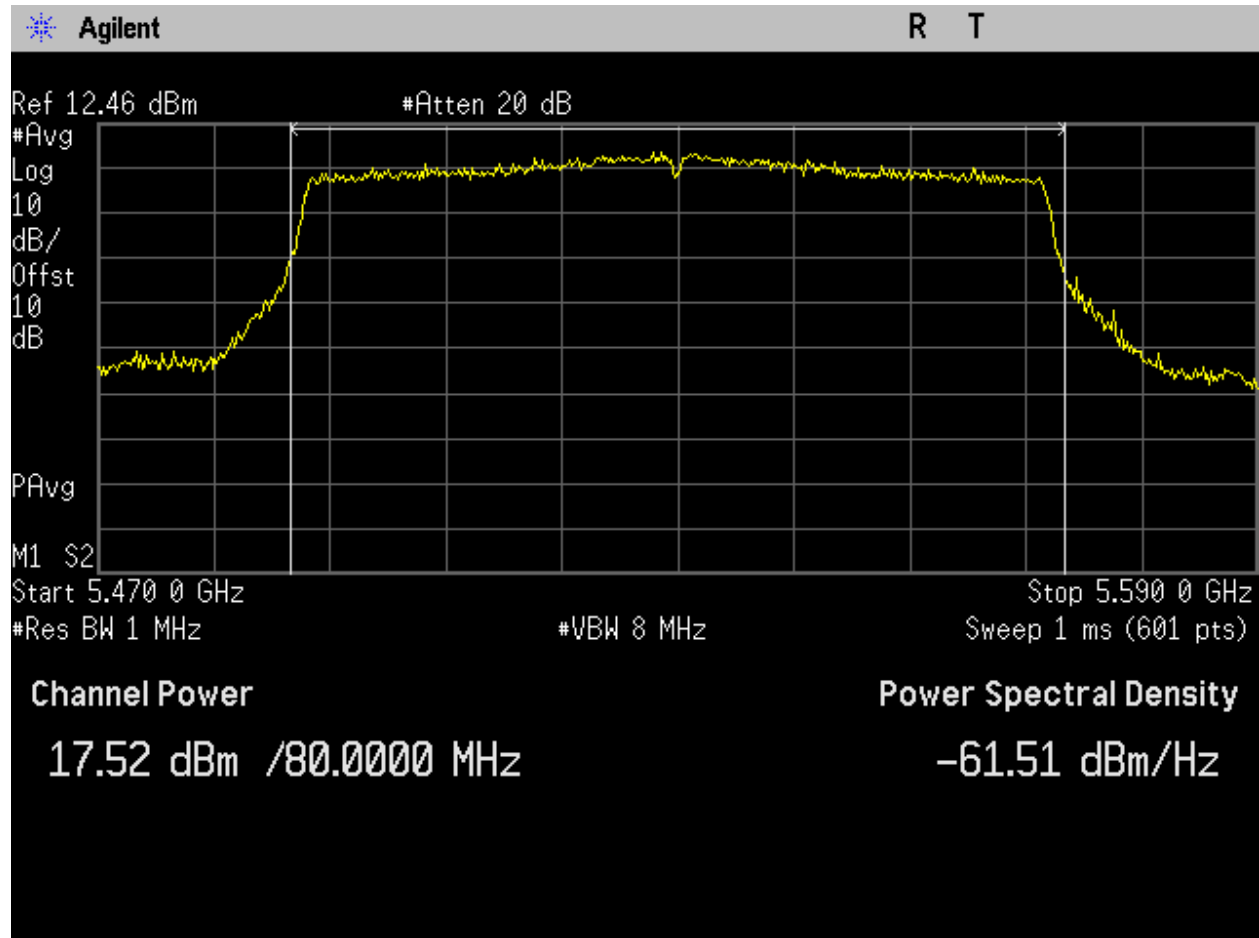


Figure 167: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_Output Power_Port 1.

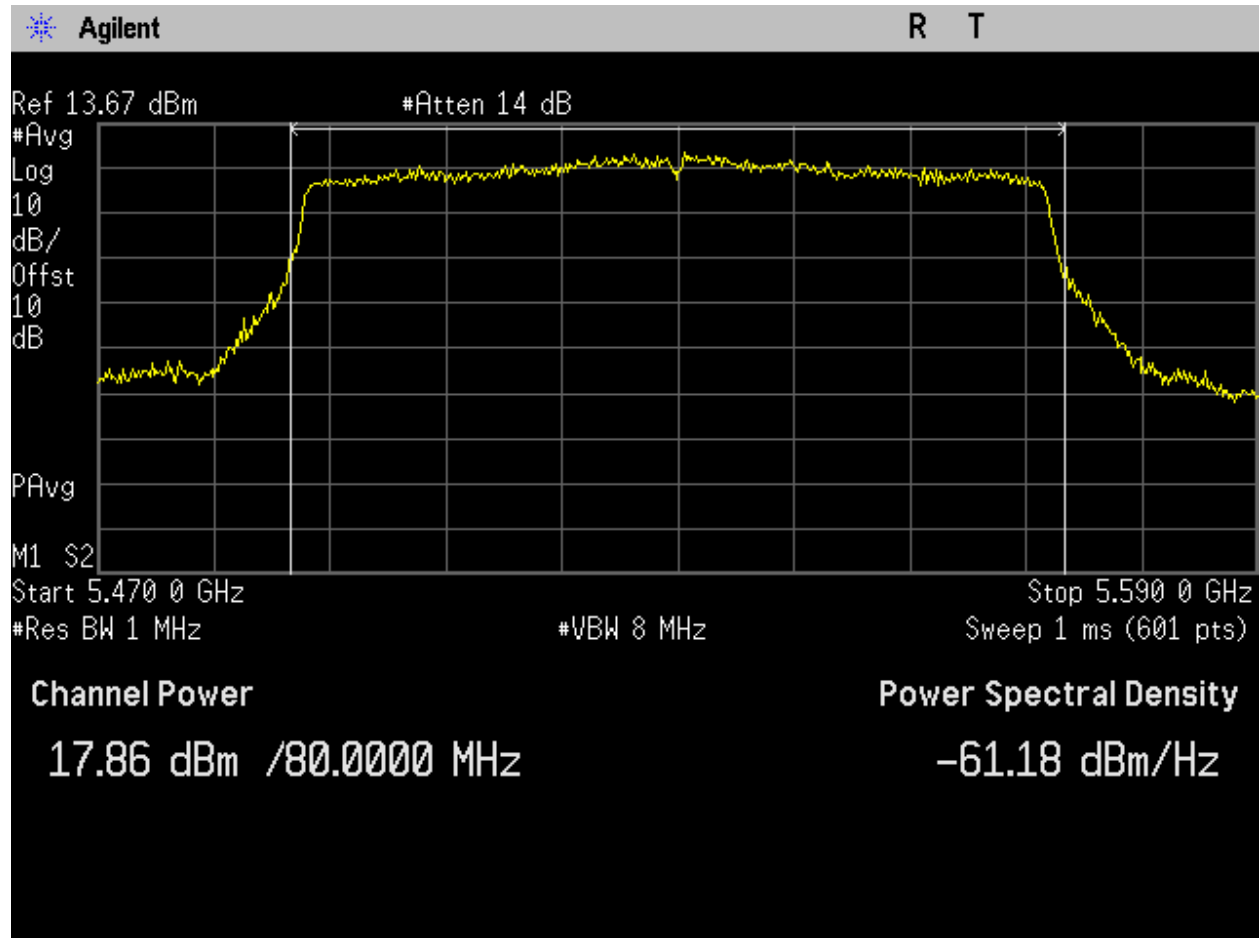


Figure 168: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_Output Power_Port 2.

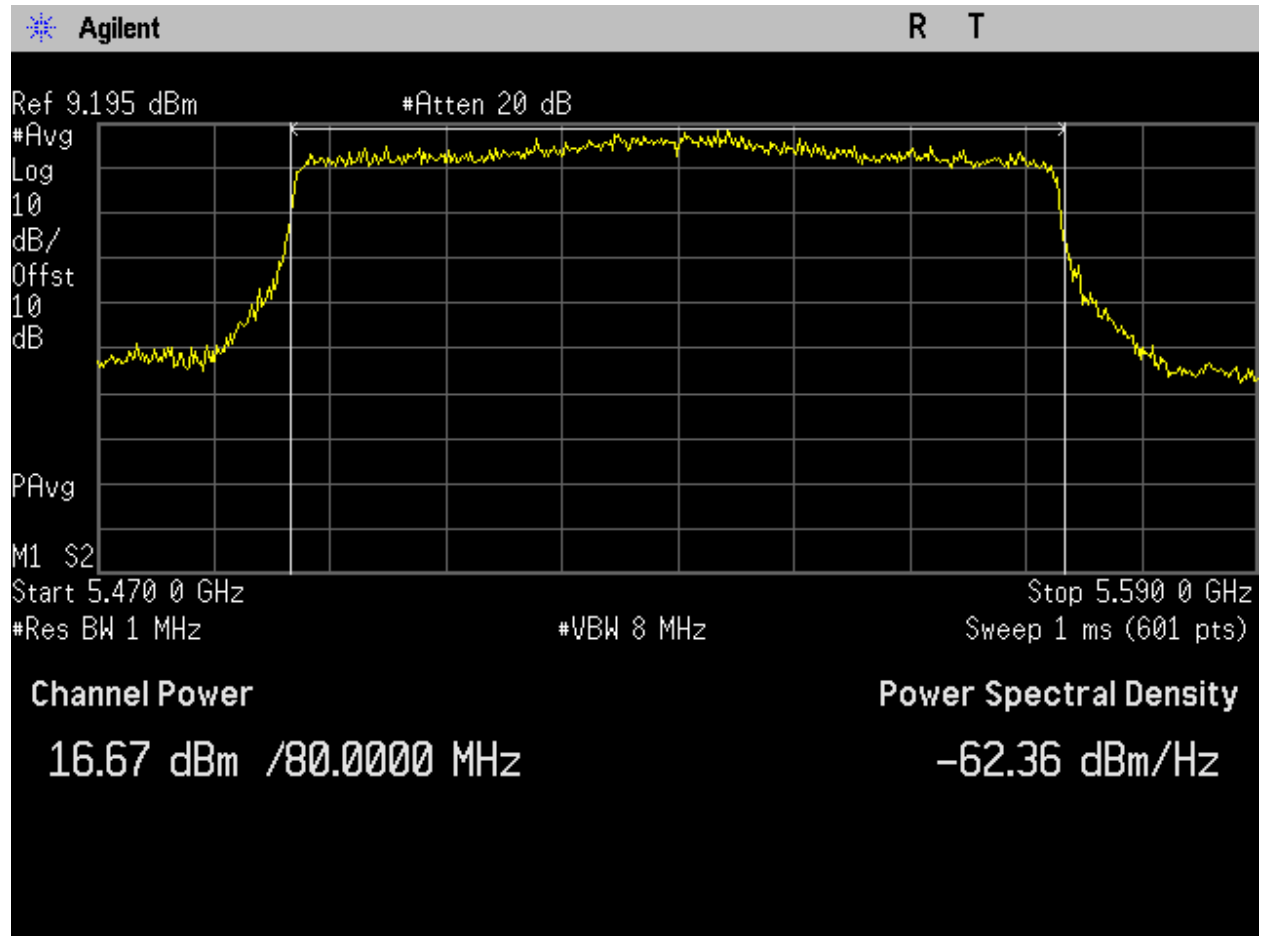


Figure 169: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_Output Power_Port 1.

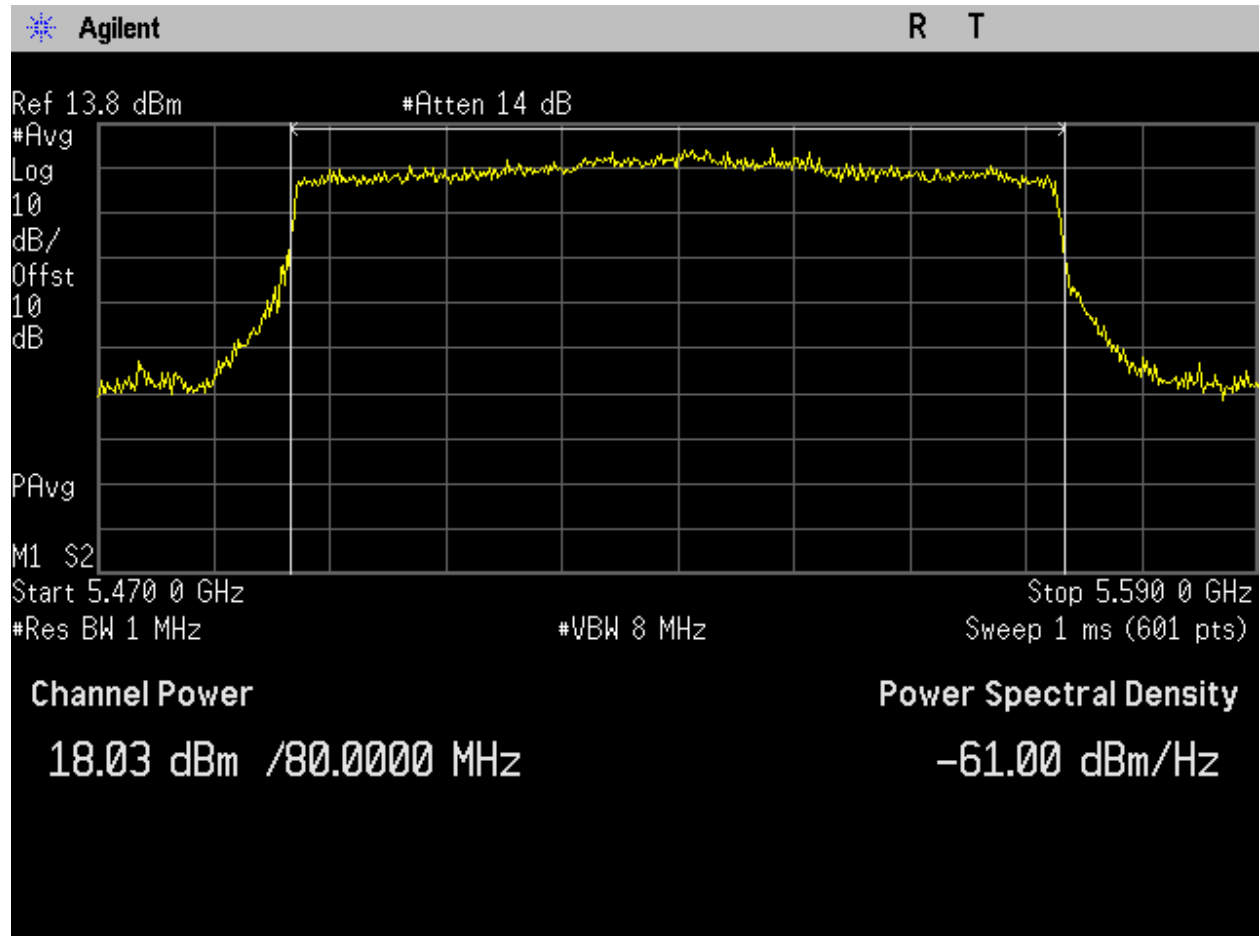


Figure 170: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_Output Power_Port 2.

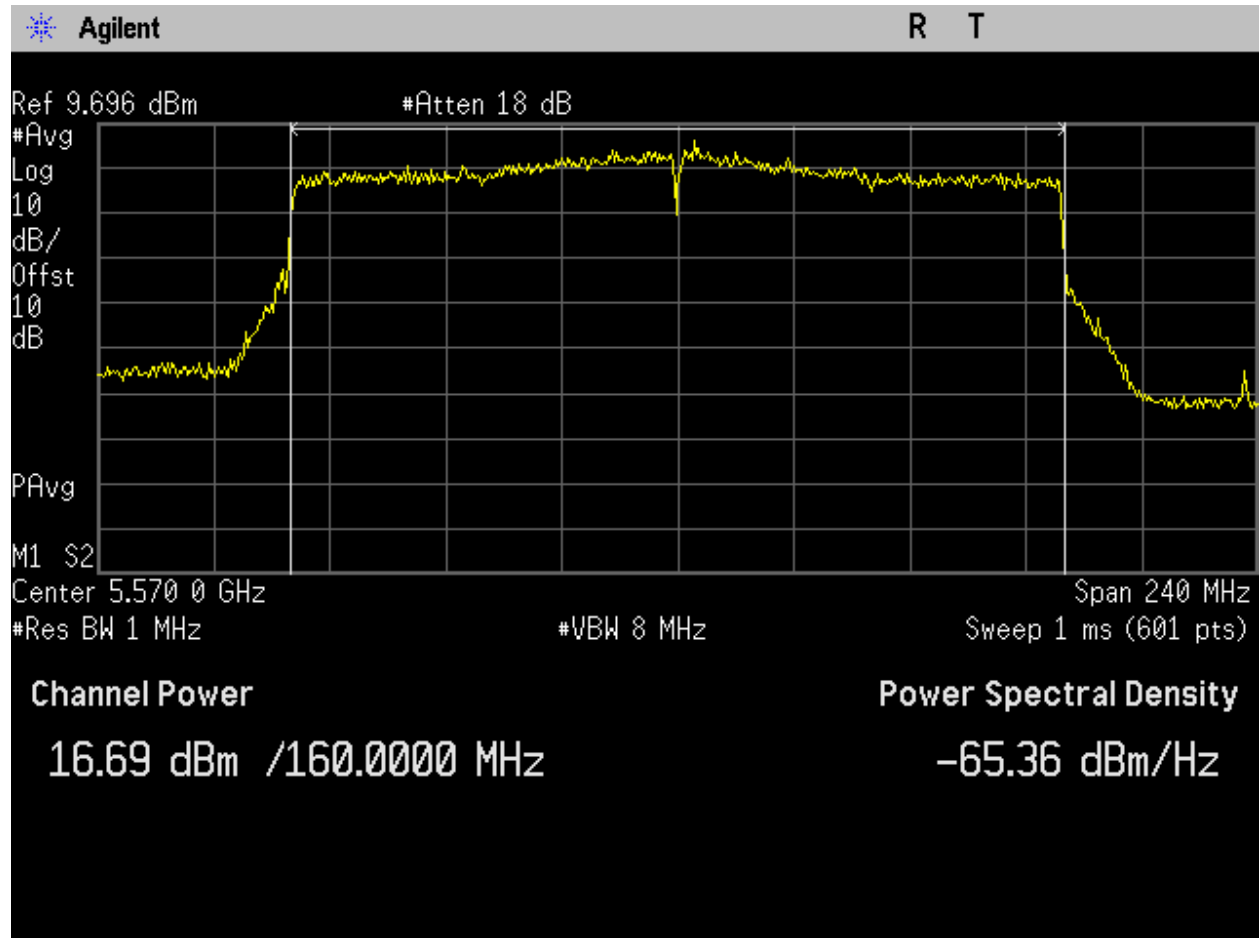


Figure 171: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_Output Power_Port 1.

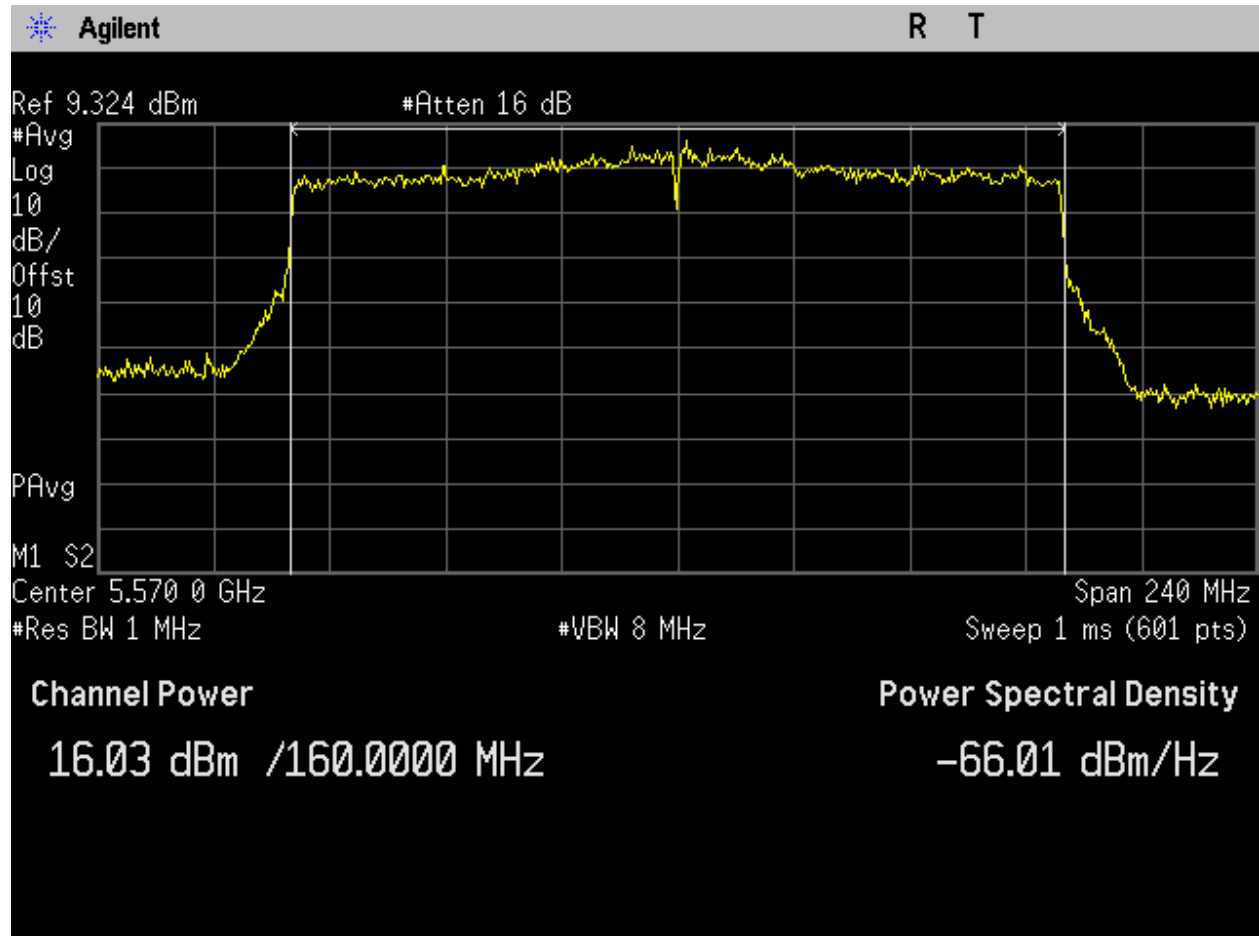


Figure 172: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_Output Power_Port 2.

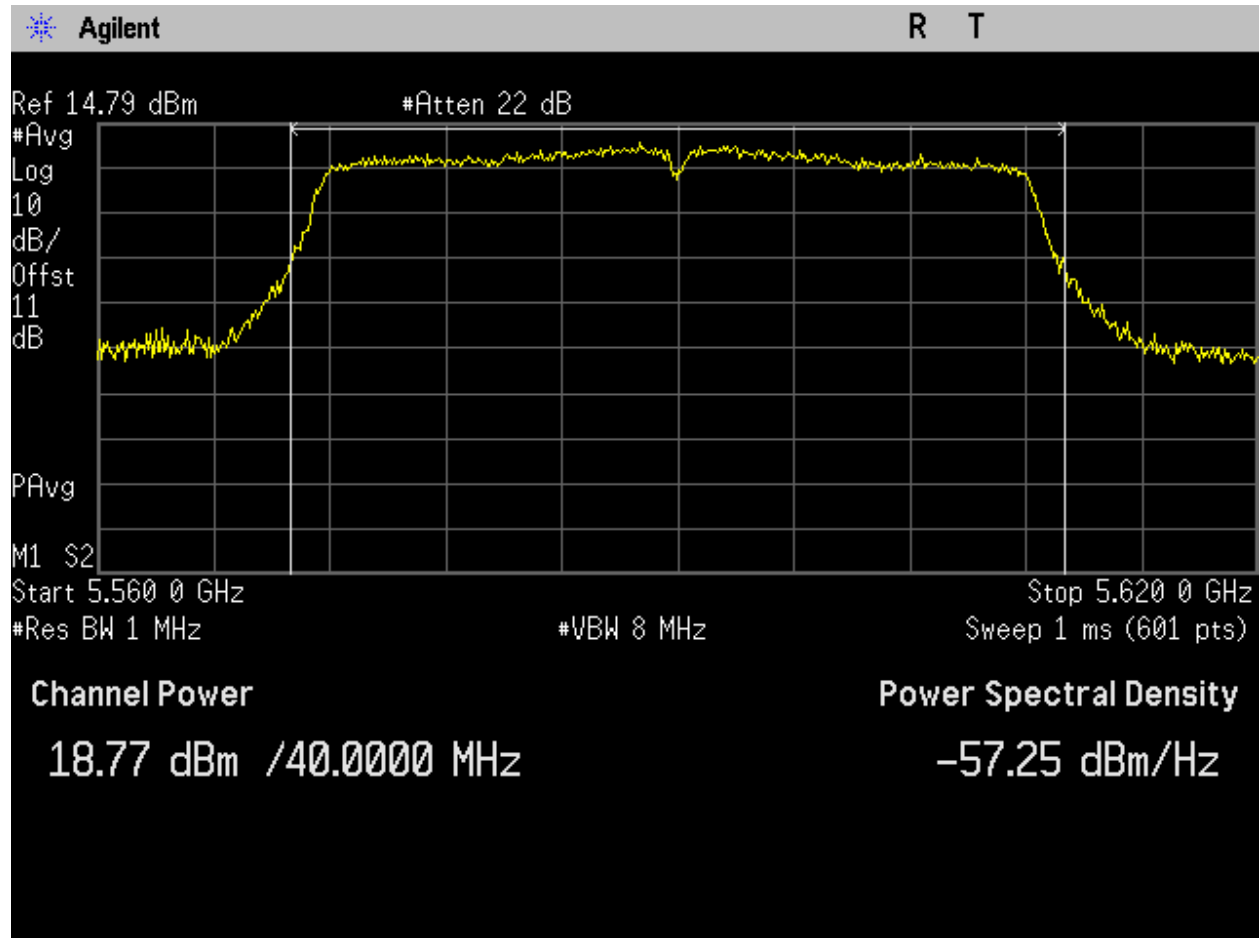


Figure 173: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ac-mode_Output Power_Port 1.

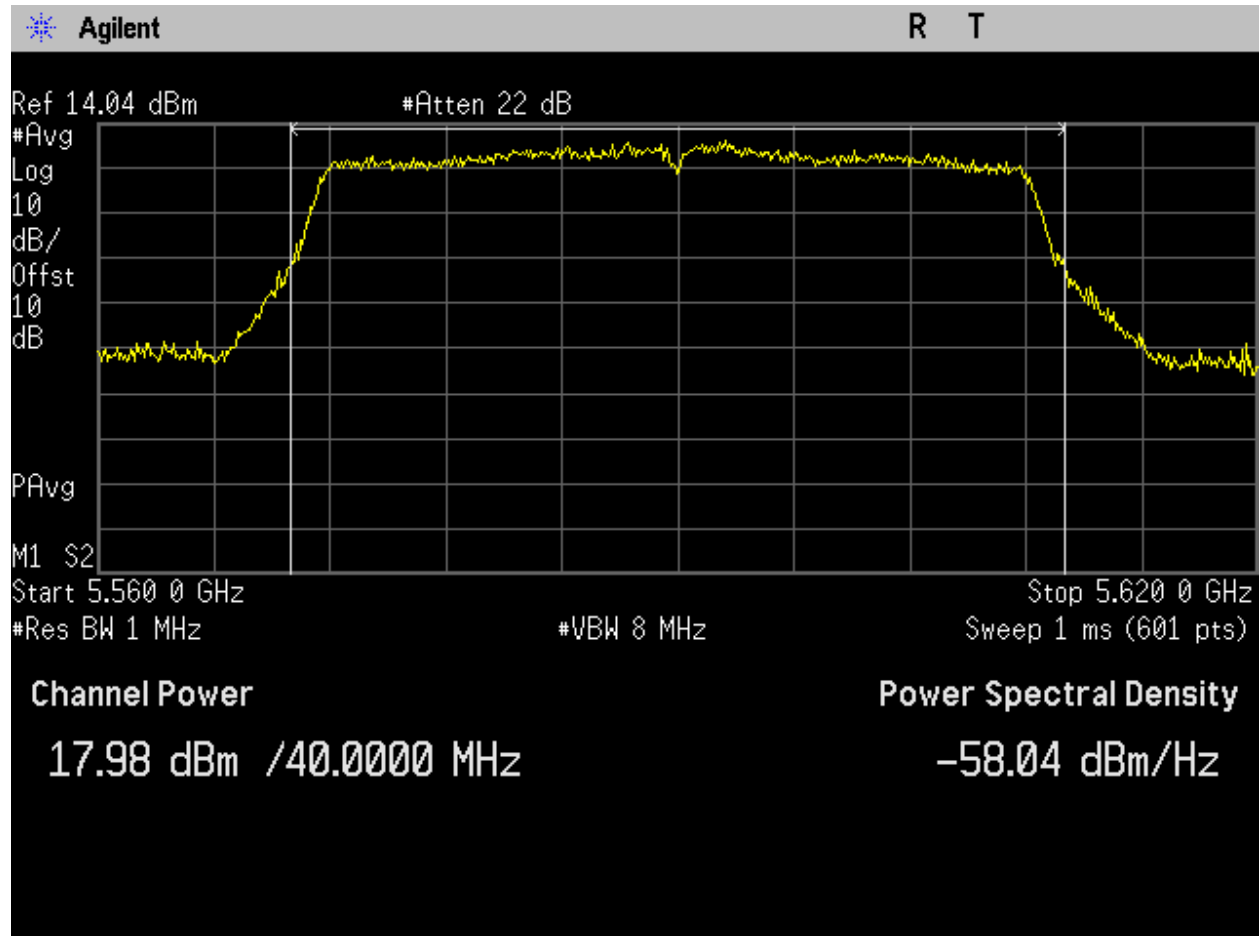


Figure 174: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ac-mode_Output Power_Port 2.

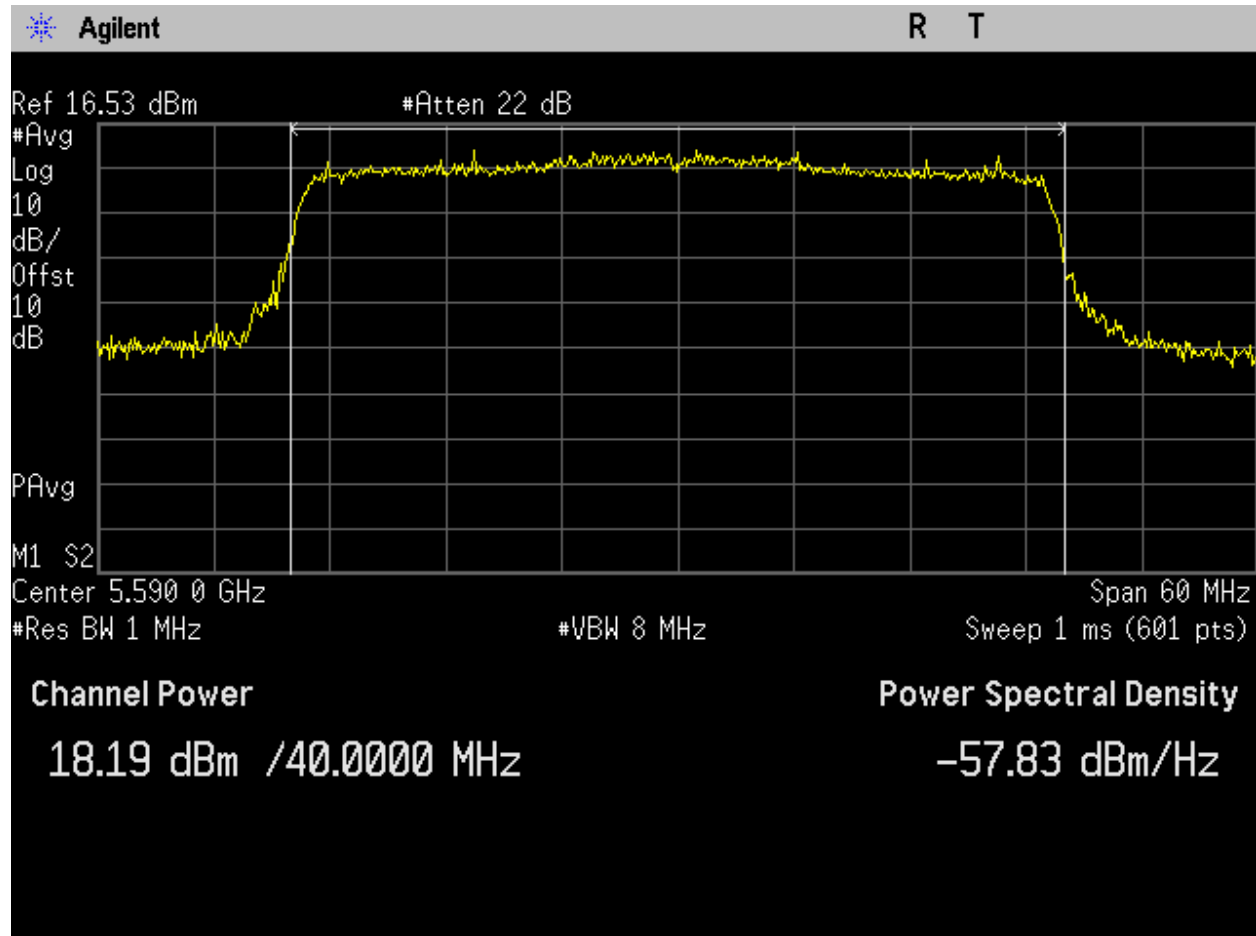


Figure 175: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ax-mode_Output Power_Port 1.

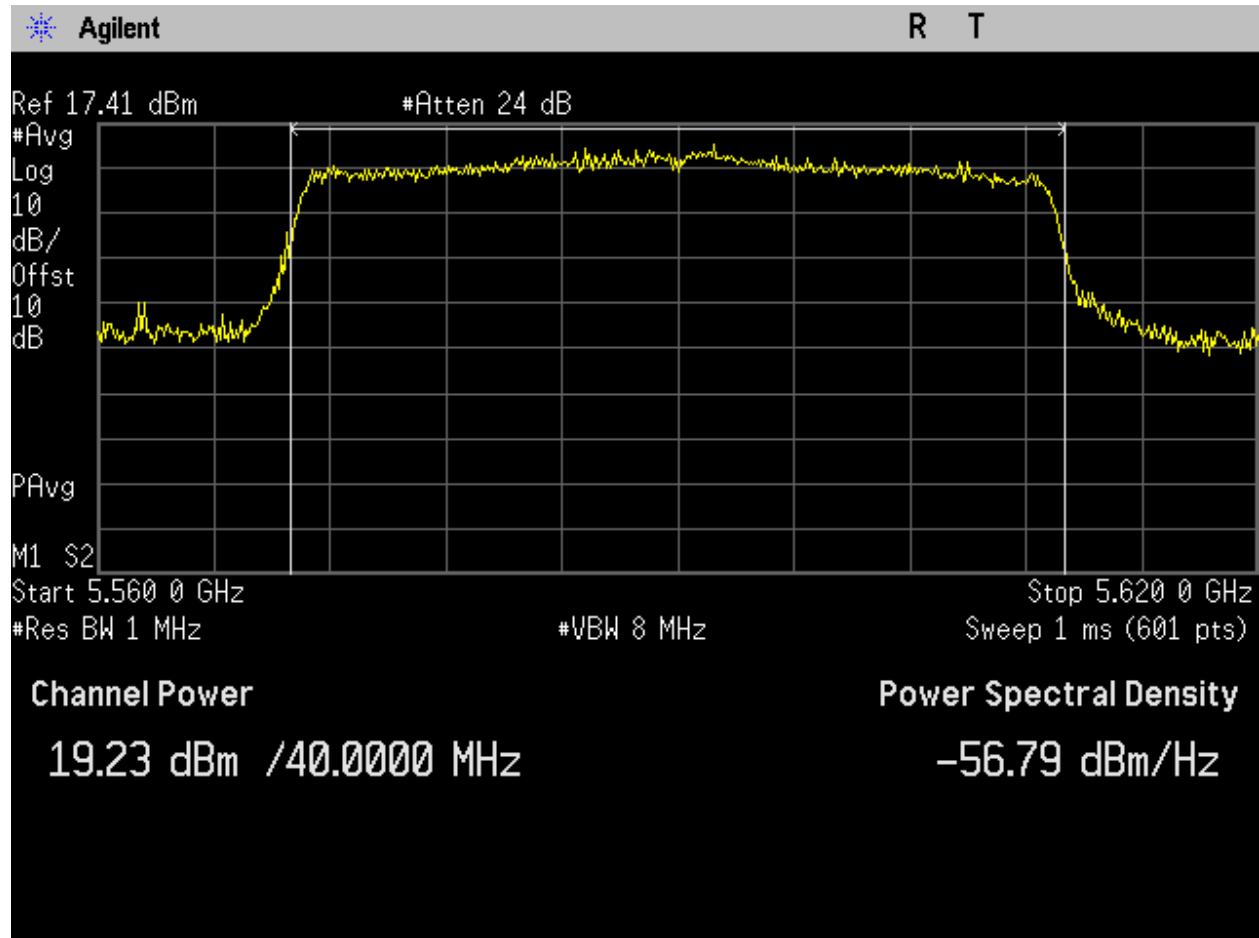


Figure 176: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ax-mode_Output Power_Port 2.

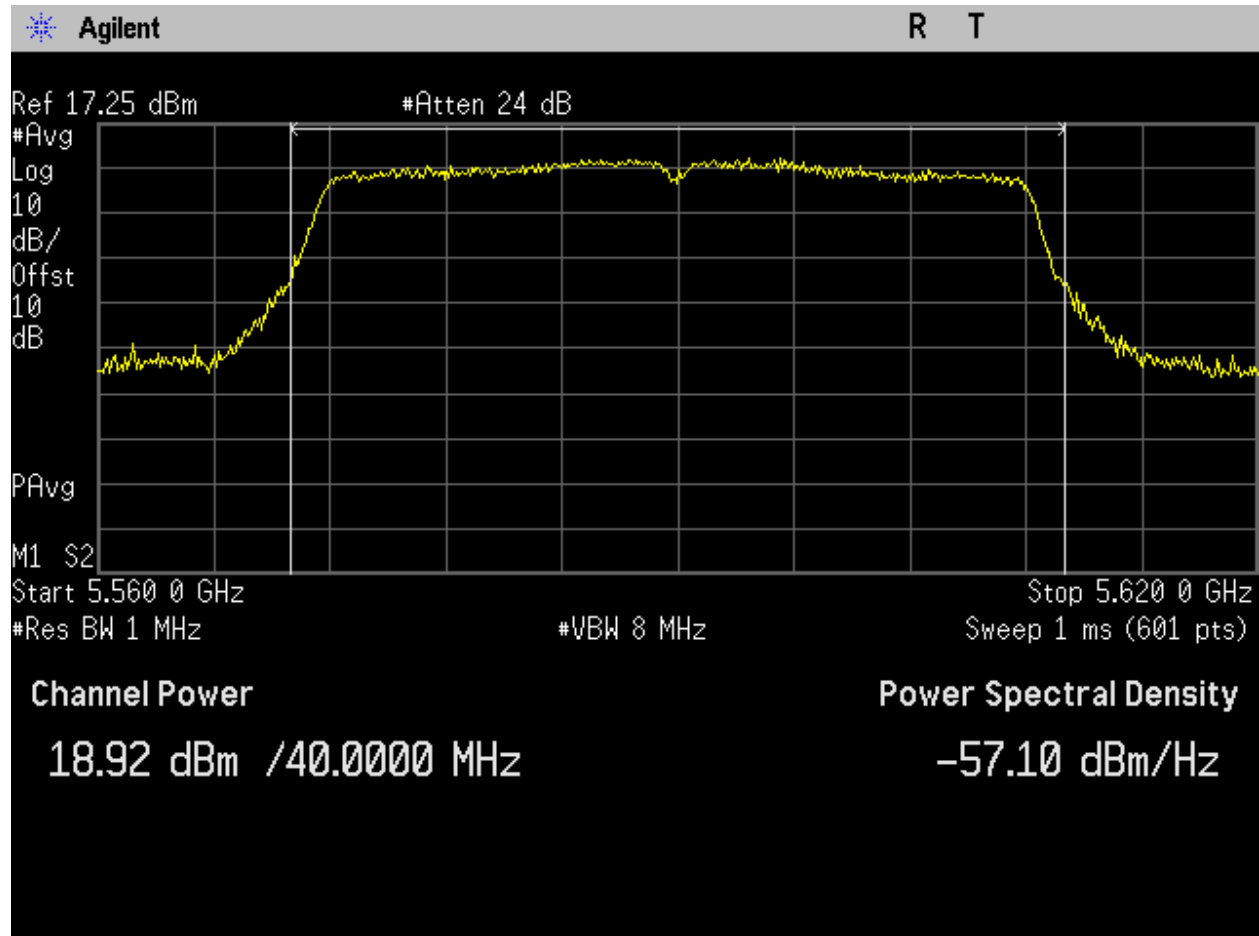


Figure 177: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-mode_Output Power_Port 1.

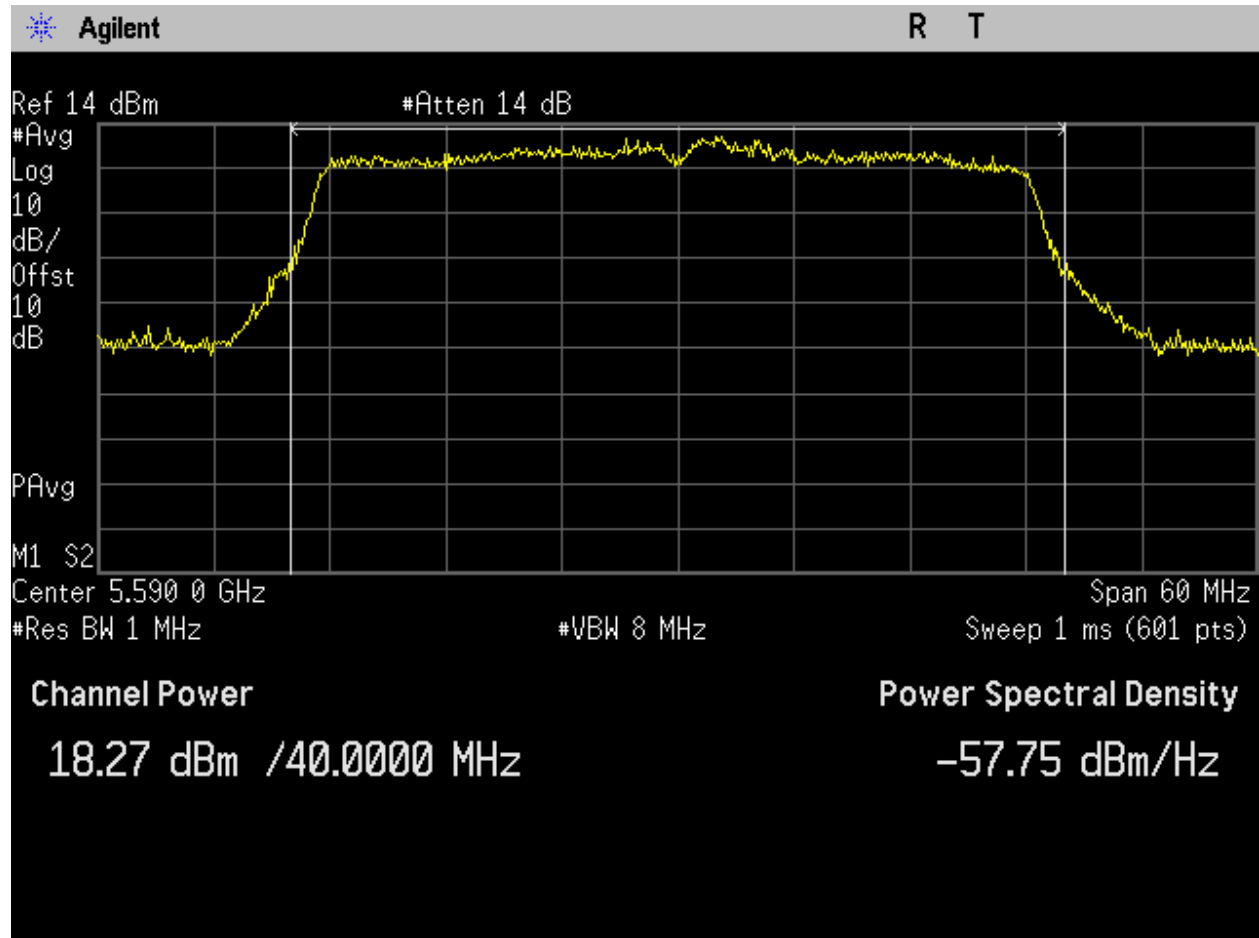


Figure 178: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-mode_Output Power_Port 2.

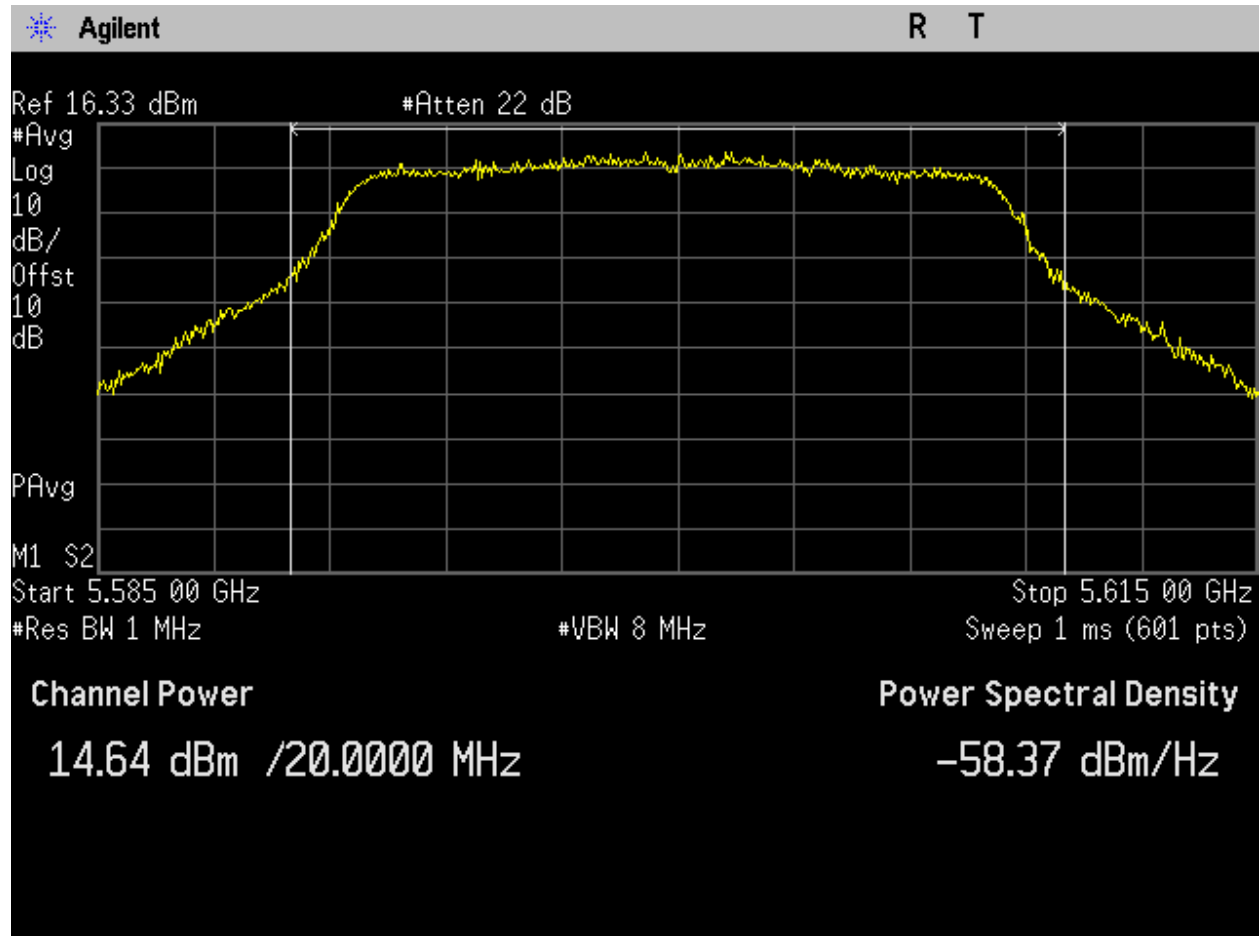


Figure 179: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Output Power_Port 1.

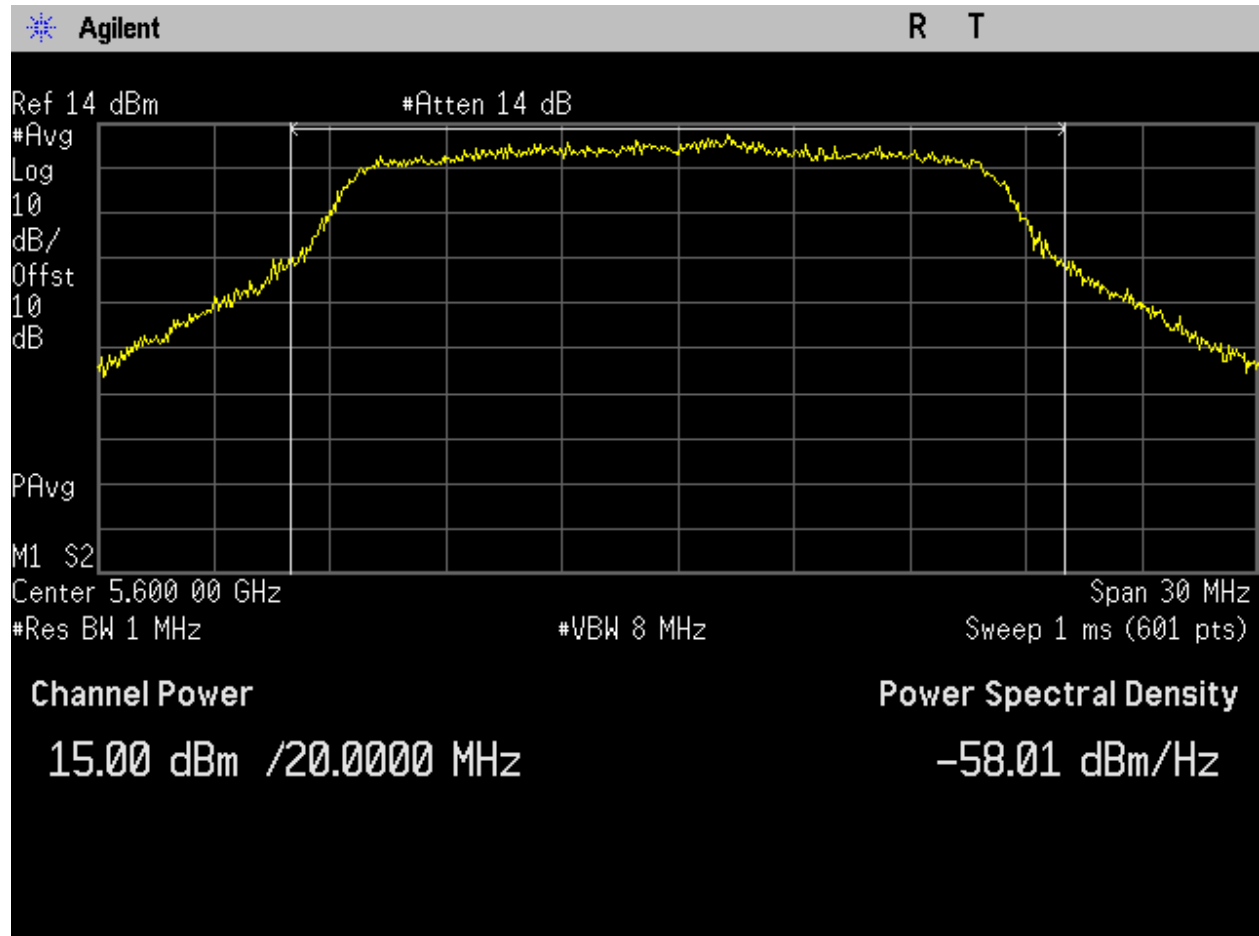


Figure 180: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Output Power_Port 2.

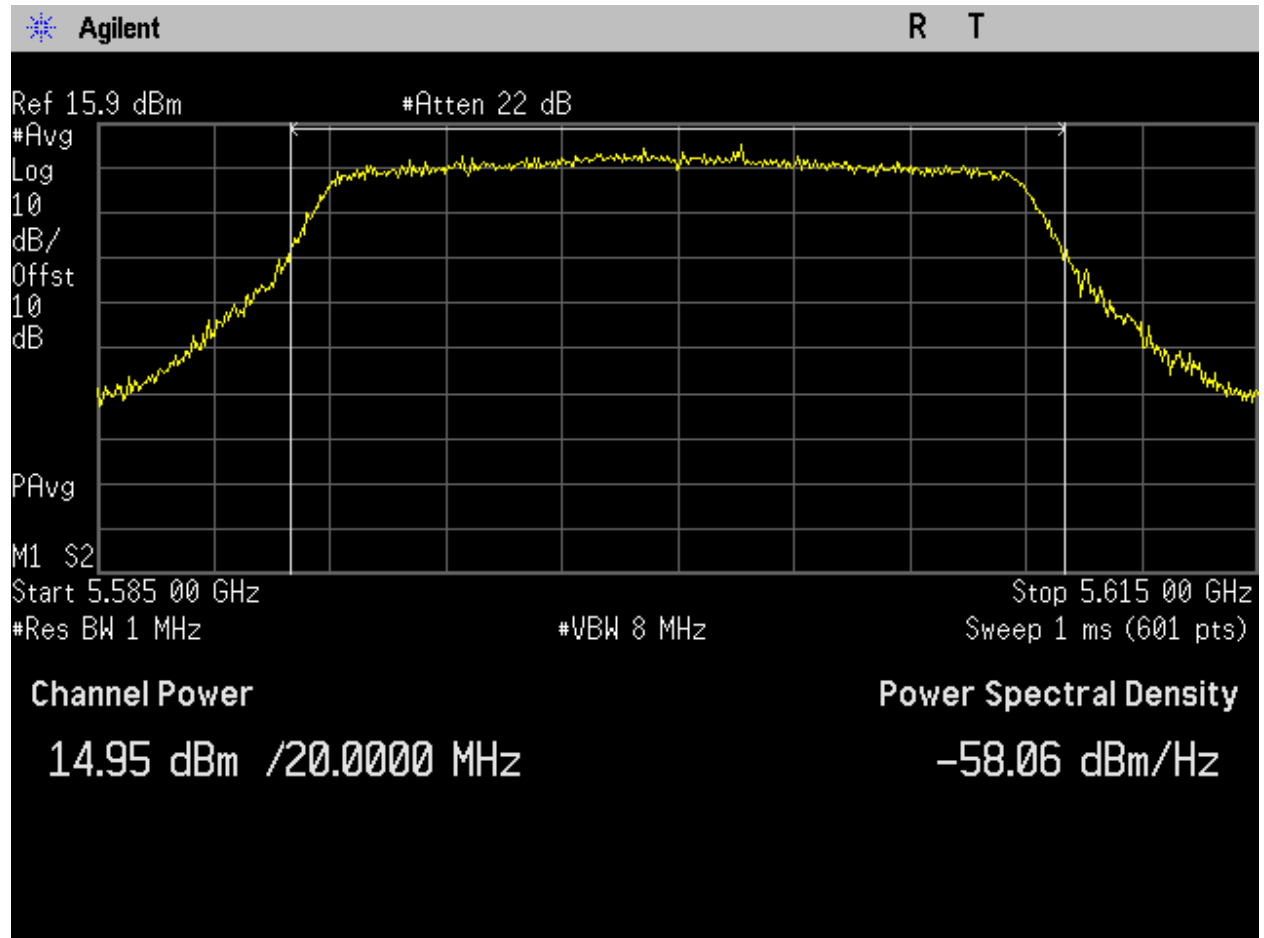


Figure 181: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Output Power_Port 1.

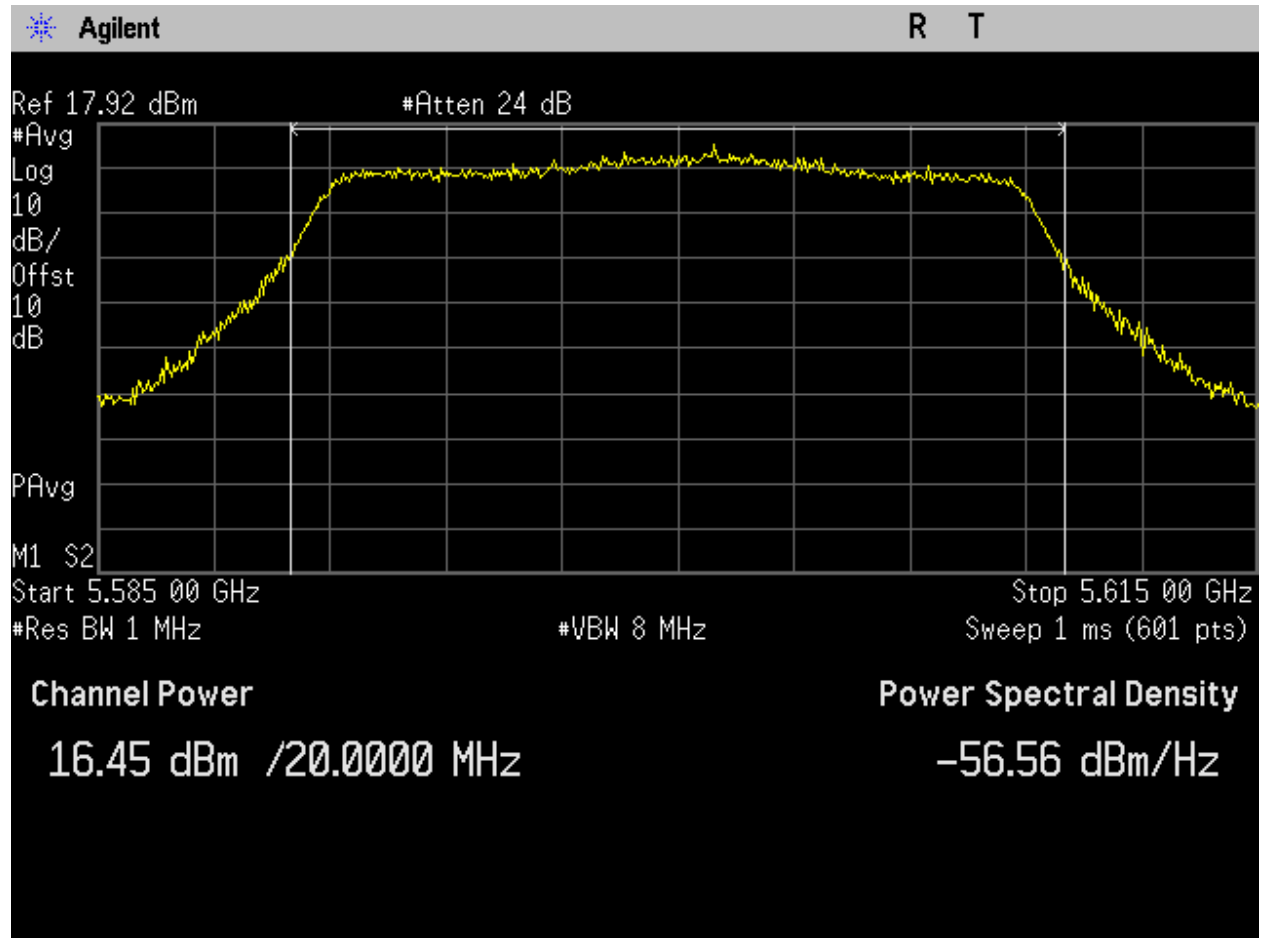


Figure 182: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Output Power_Port 2.

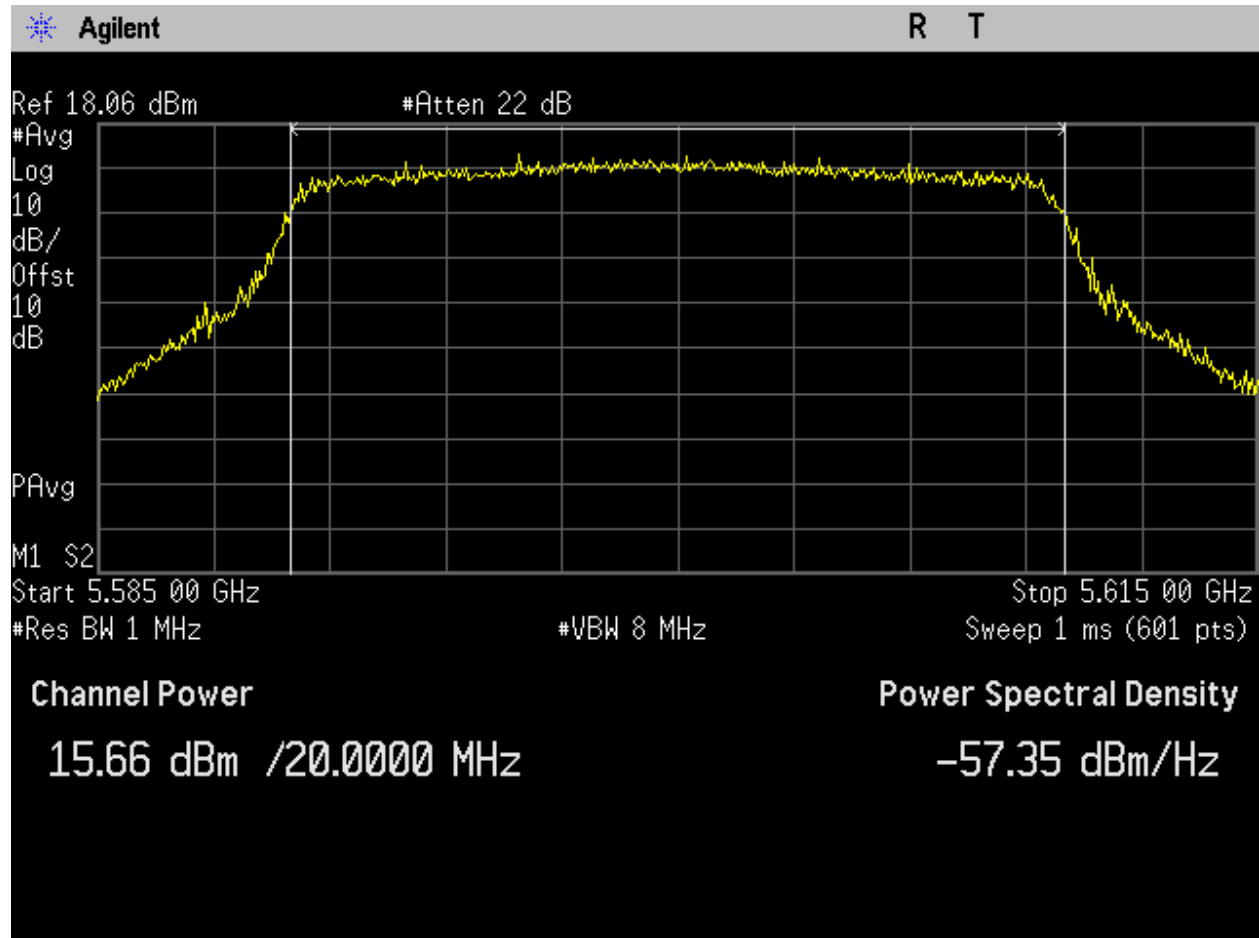


Figure 183: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ax-mode_Output Power_Port 1.

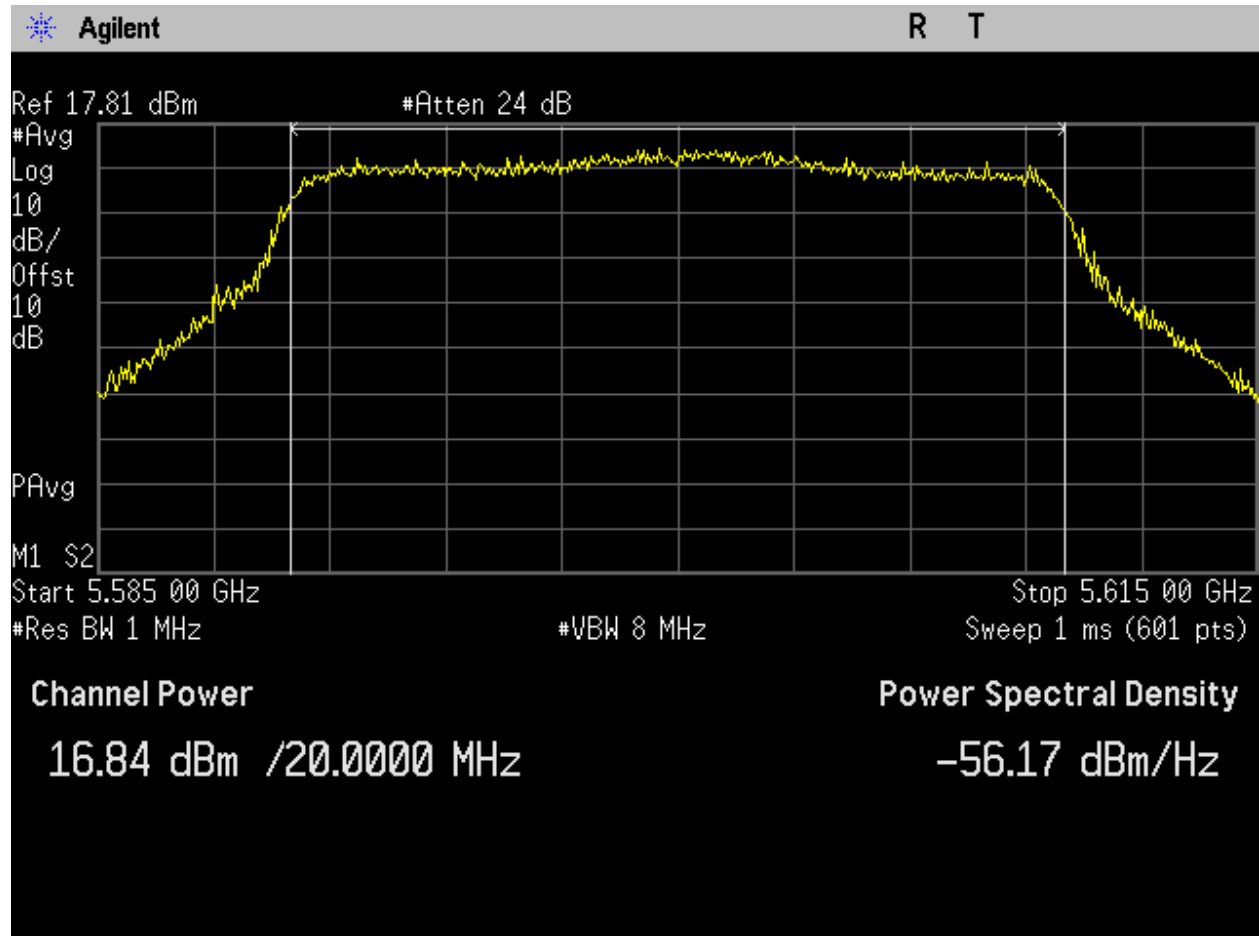


Figure 184: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ax-mode_Output Power_Port 2.

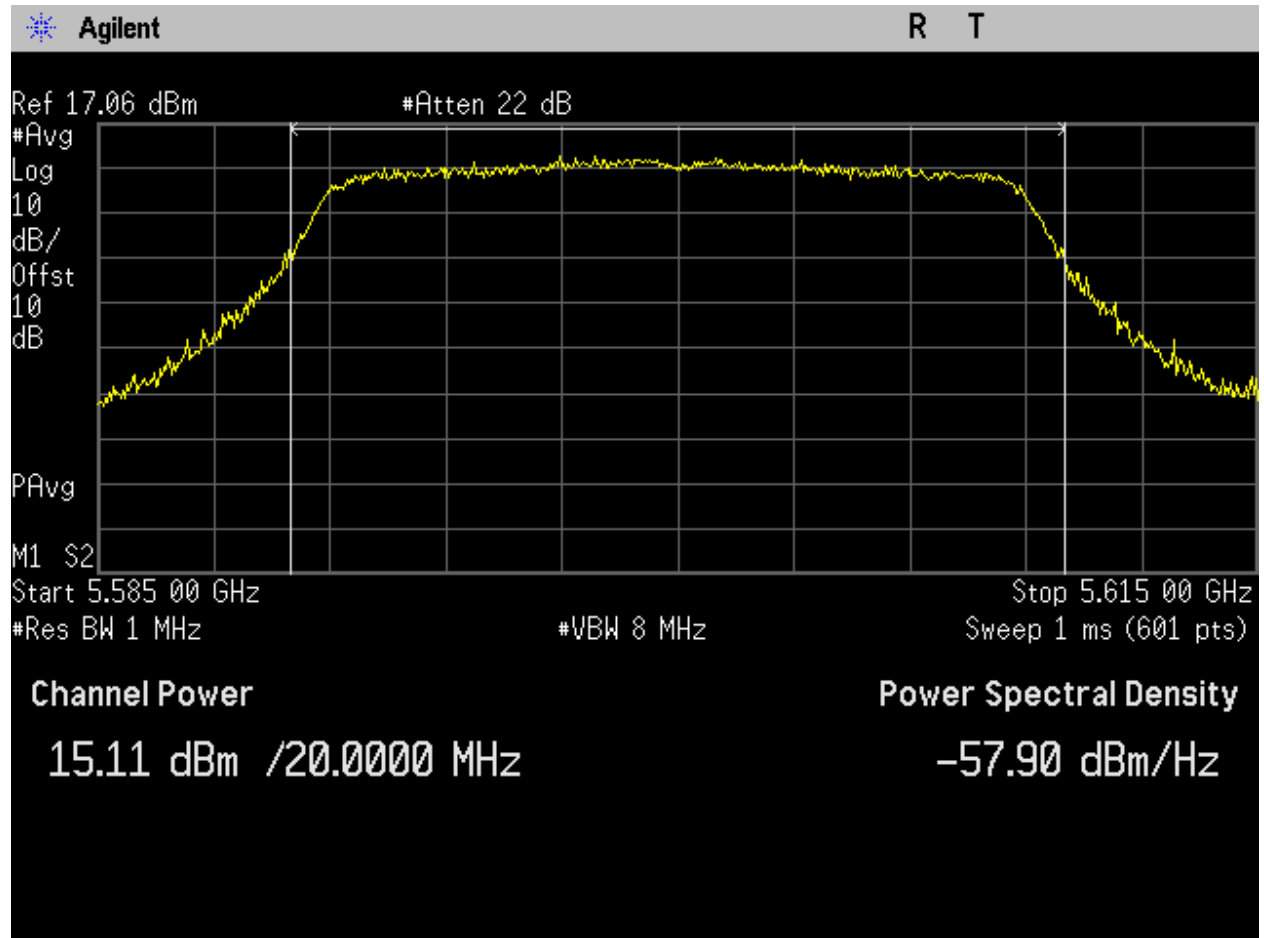


Figure 185: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_n-mode_Output Power_Port 1.

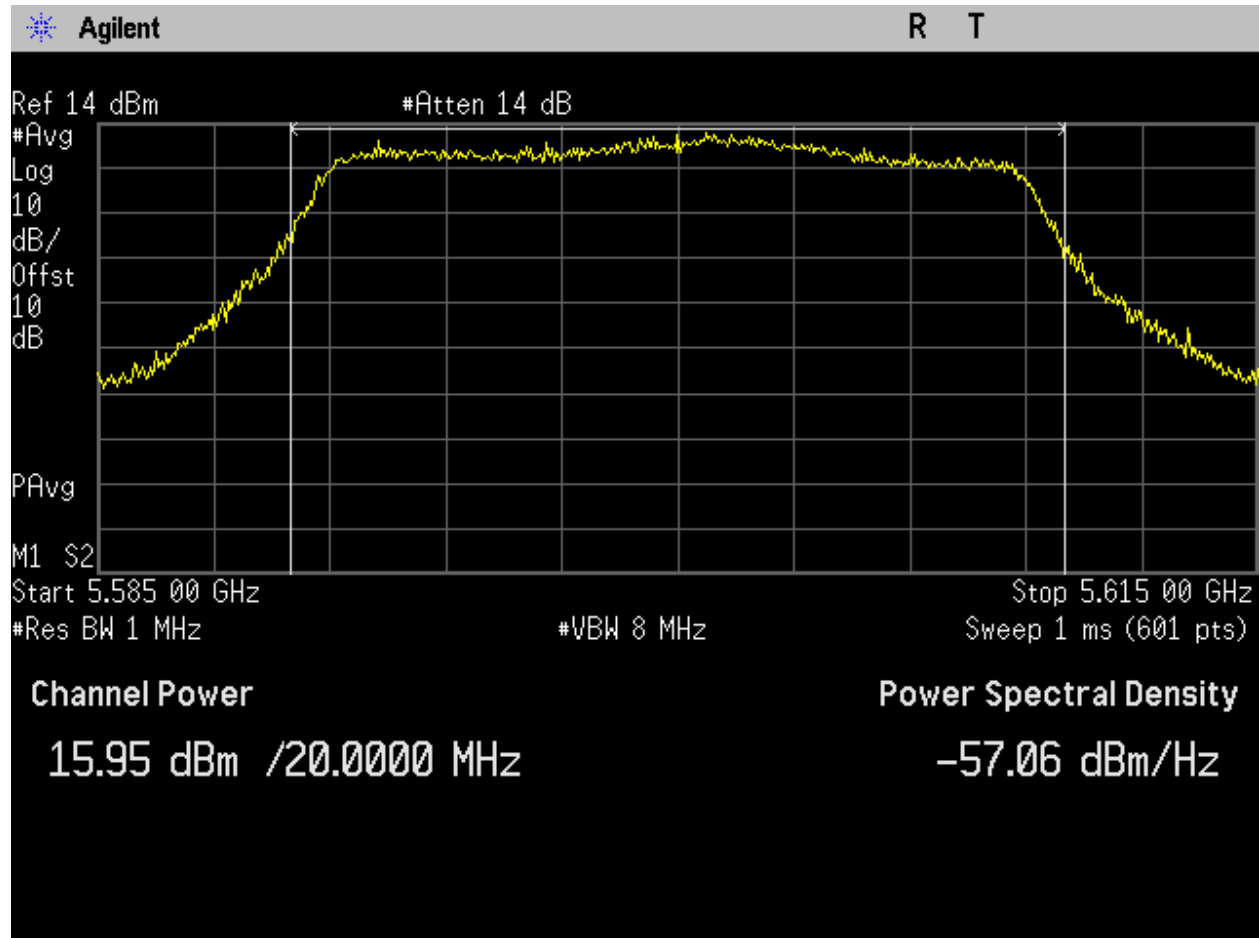


Figure 186: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_n-mode_Output Power_Port 2.

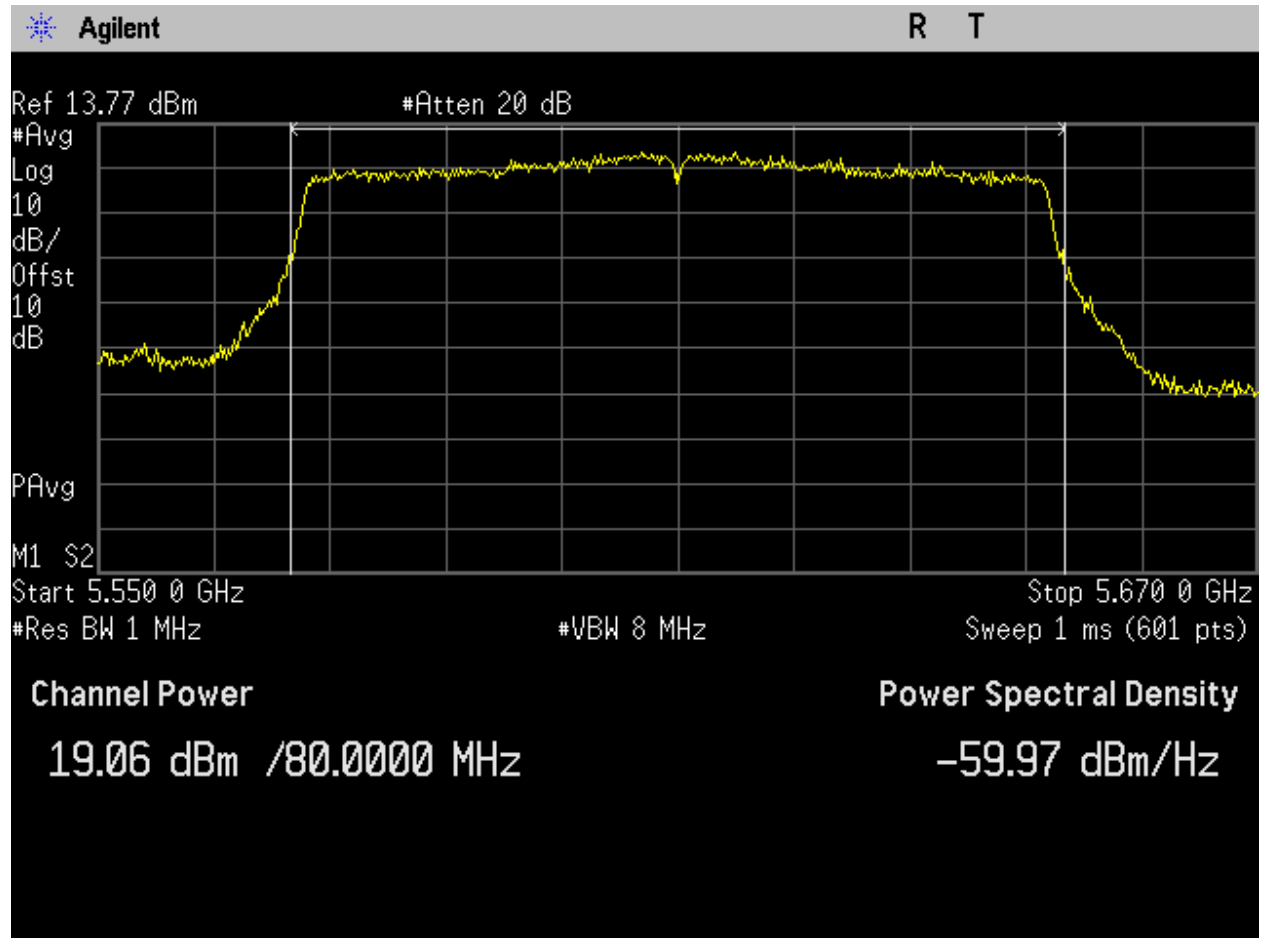


Figure 187: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Output Power_Port 1.

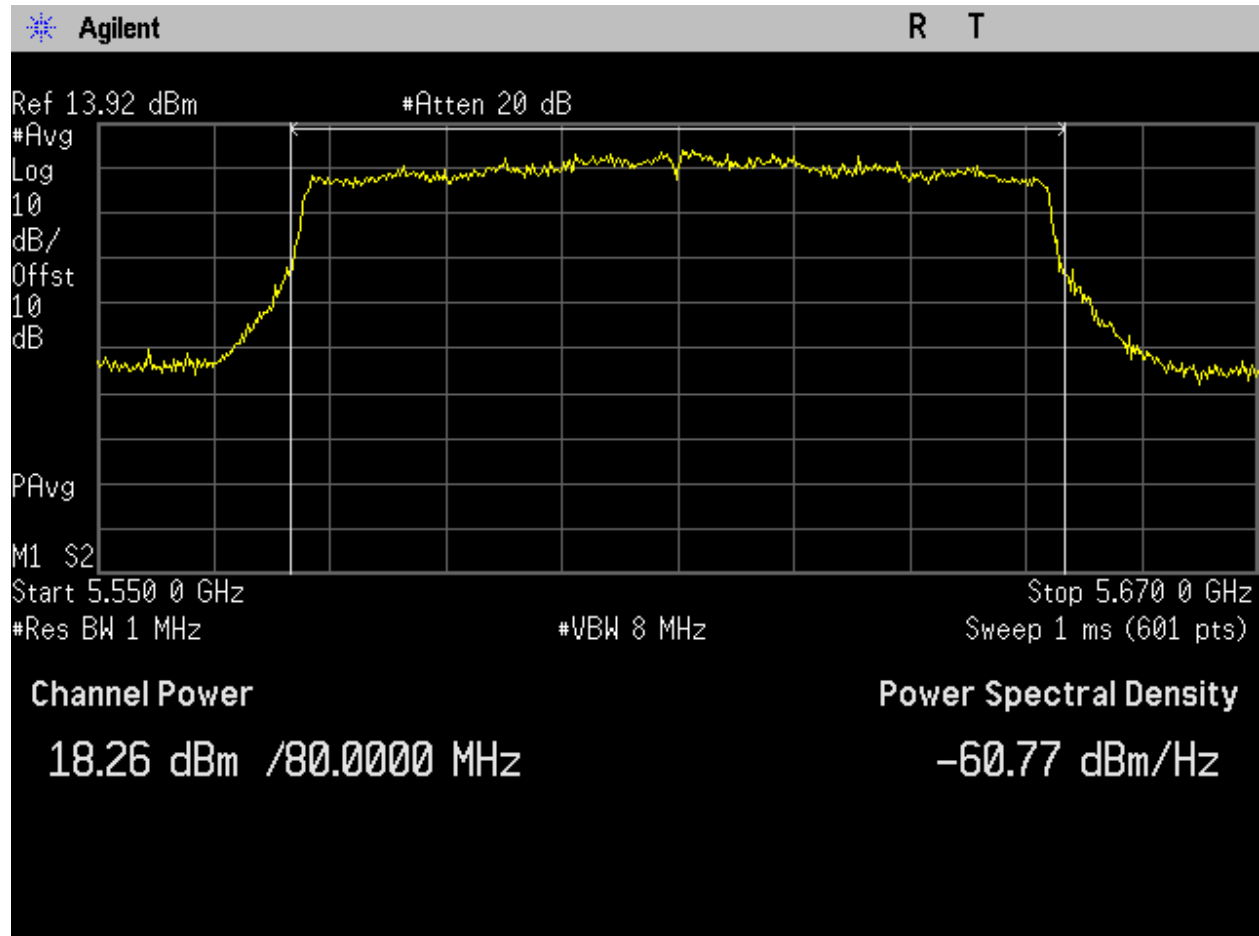


Figure 188: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Output Power_Port 2.

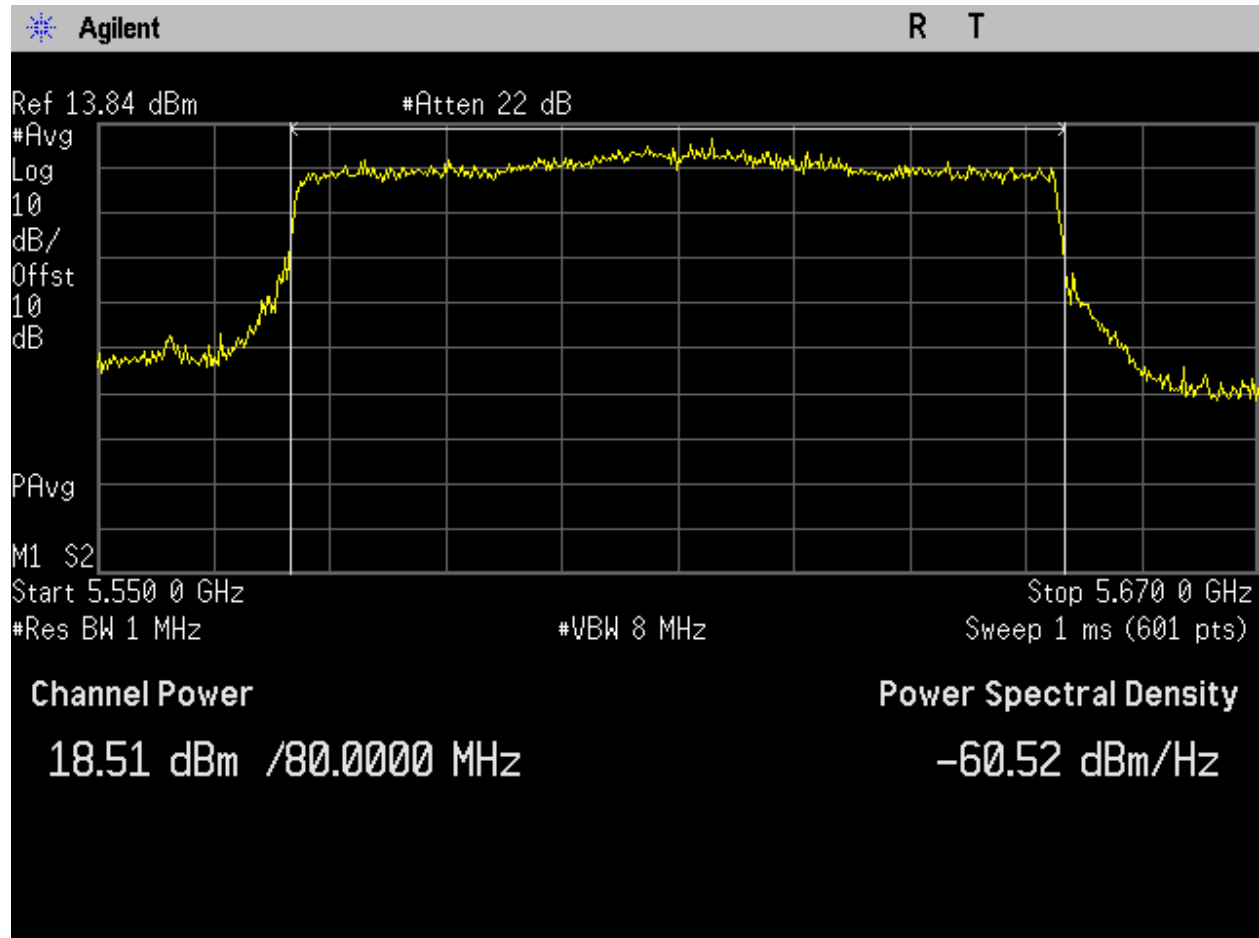


Figure 189: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Output Power_Port 1.

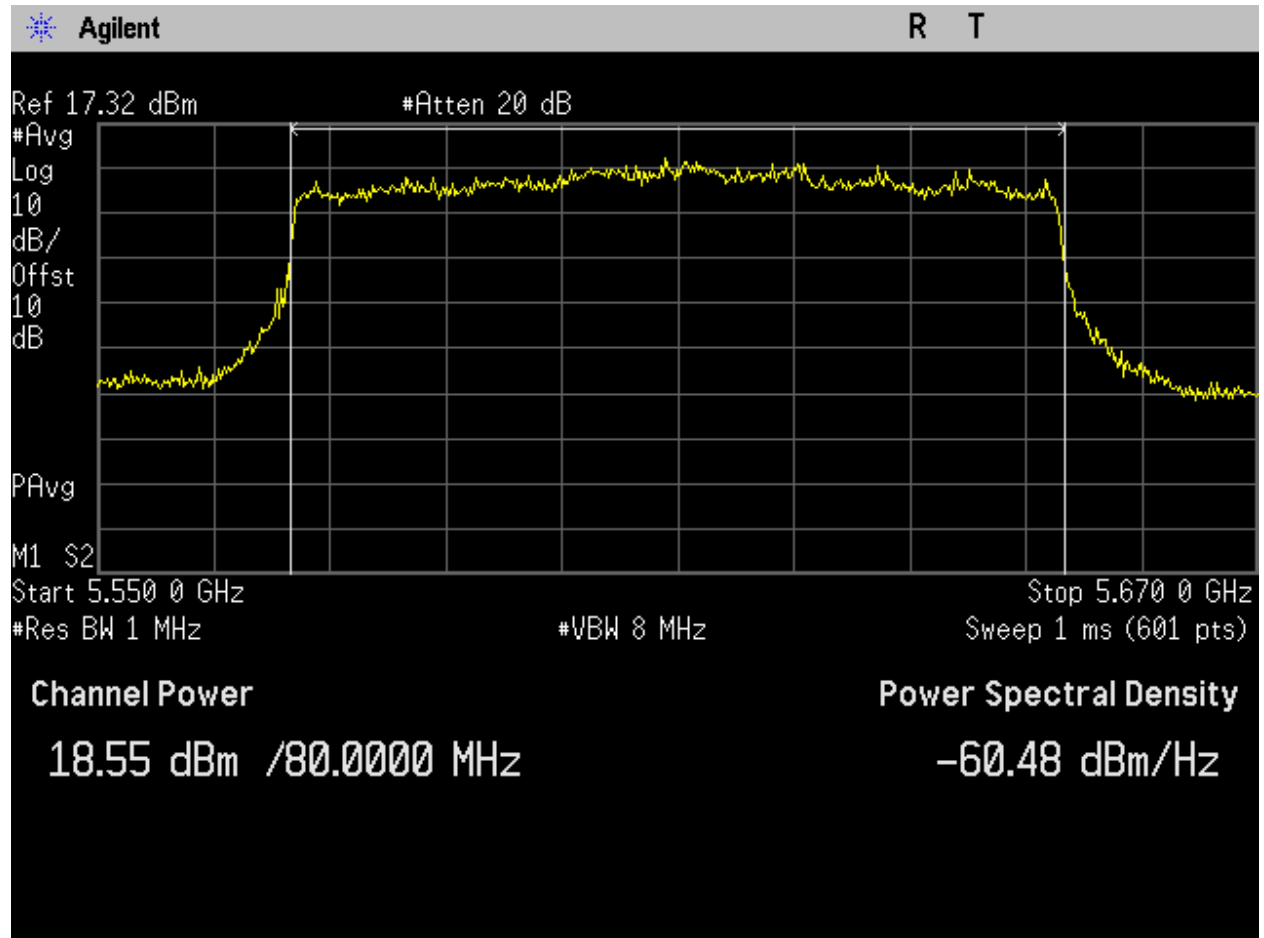


Figure 190: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Output Power_Port 2.

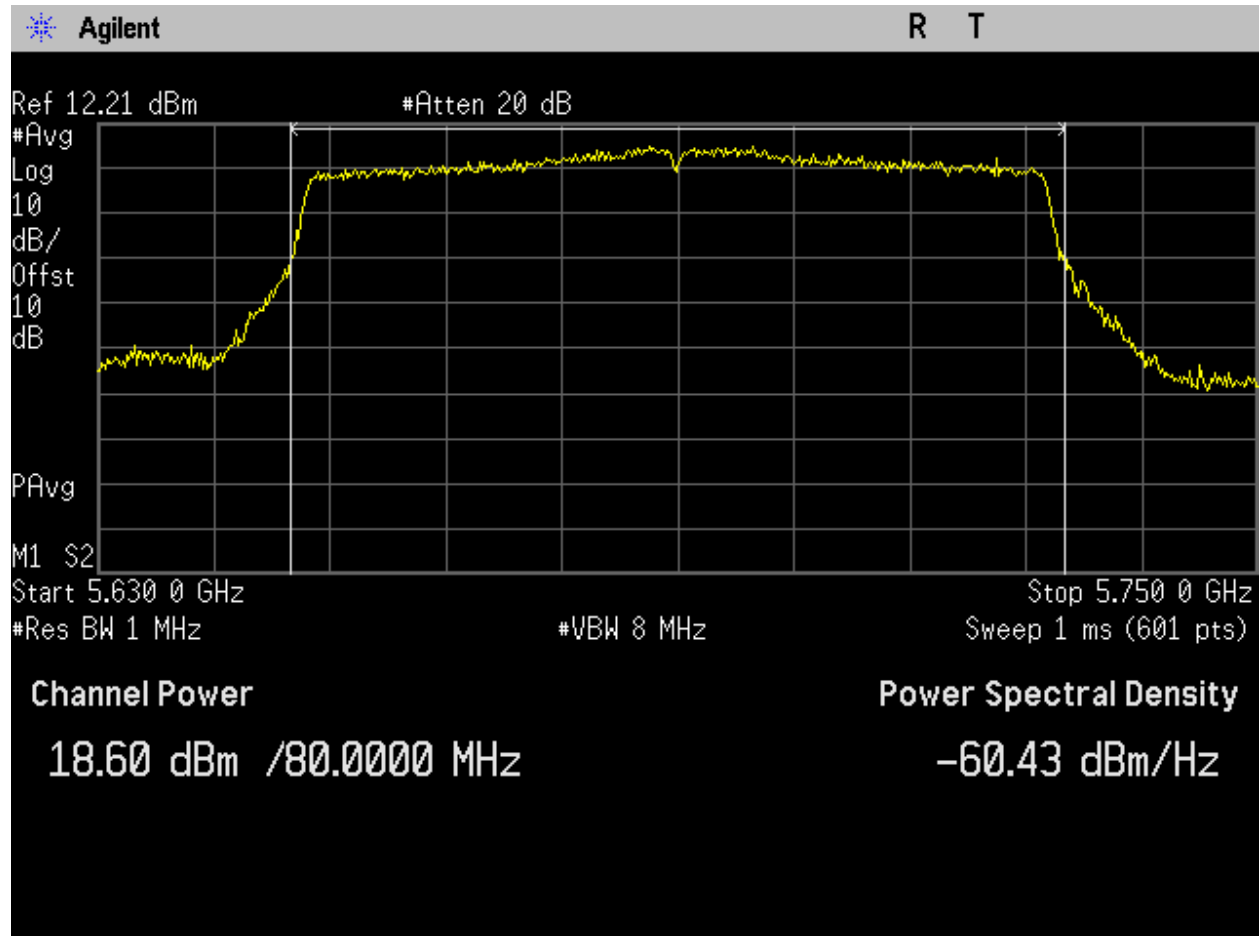


Figure 191: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Output Power_Port 1.

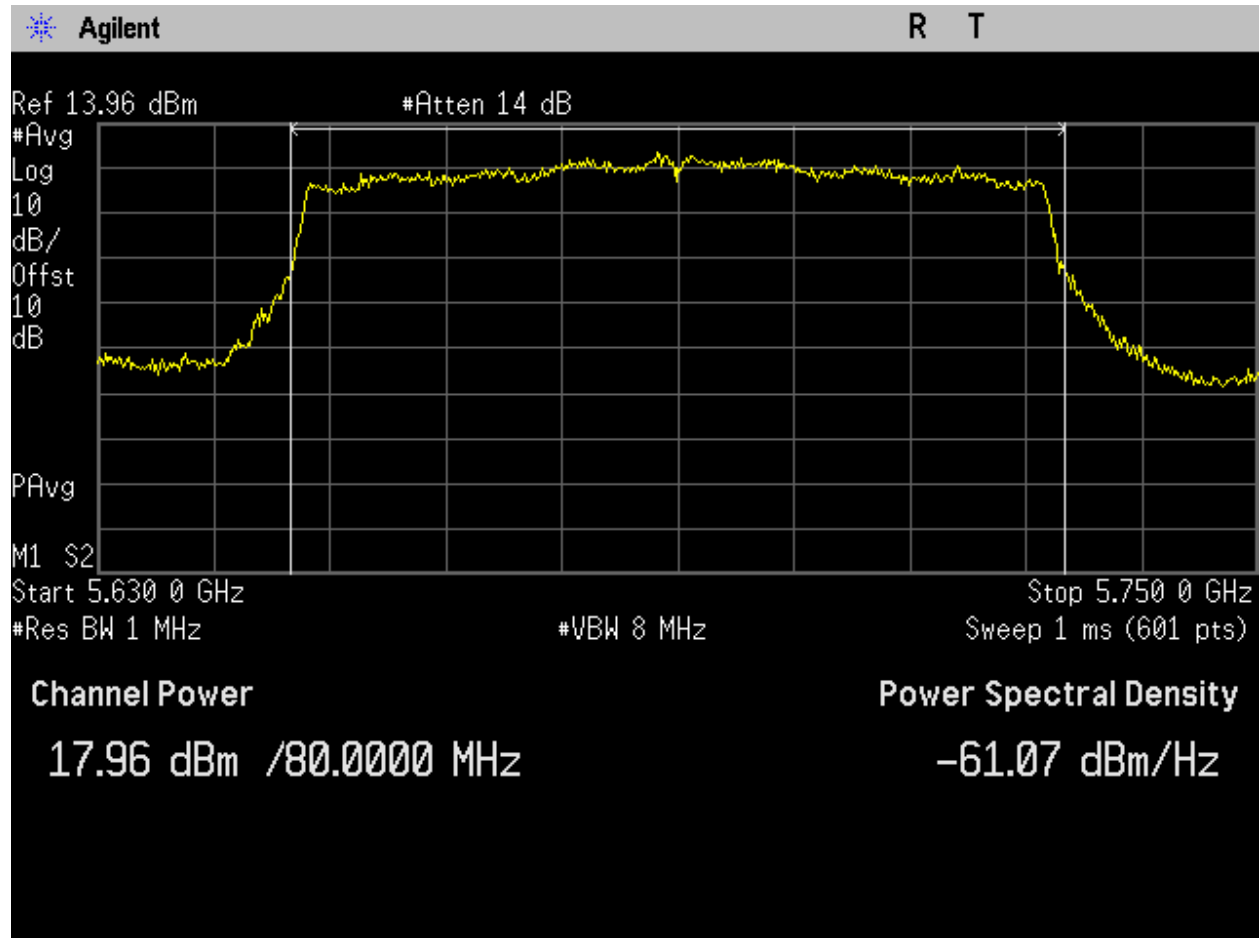


Figure 192: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Output Power_Port 2.

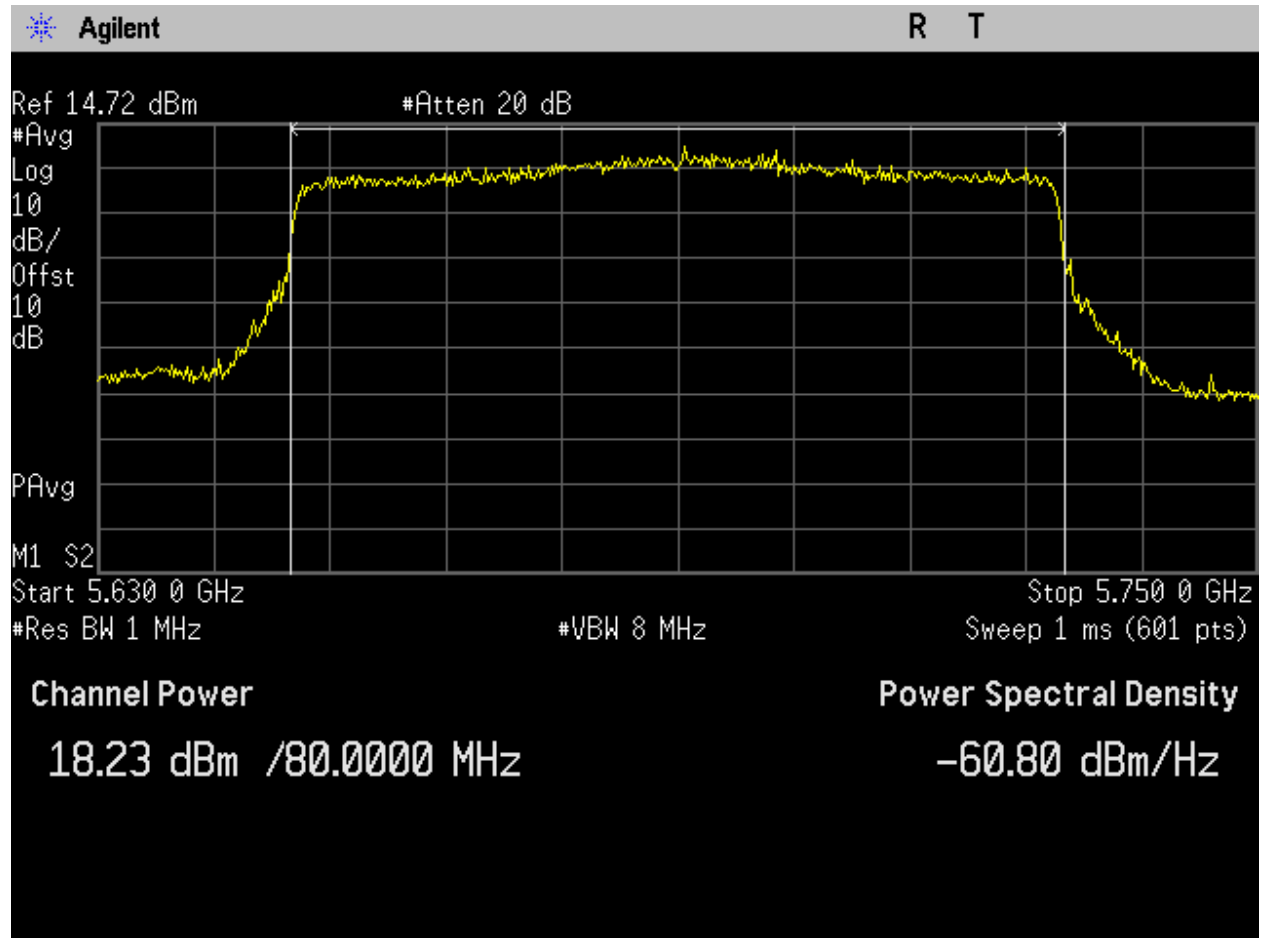


Figure 193: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ax-mode_Output Power_Port 1.

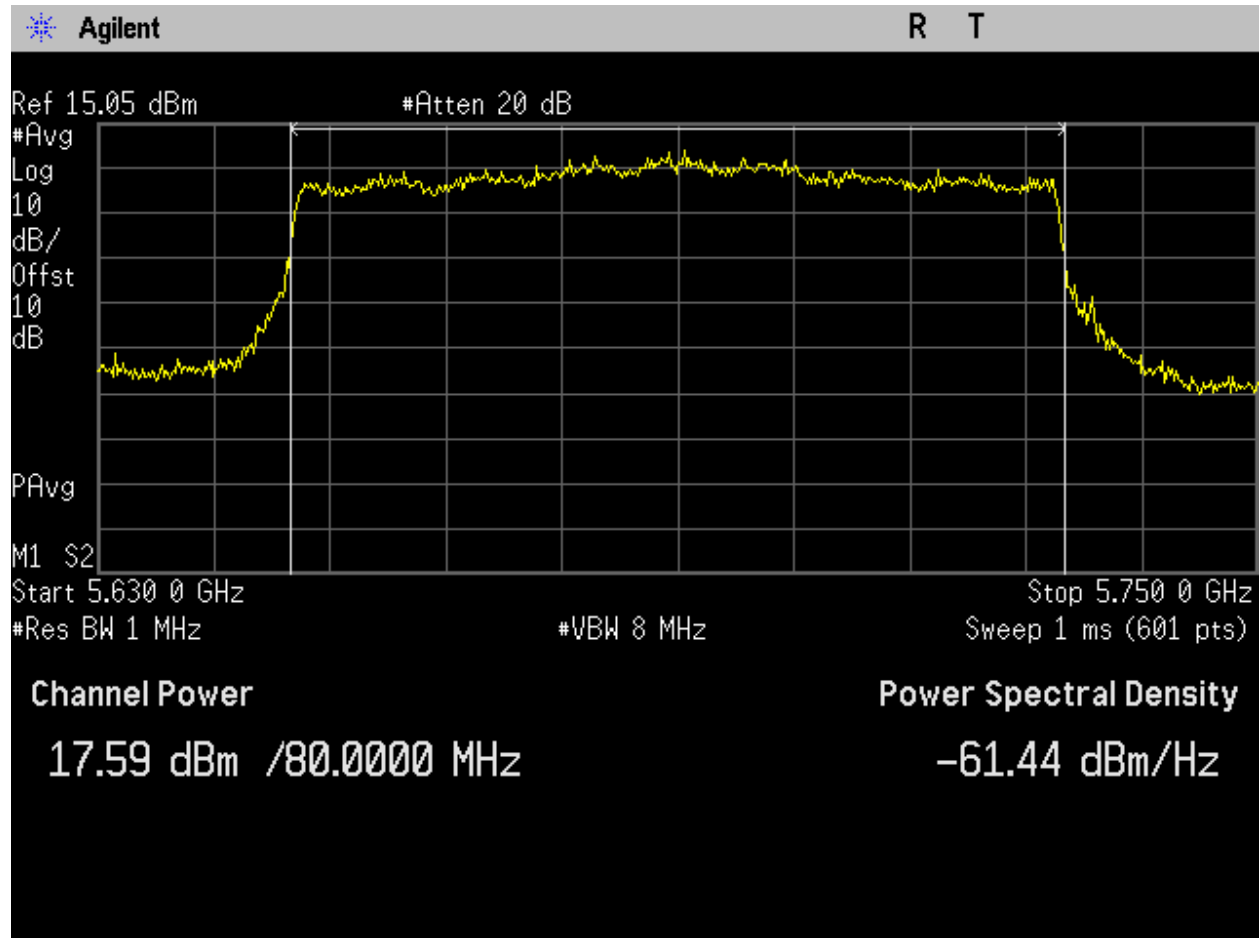


Figure 194: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ax-mode_Output Power_Port 2.

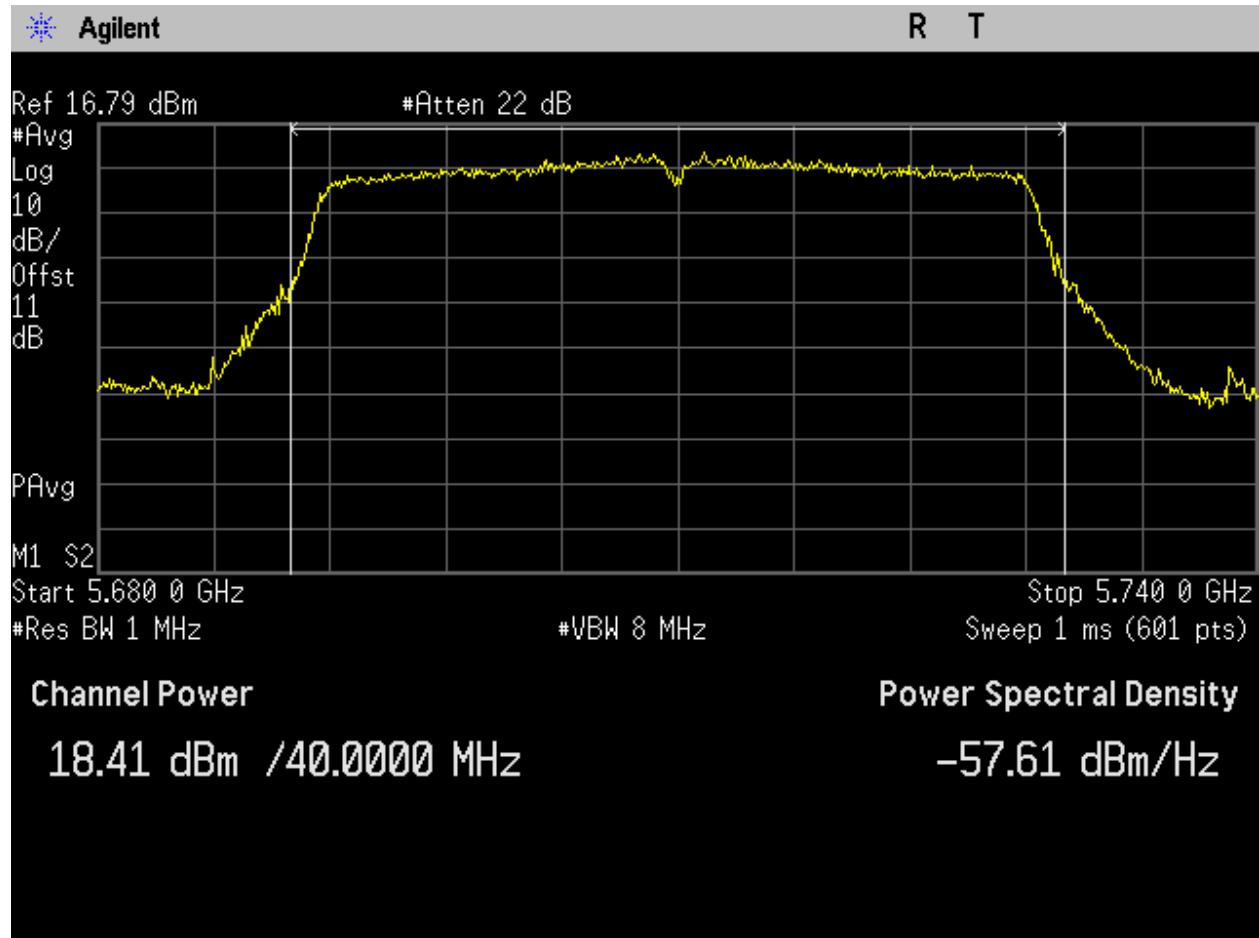


Figure 195: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Output Power_Port 1.

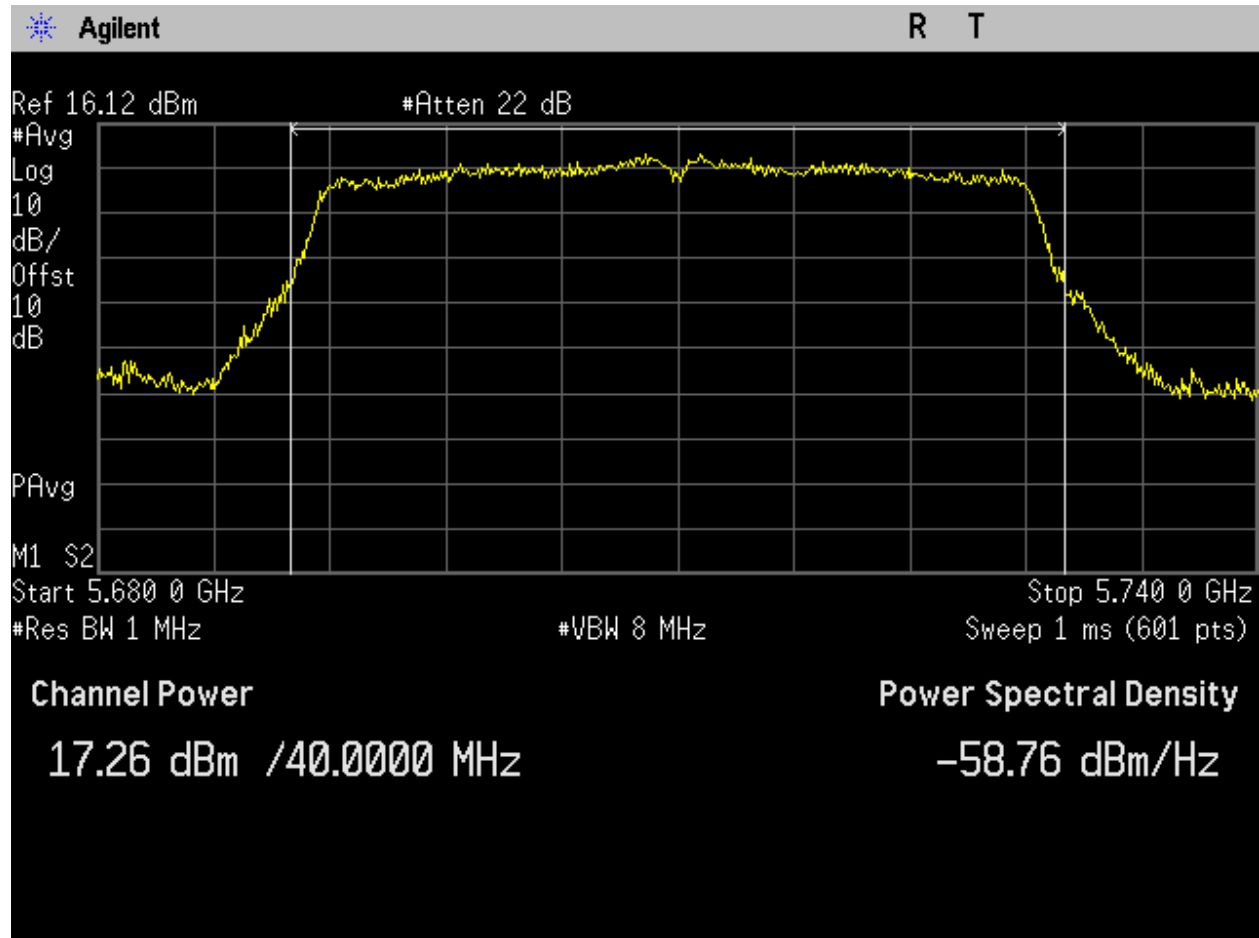


Figure 196: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Output Power_Port 2.

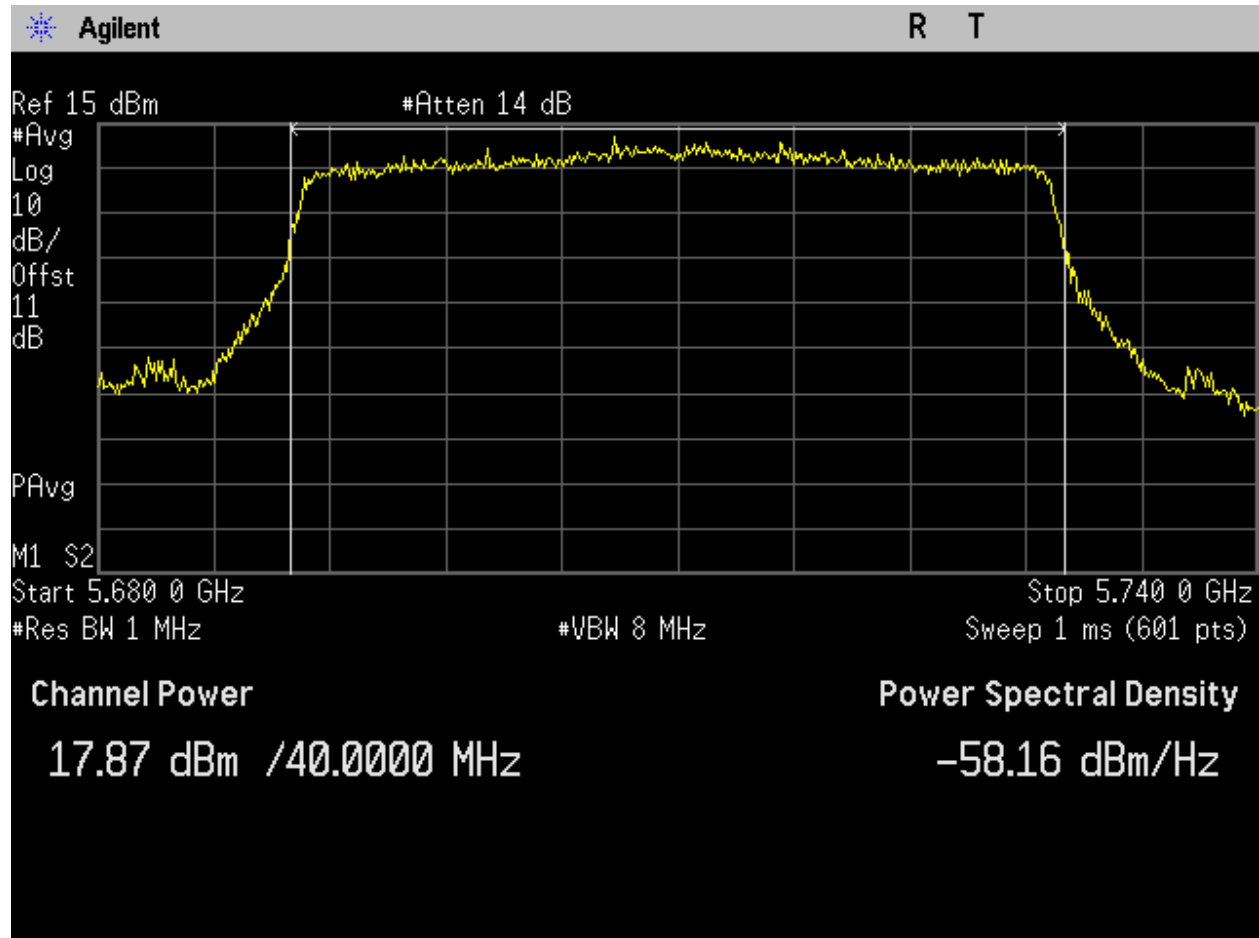


Figure 197: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ax-mode_Output Power_Port 1.

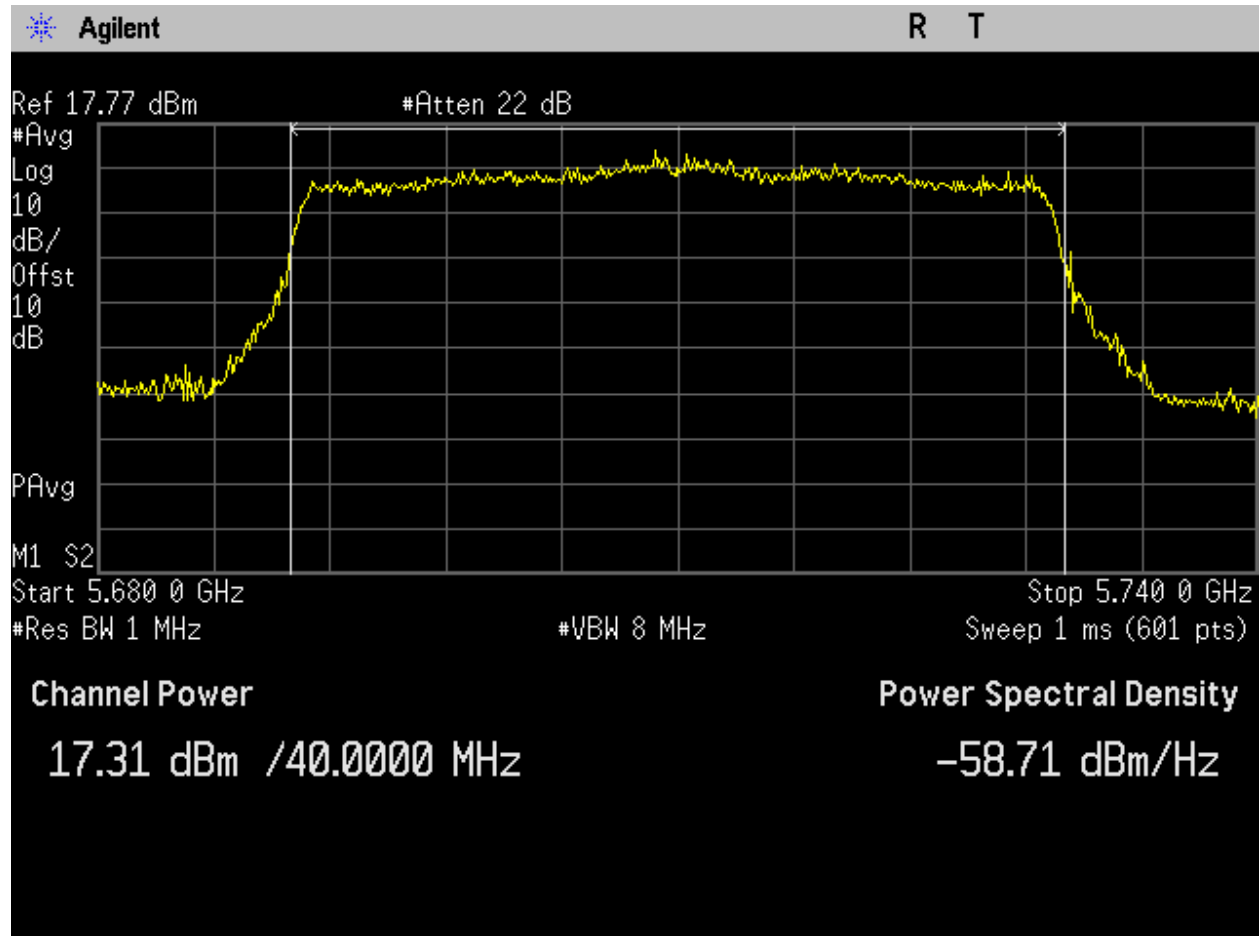


Figure 198: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ax-mode_Output Power_Port 2.

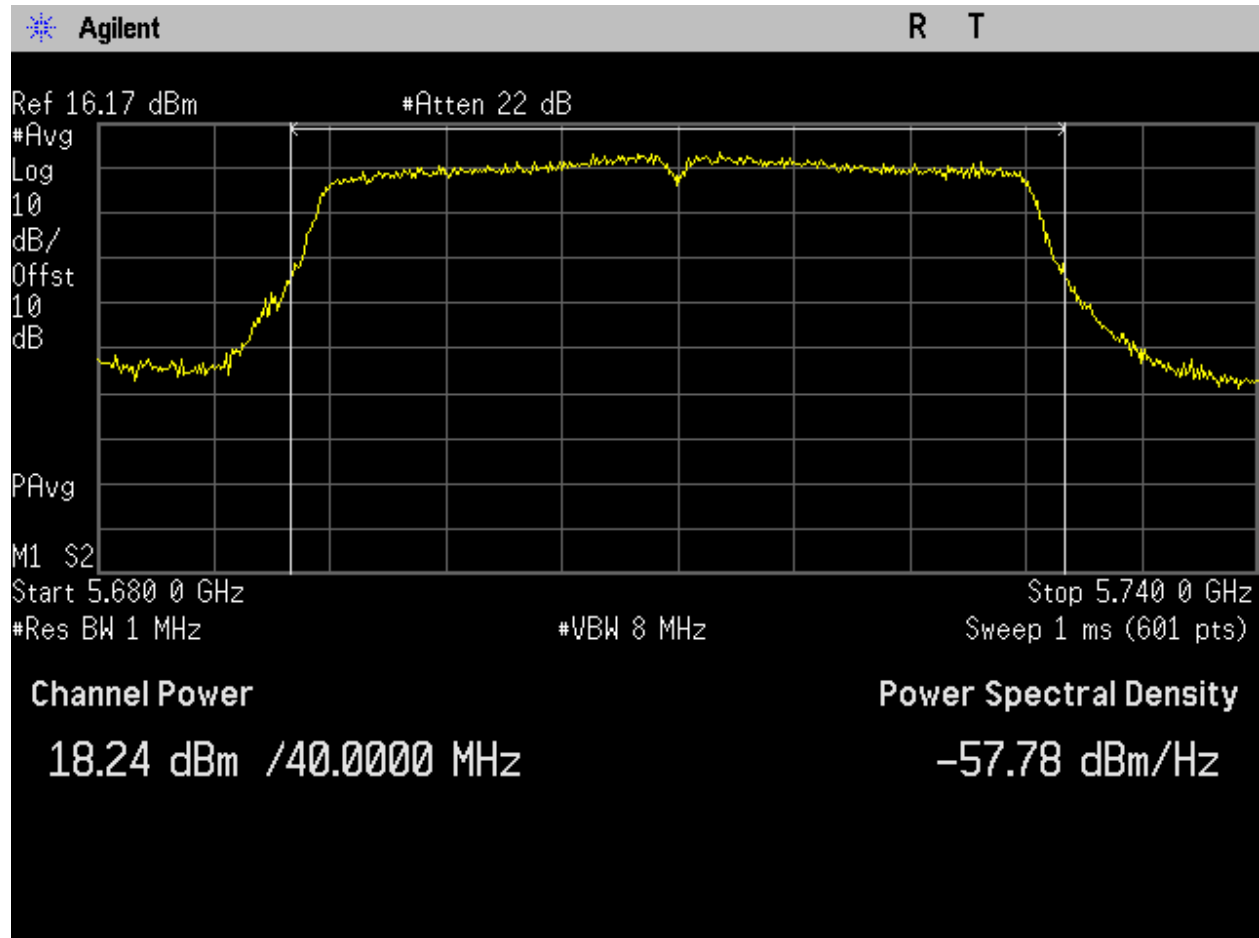


Figure 199: U-NII-2C_5710MHz_High Ch_142_40MHz BW_n-mode_Output Power_Port 1.

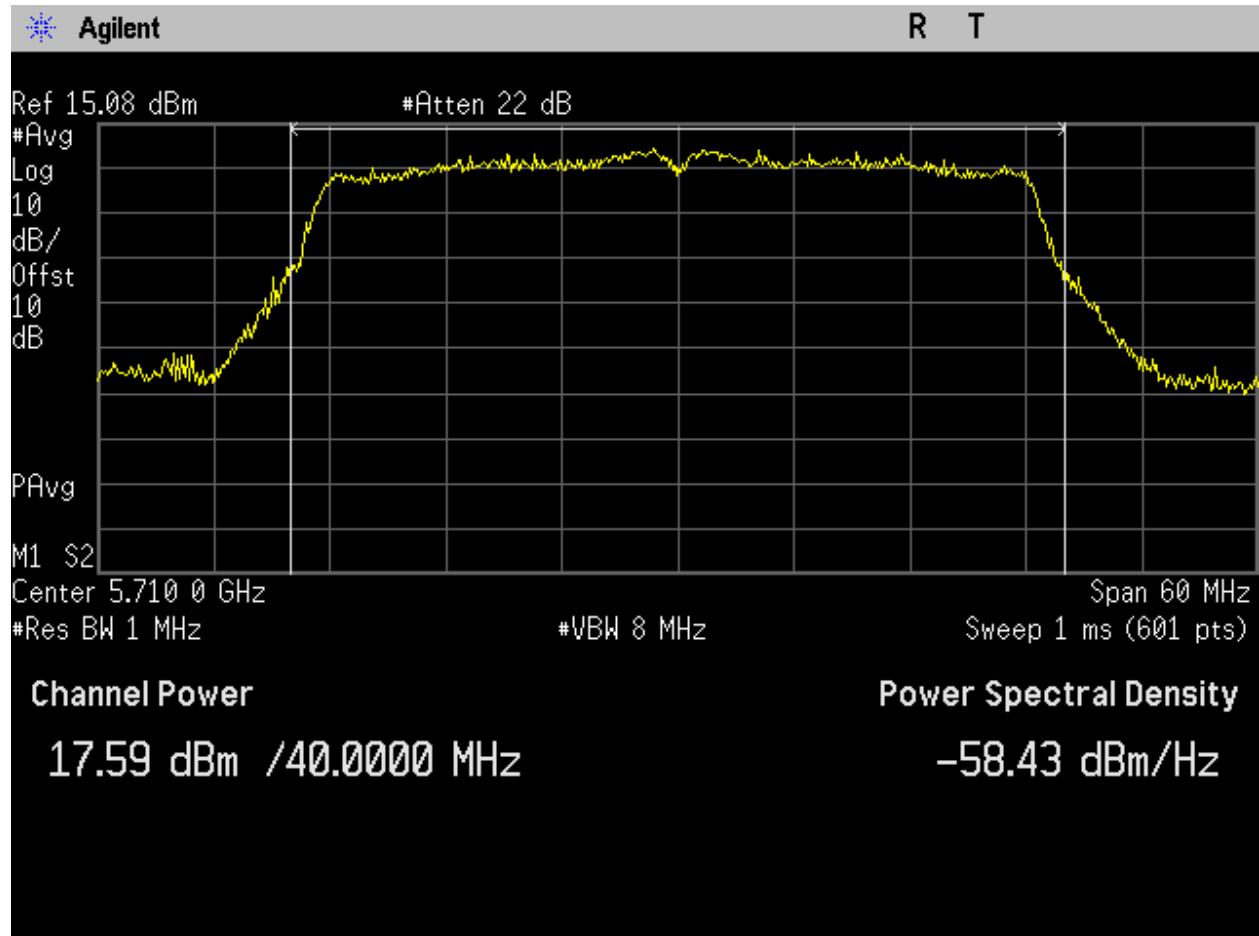


Figure 200: U-NII-2C_5710MHz_High Ch_142_40MHz BW_n-mode_Output Power_Port 2.

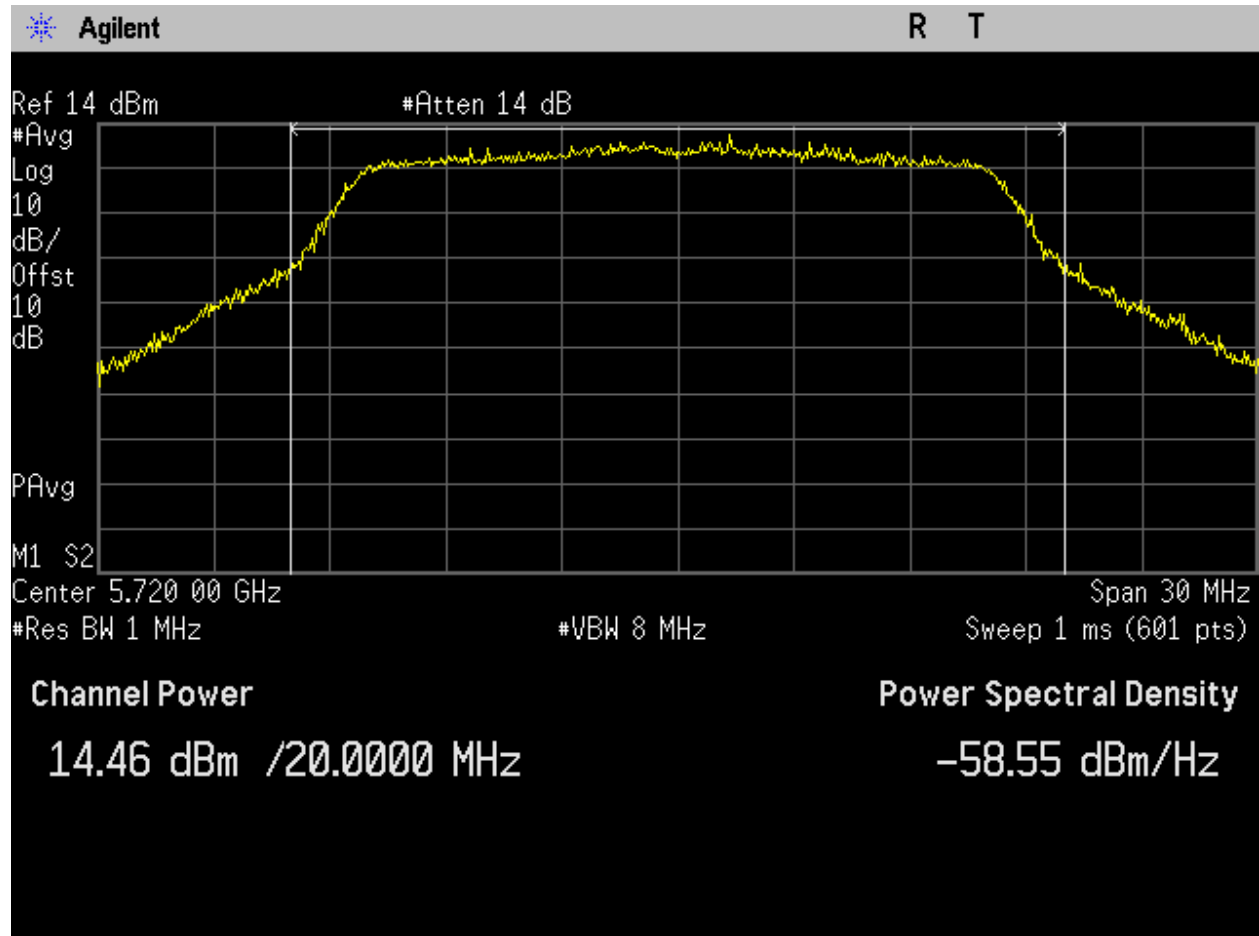


Figure 201: U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Output Power_Port 1.

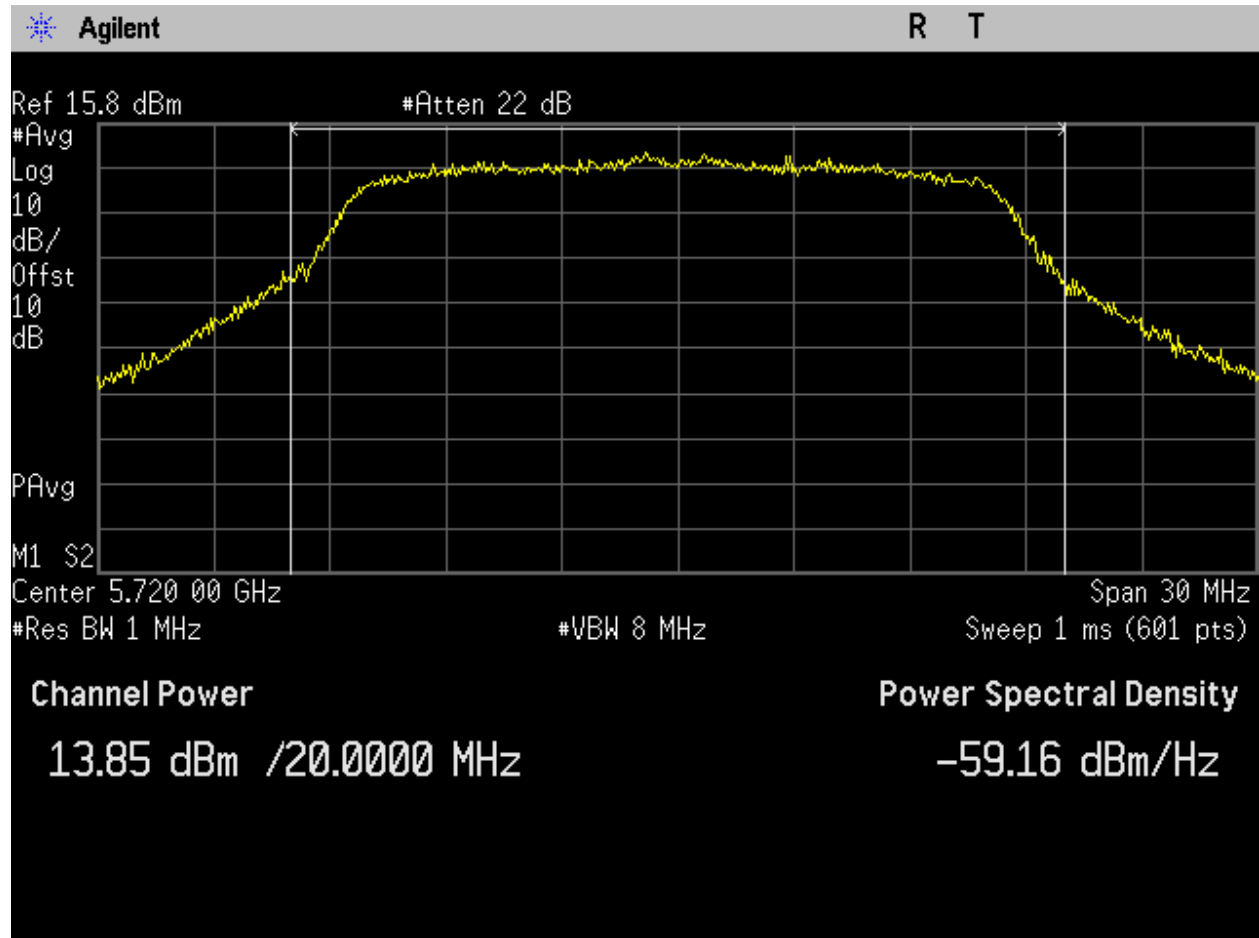


Figure 202: U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Output Power_Port 2.

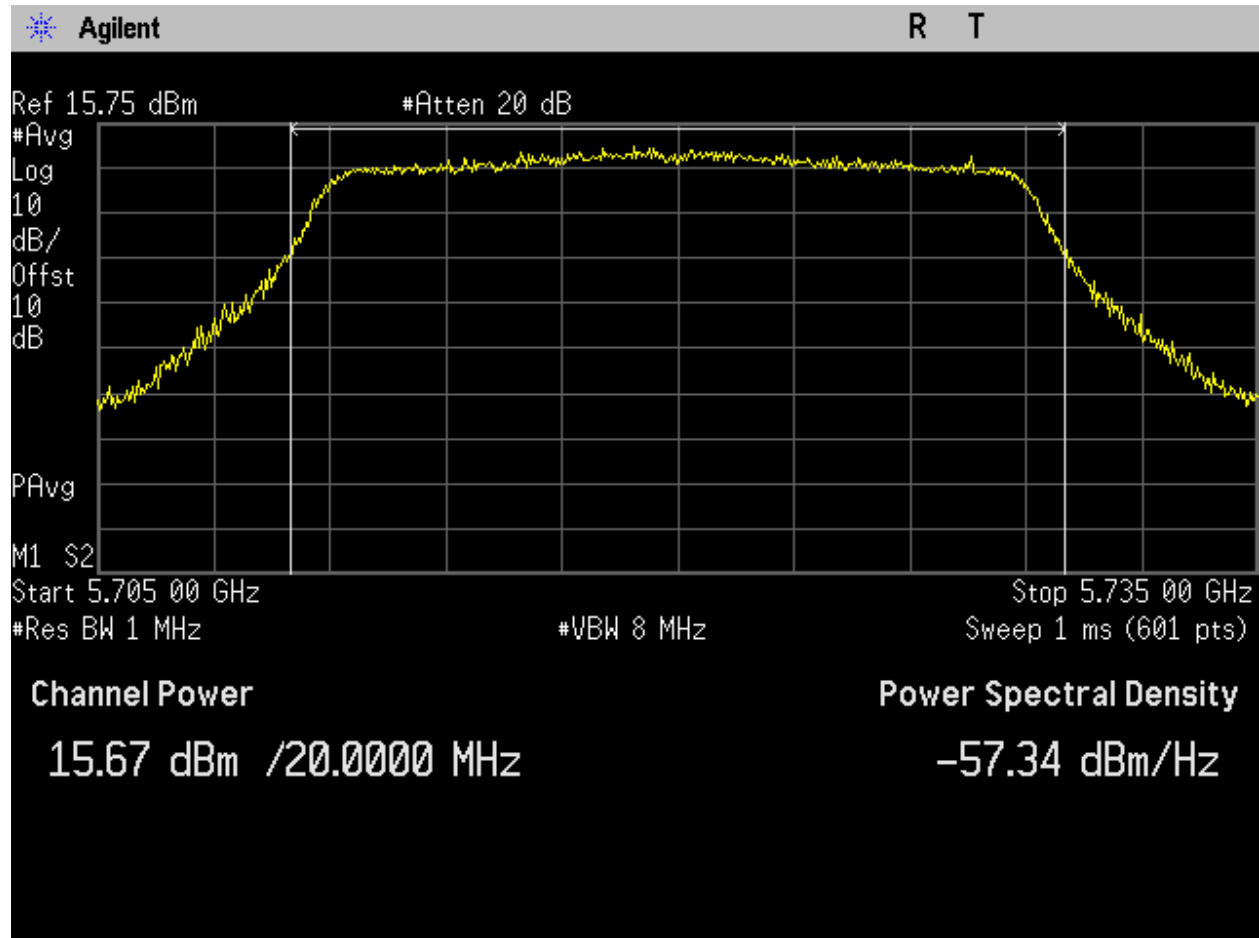


Figure 203: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Output Power_Port 1.

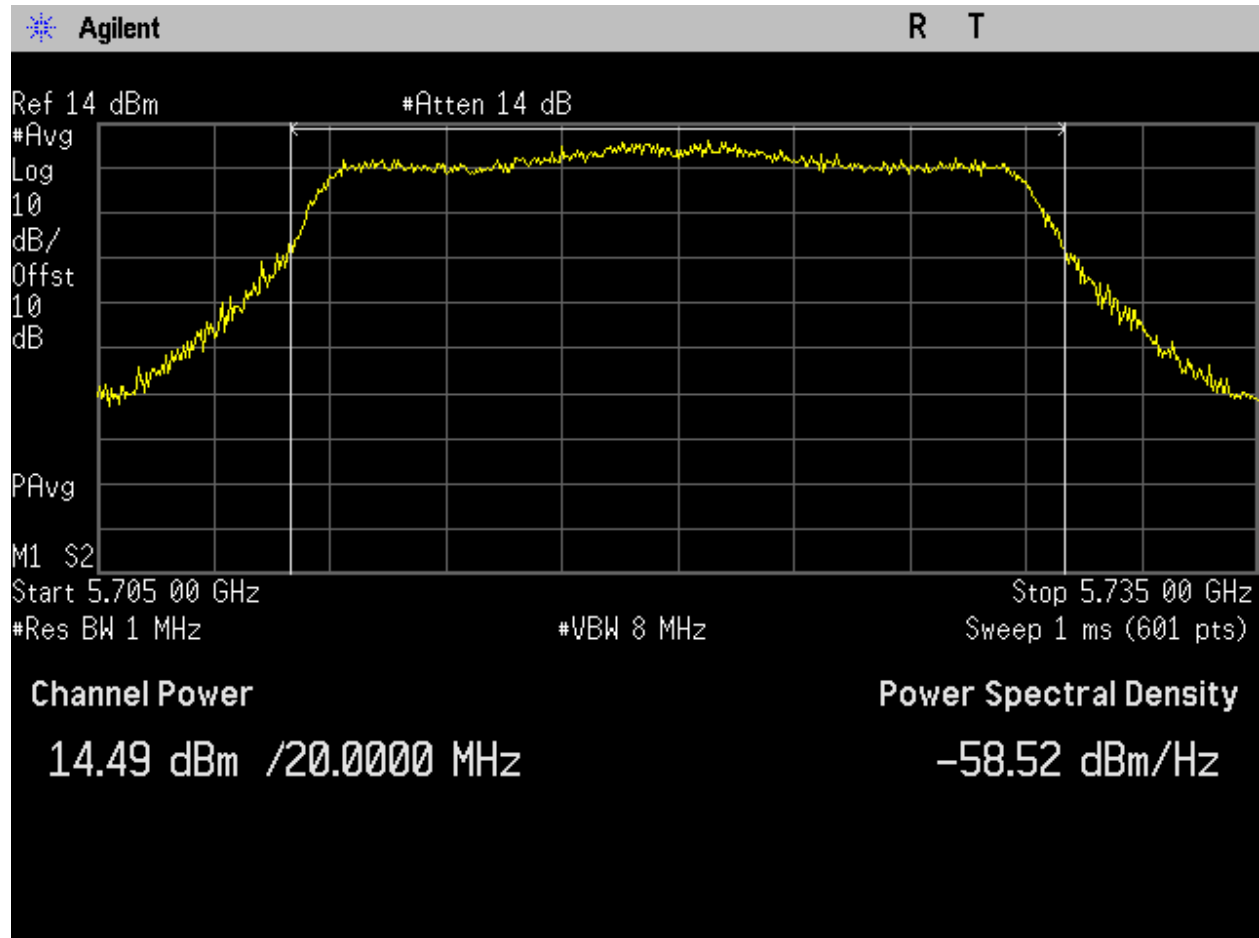


Figure 204: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Output Power_Port 2.

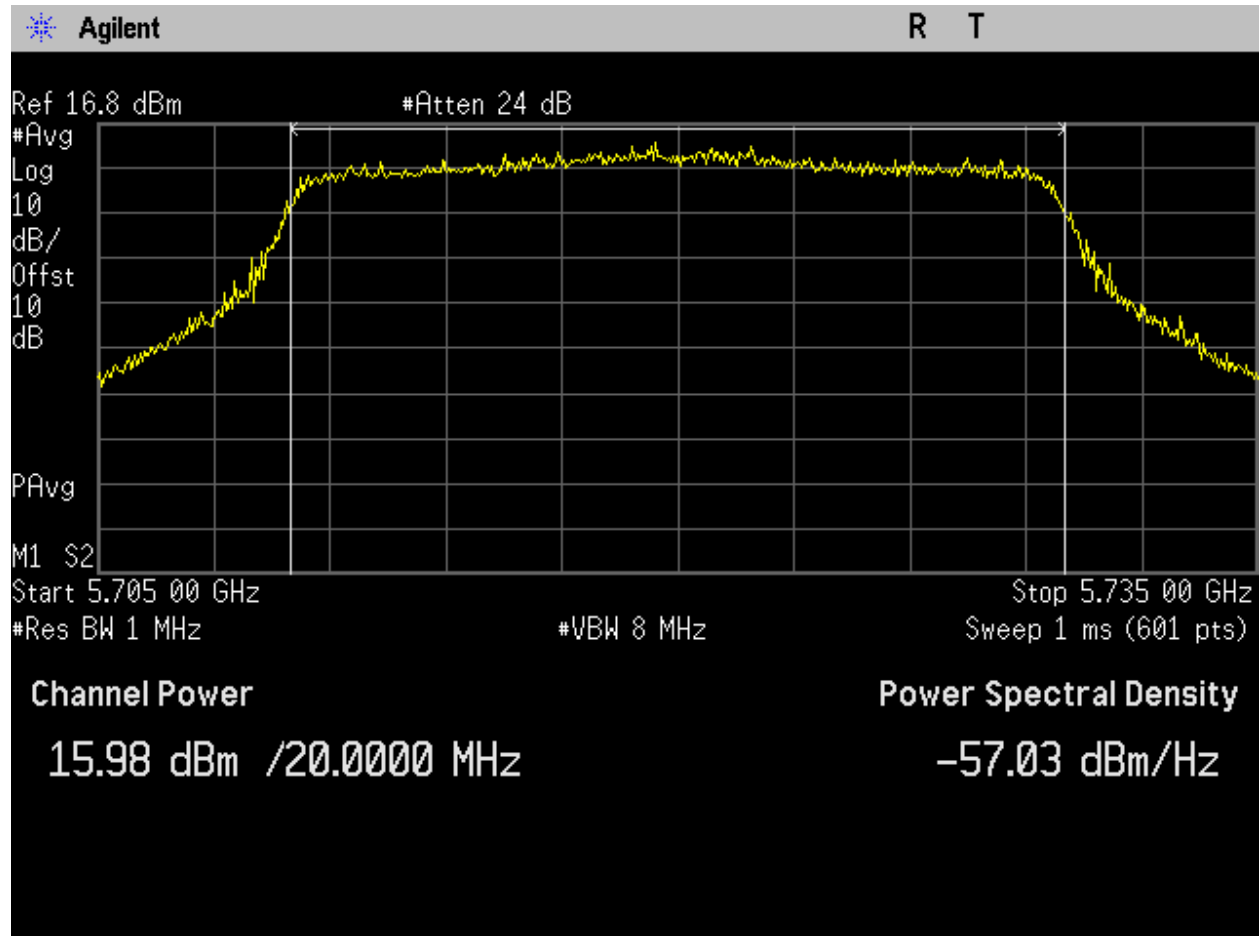


Figure 205: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ax-mode_Output Power_Port 1.

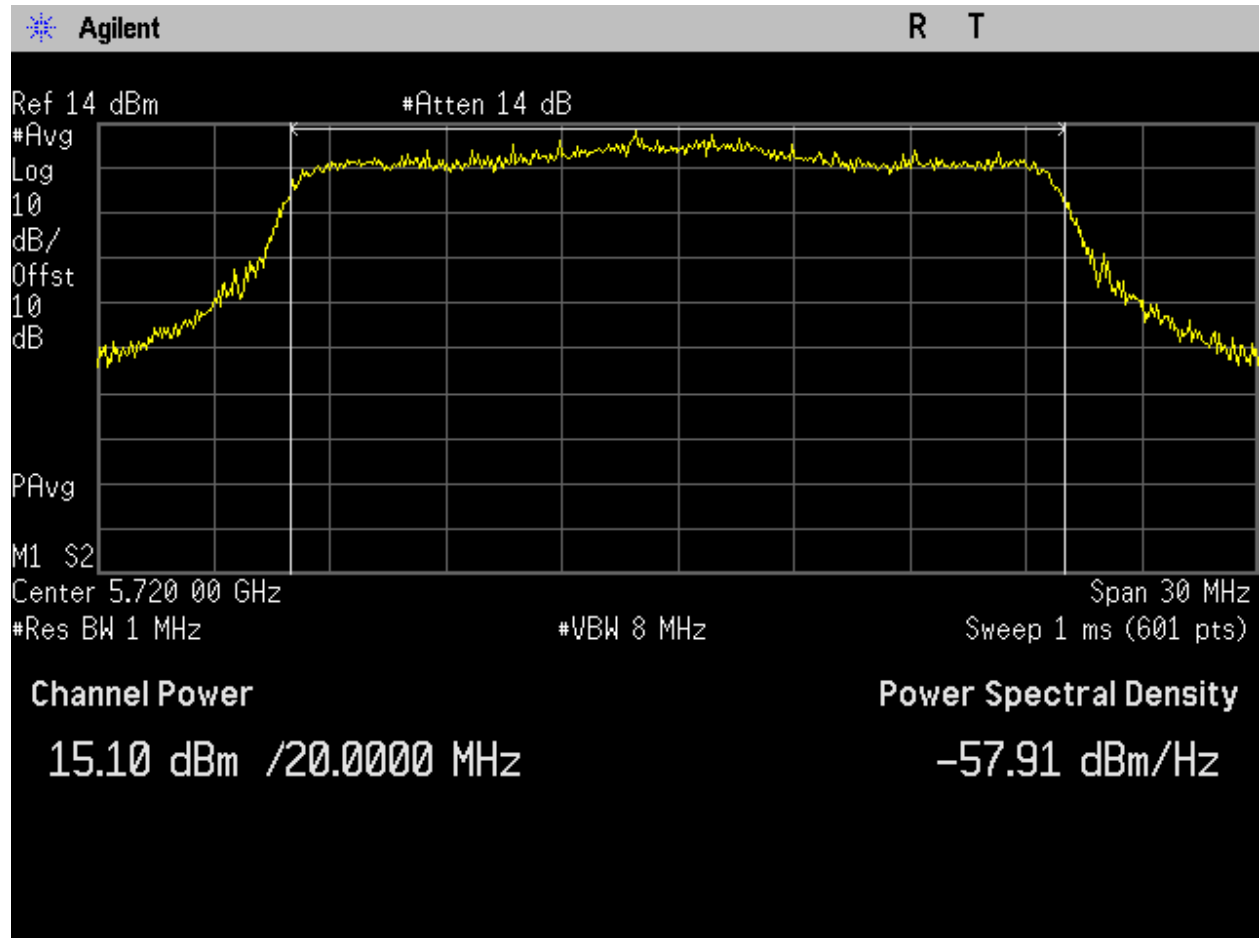


Figure 206: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ax-mode_Output Power_Port 2.

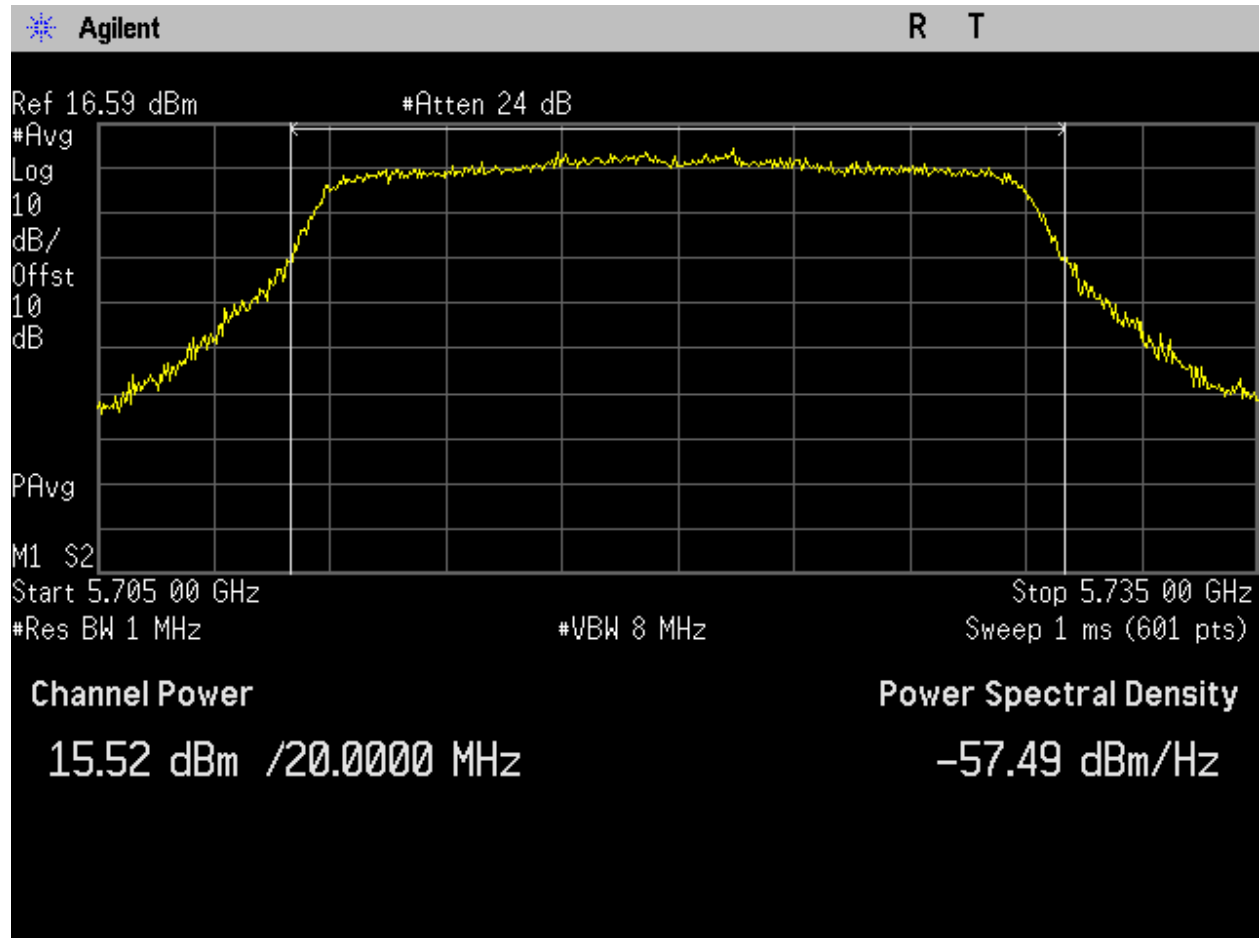


Figure 207: U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Output Power_Port 1.



Figure 208: U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Output Power_Port 2.

TPC (24dBm limit)

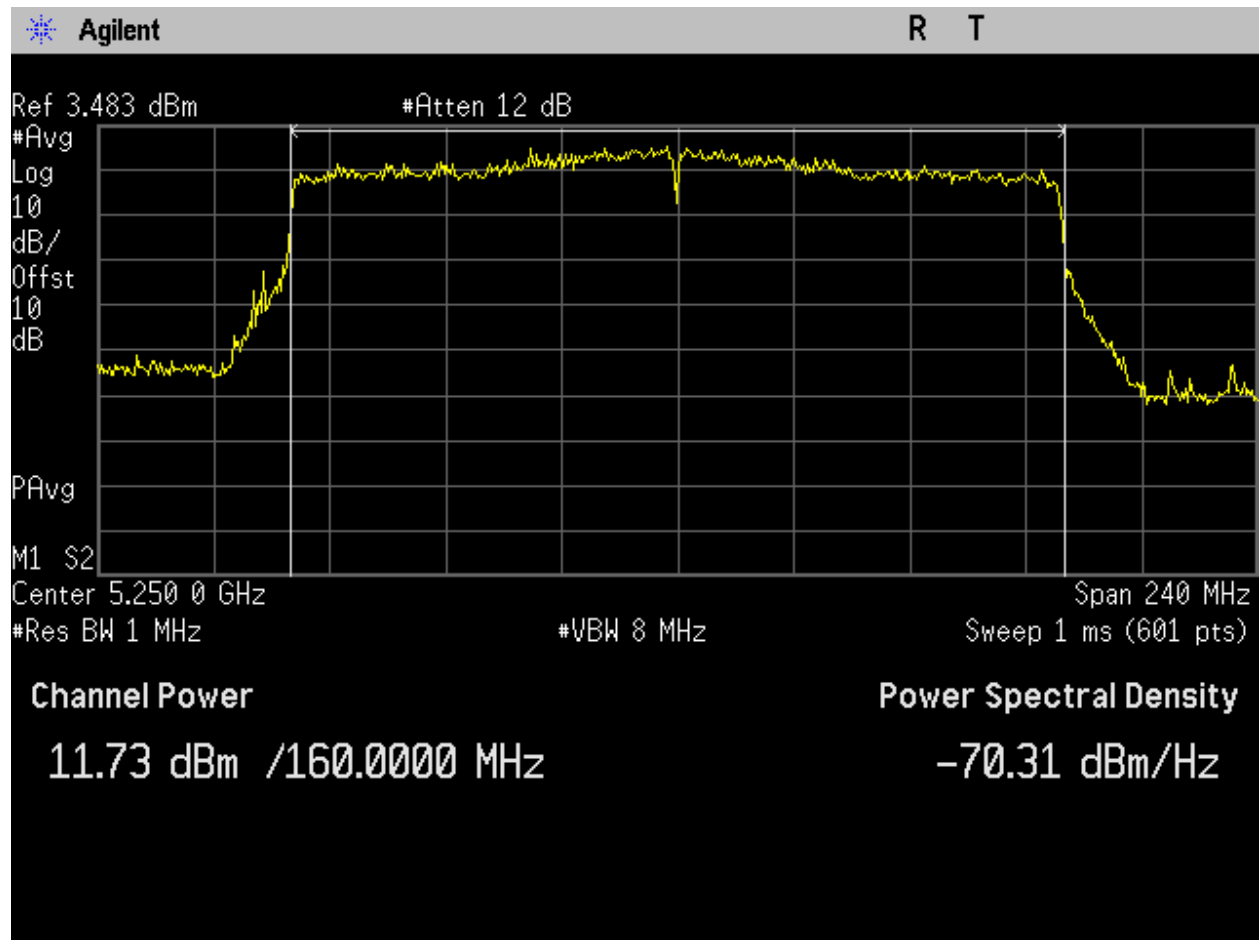


Figure 209: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Output Power_Port 1.

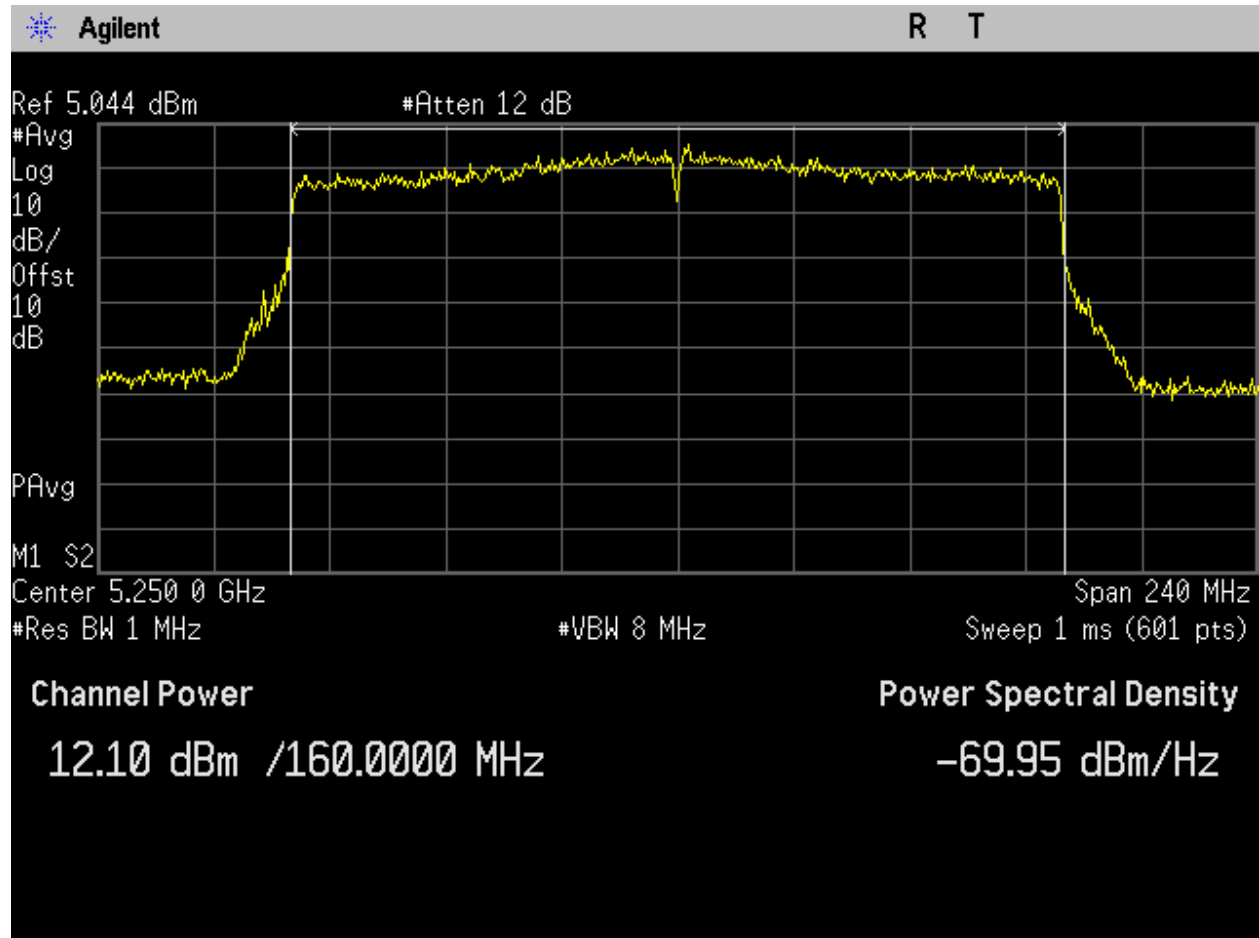


Figure 210: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Output Power_Port 2.

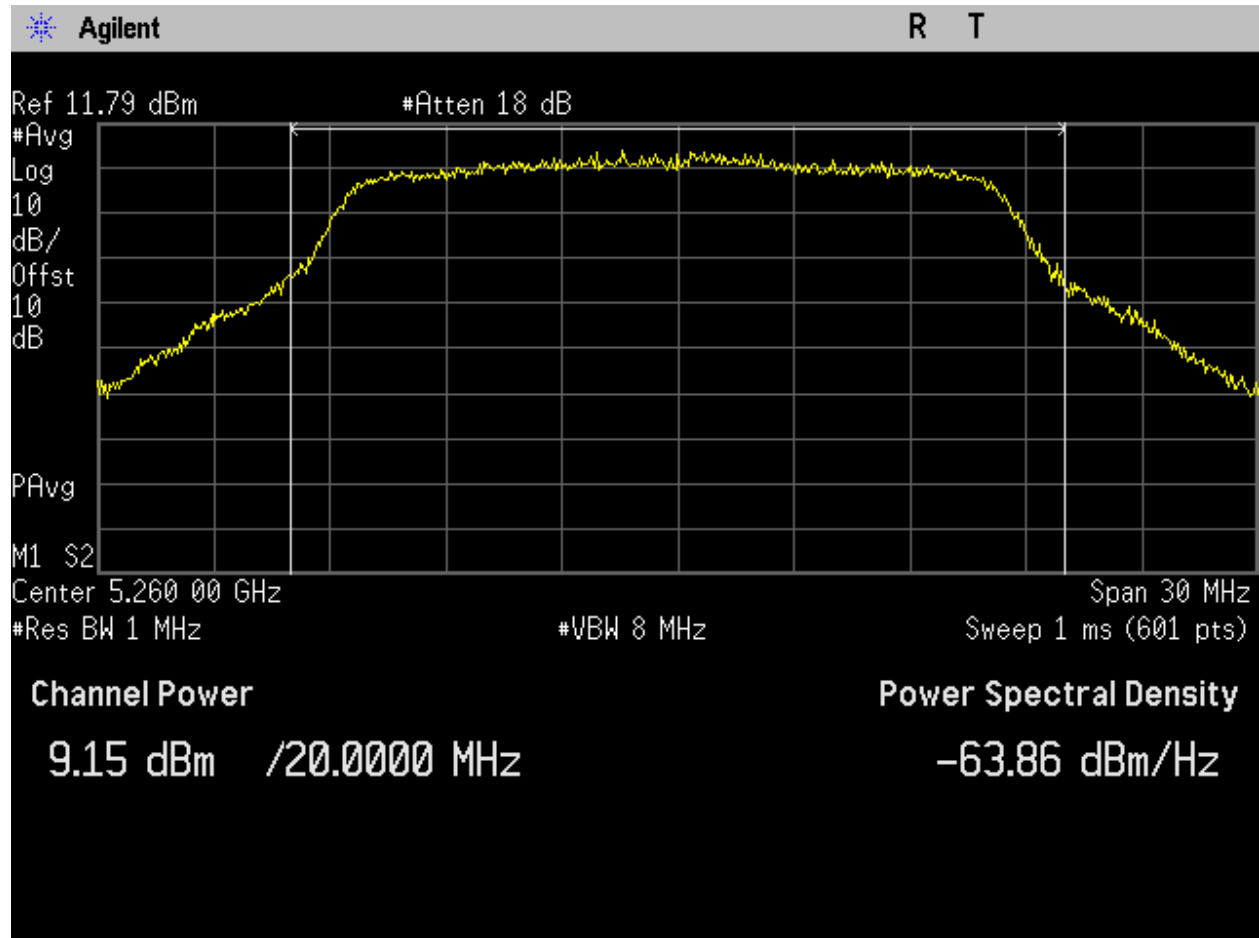


Figure 211: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_Output Power_Port 2.

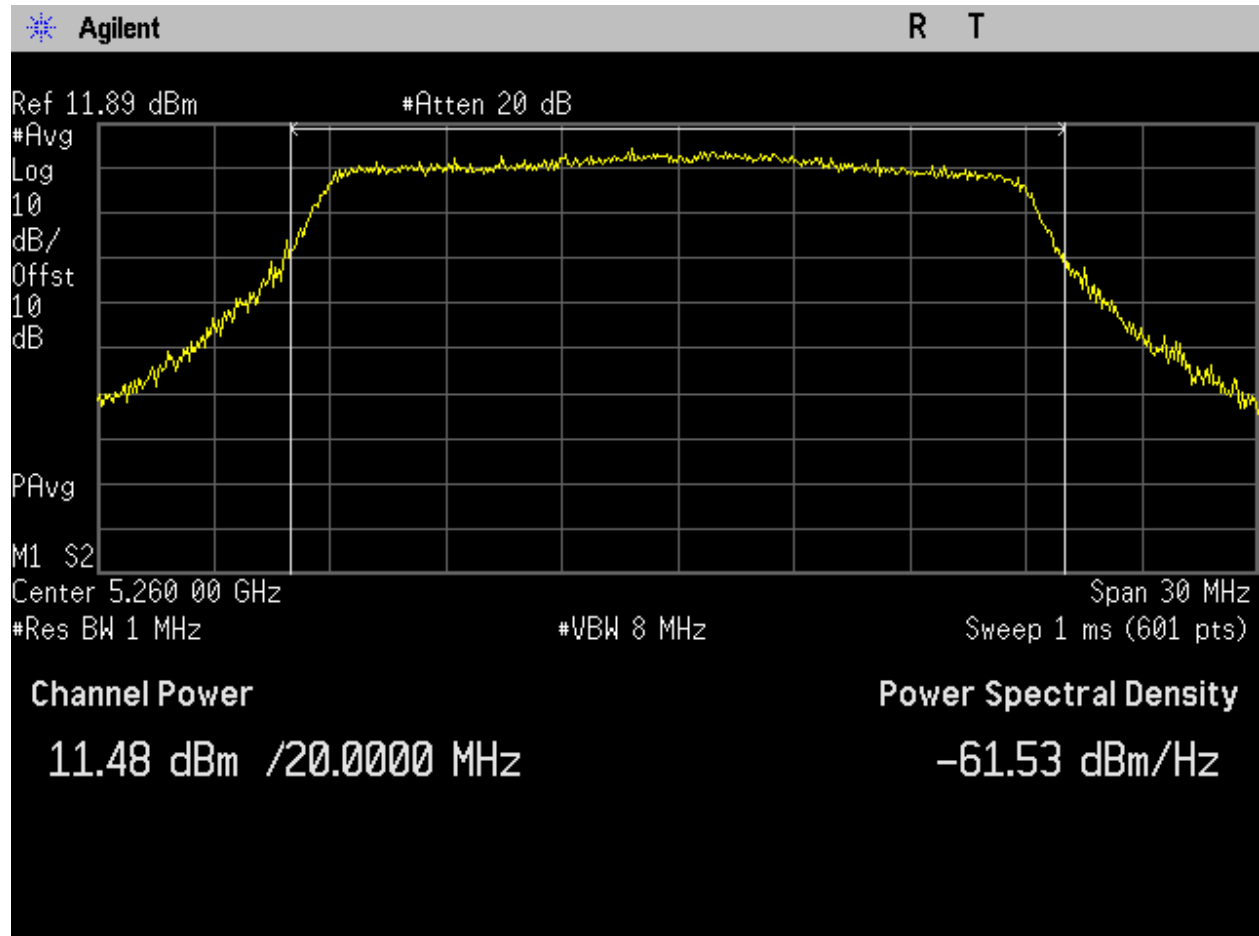


Figure 212: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_Output Power_Port 1.

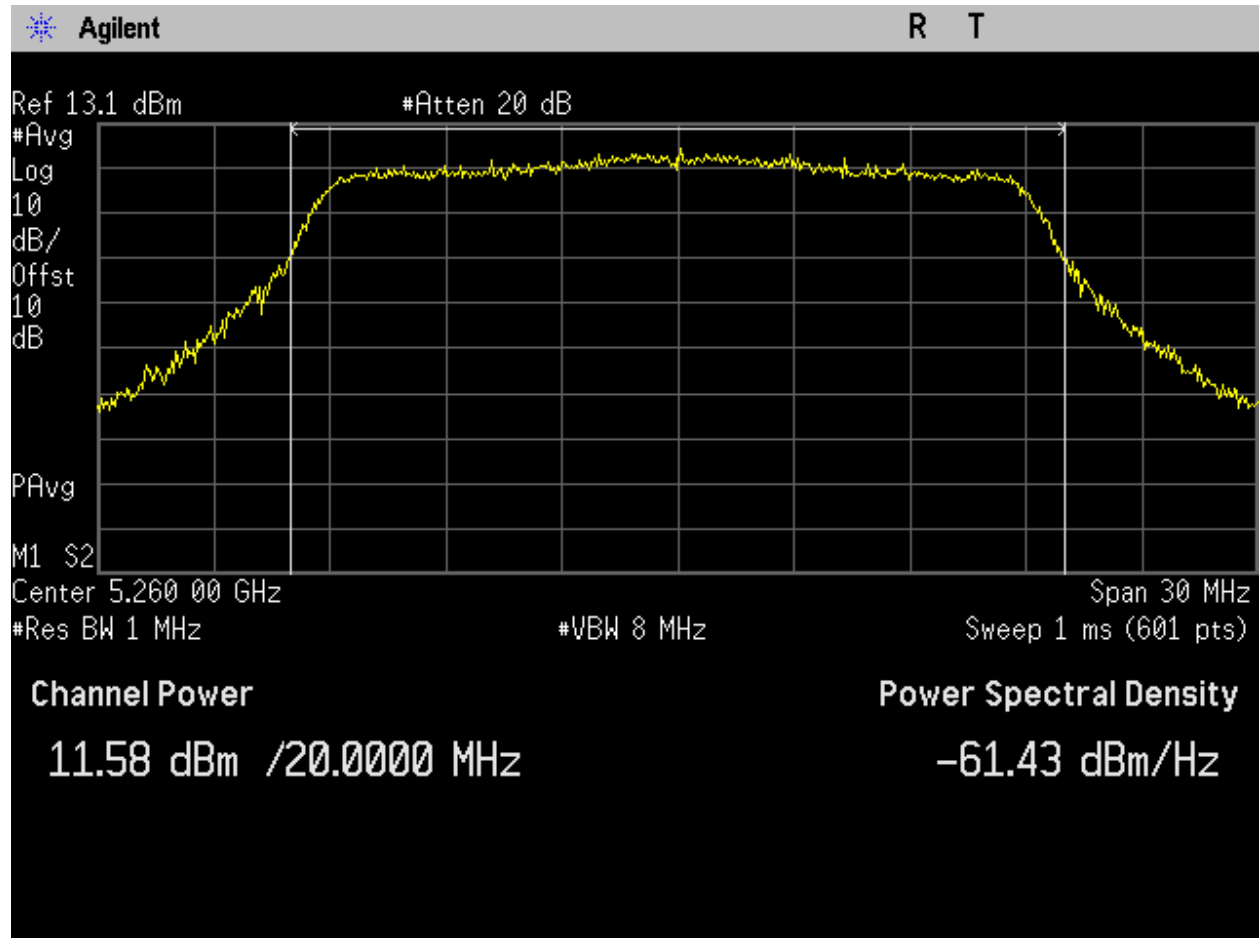


Figure 213: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_Output Power_Port 2.

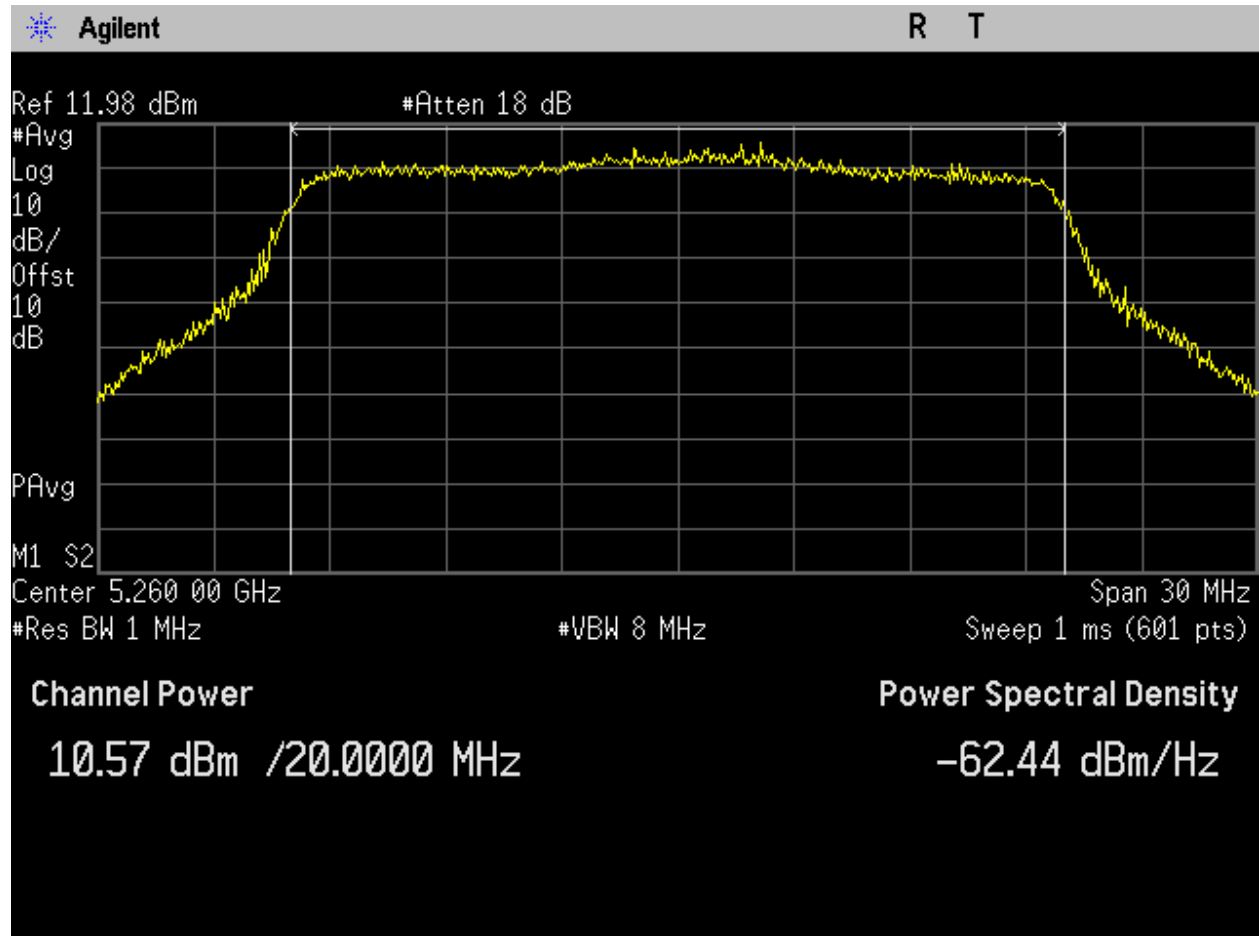


Figure 214: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_Output Power_Port 1.

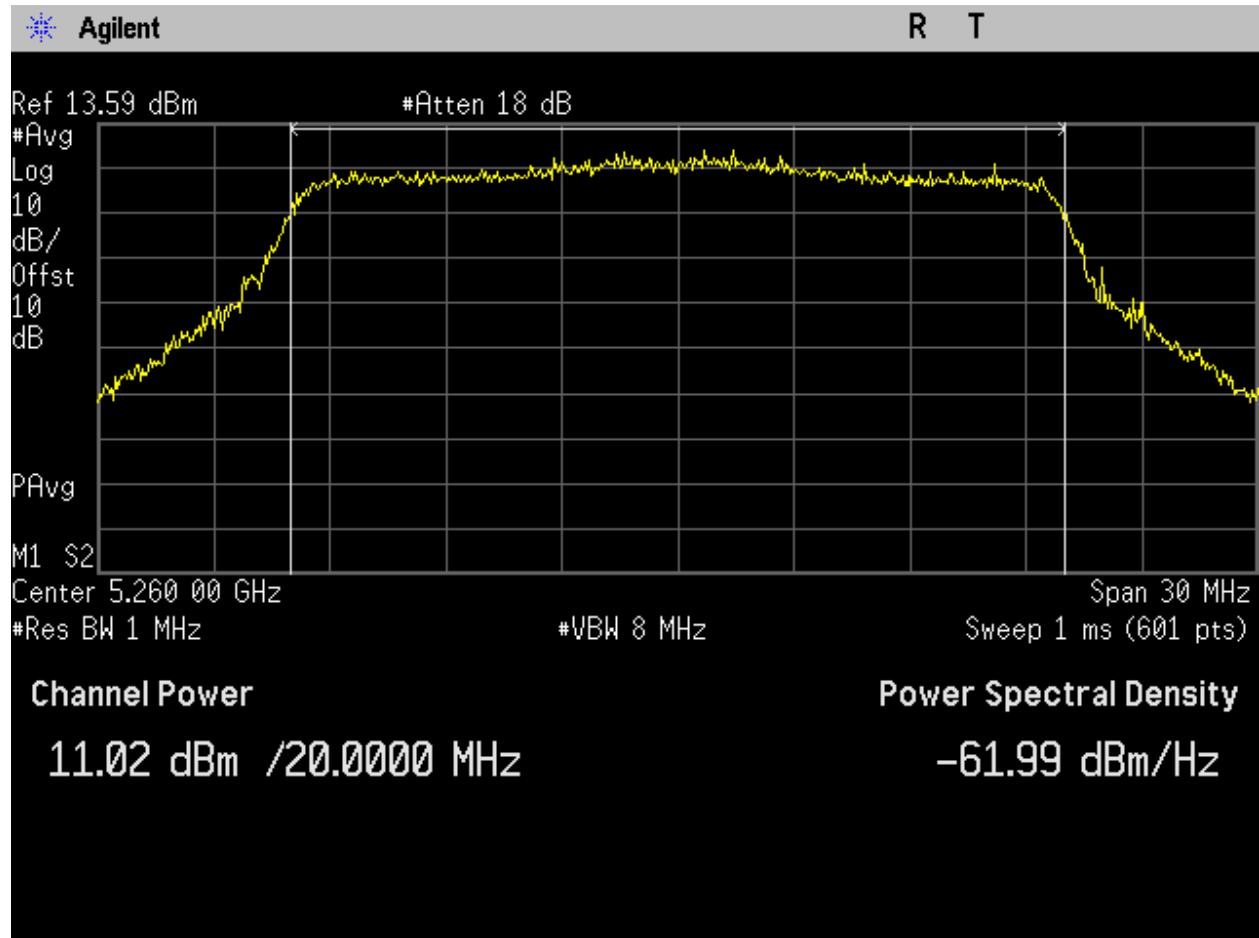


Figure 215: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_Output Power_Port 2.

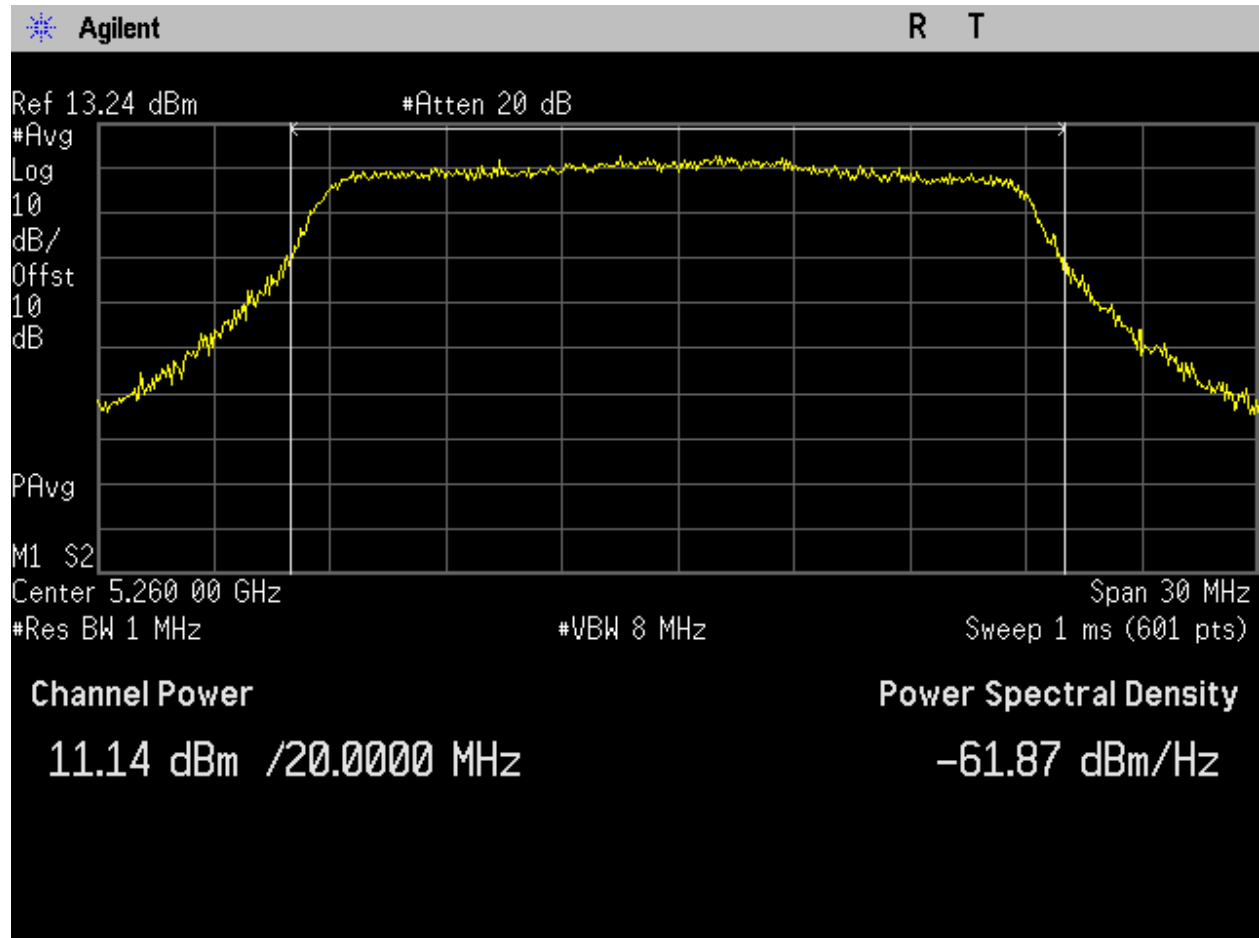


Figure 216: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_Output Power_Port 1.

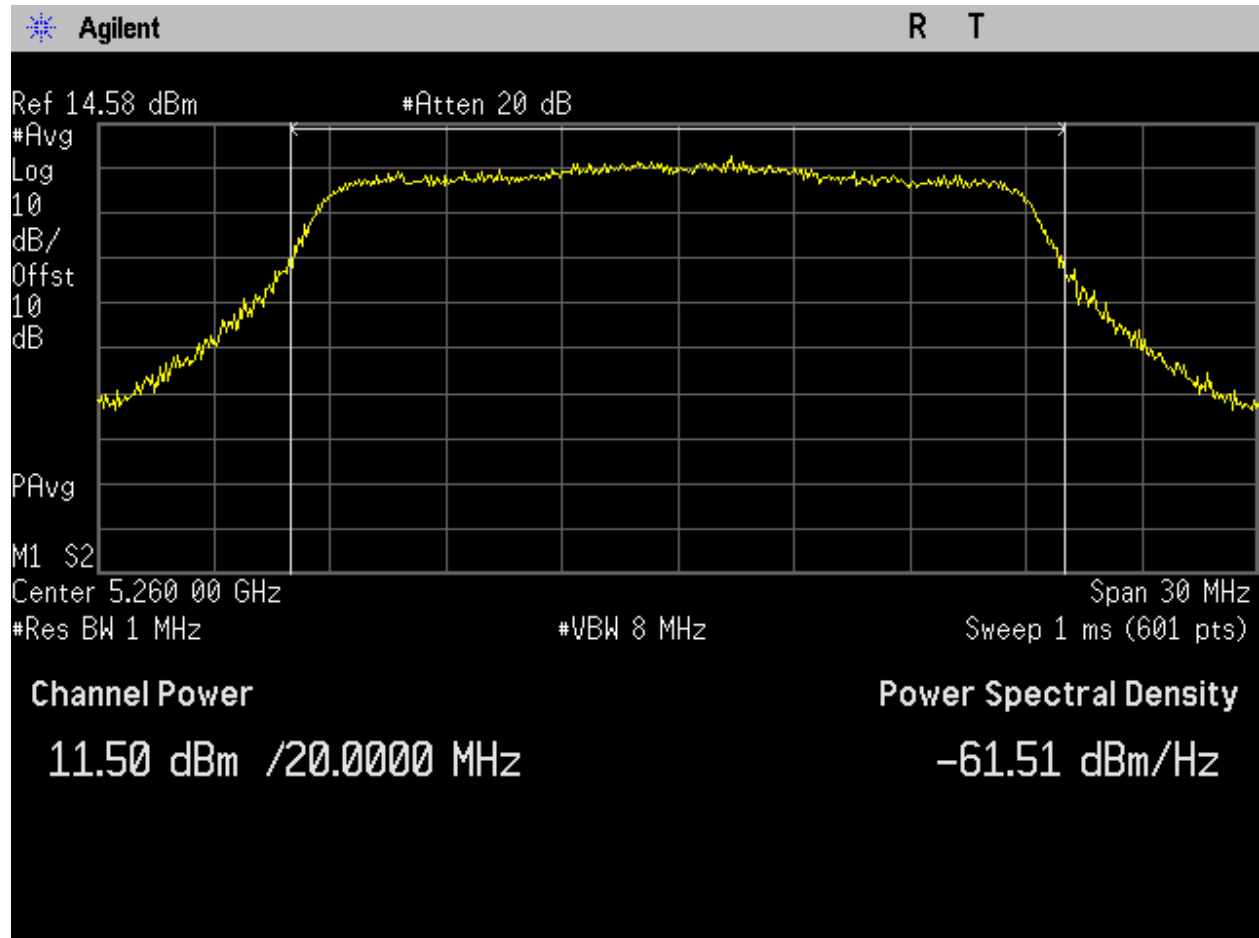


Figure 217: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_Output Power_Port 2.

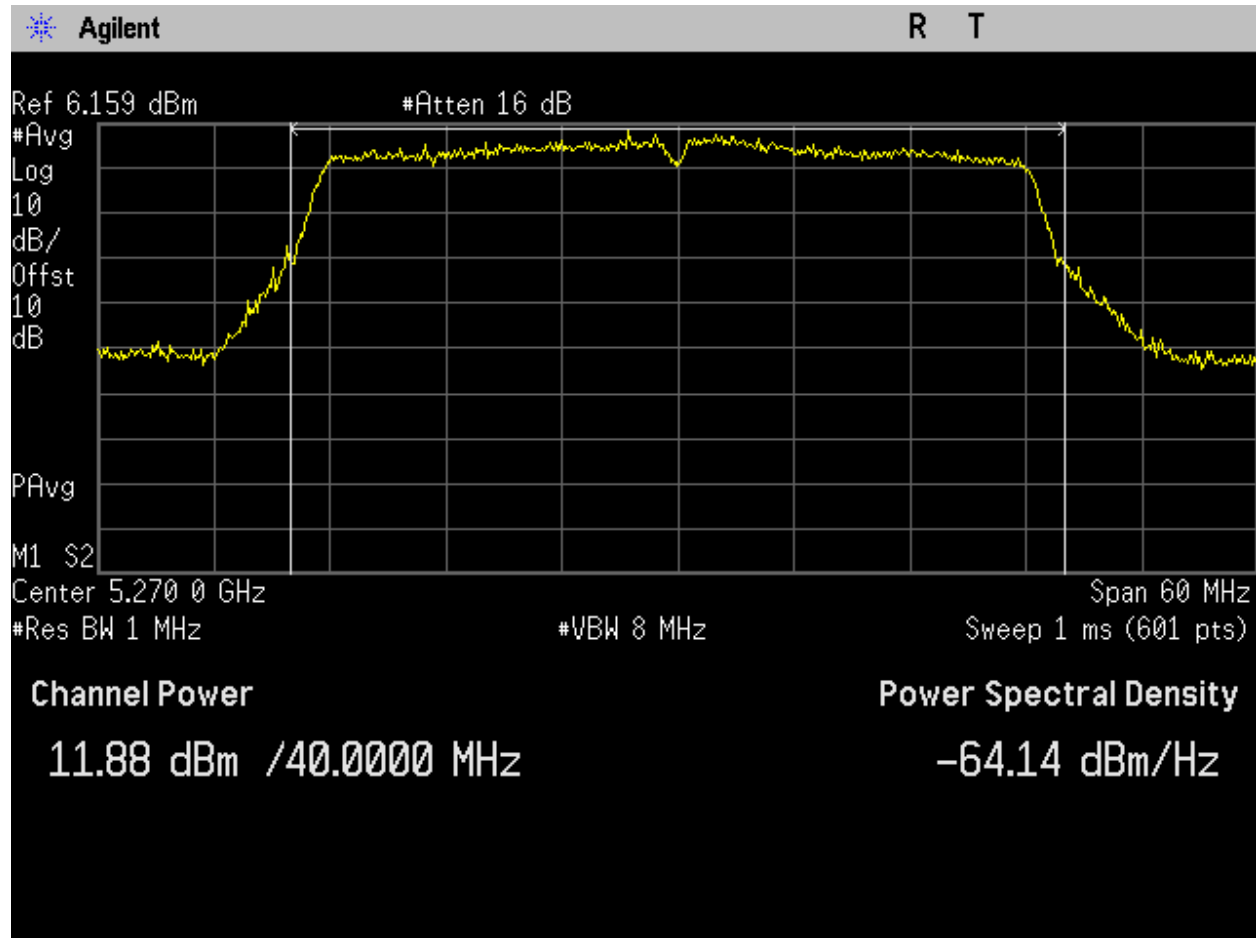


Figure 218: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Output Power_Port 1.

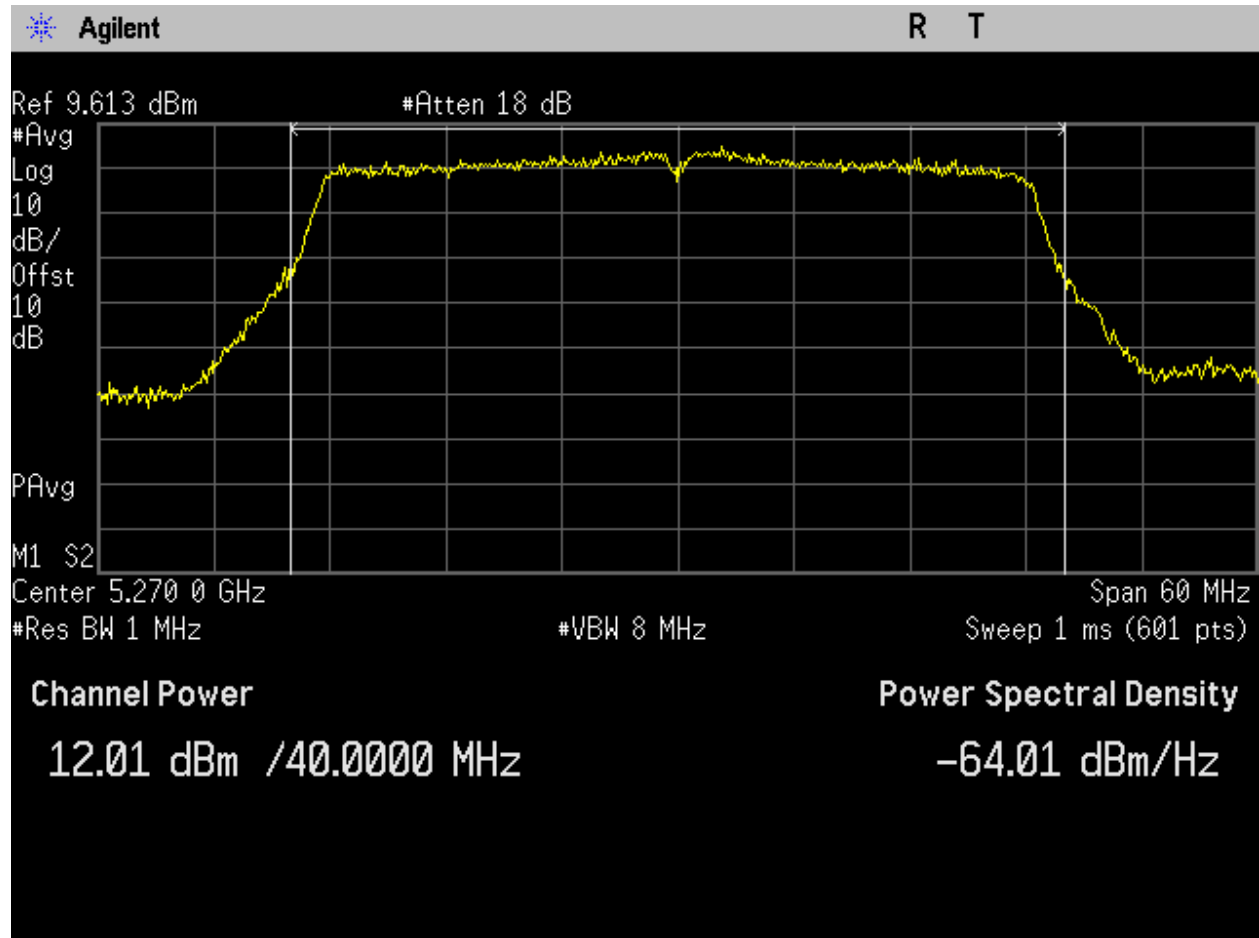


Figure 219: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Output Power_Port 2.

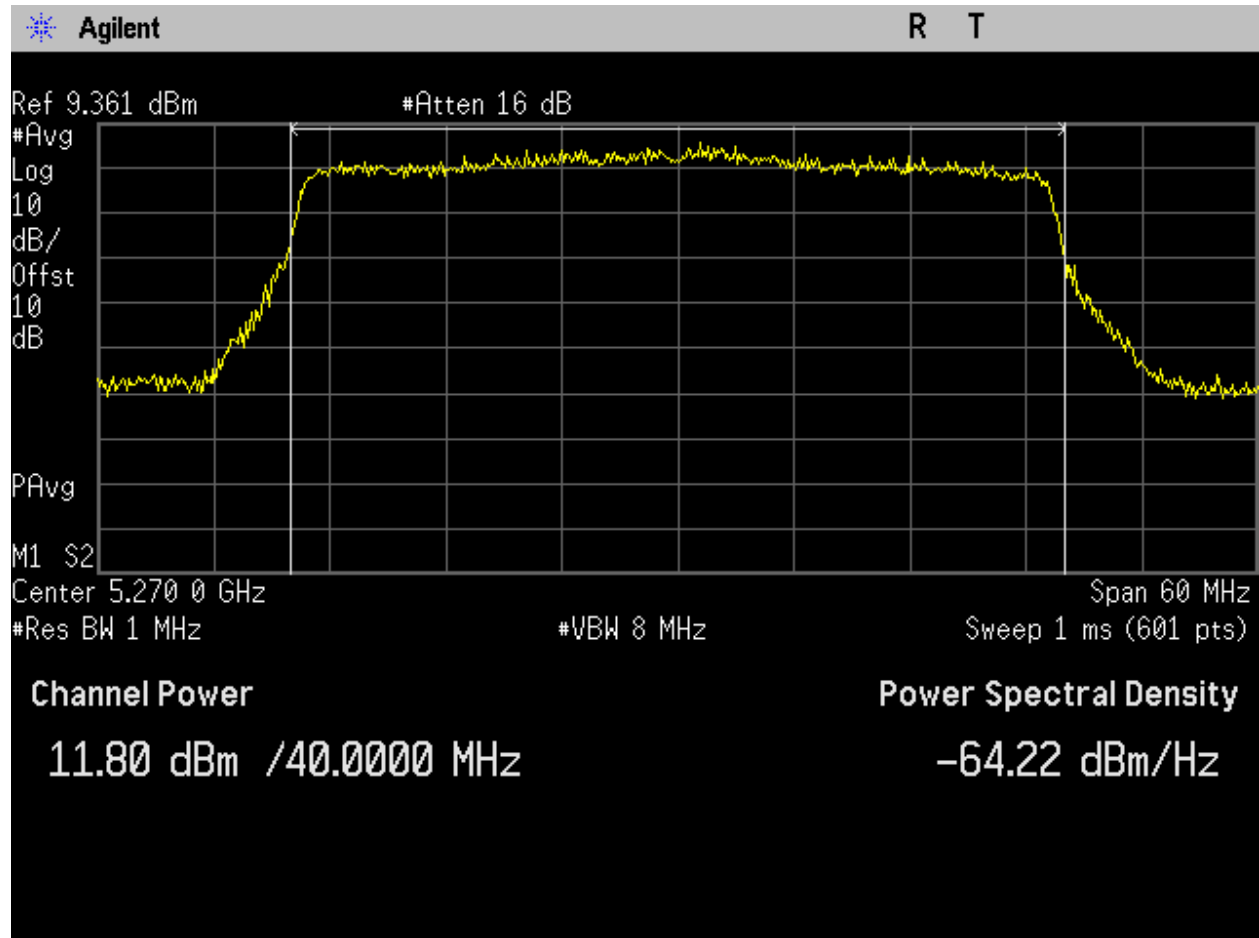


Figure 220: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Output Power_Port 1.

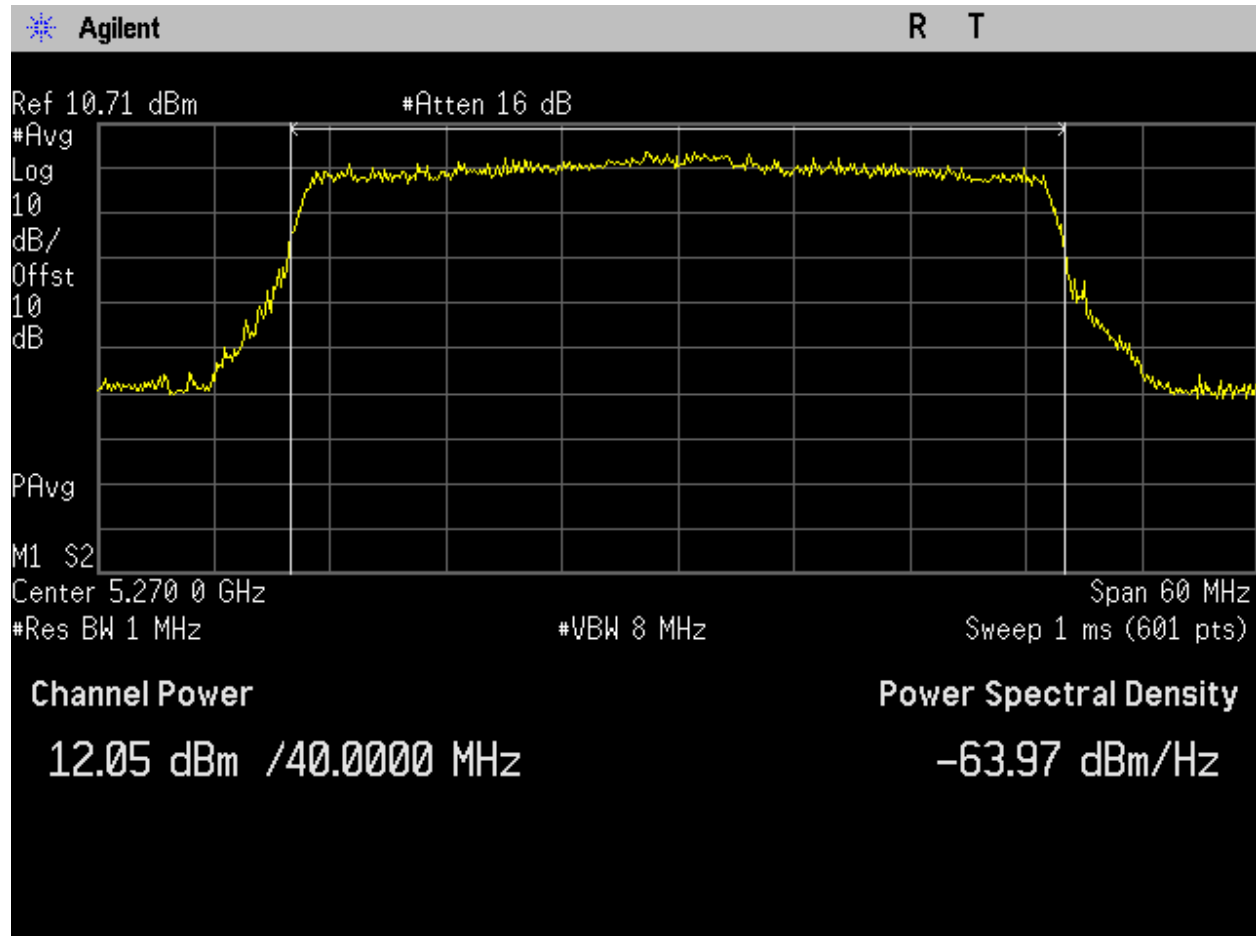


Figure 221: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Output Power_Port 2.

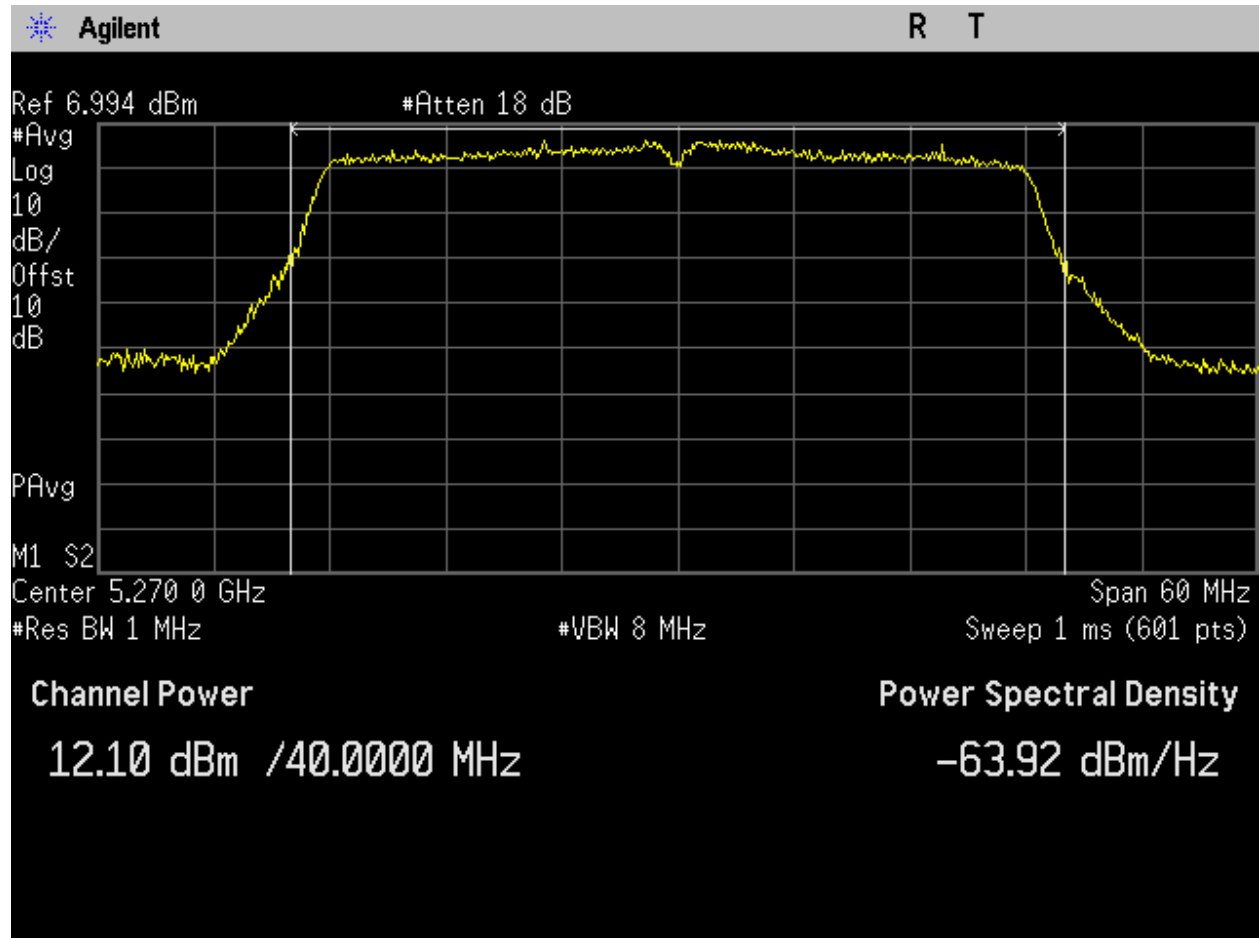


Figure 222: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Output Power_Port 1.

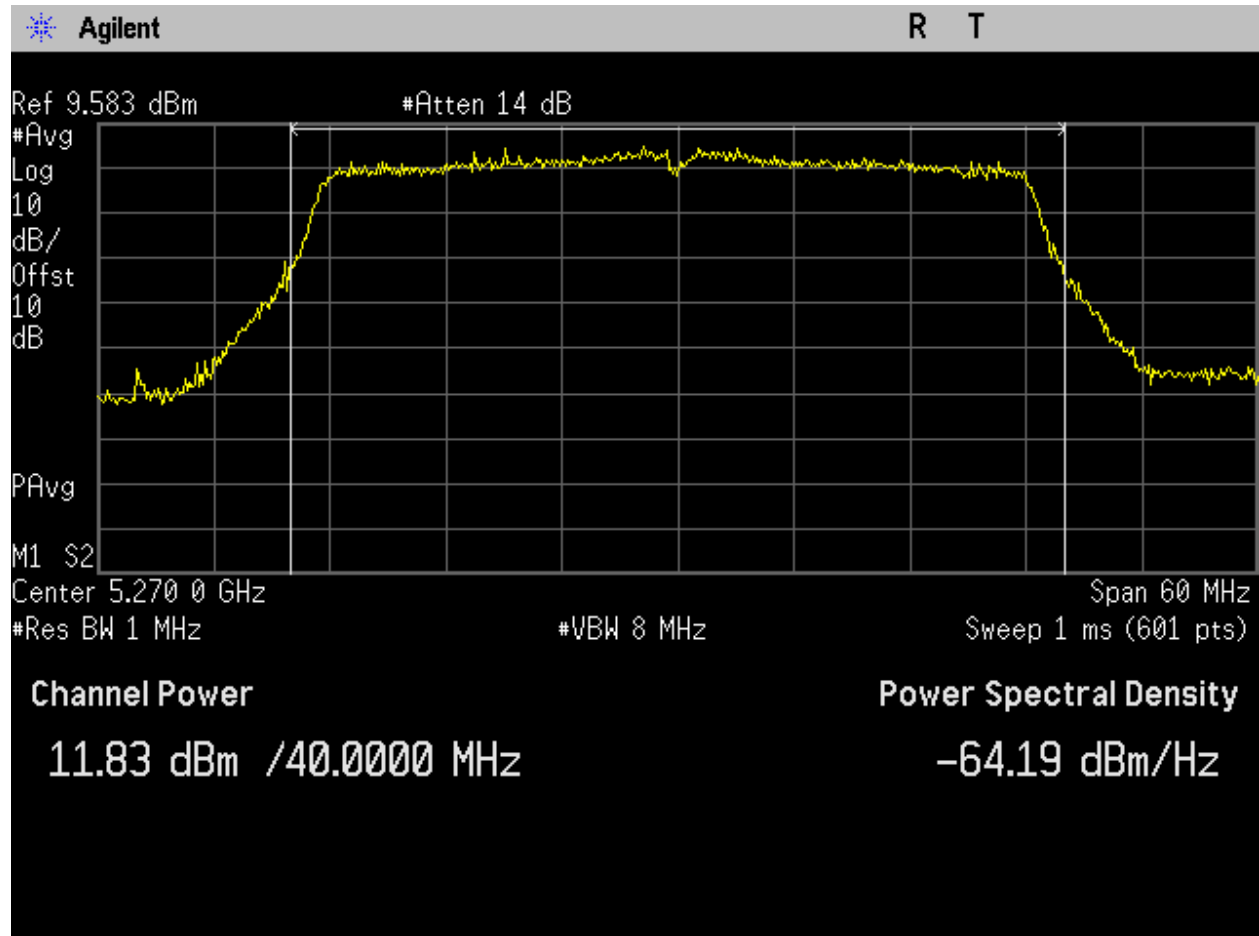


Figure 223: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Output Power_Port 2.

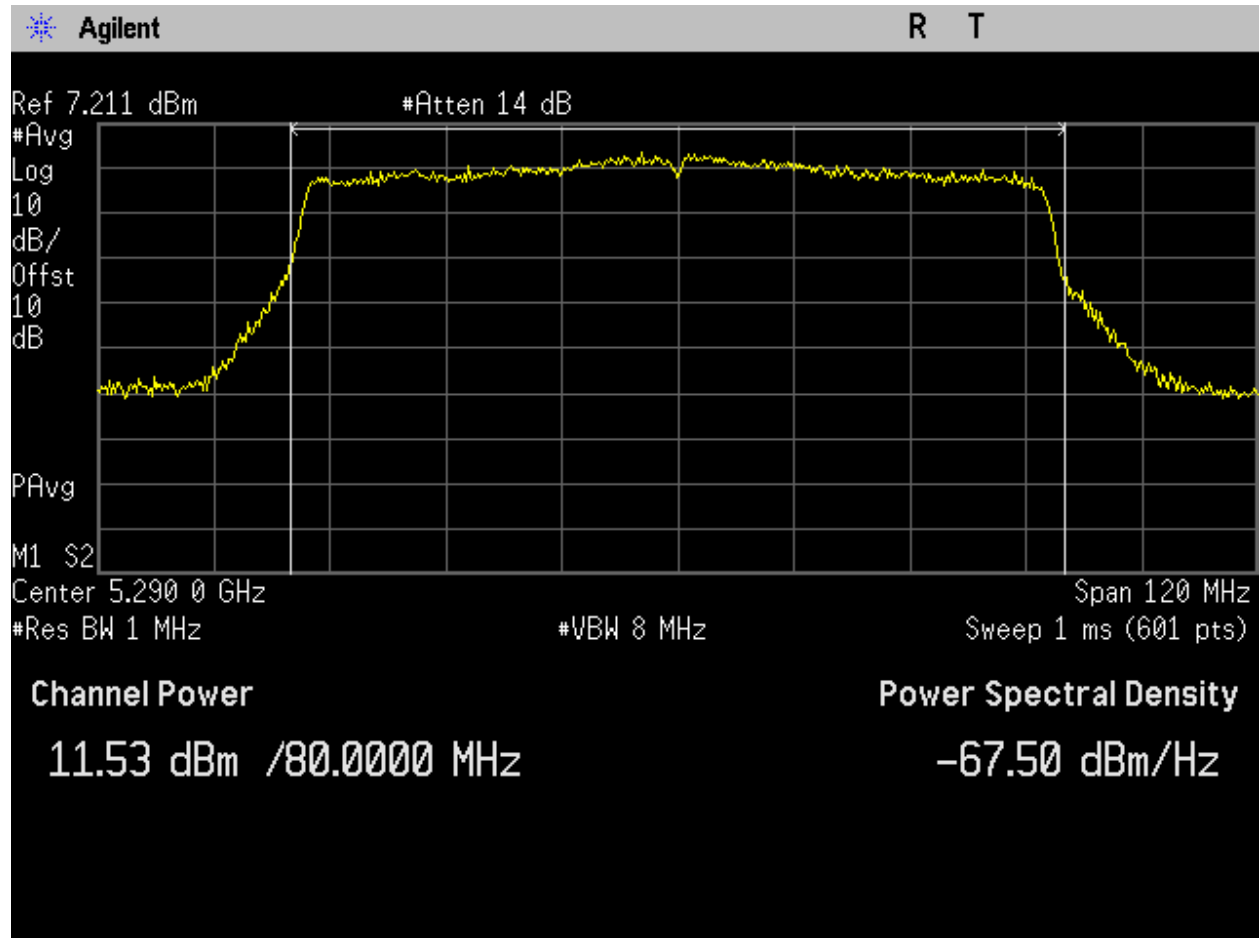


Figure 224: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Output Power_Port 1.

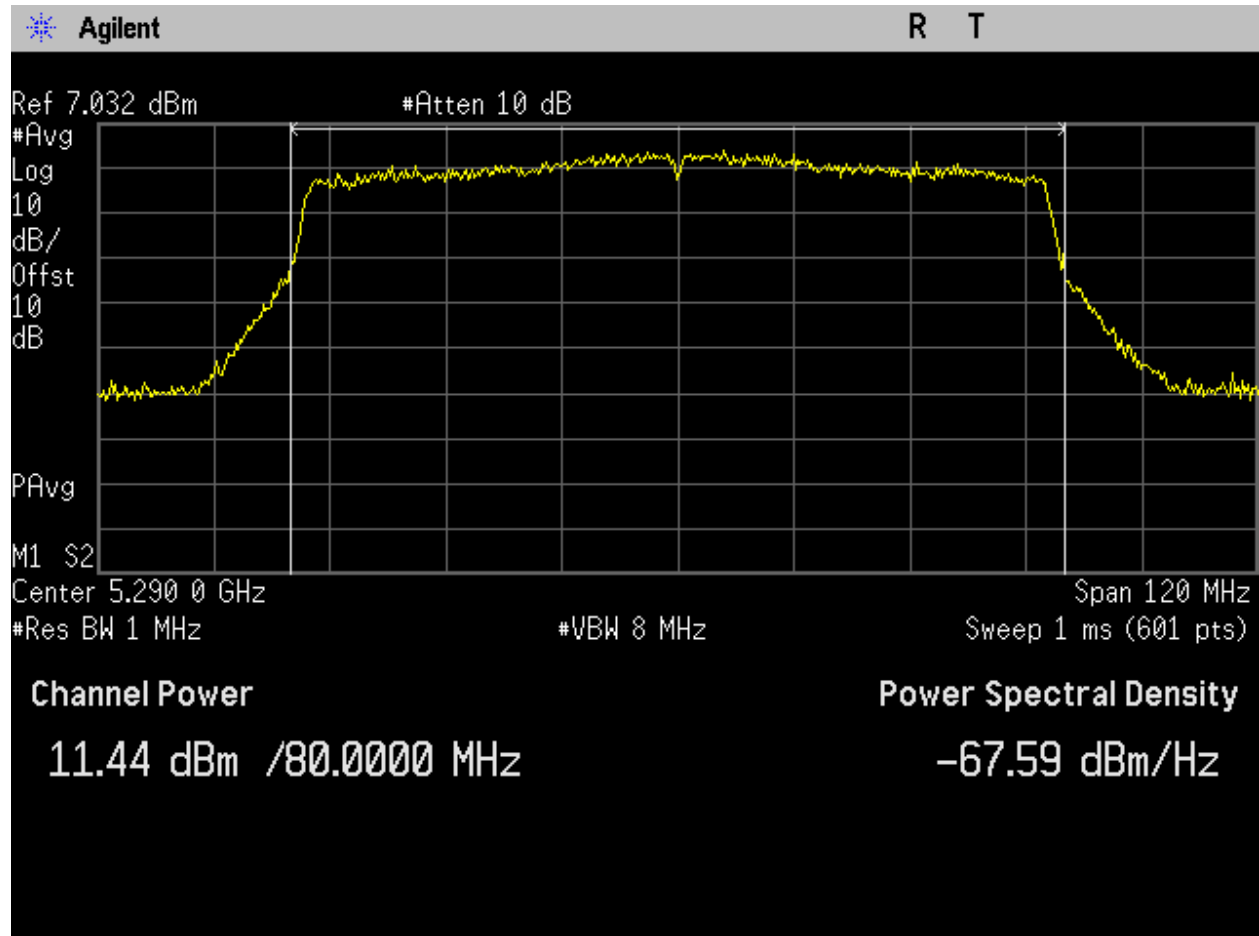


Figure 225: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Output Power_Port 2.

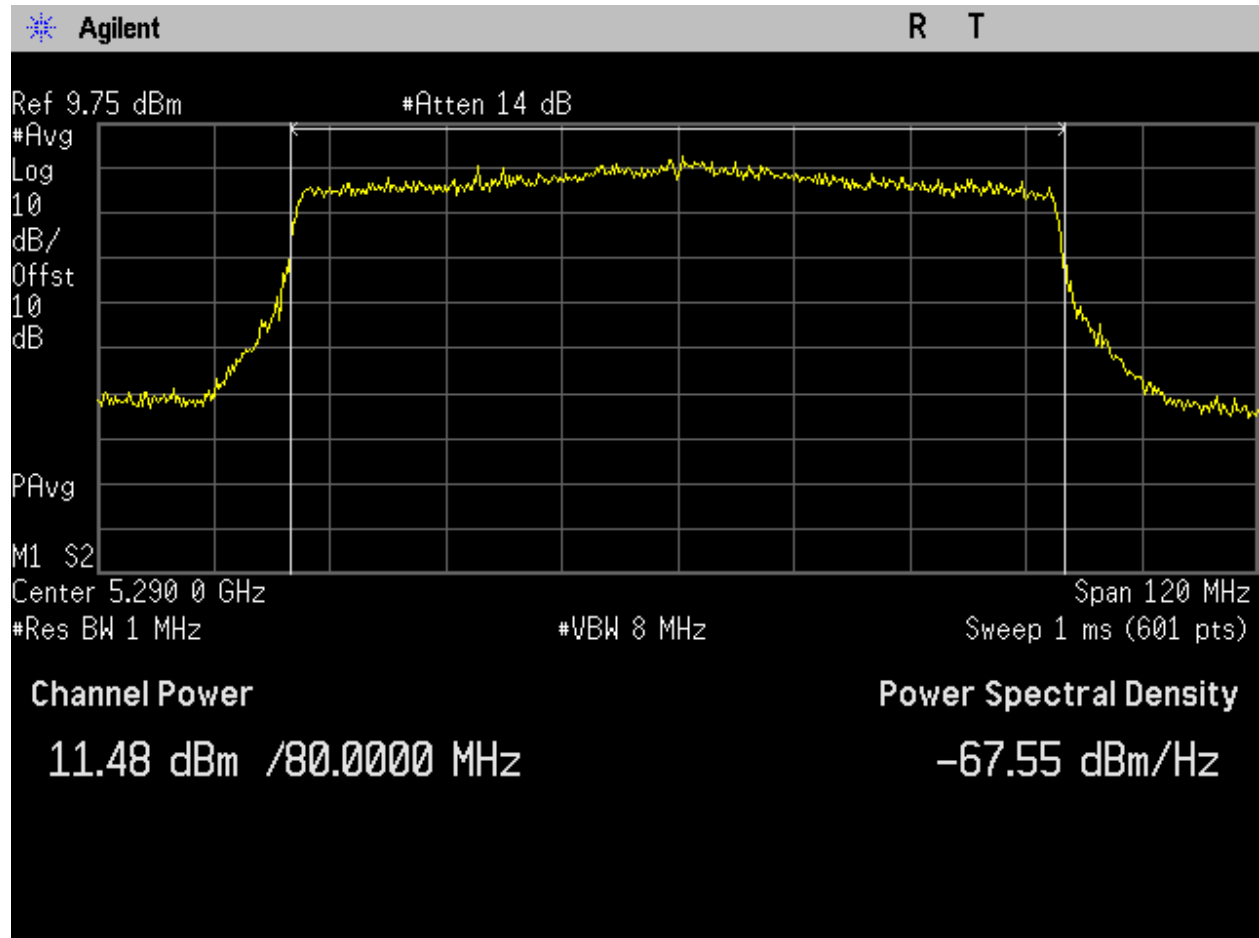


Figure 226: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Output Power_Port 1.

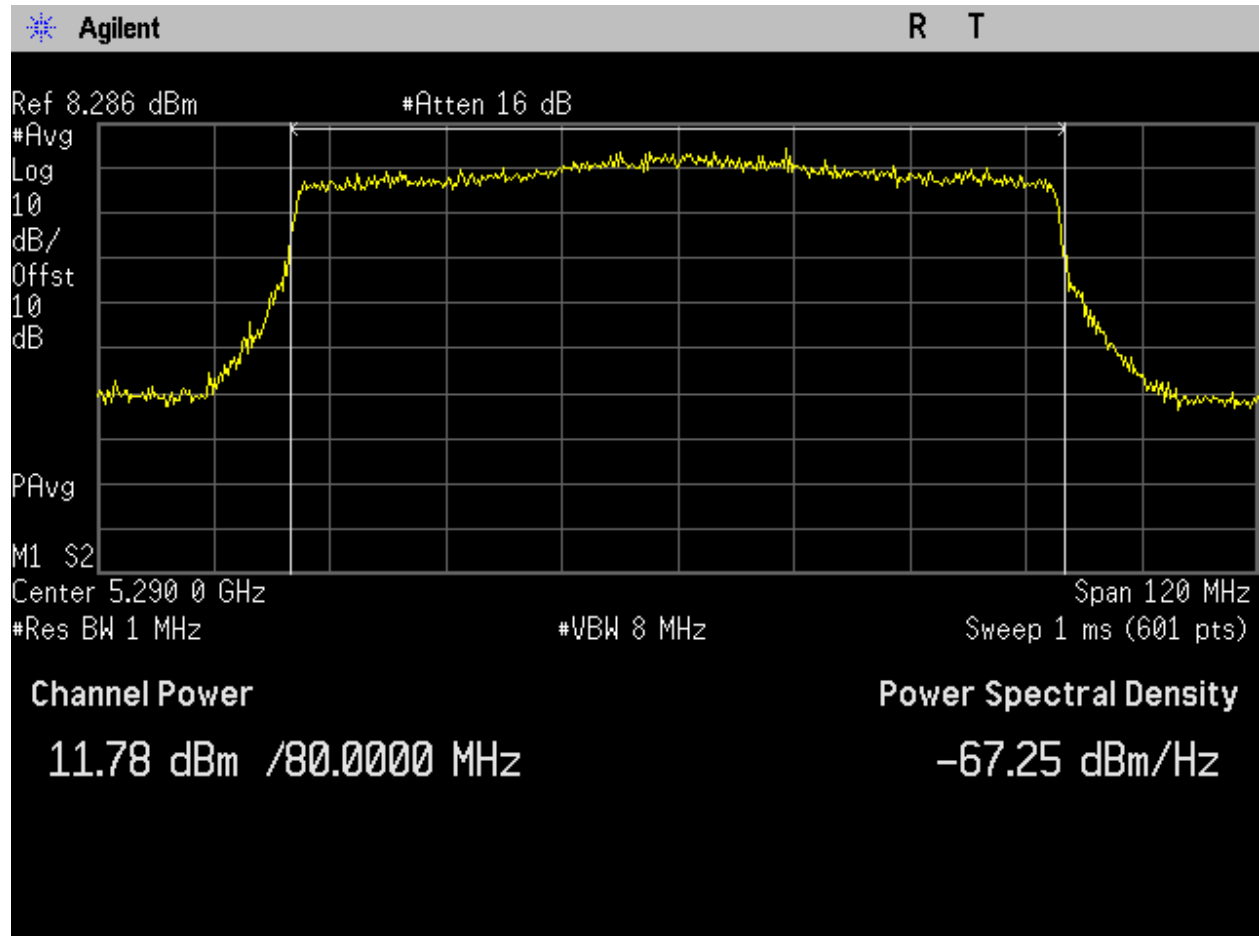


Figure 227: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Output Power_Port 2.

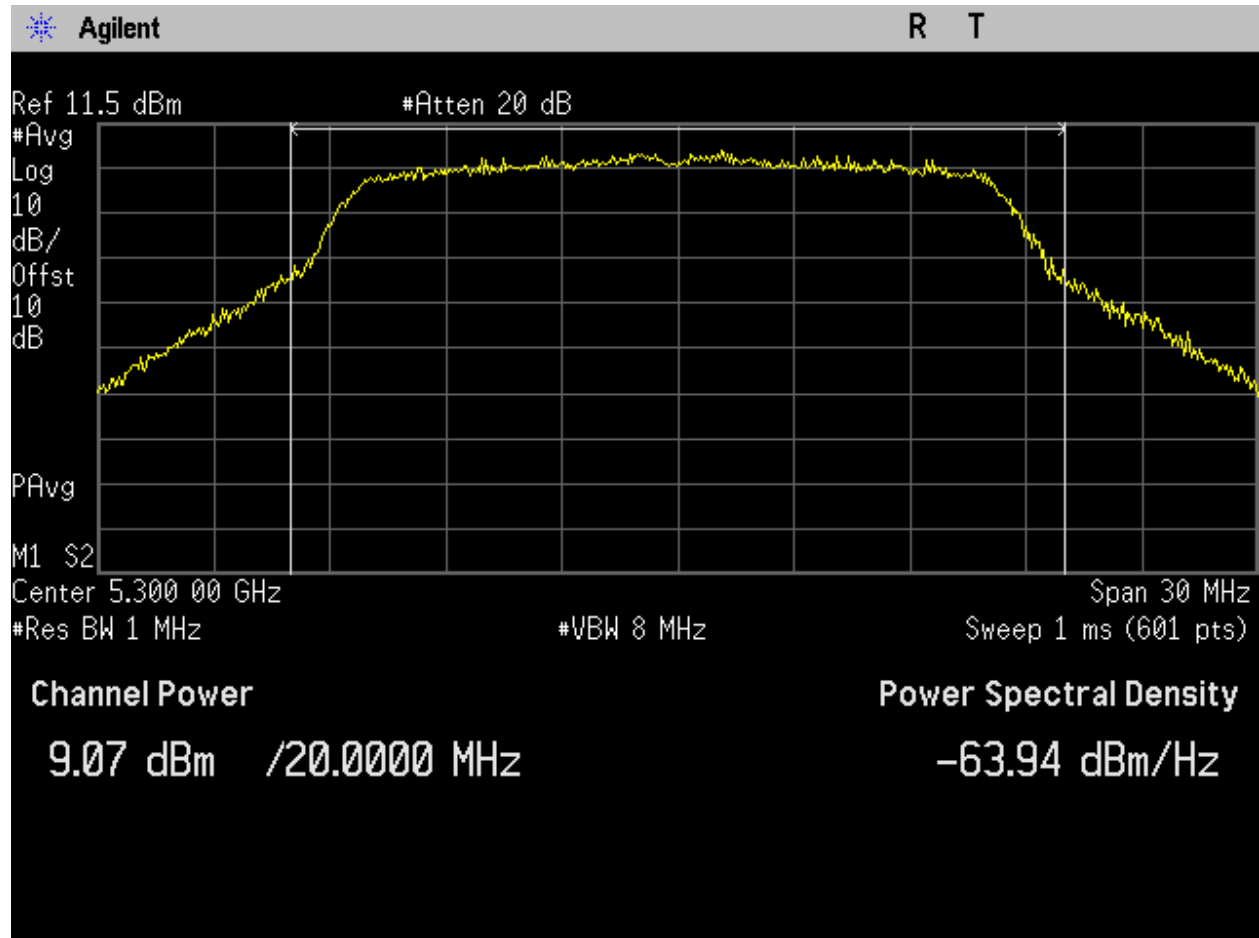


Figure 228: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Output Power_Port 1.

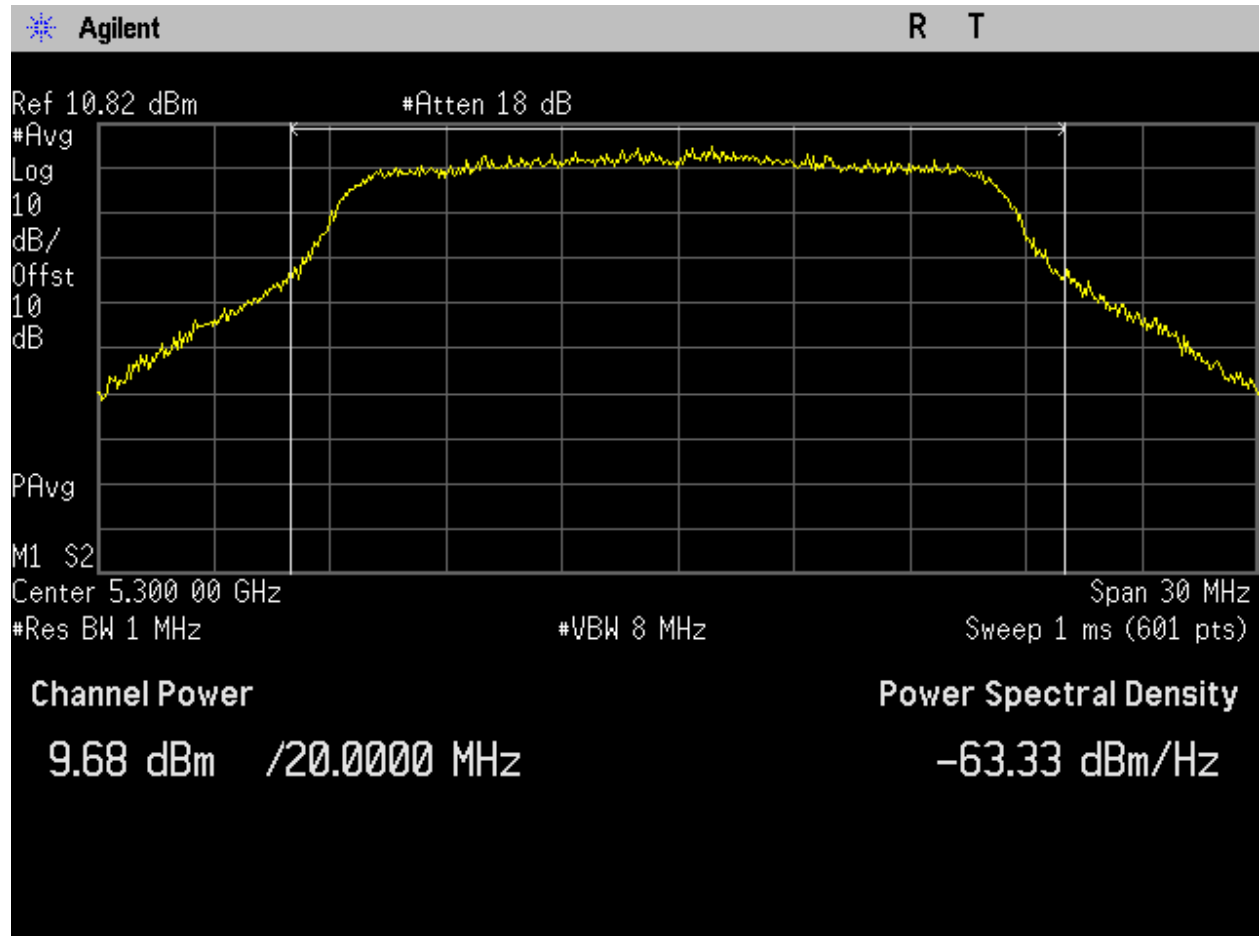


Figure 229: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Output Power_Port 2.

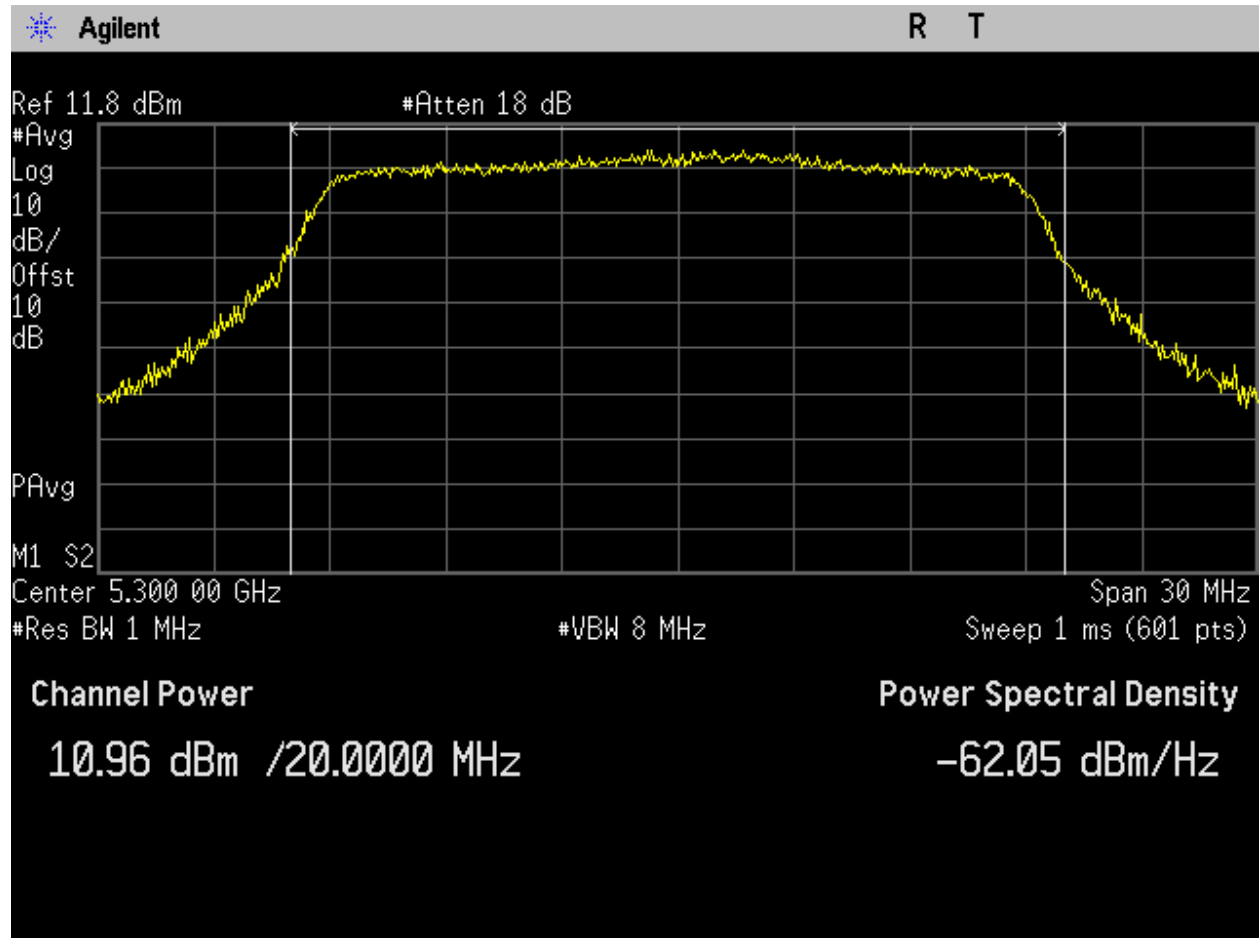


Figure 230: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Output Power_Port 1.

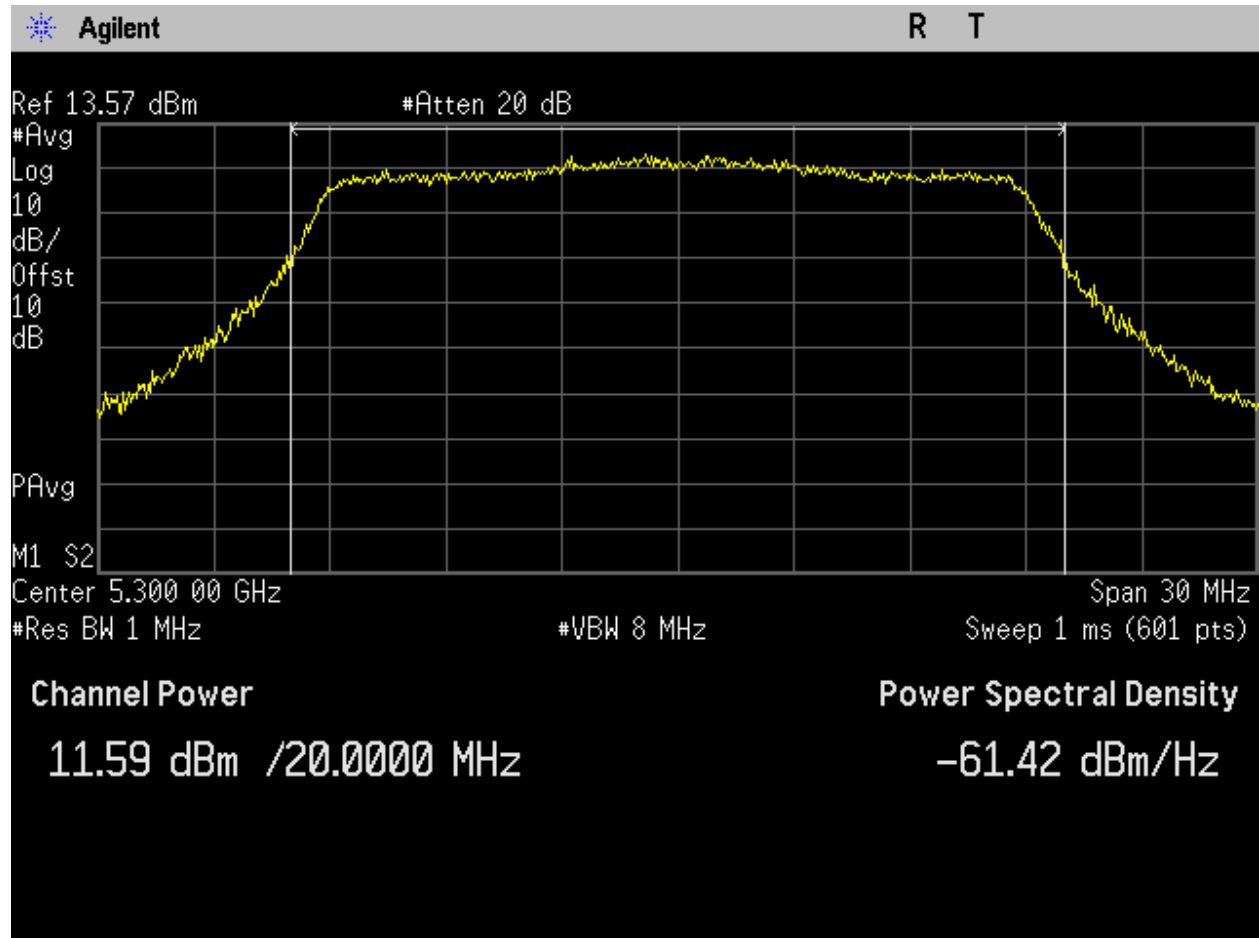


Figure 231: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Output Power_Port 2.

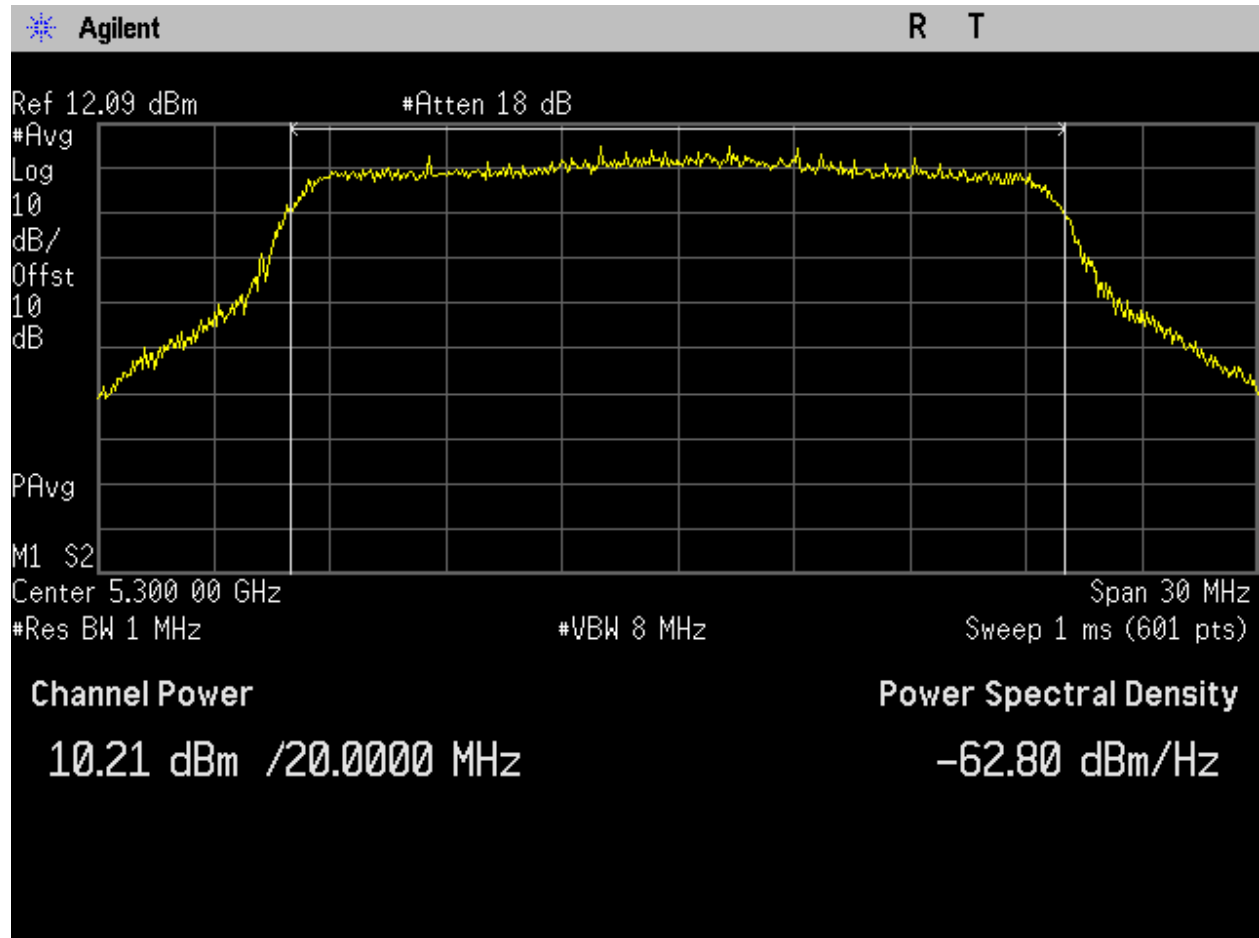


Figure 232: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_Output Power_Port 1.

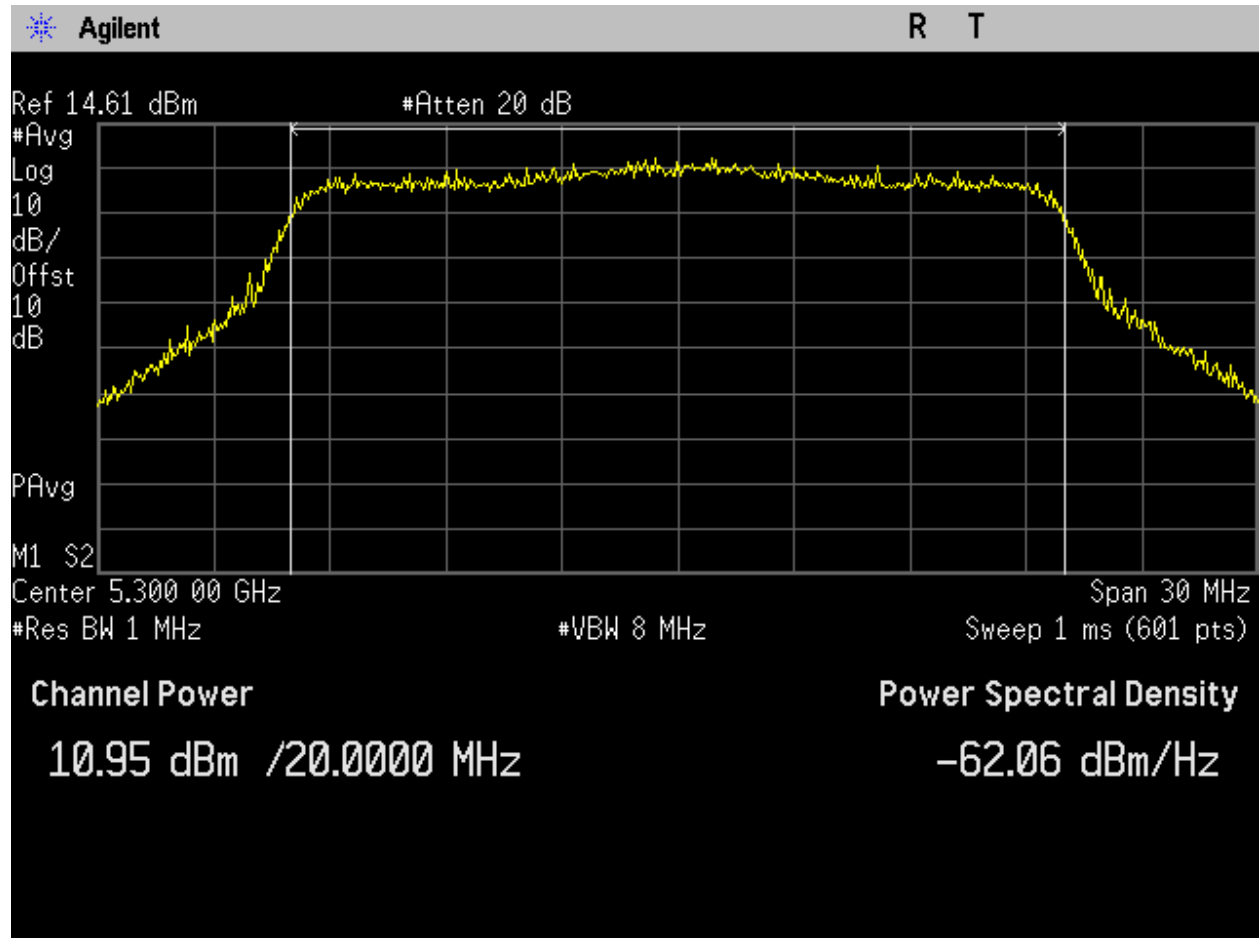


Figure 233: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_Output Power_Port 2.

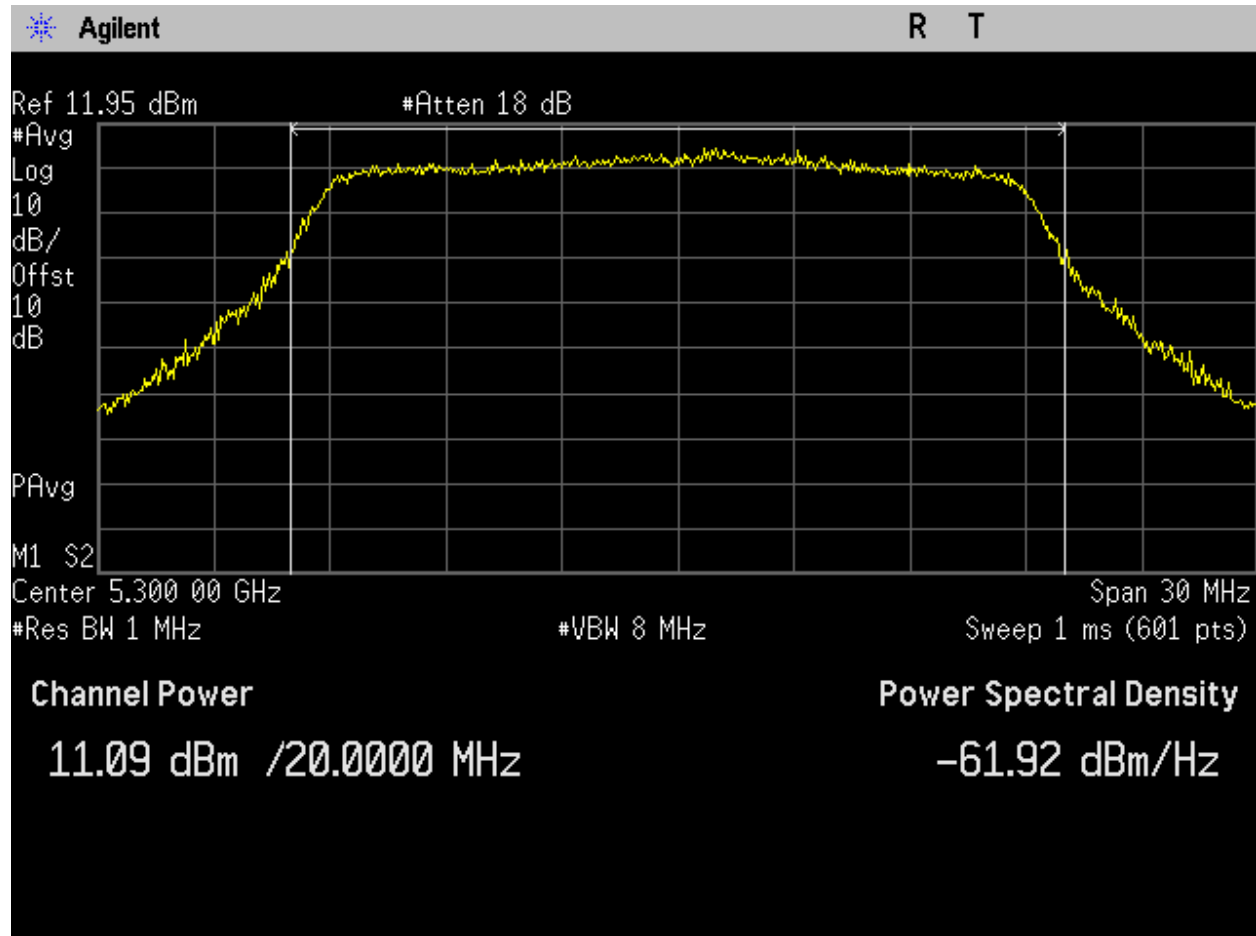


Figure 234: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Output Power_Port 1.

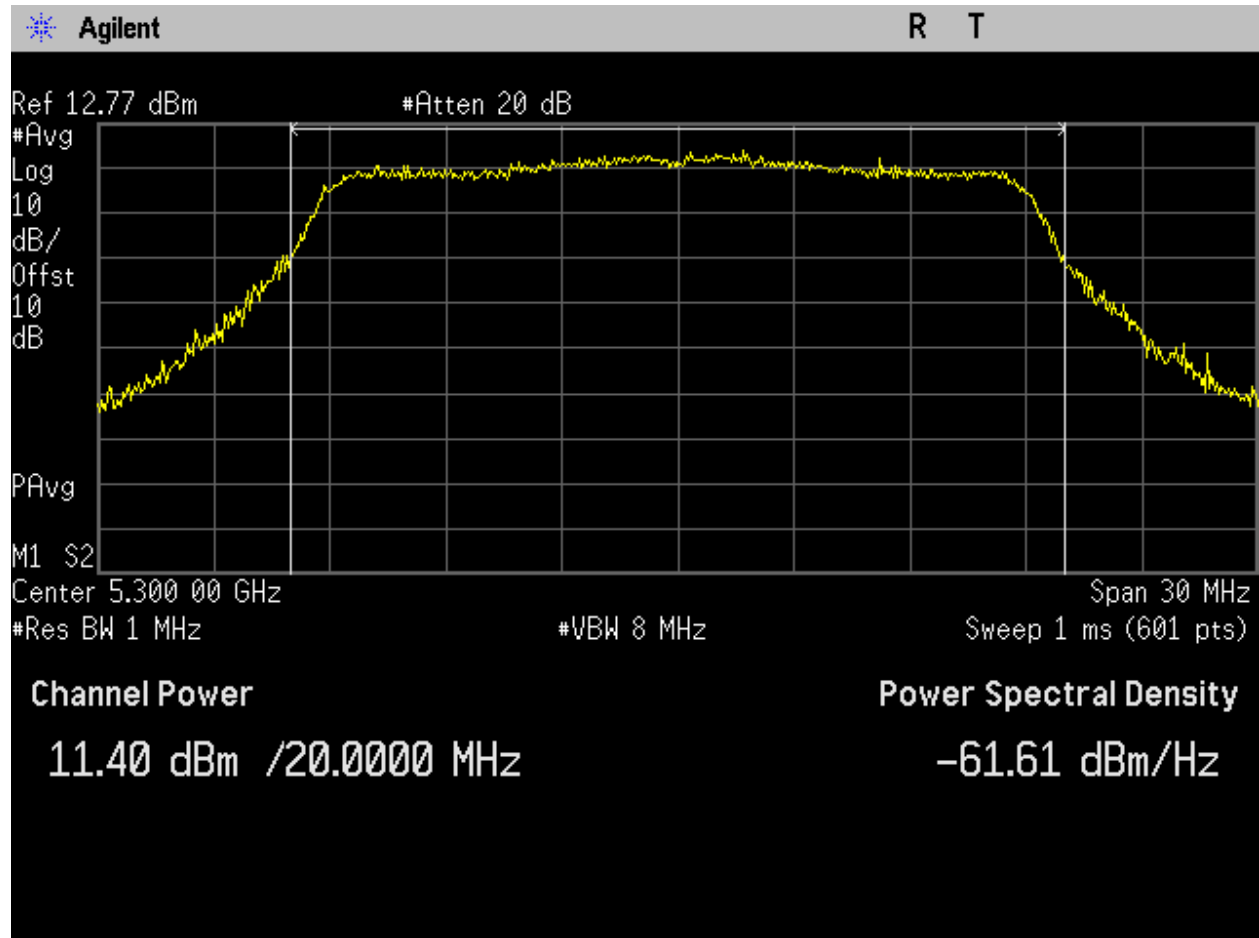


Figure 235: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Output Power_Port 2.

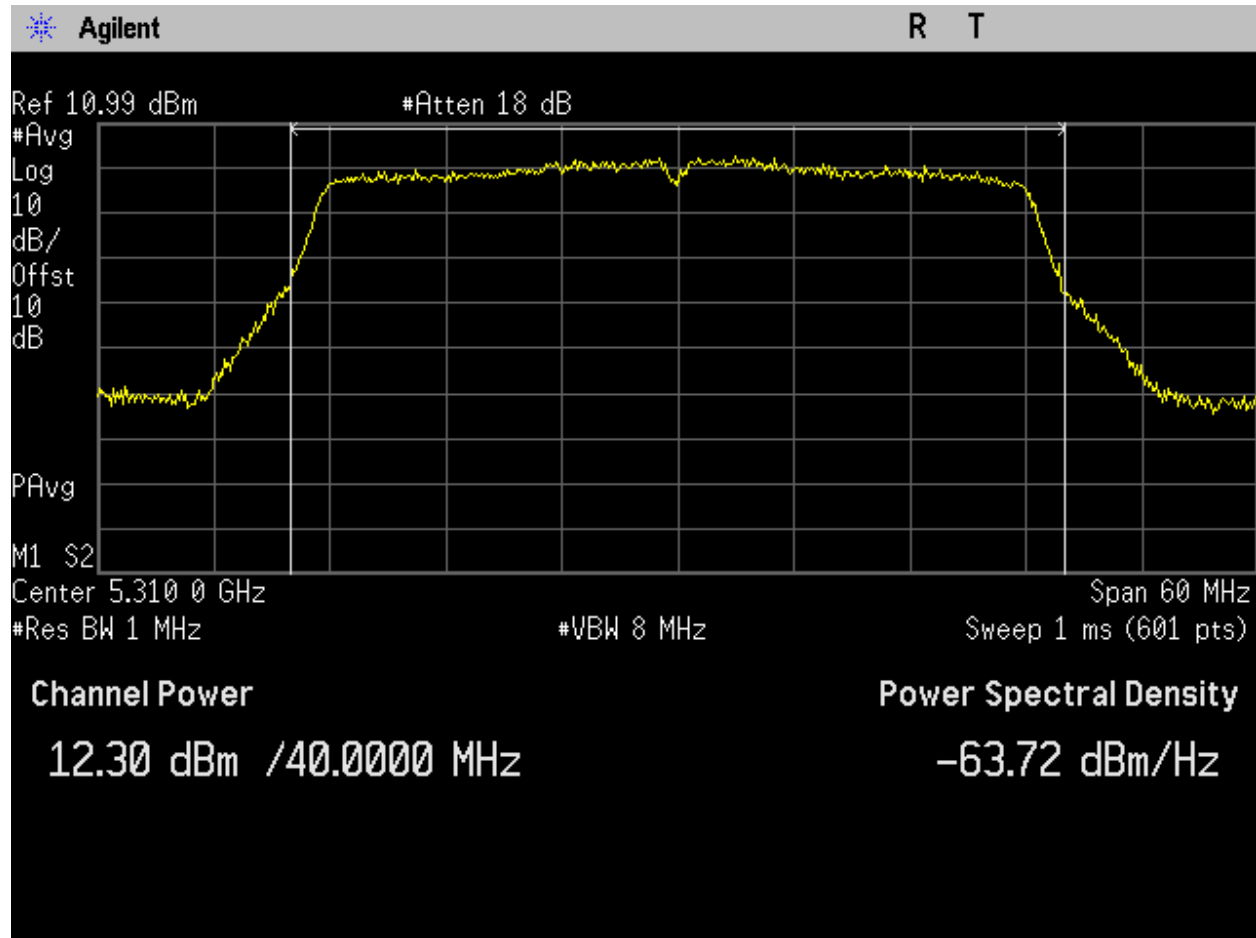


Figure 236: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_Output Power_Port 1.

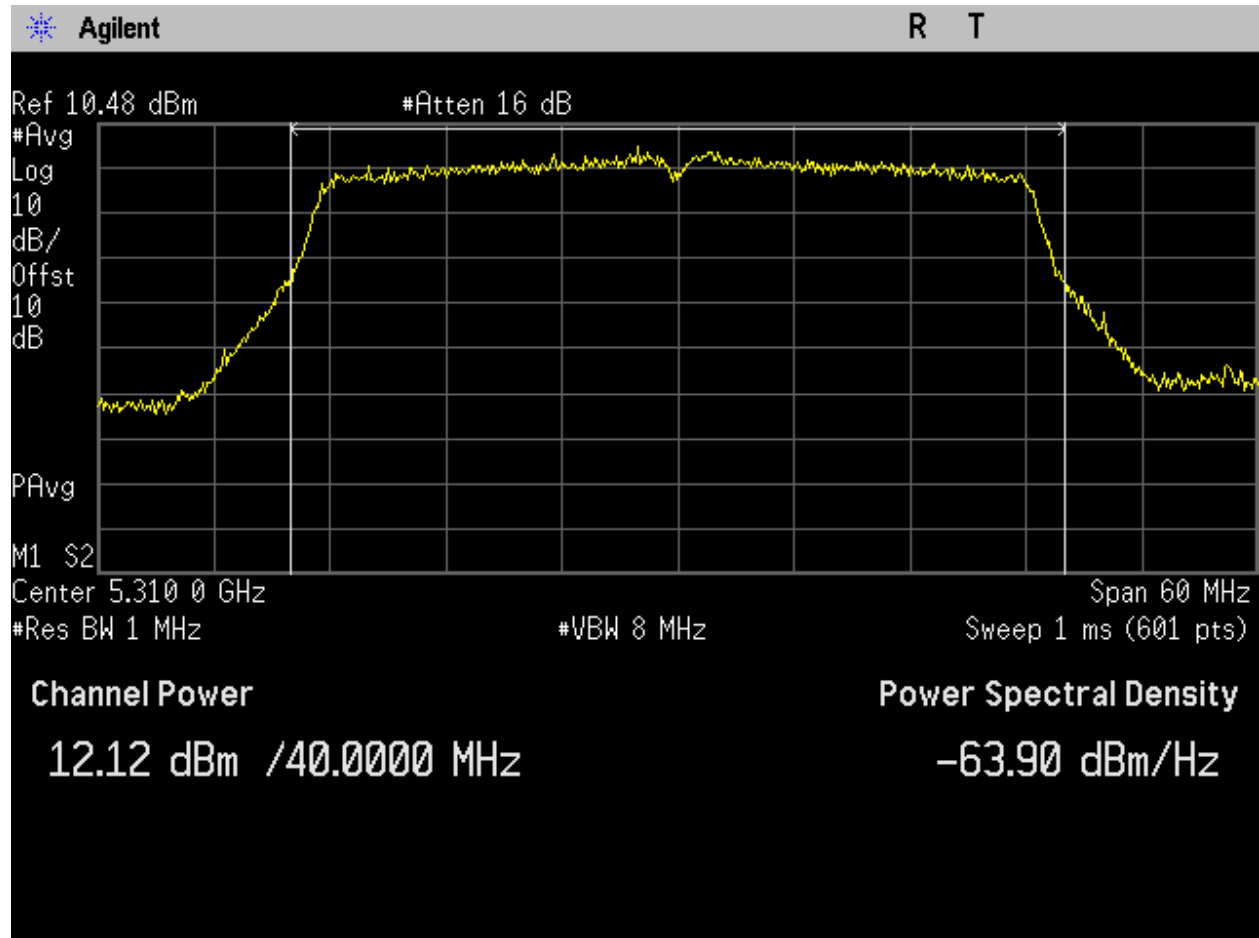


Figure 237: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_Output Power_Port 2.

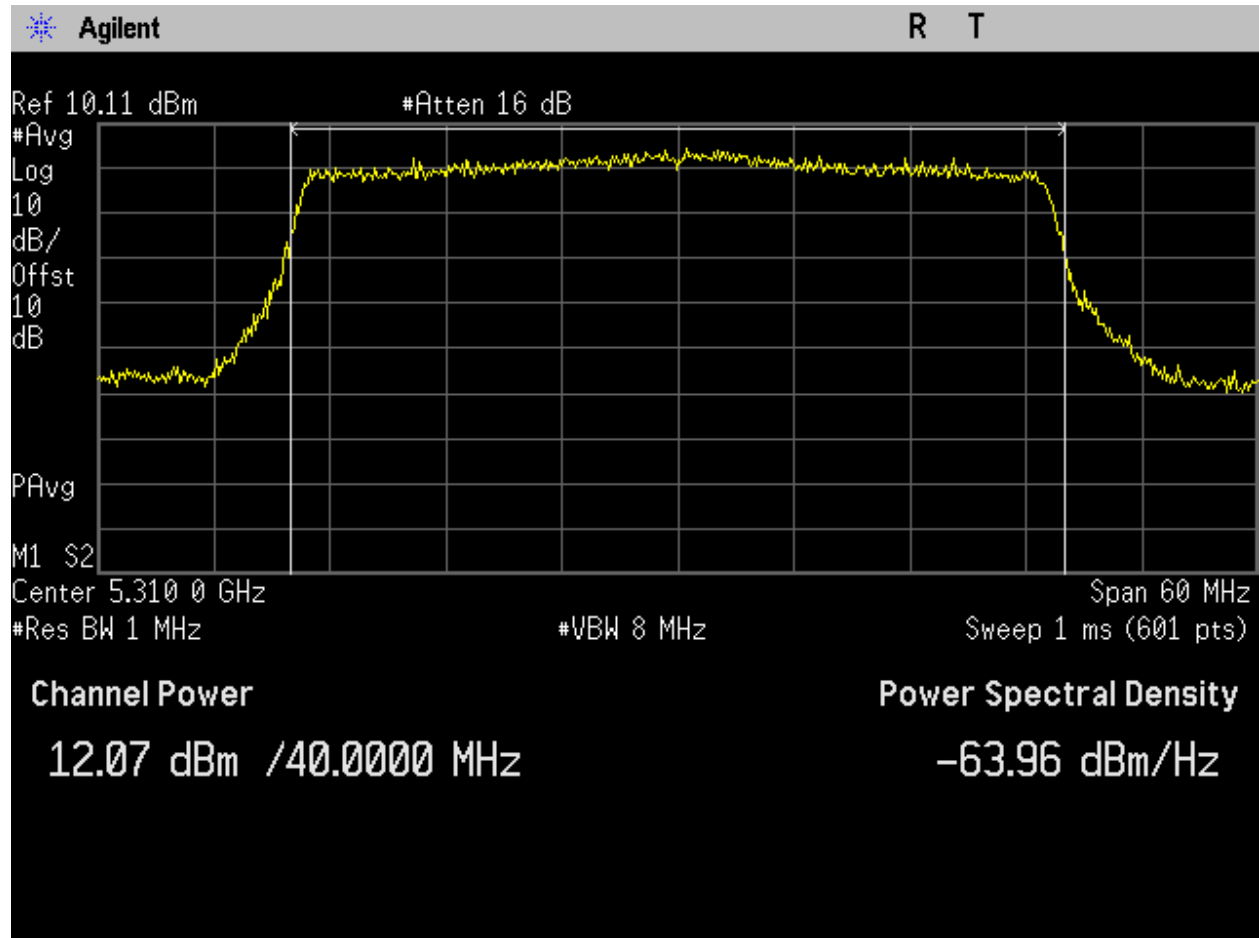


Figure 238: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_Output Power_Port 1.

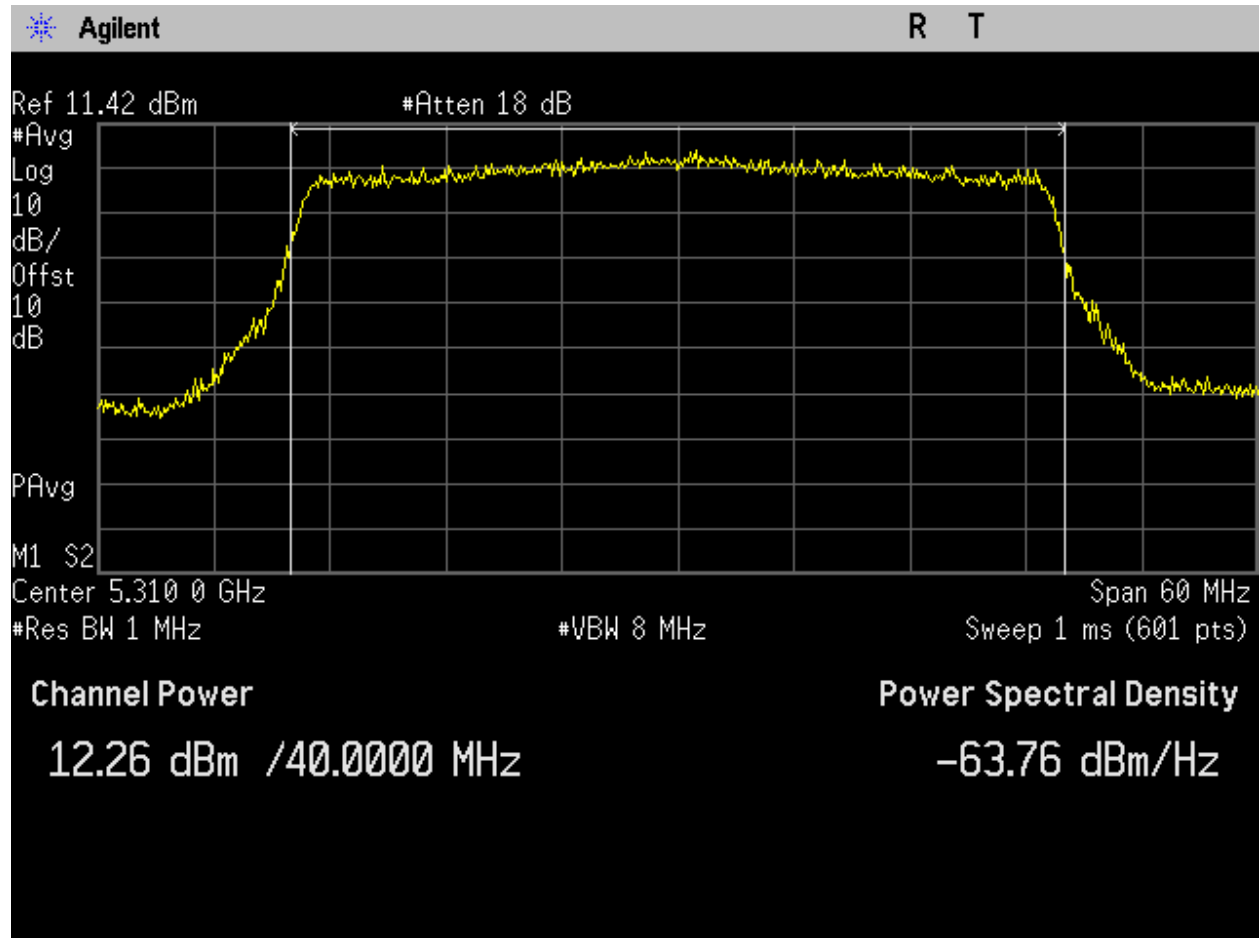


Figure 239: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_Output Power_Port 2.

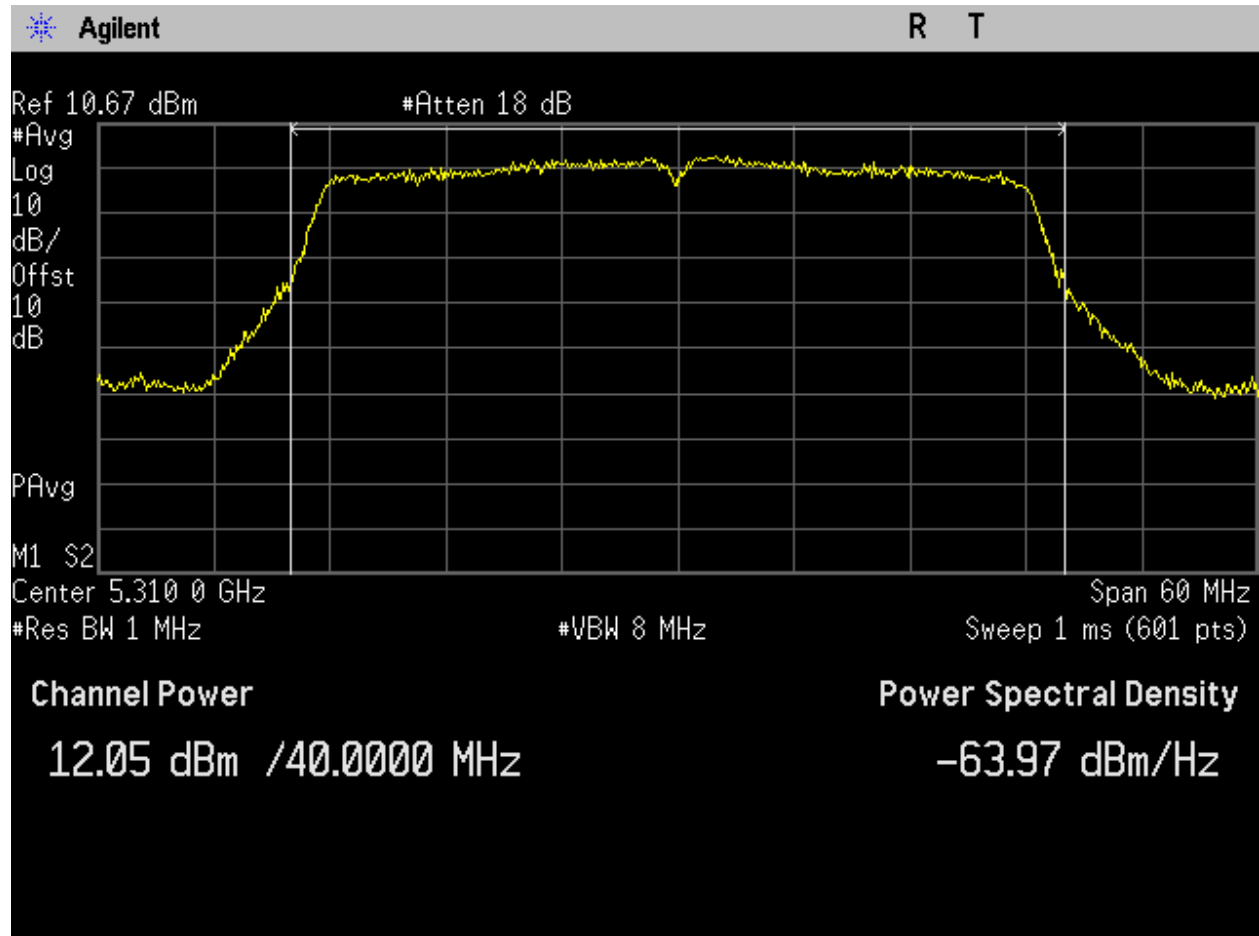


Figure 240: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Output Power_Port 1.

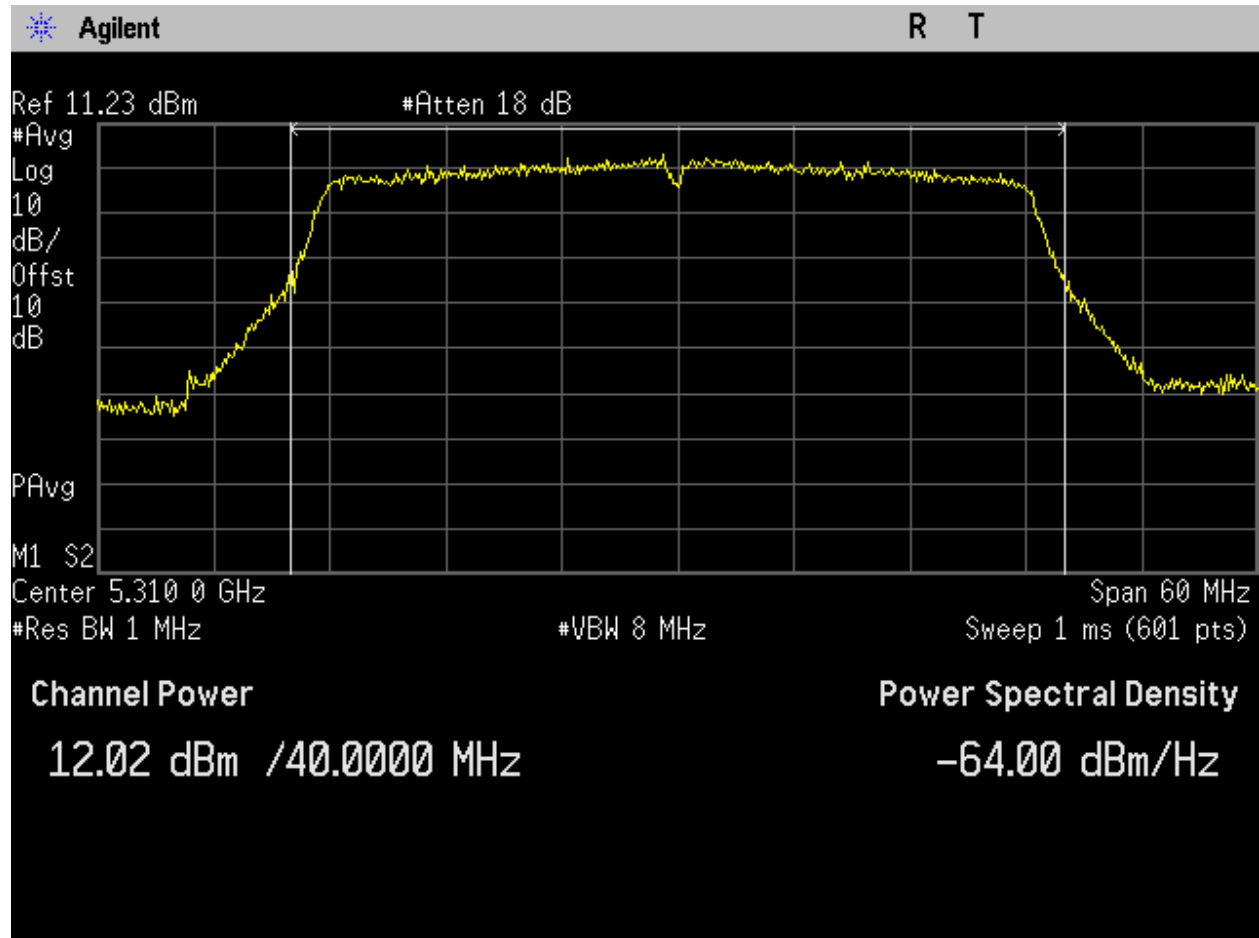


Figure 241: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Output Power_Port 2.

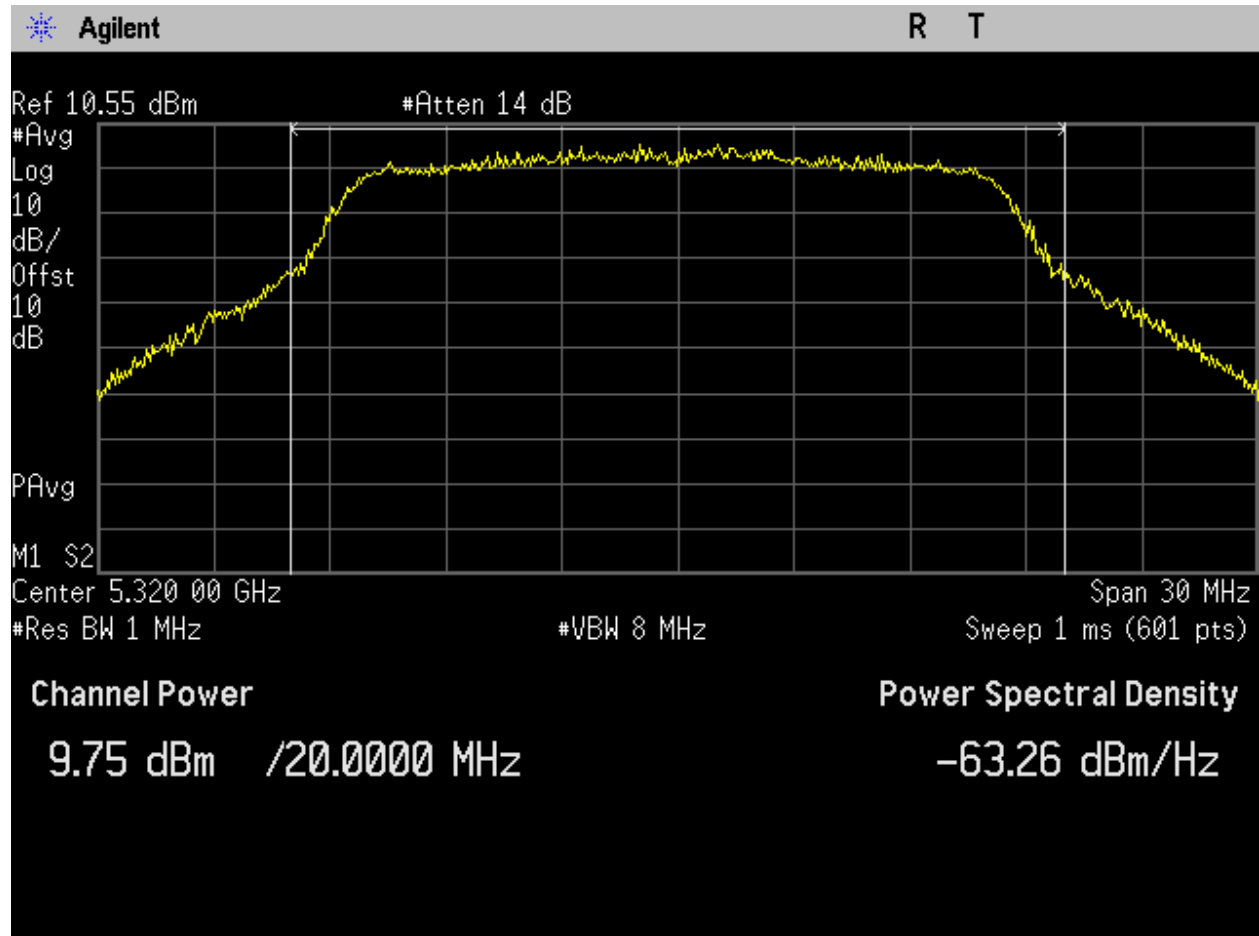


Figure 242: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_Output Power_Port 1.

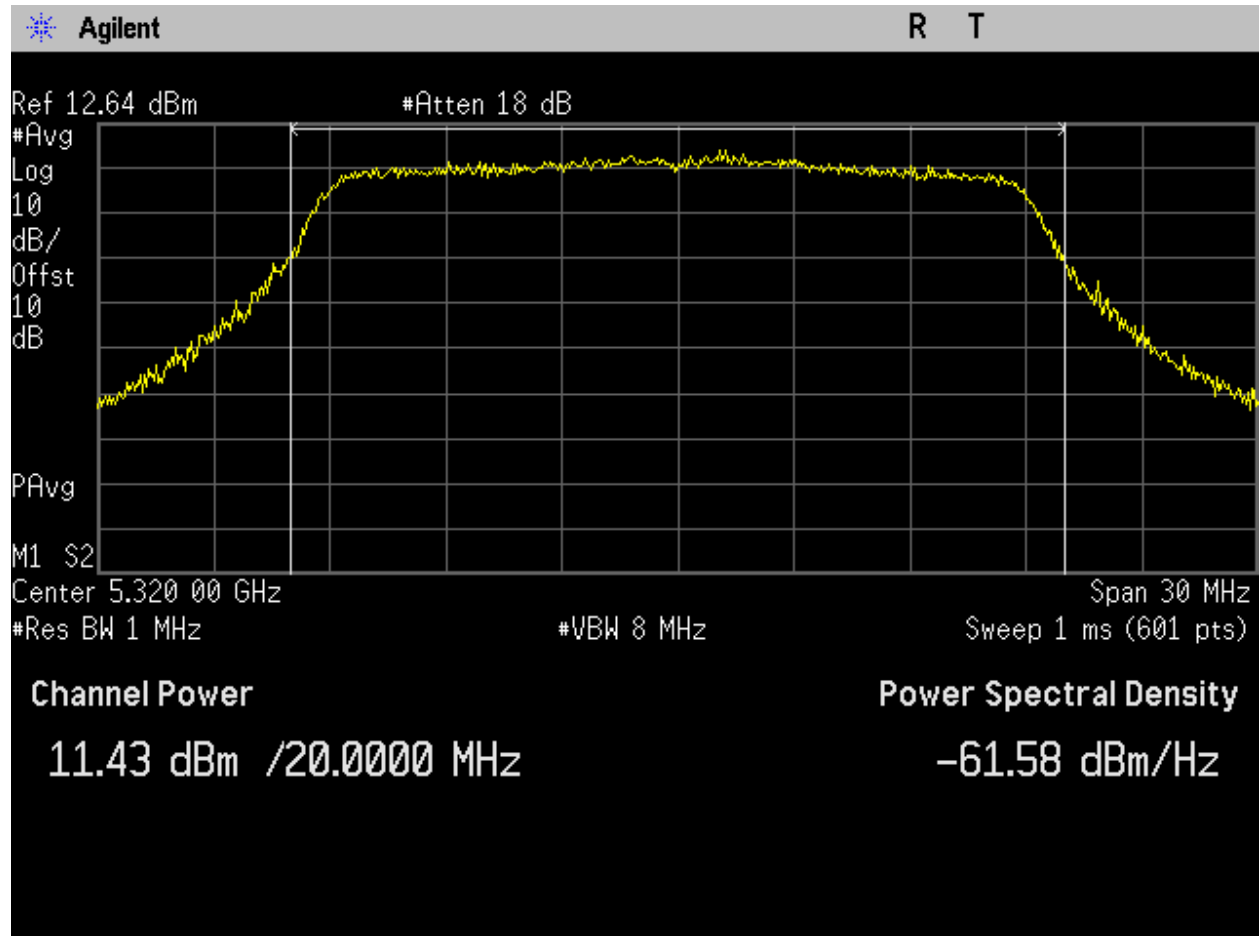


Figure 243: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_Output Power_Port 1.

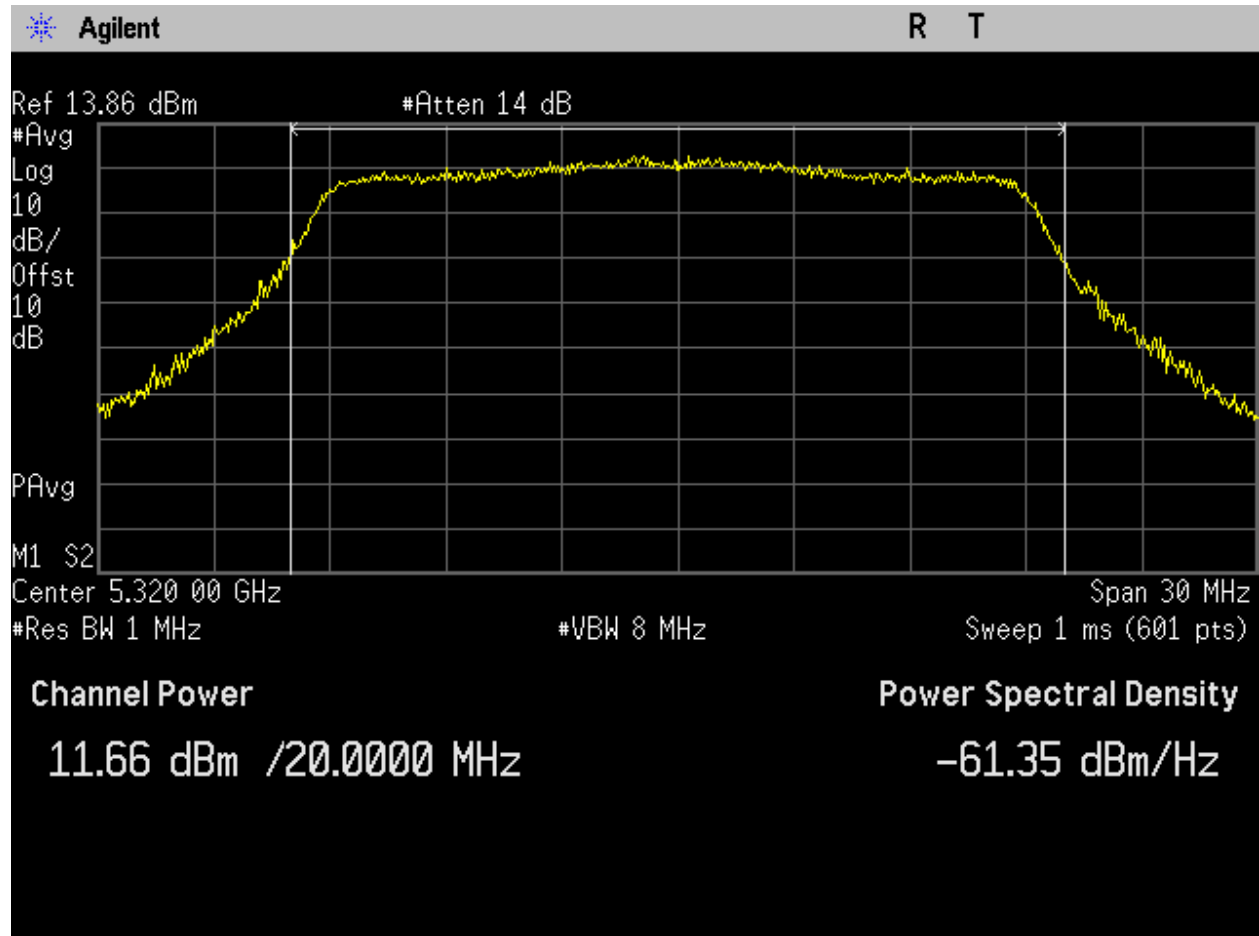


Figure 244: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_Output Power_Port 2.

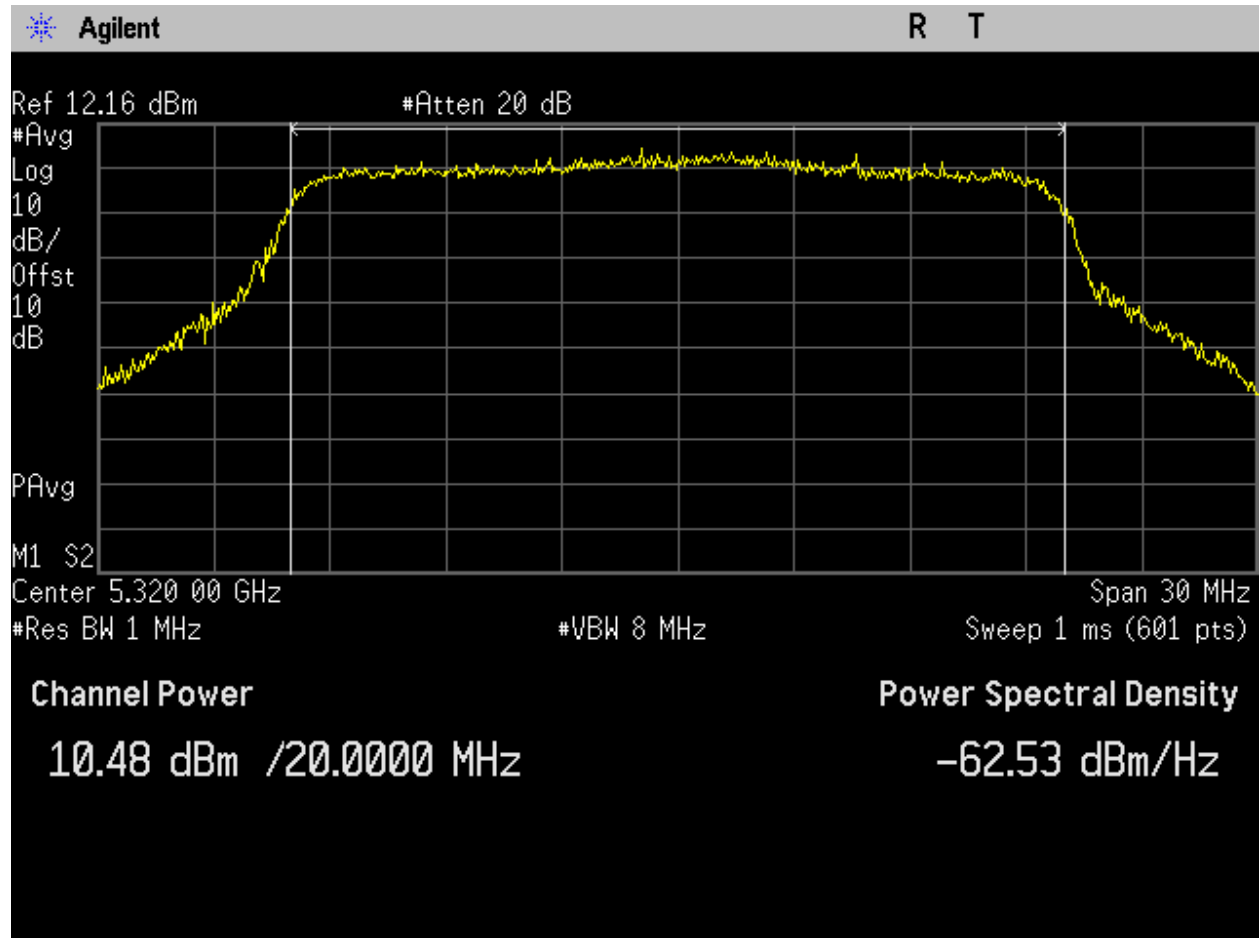


Figure 245: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_Output Power_Port 1.

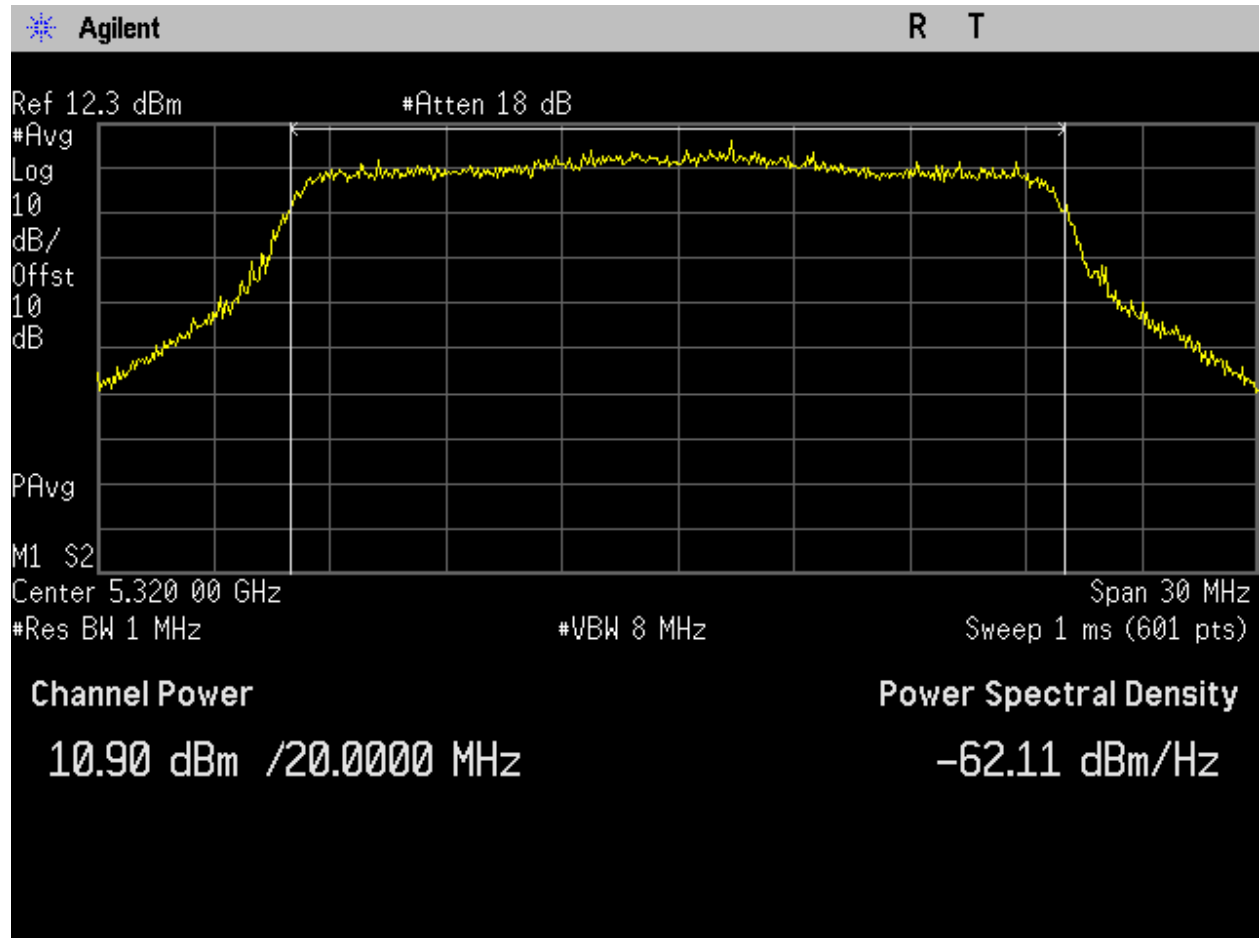


Figure 246: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_Output Power_Port 2.

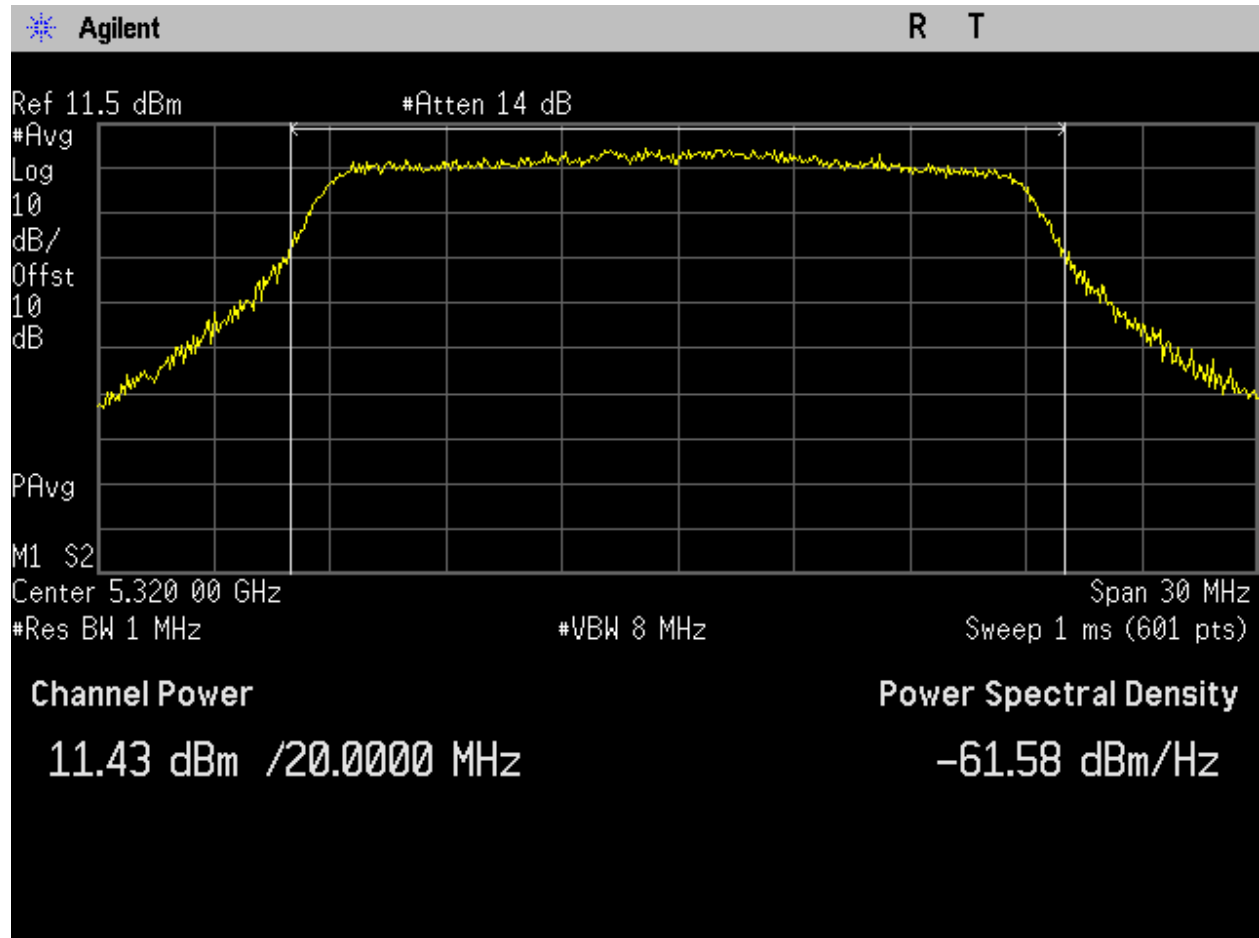


Figure 247: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Output Power_Port 1.

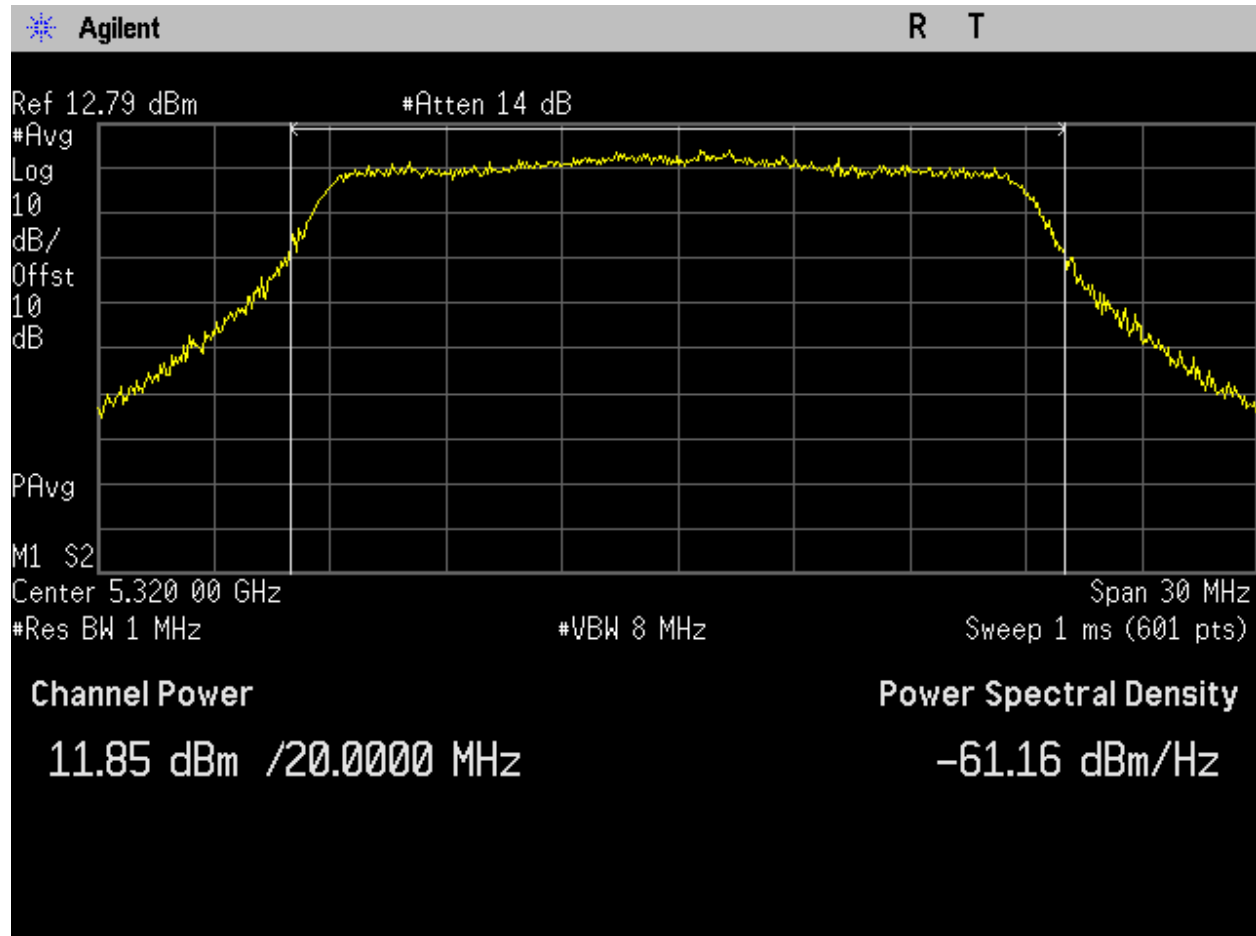


Figure 248: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Output Power_Port 2.

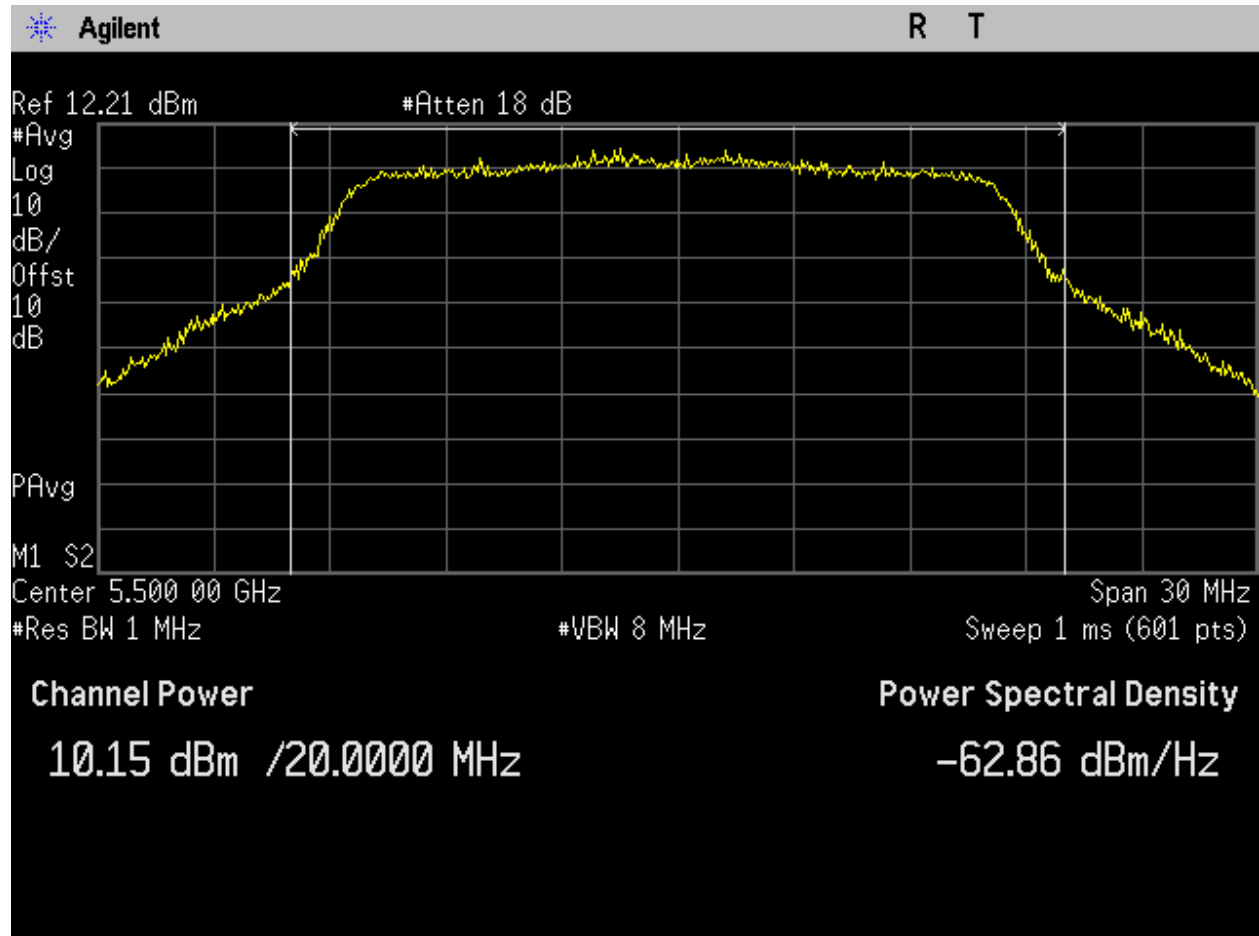


Figure 249: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_Output Power_Port 1.

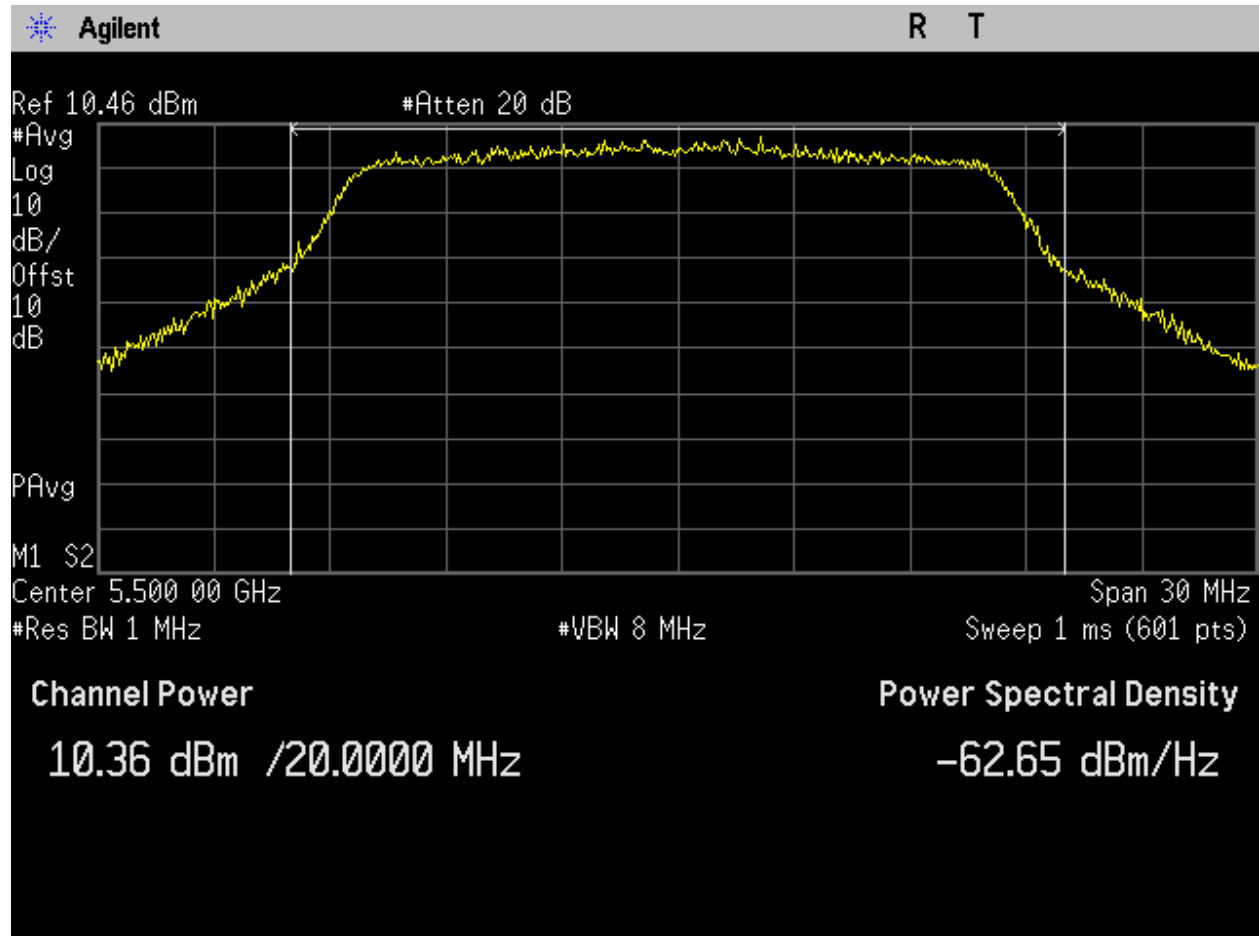


Figure 250: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_Output Power_Port 2.

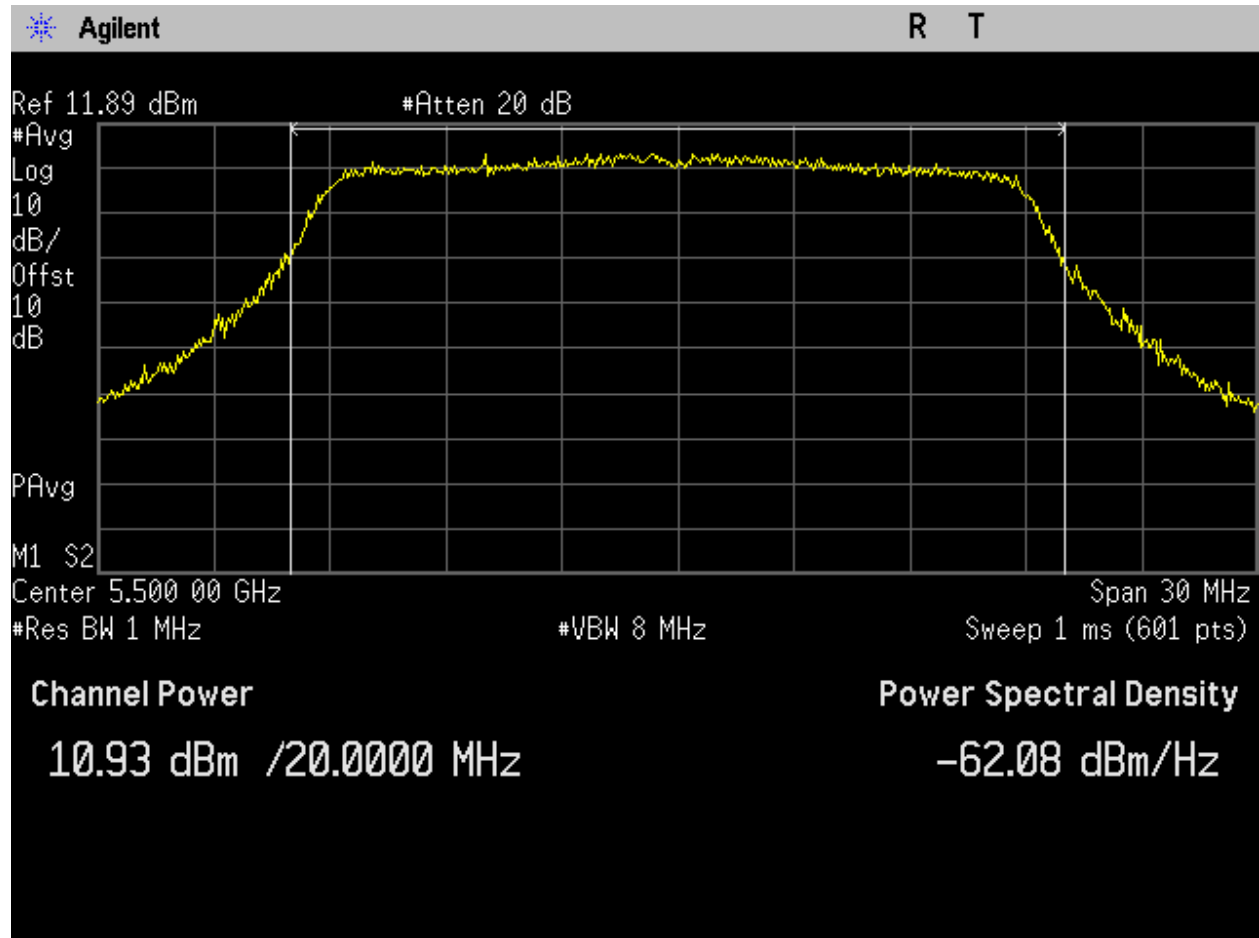


Figure 251: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_Output Power_Port 1.

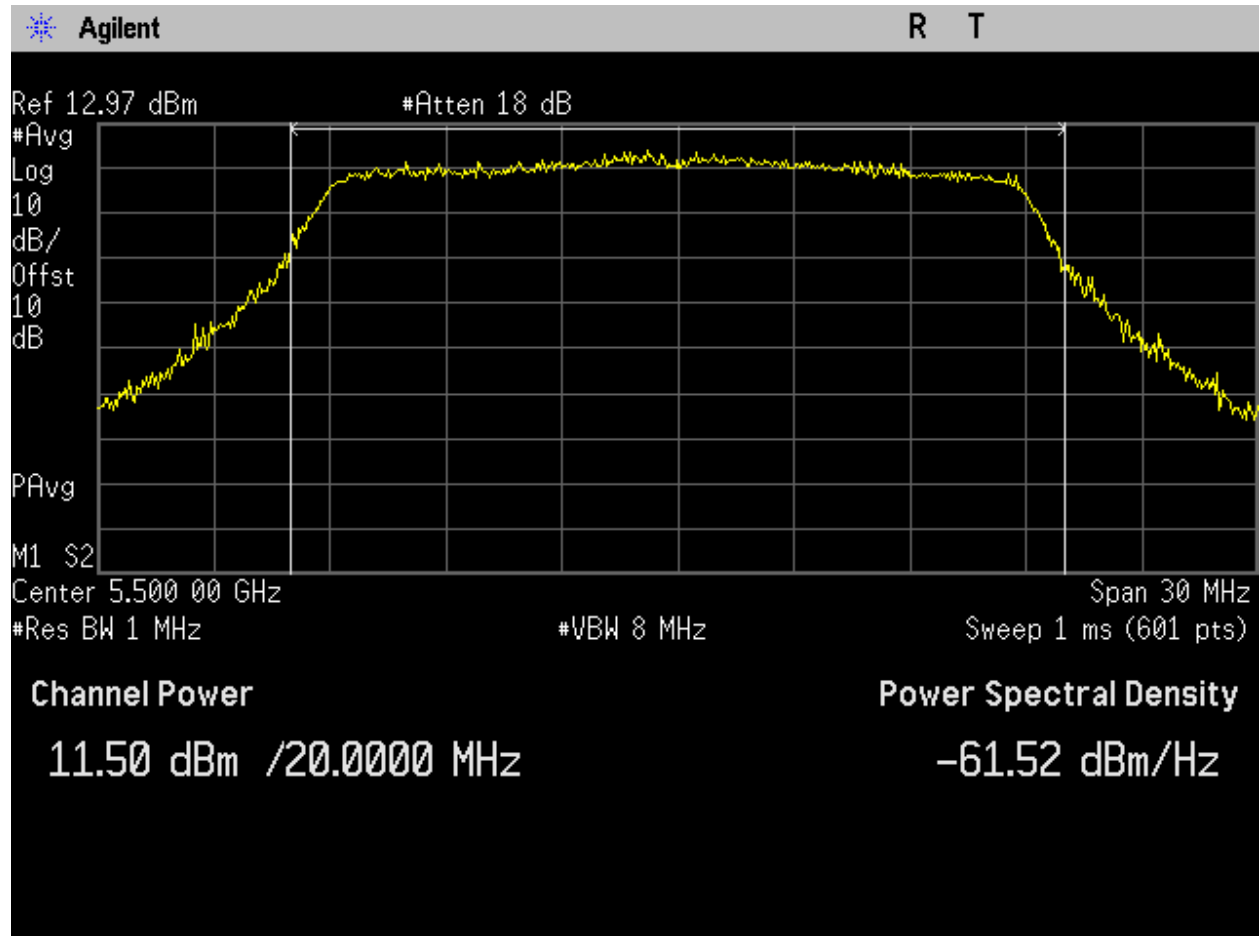


Figure 252: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ac-mode_Output Power_Port 2.

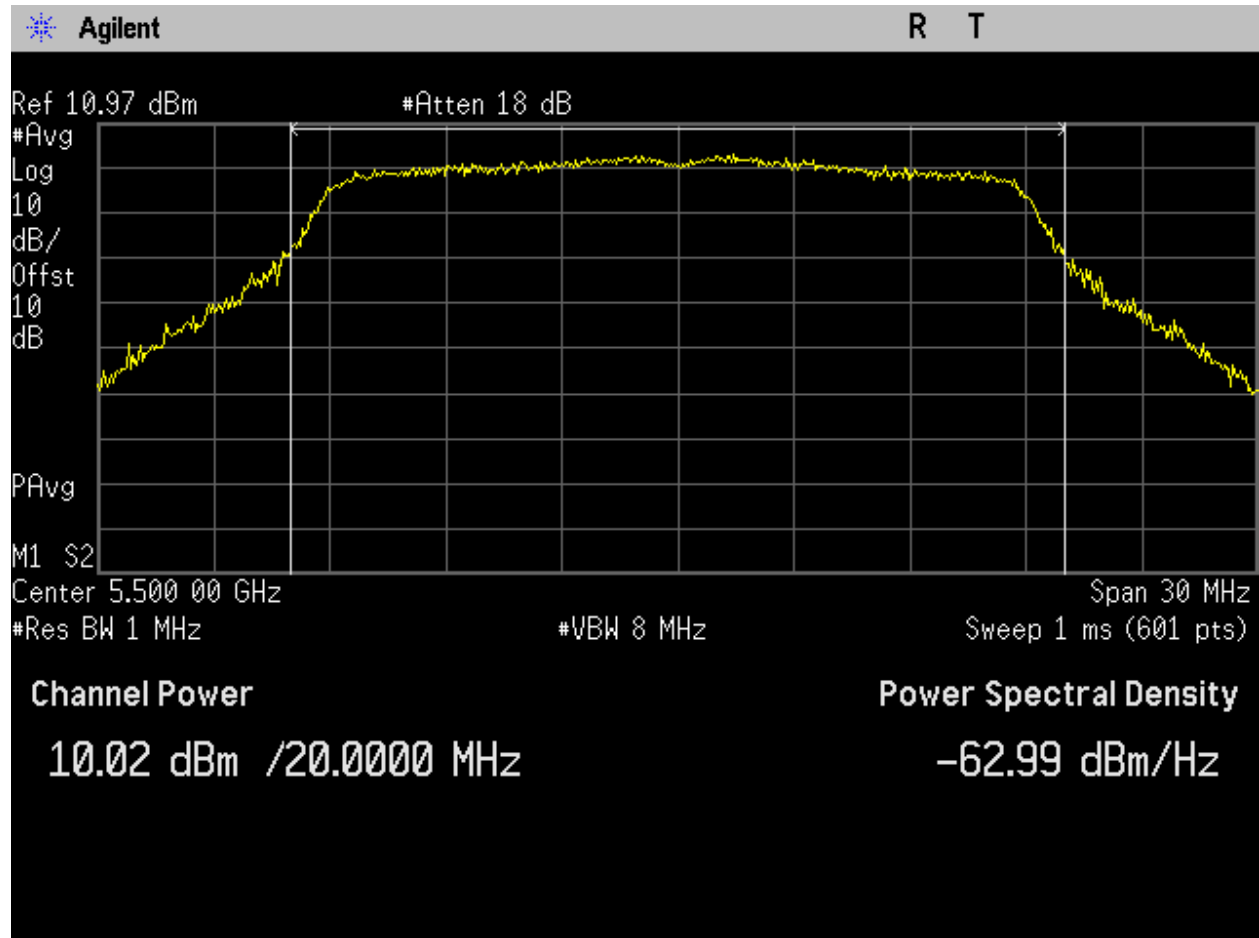


Figure 253: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_Output Power_Port 1.

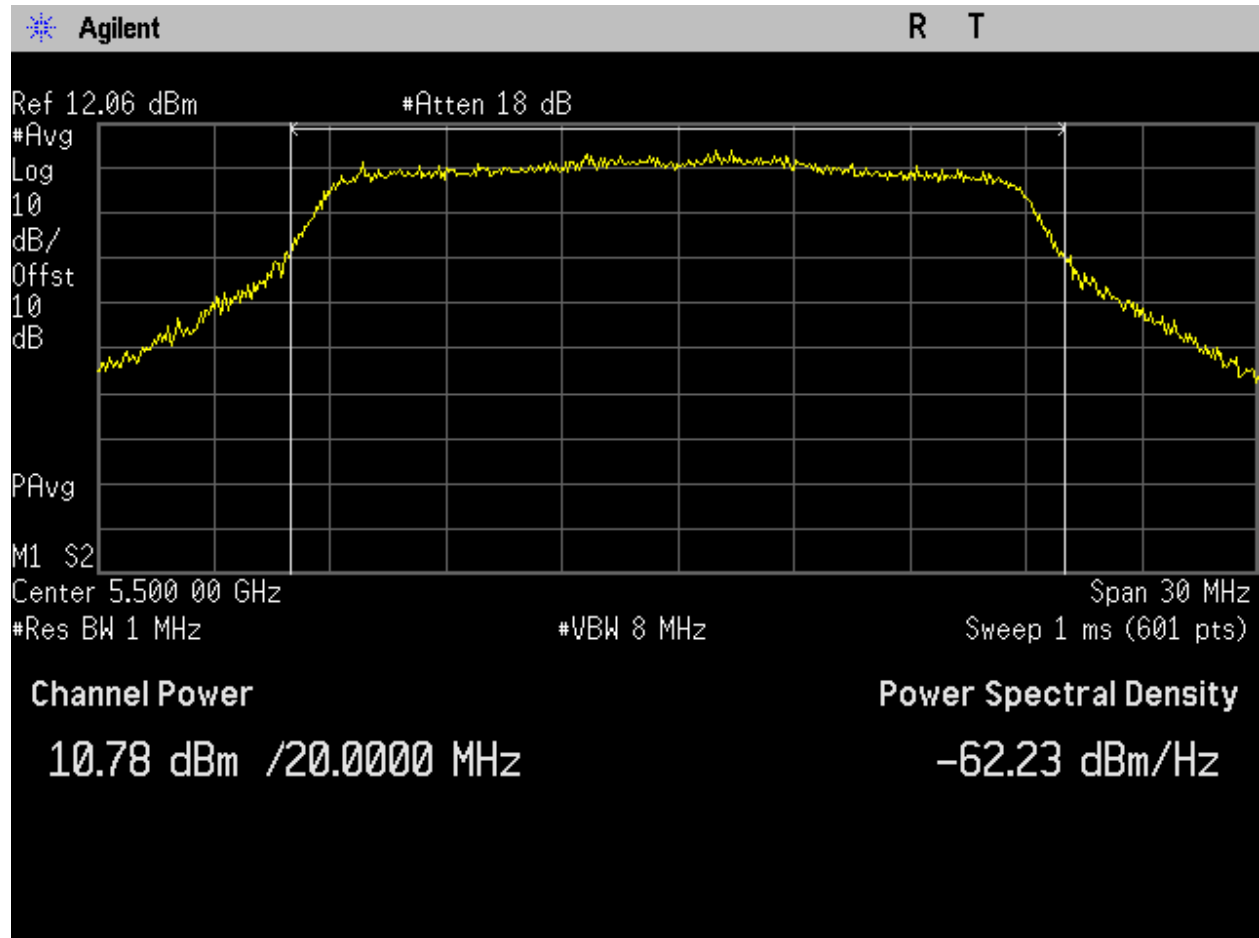


Figure 254: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_ax-mode_Output Power_Port 2.

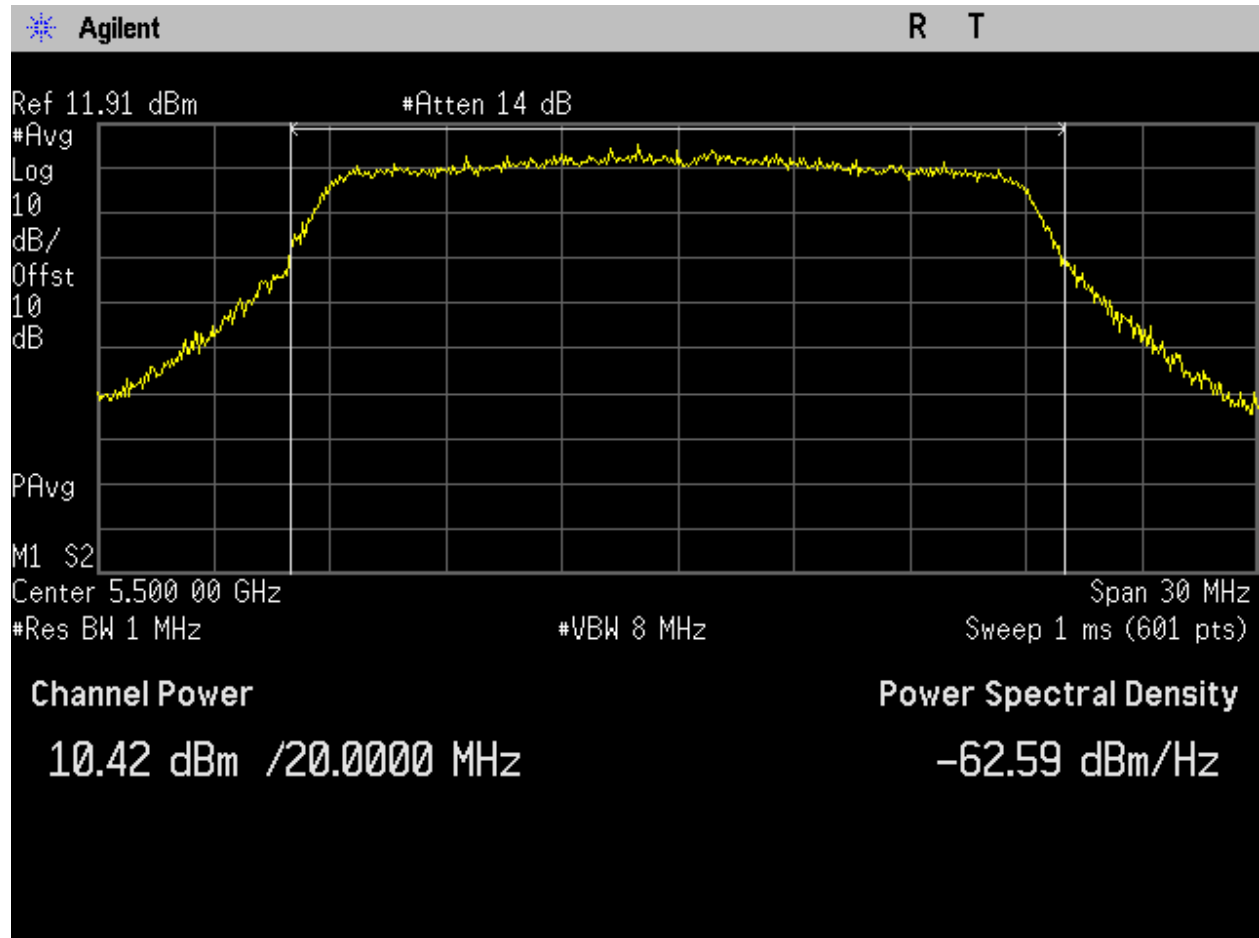


Figure 255: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_Output Power_Port 1.

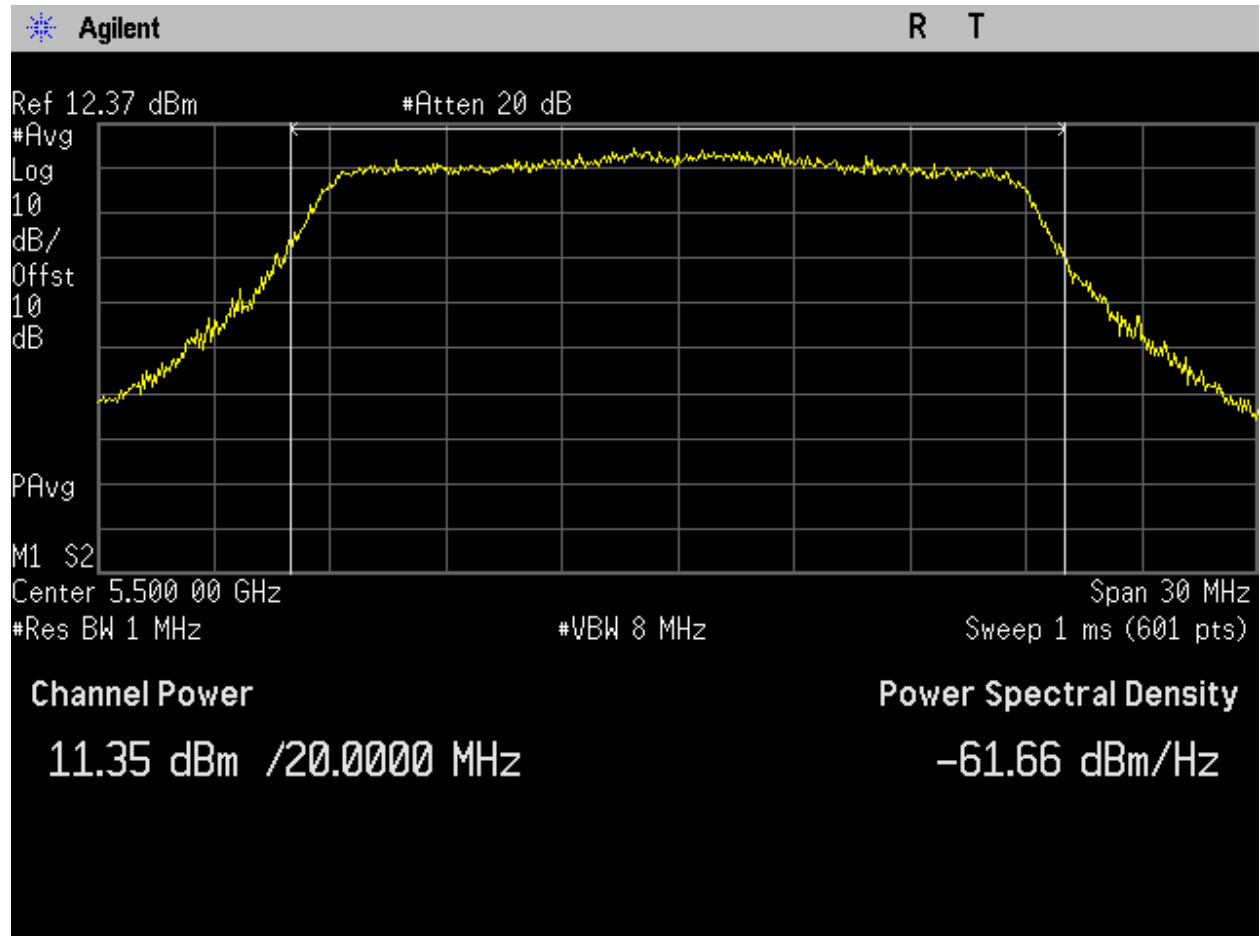


Figure 256: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_n-mode_Output Power_Port 2.

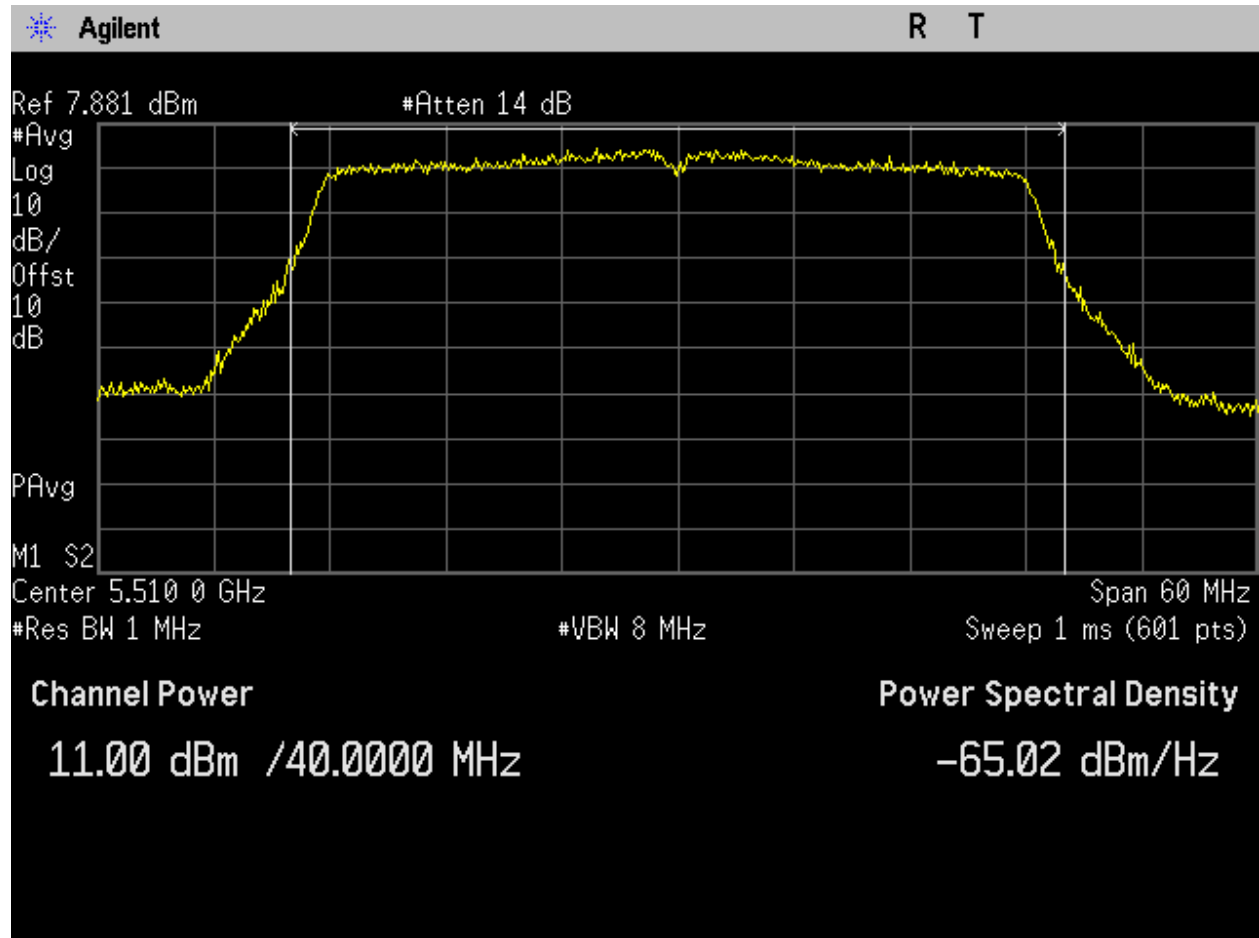


Figure 257: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_Output Power_Port 1.

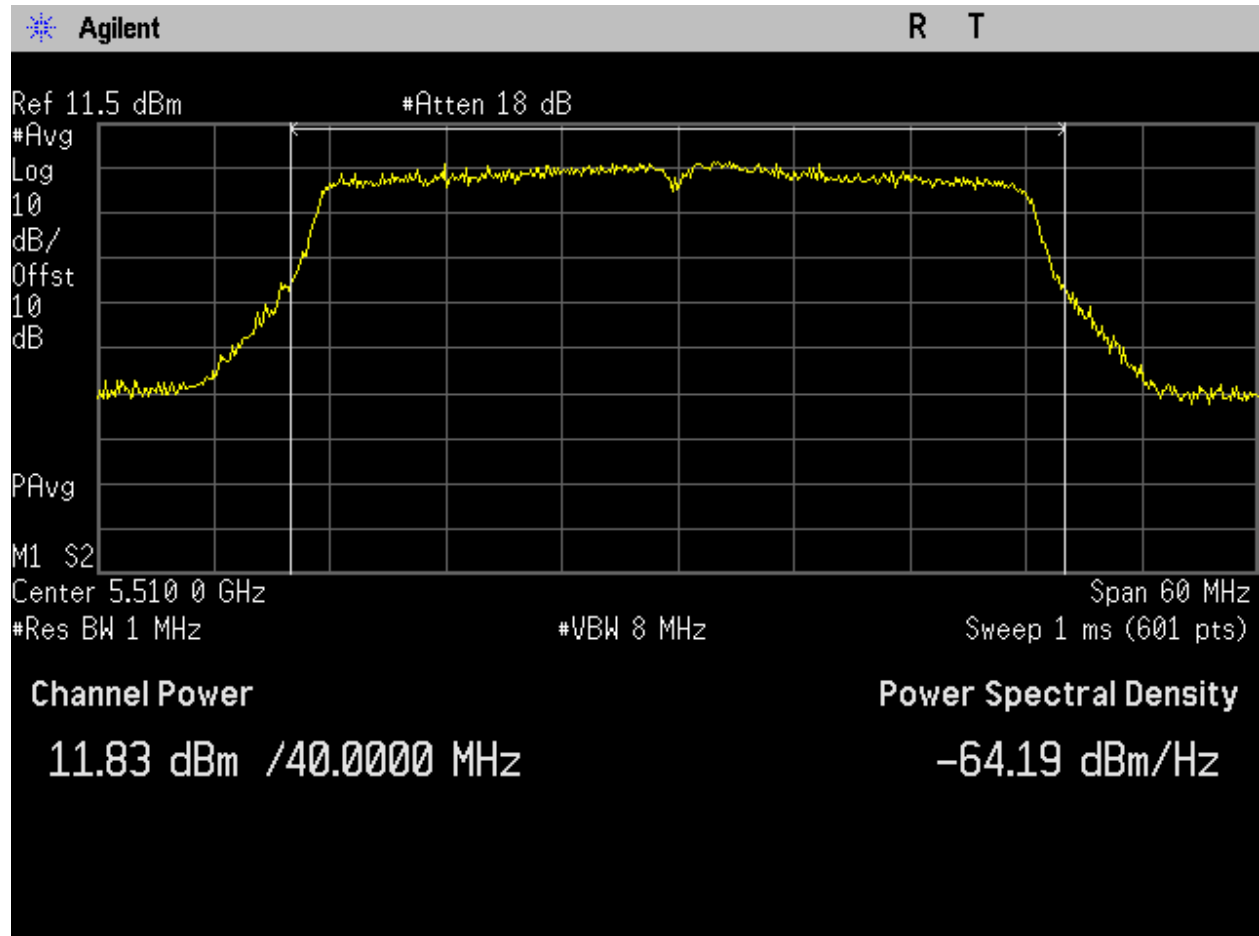


Figure 258: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ac-mode_Output Power_Port 2.

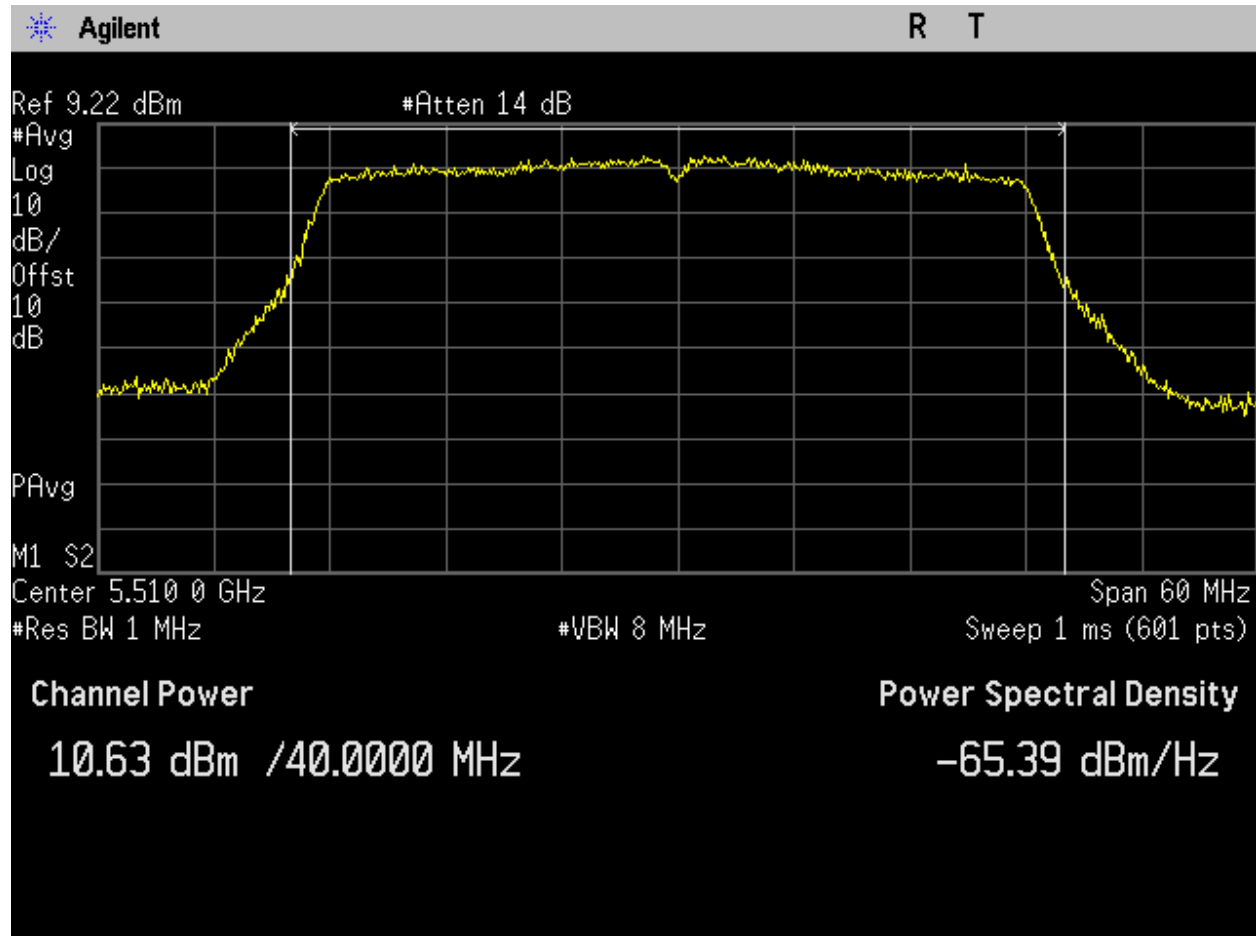


Figure 259: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_Output Power_Port 1.

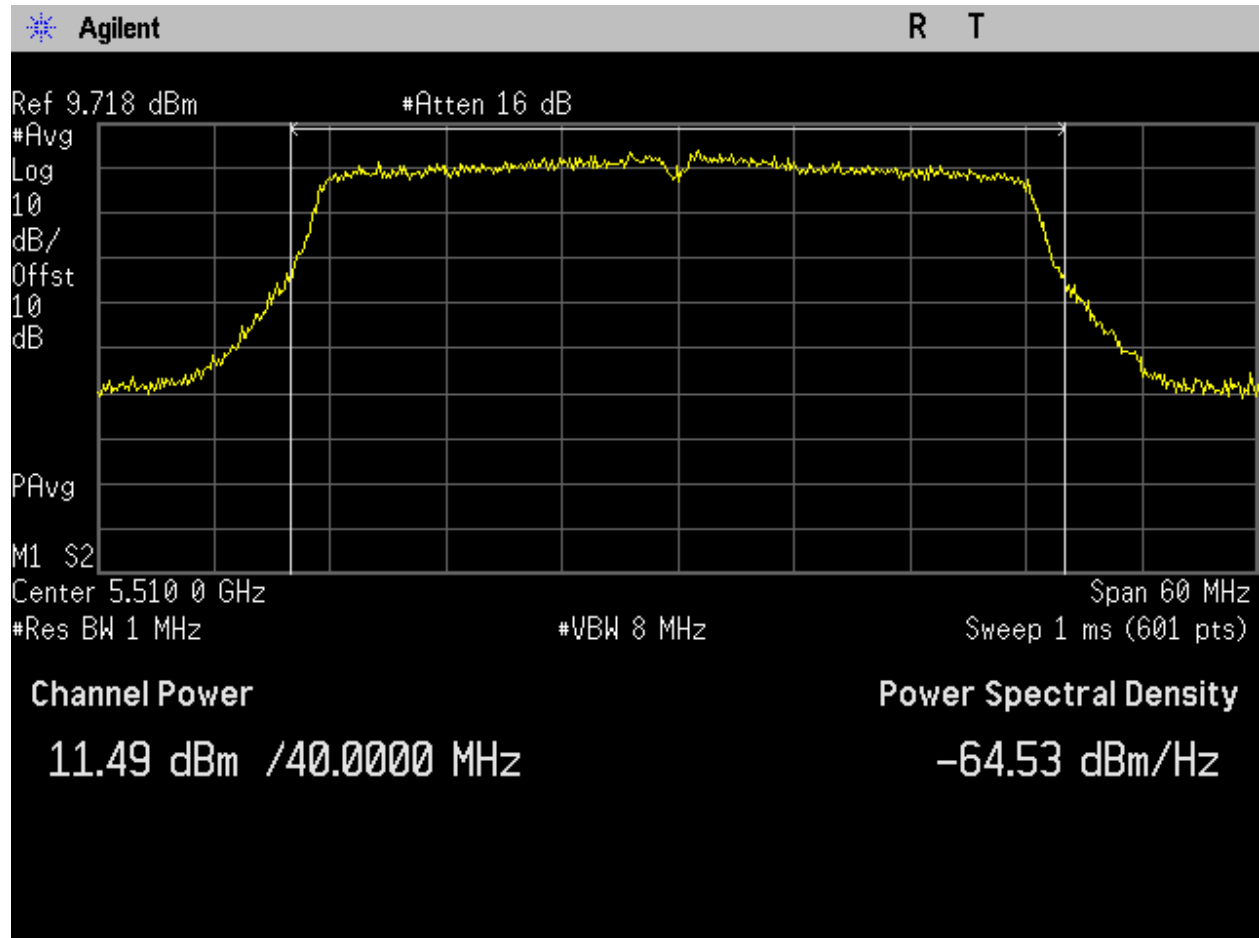


Figure 260: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_ax-mode_Output Power_Port 2.

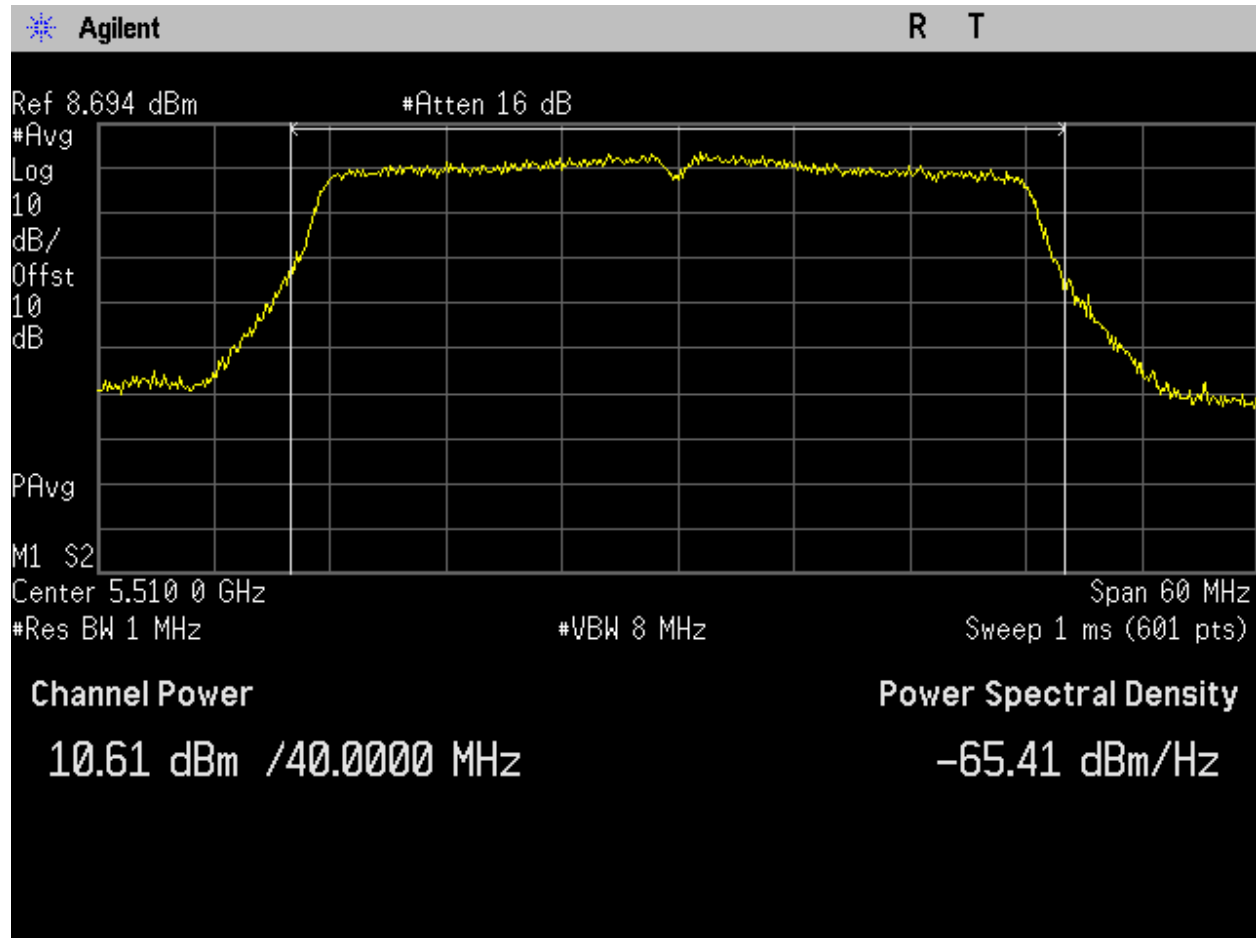


Figure 261: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_Output Power_Port 1.

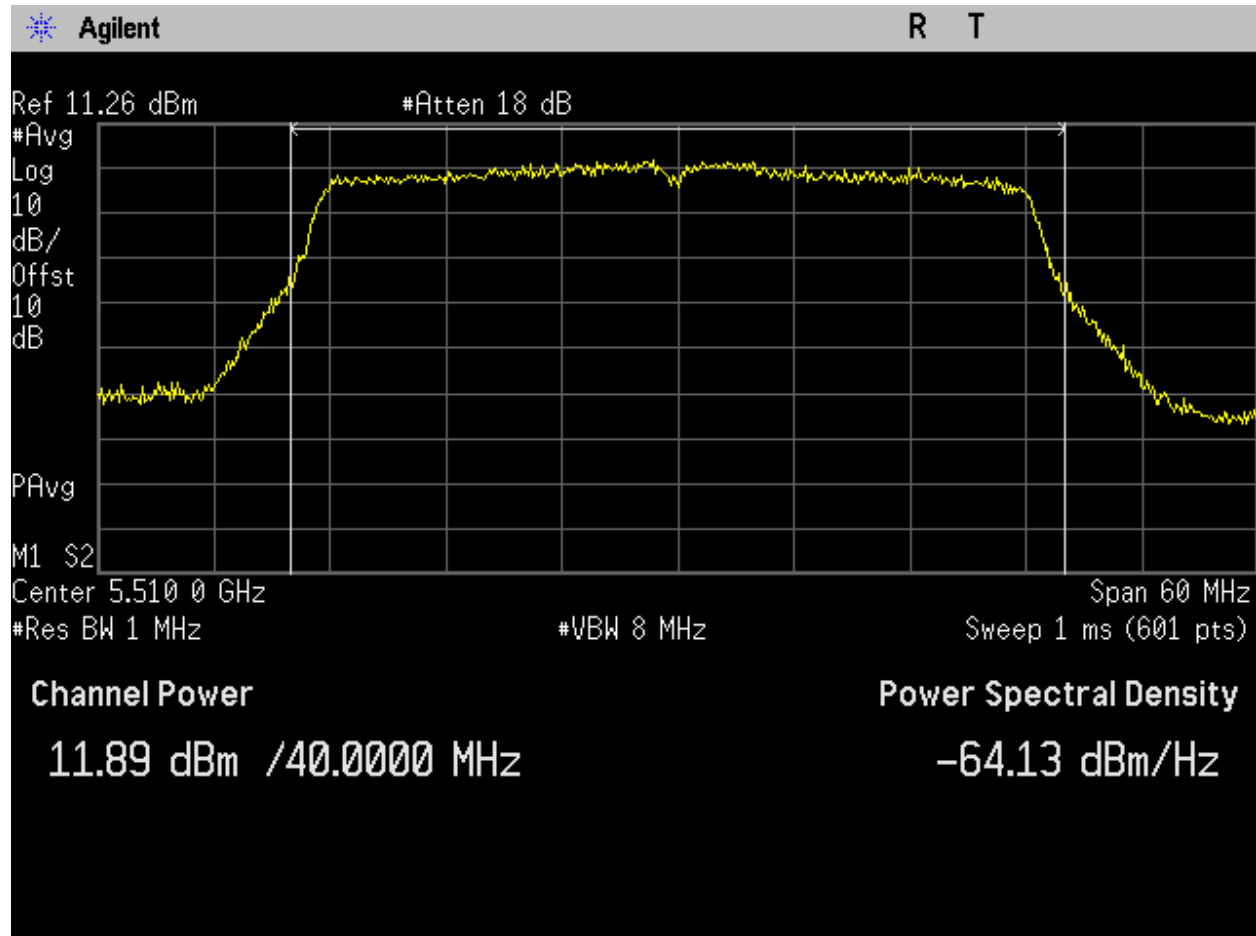


Figure 262: U-NII-2C_5510MHz_Low Ch_102_40MHz BW_n-mode_Output Power_Port 2.

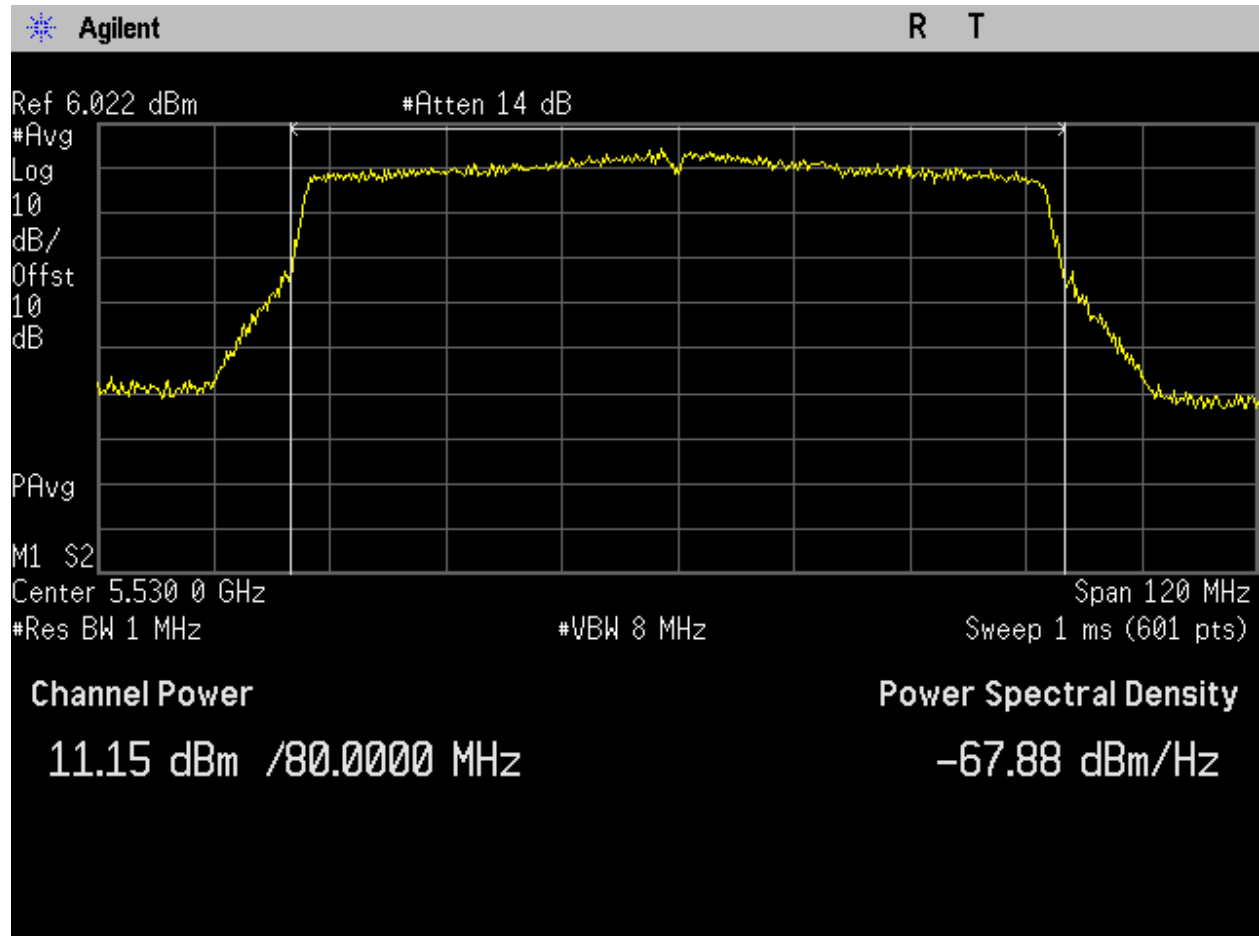


Figure 263: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_Output Power_Port 1.

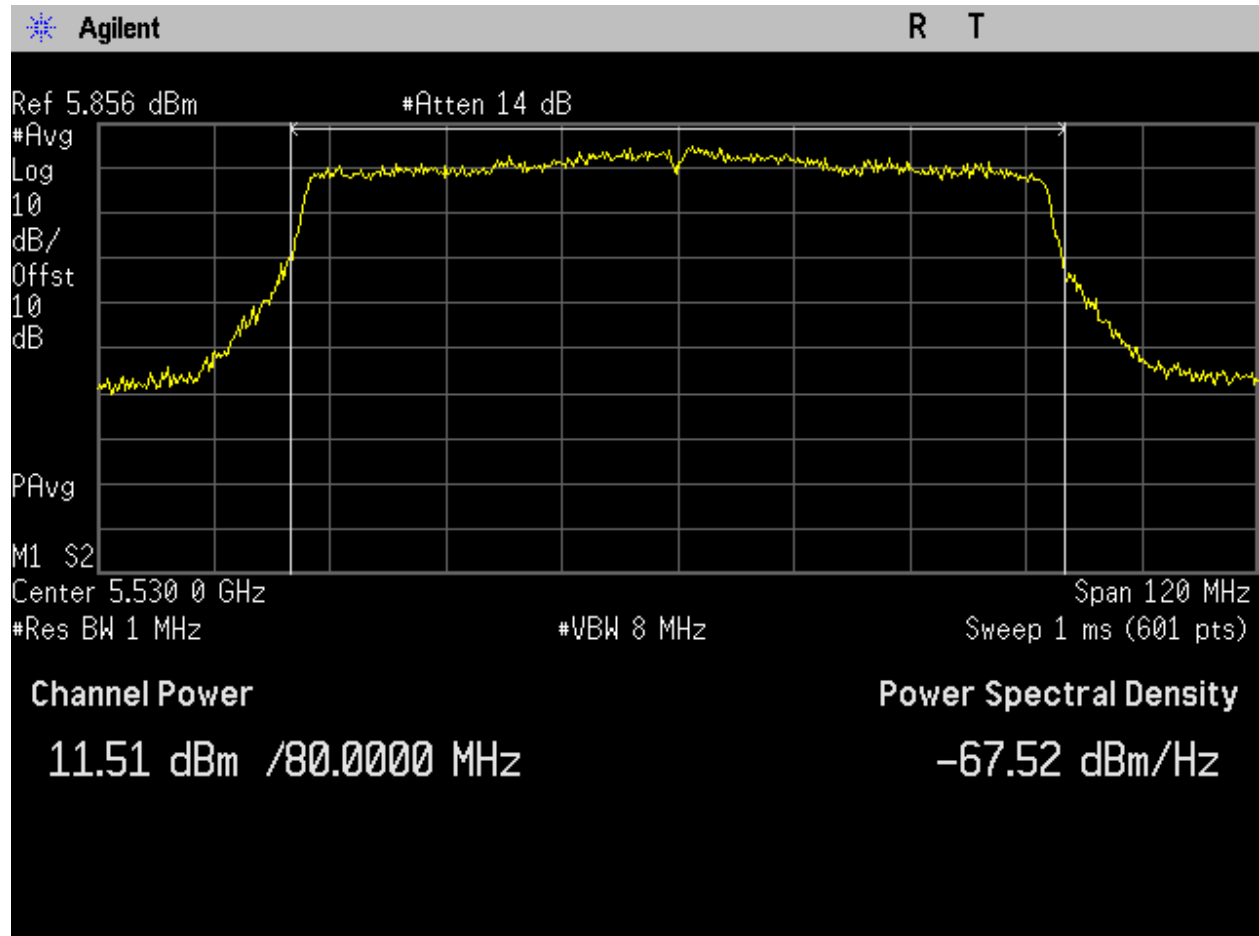


Figure 264: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-mode_Output Power_Port 2.

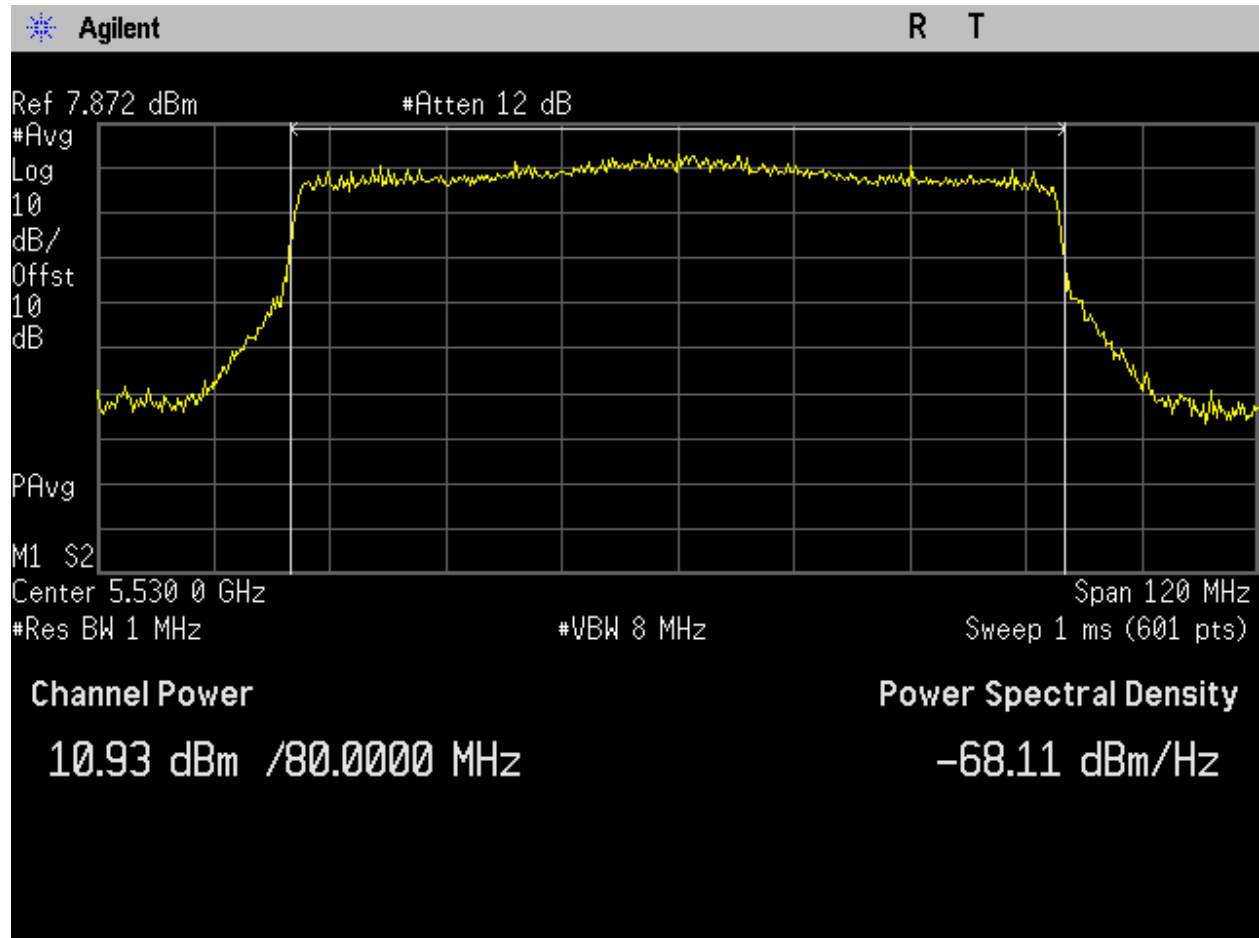


Figure 265: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_Output Power_Port 1.

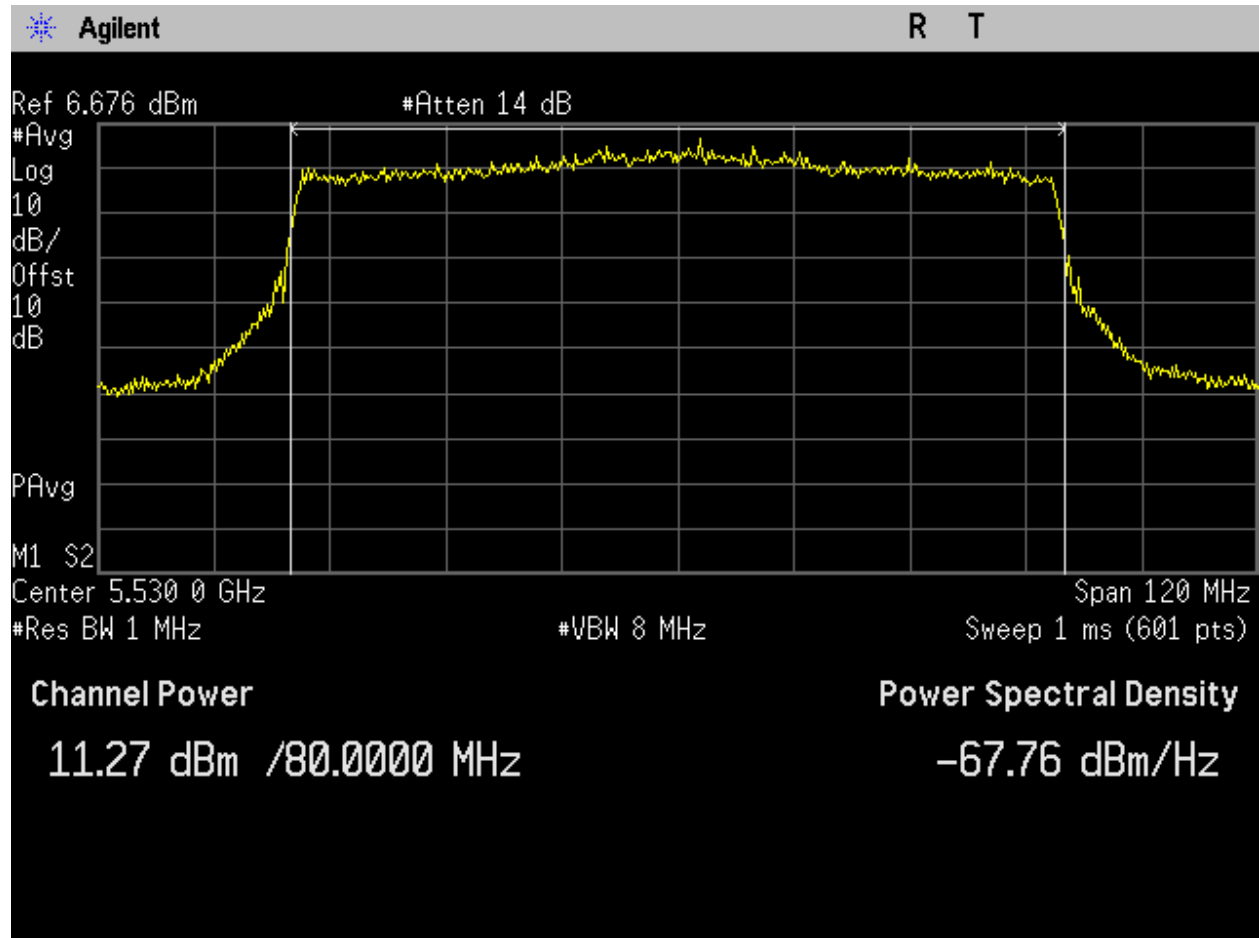


Figure 266: U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ax-mode_Output Power_Port 2.

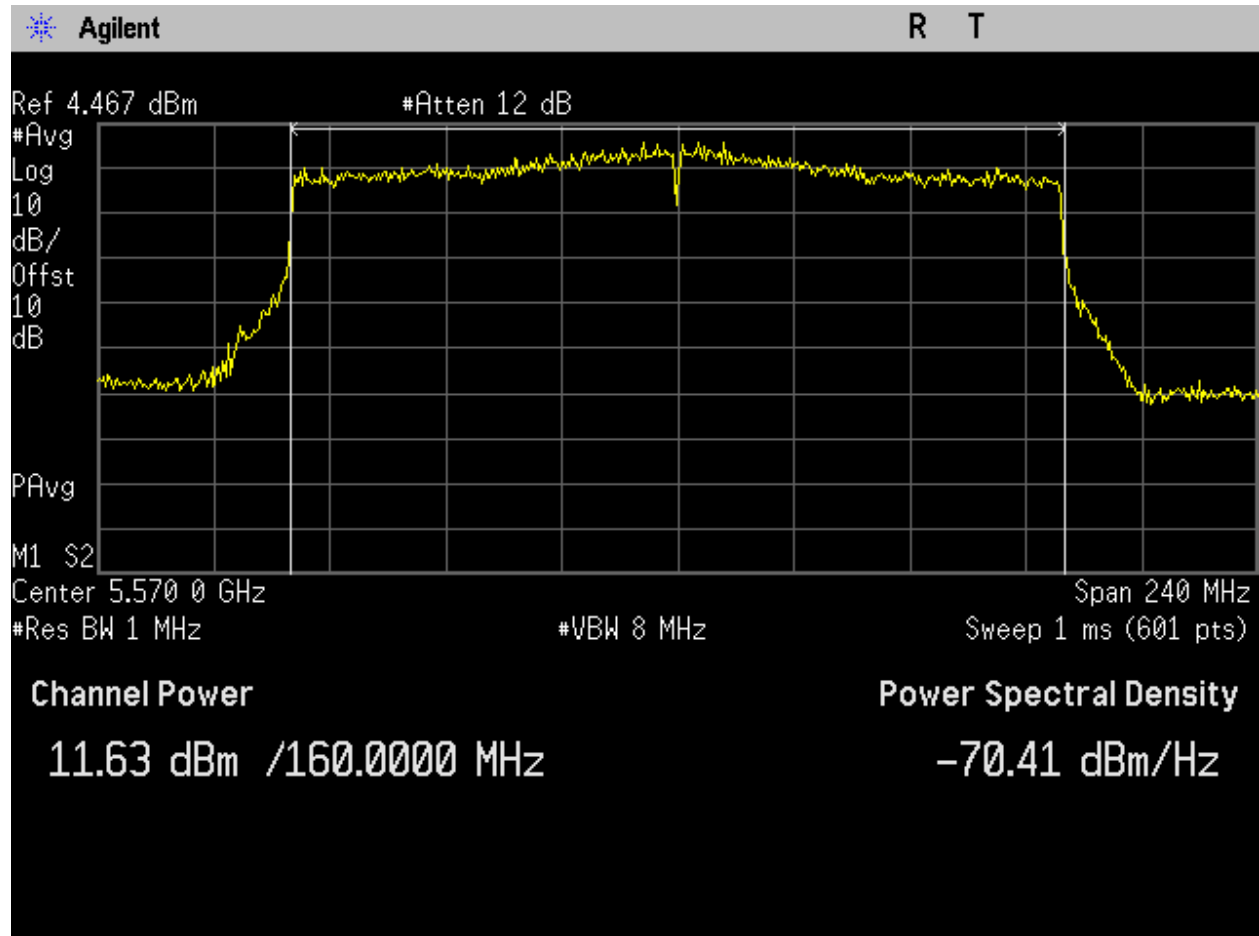


Figure 267: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_Output Power_Port 1.

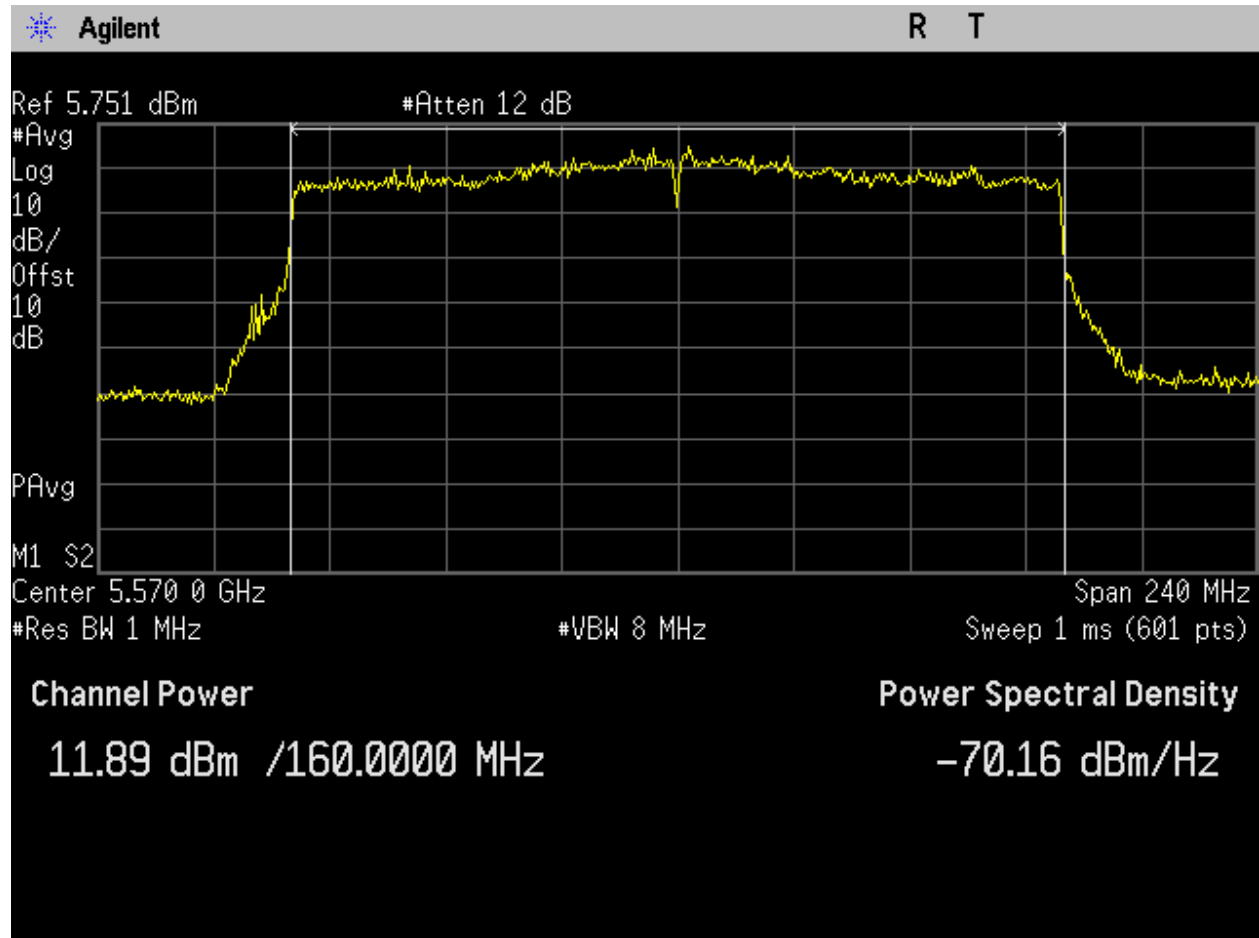


Figure 268: U-NII-2C_5570MHz_Low Ch_114_160MHz BW_ax-mode_Output Power_Port 2.

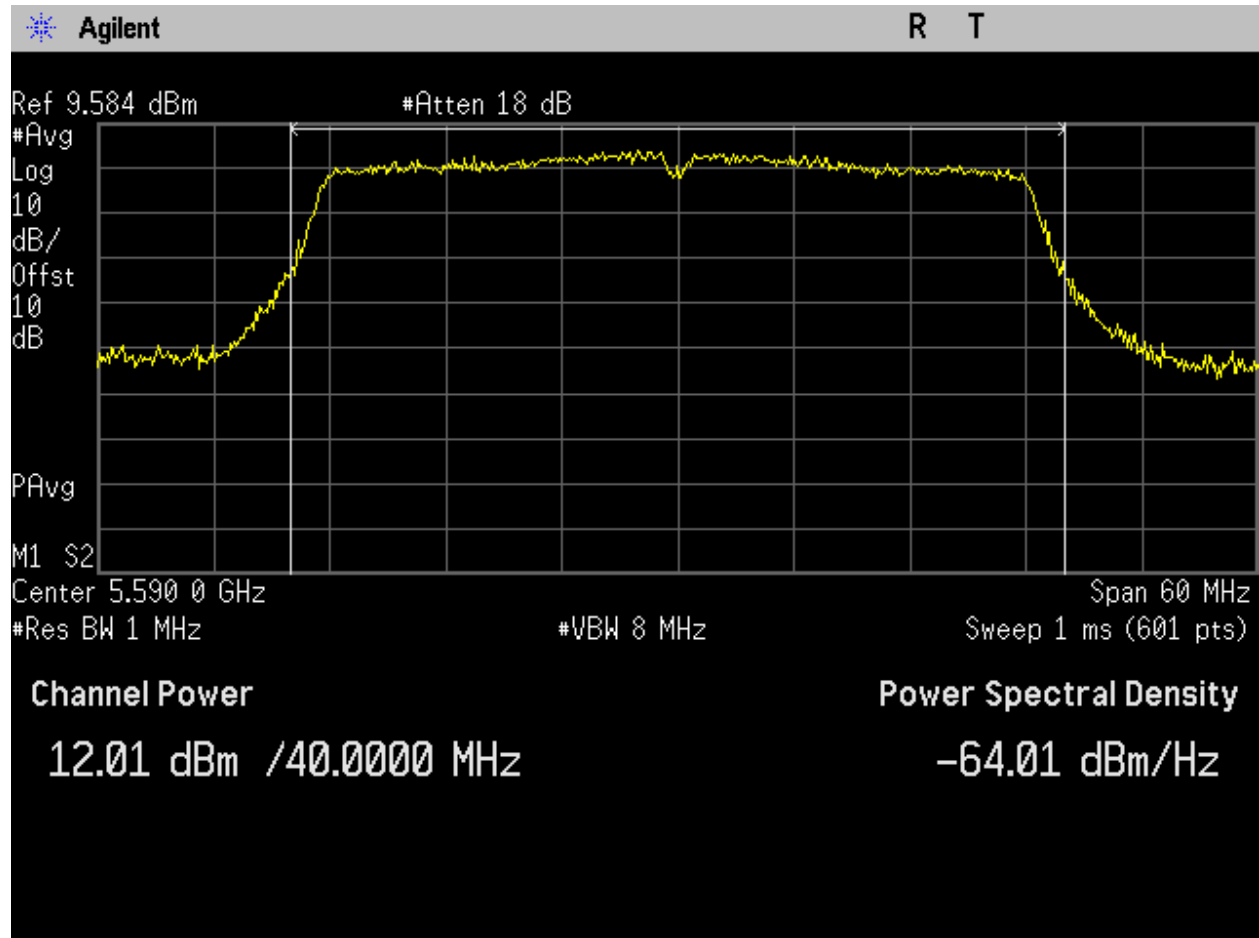


Figure 269: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ac-mode_Output Power_Port 1.

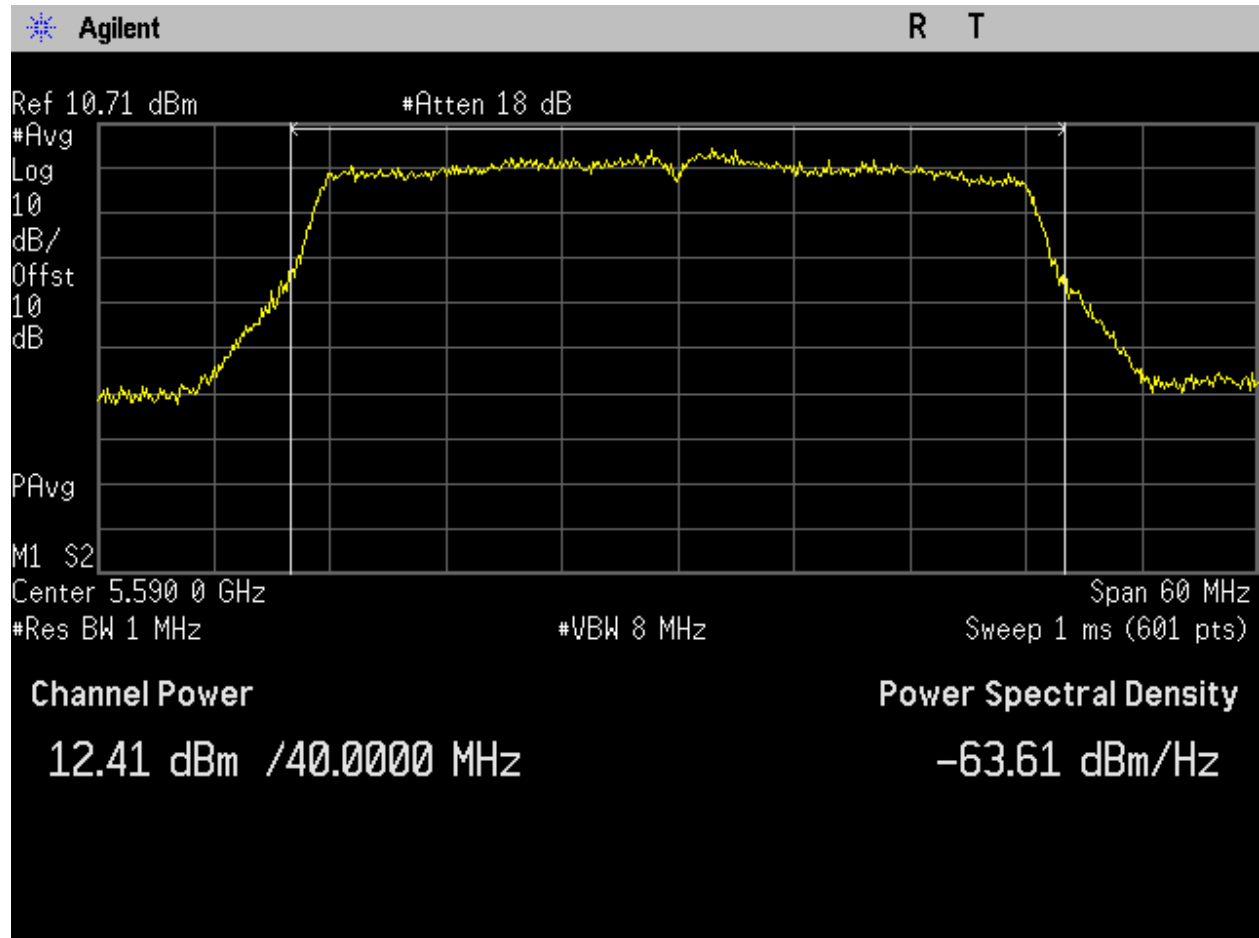


Figure 270: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ac-mode_Output Power_Port 2.

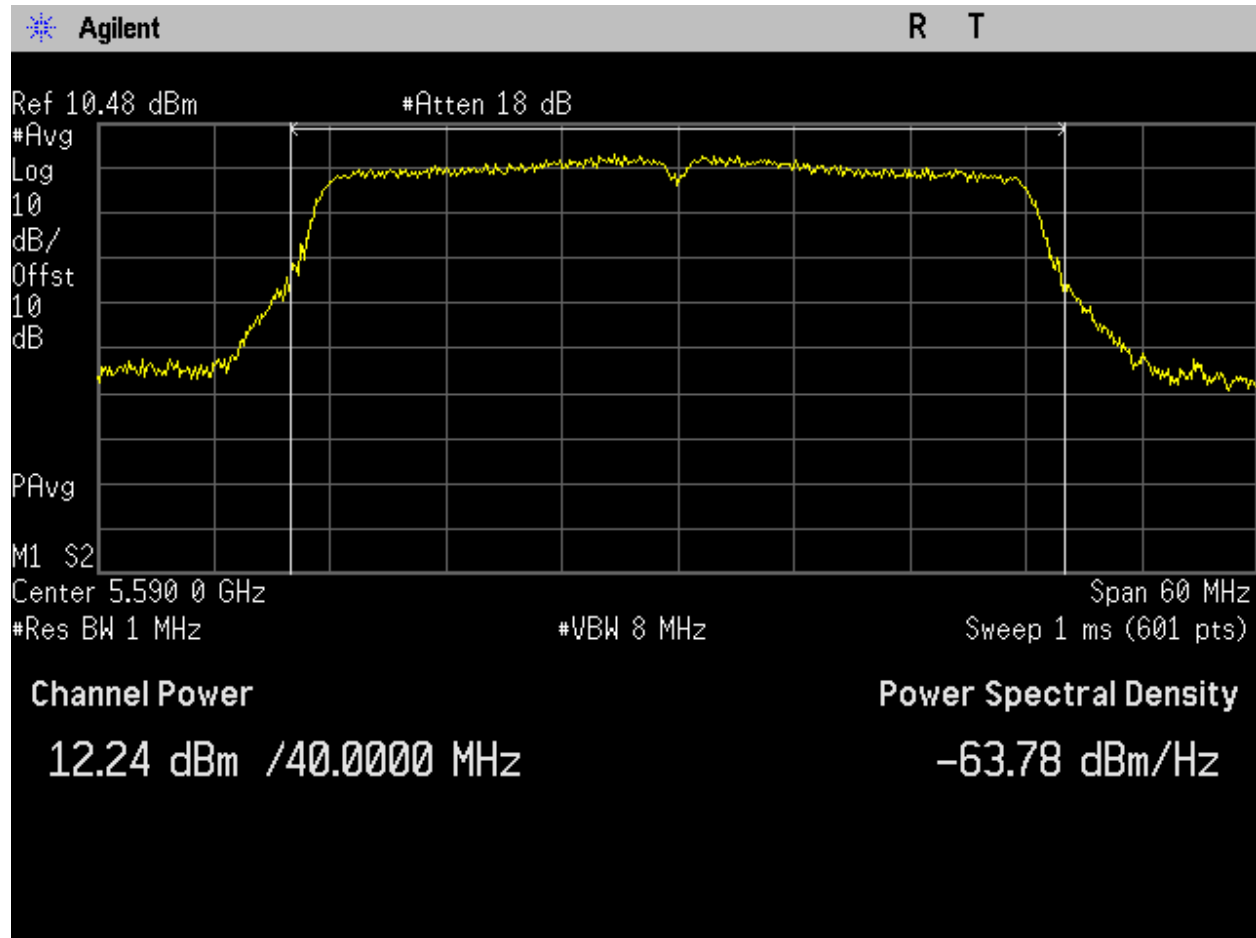


Figure 271: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ax-mode_Output Power_Port 1.

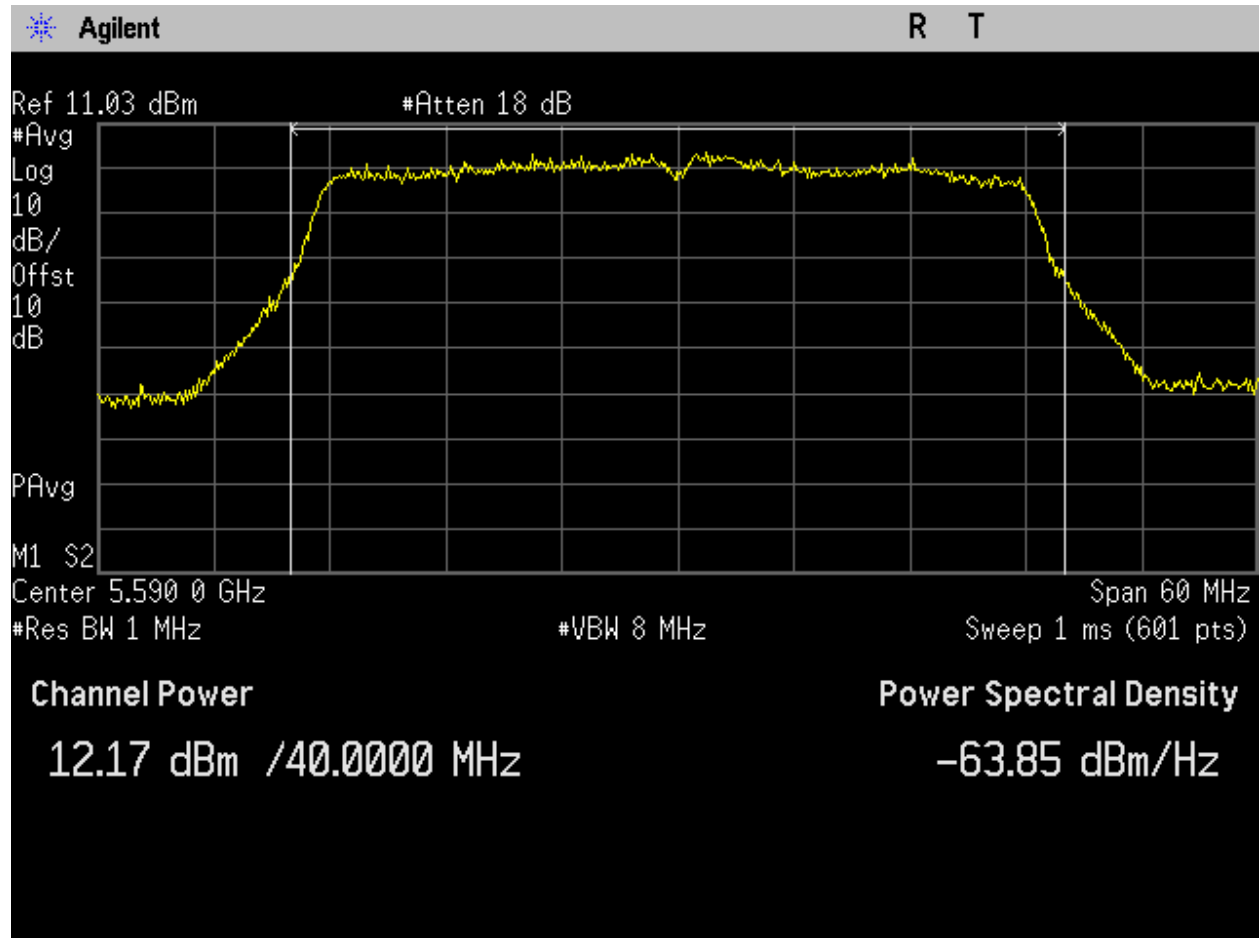


Figure 272: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_ax-mode_Output Power_Port 2.

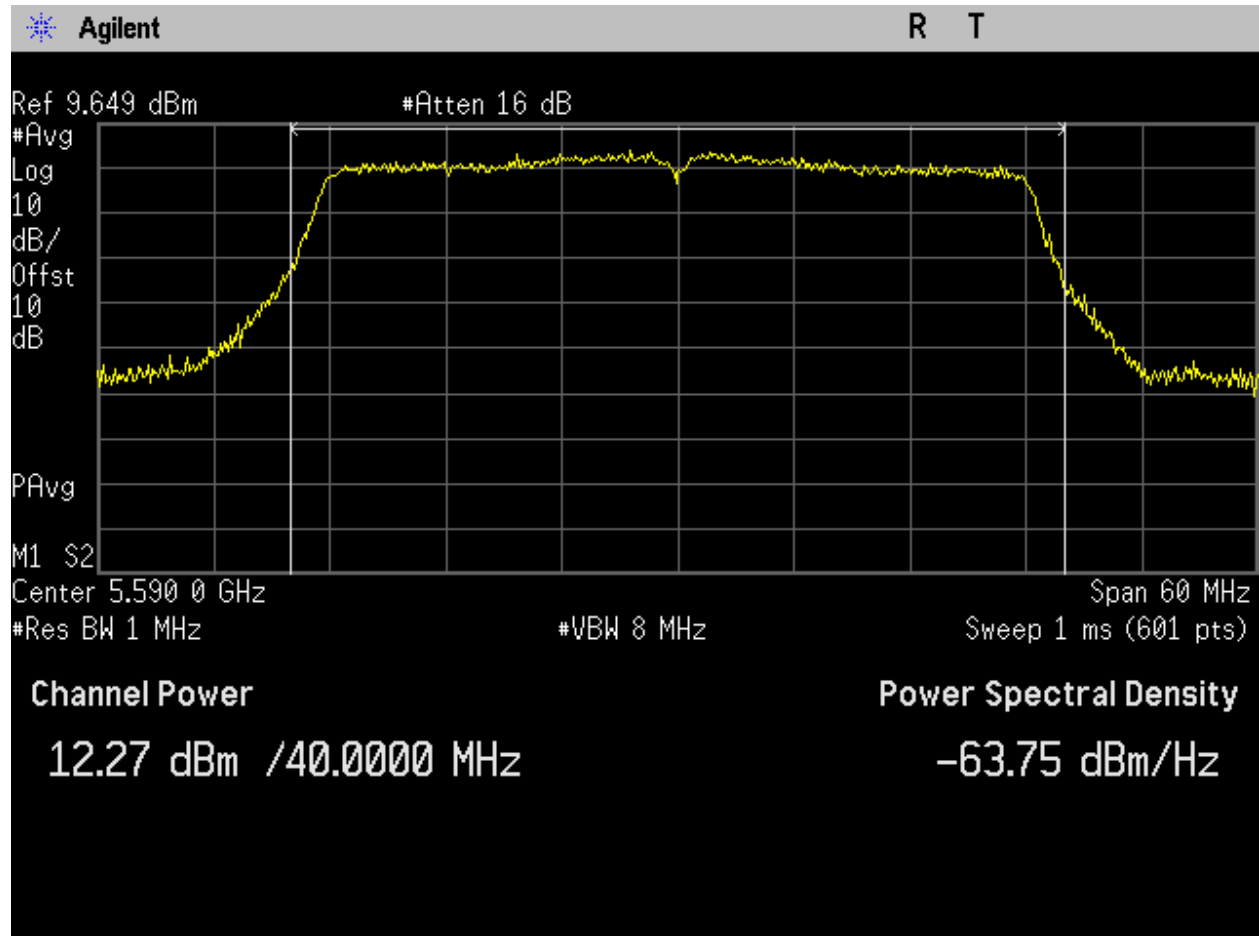


Figure 273: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-mode_Output Power_Port 1.

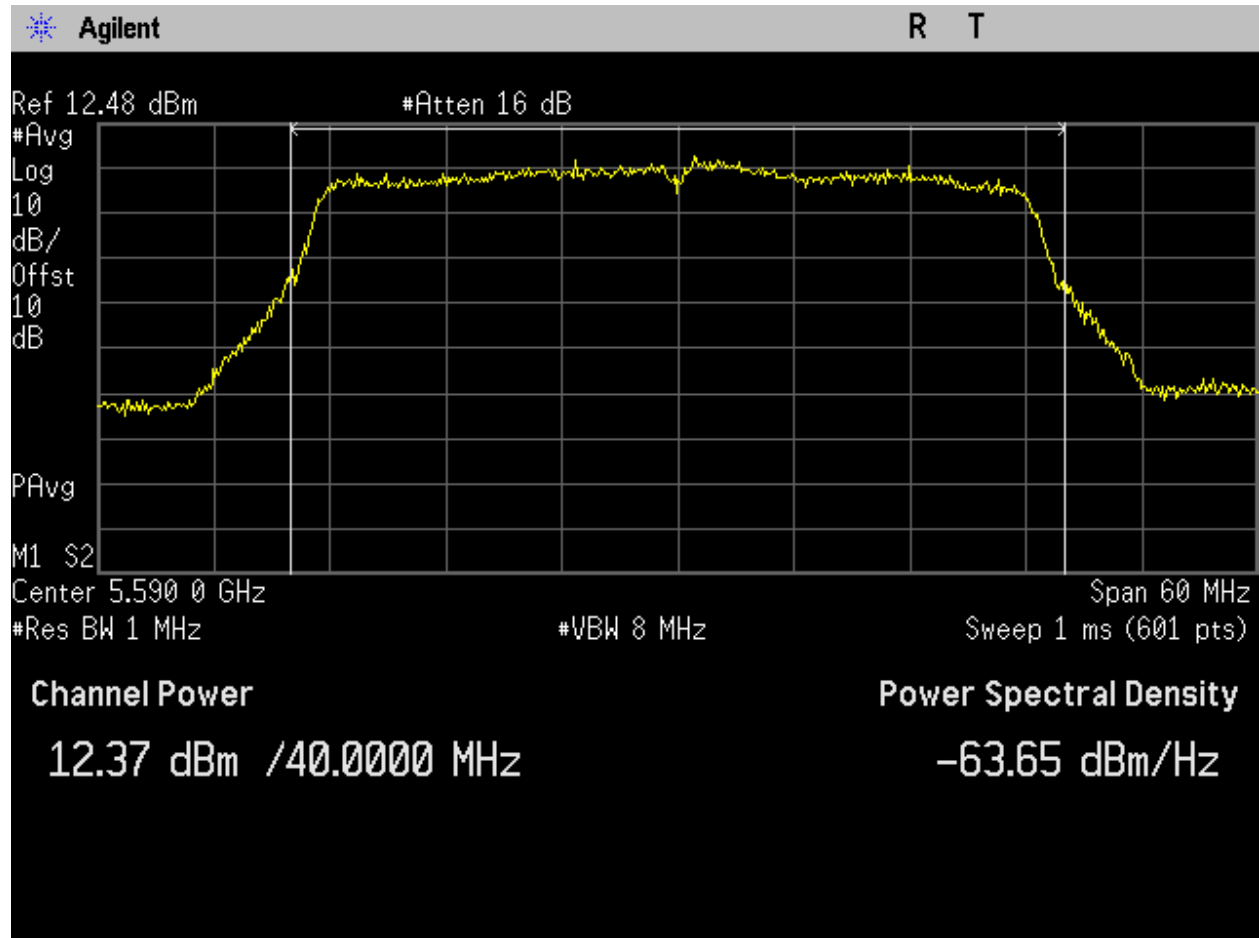


Figure 274: U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-mode_Output Power_Port 2.

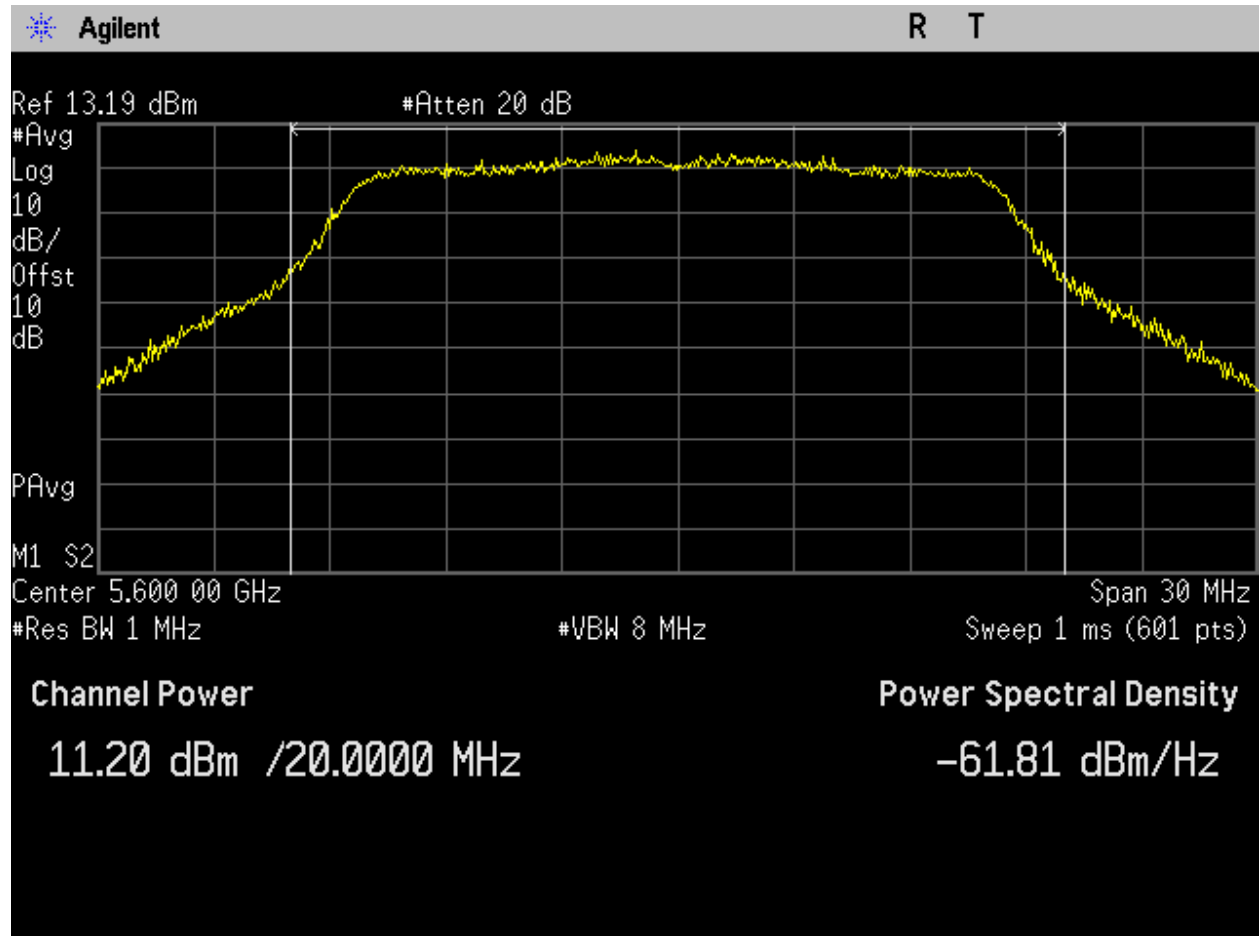


Figure 275: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Output Power_Port 1.

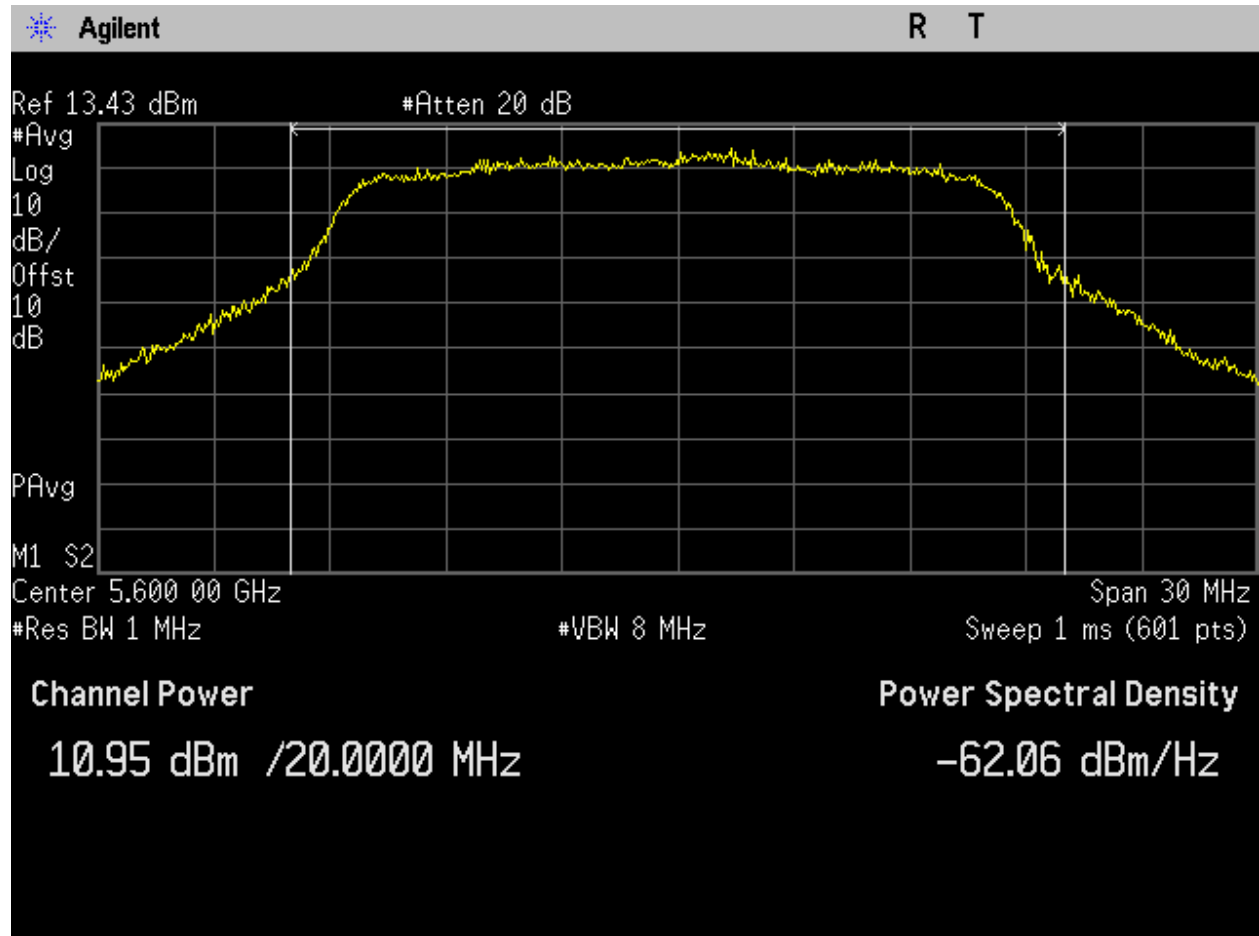


Figure 276: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Output Power_Port 2.

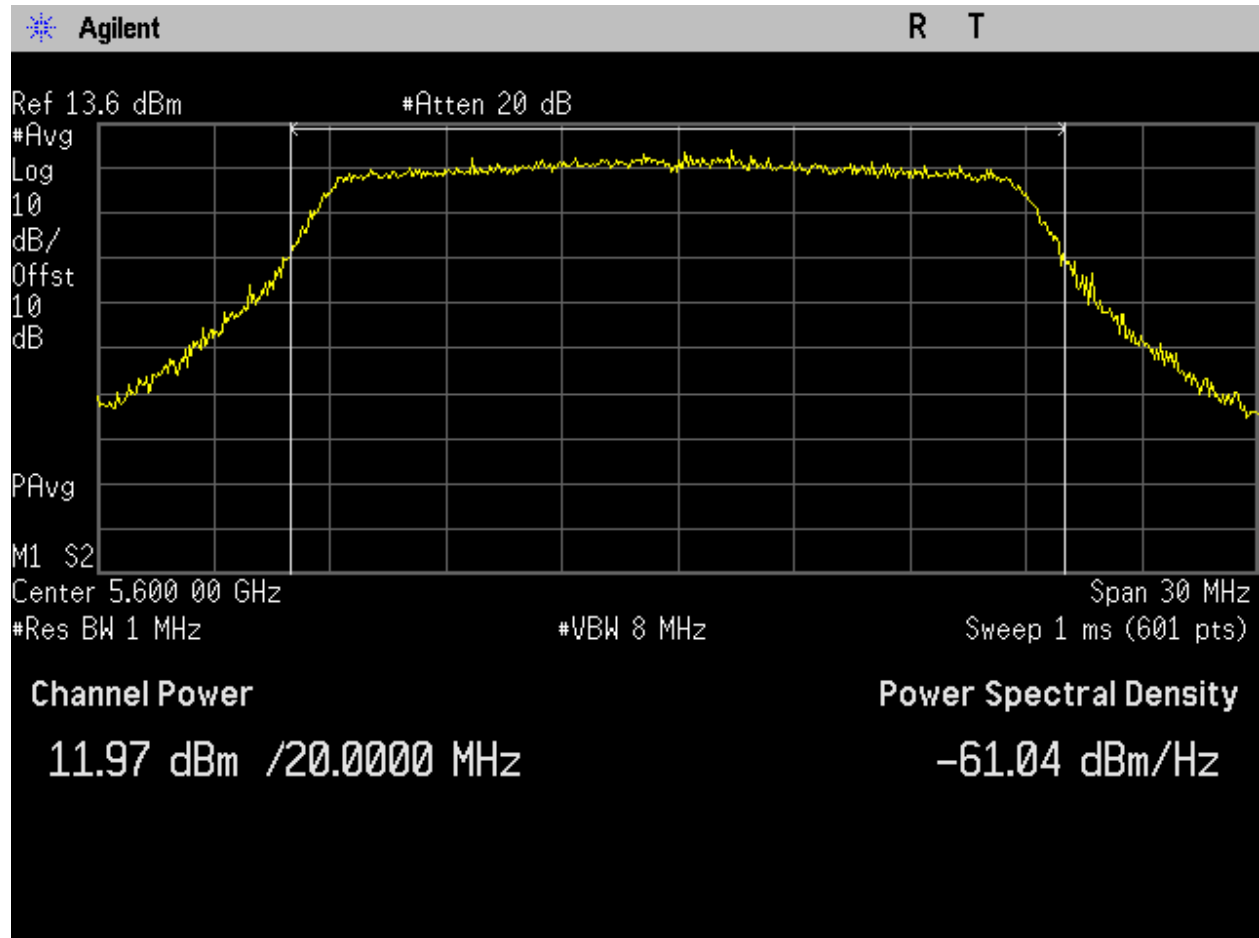


Figure 277: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Output Power_Port 1.

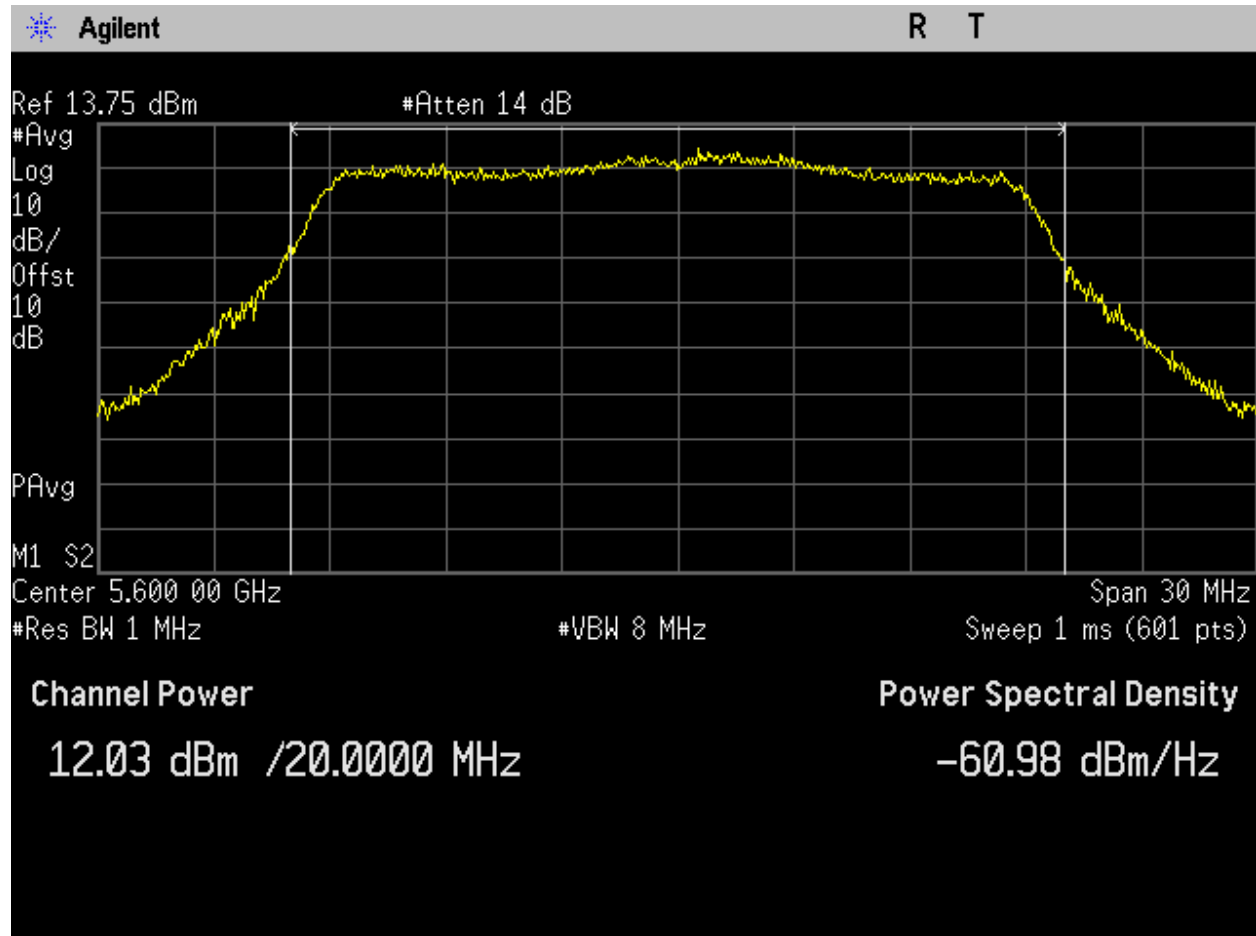


Figure 278: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Output Power_Port 2.

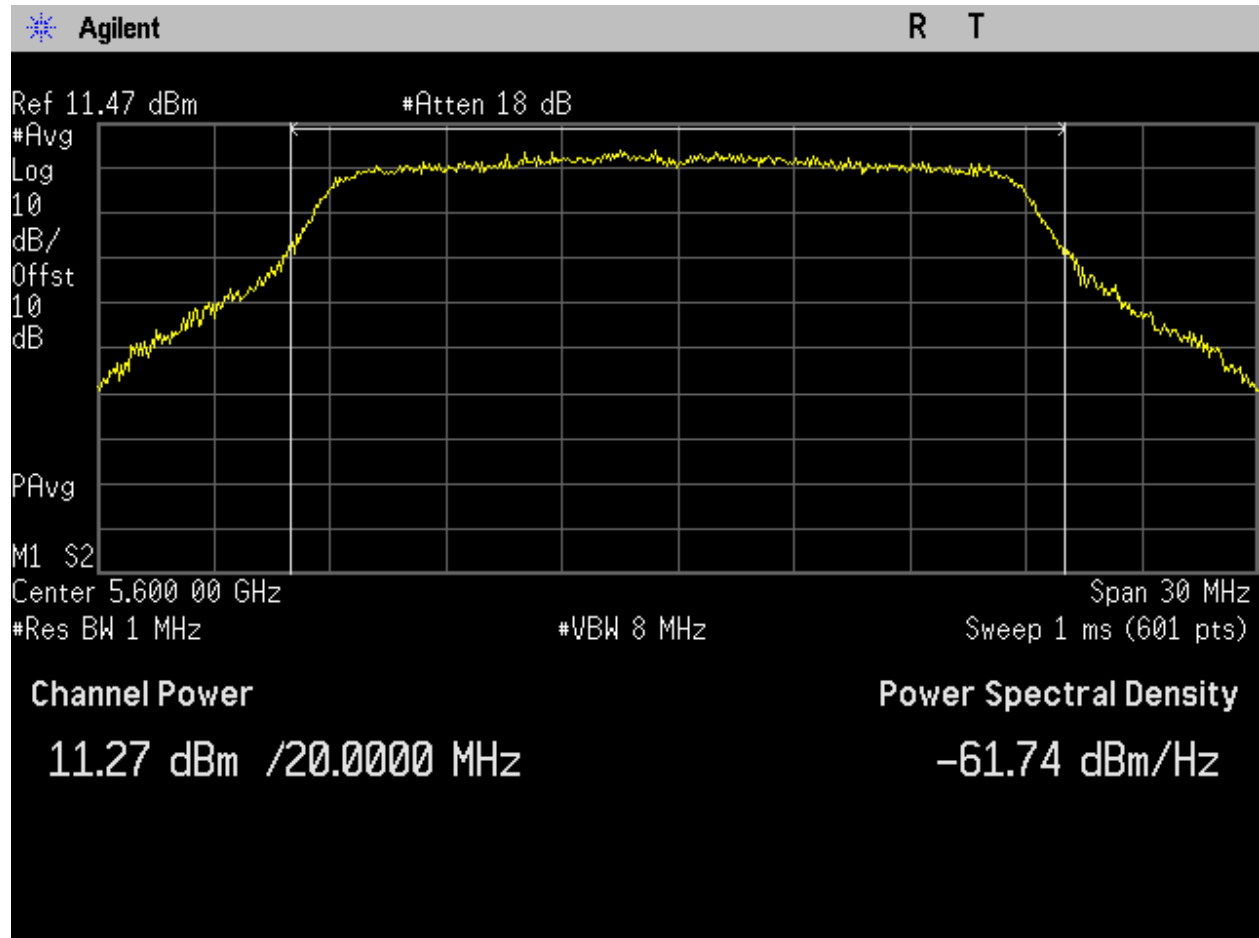


Figure 279: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ax-mode_Output Power_Port 1.

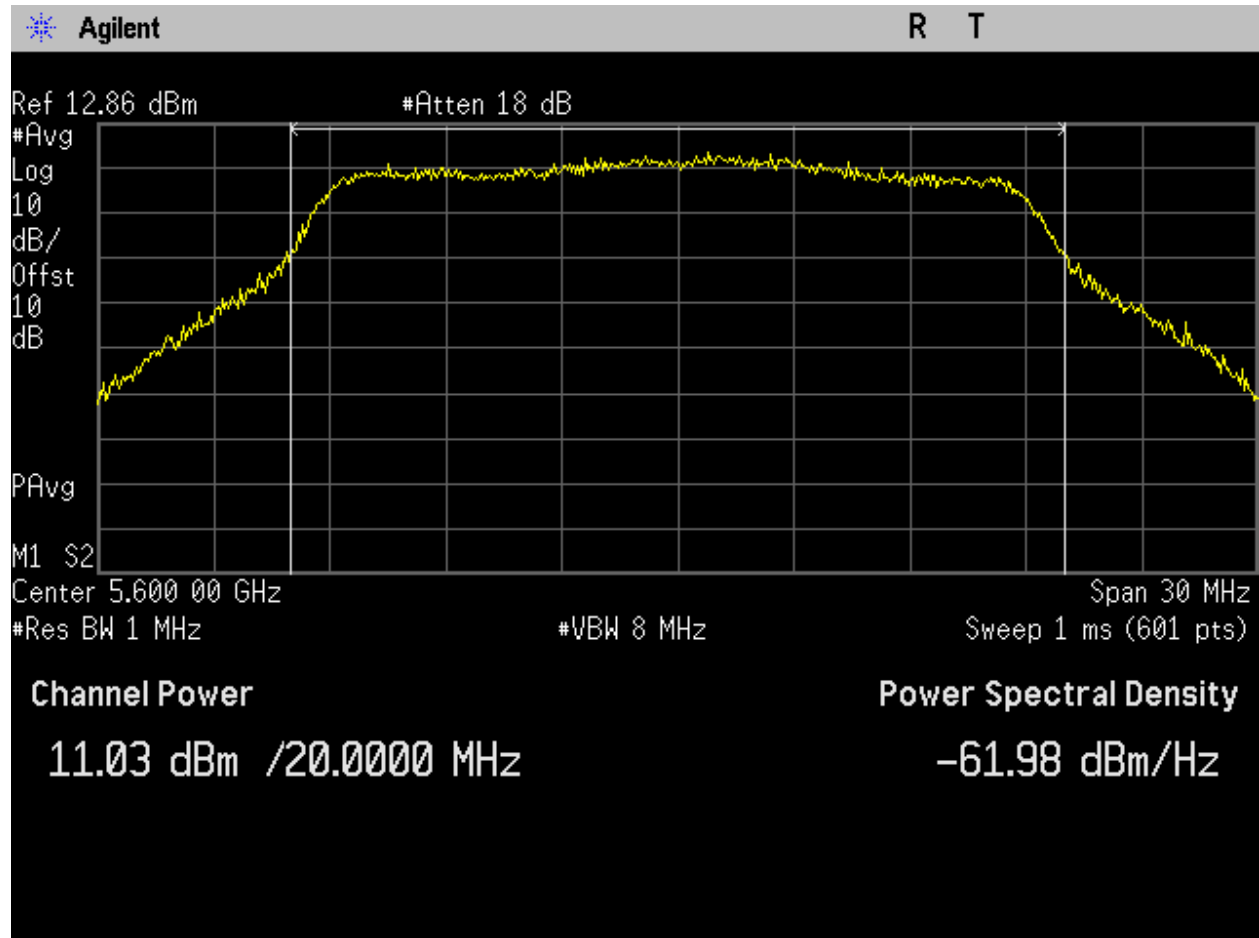


Figure 280: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ax-mode_Output Power_Port 2.

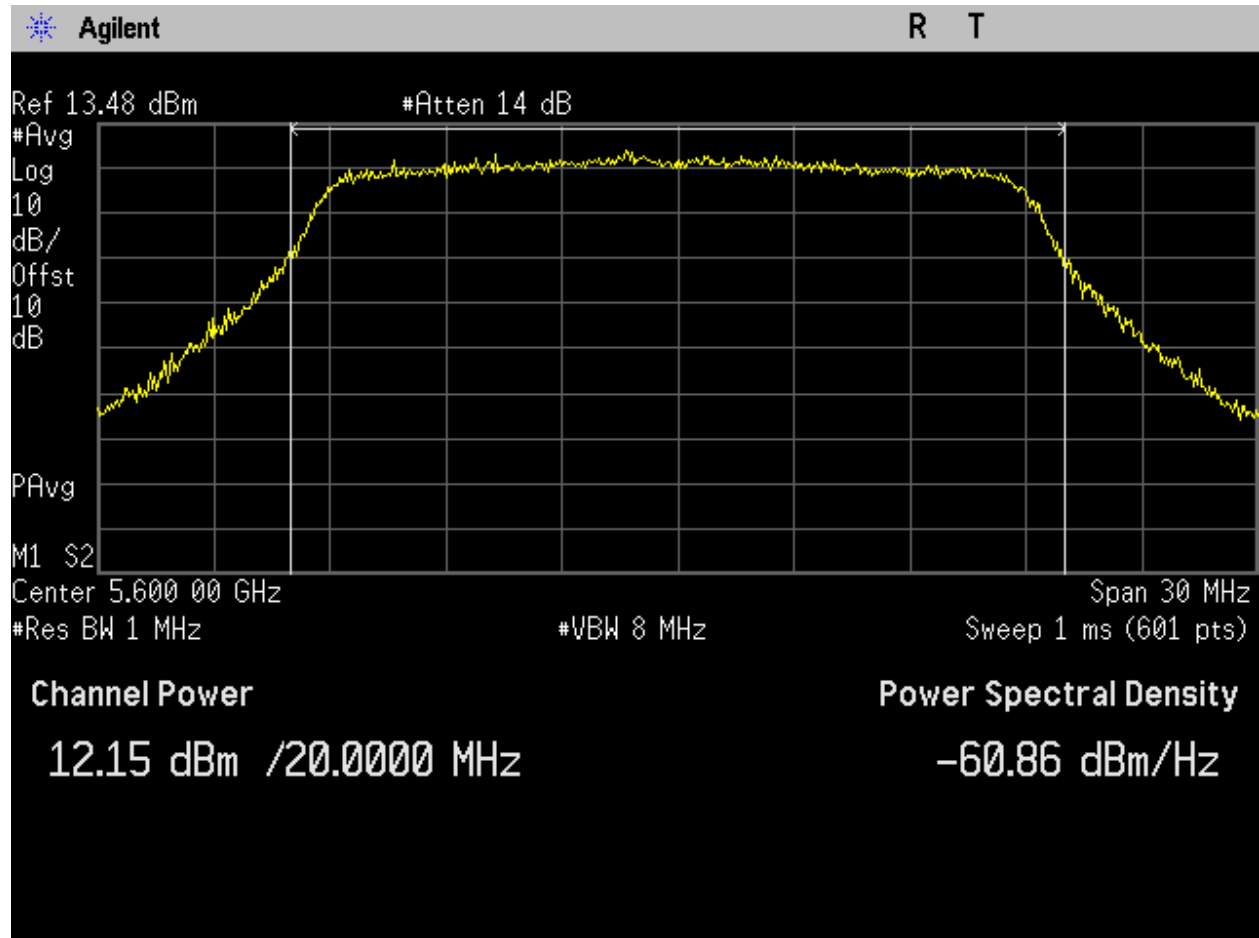


Figure 281: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_n-mode_Output Power_Port 1.

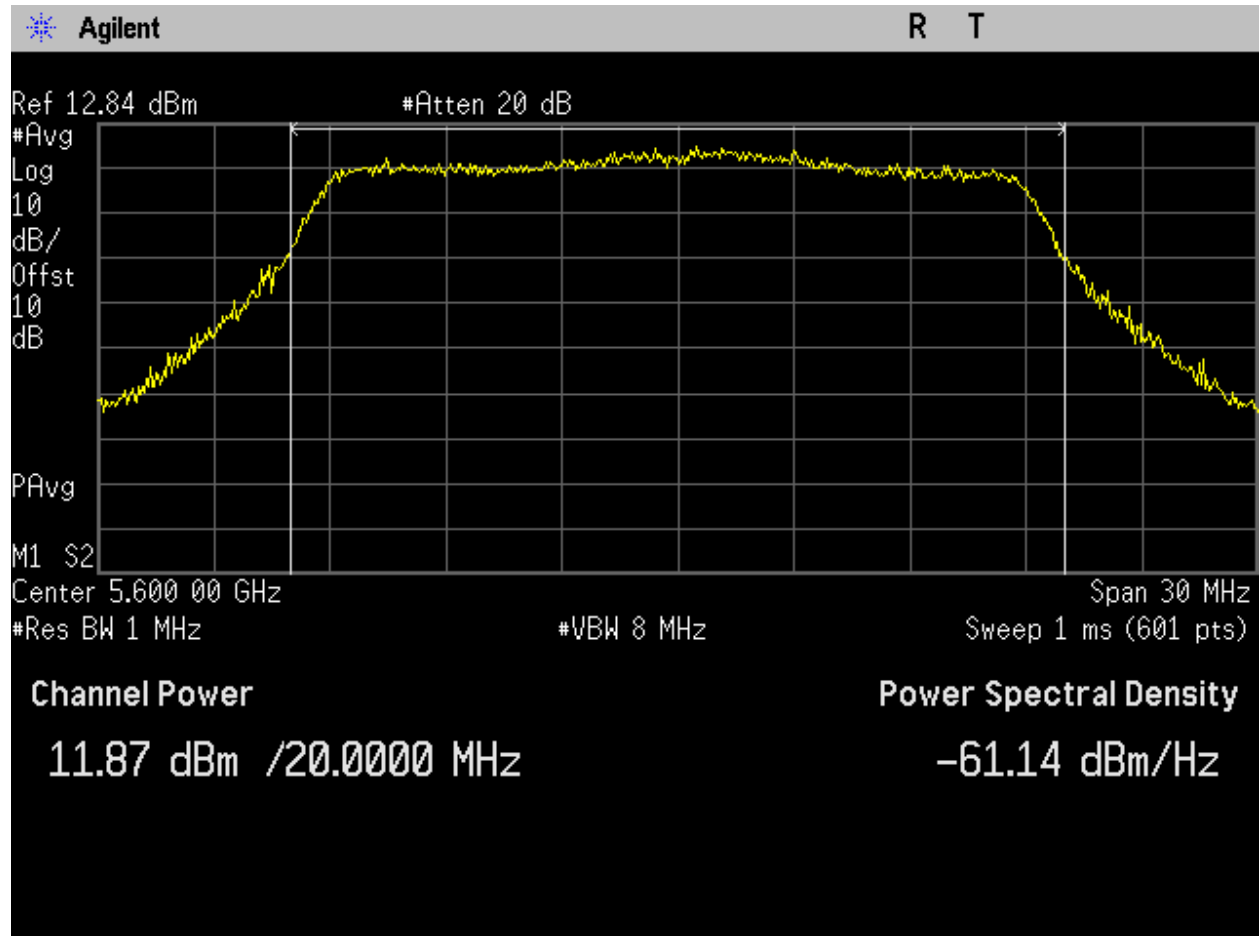


Figure 282: U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_n-mode_Output Power_Port 2.

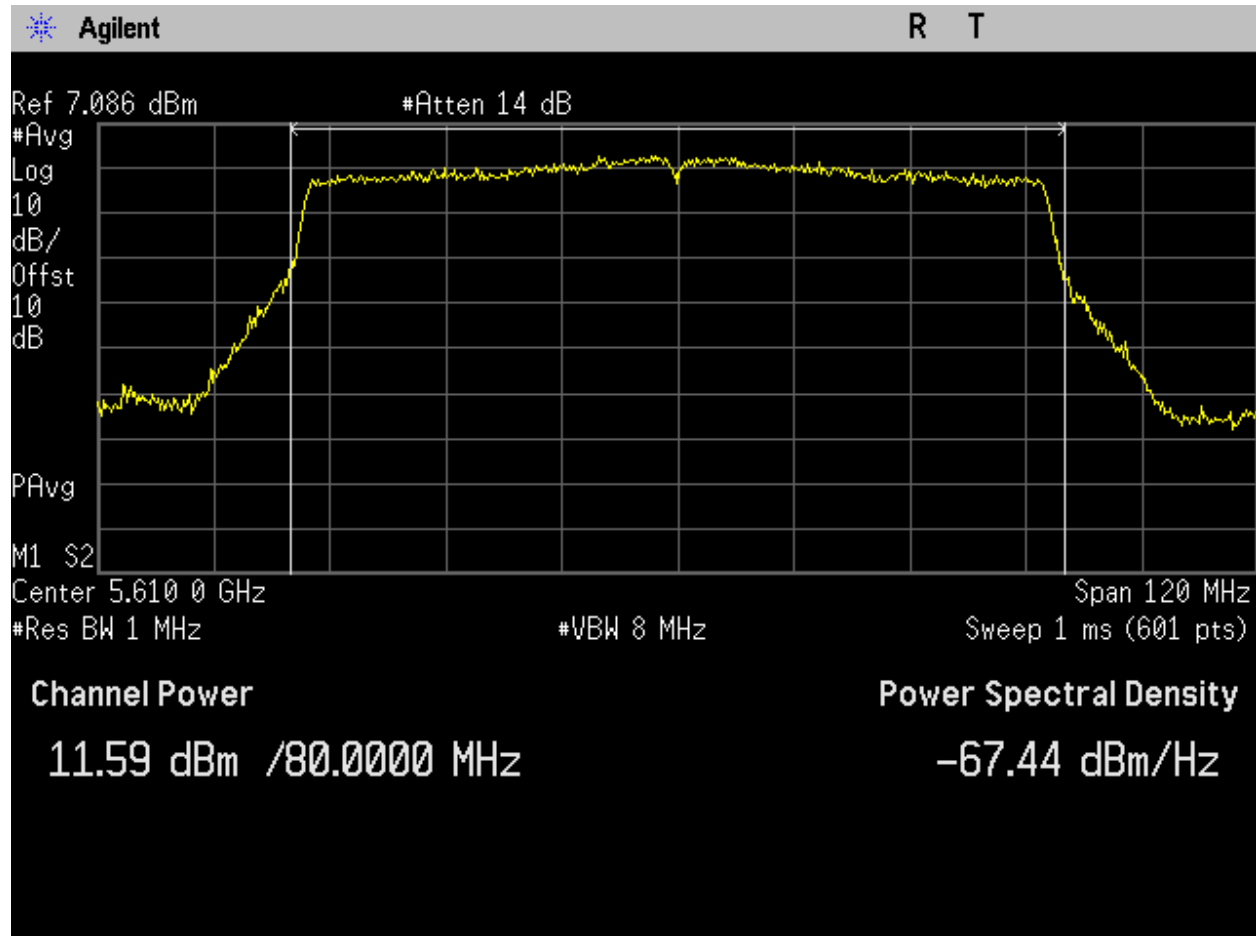


Figure 283: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Output Power_Port 1.

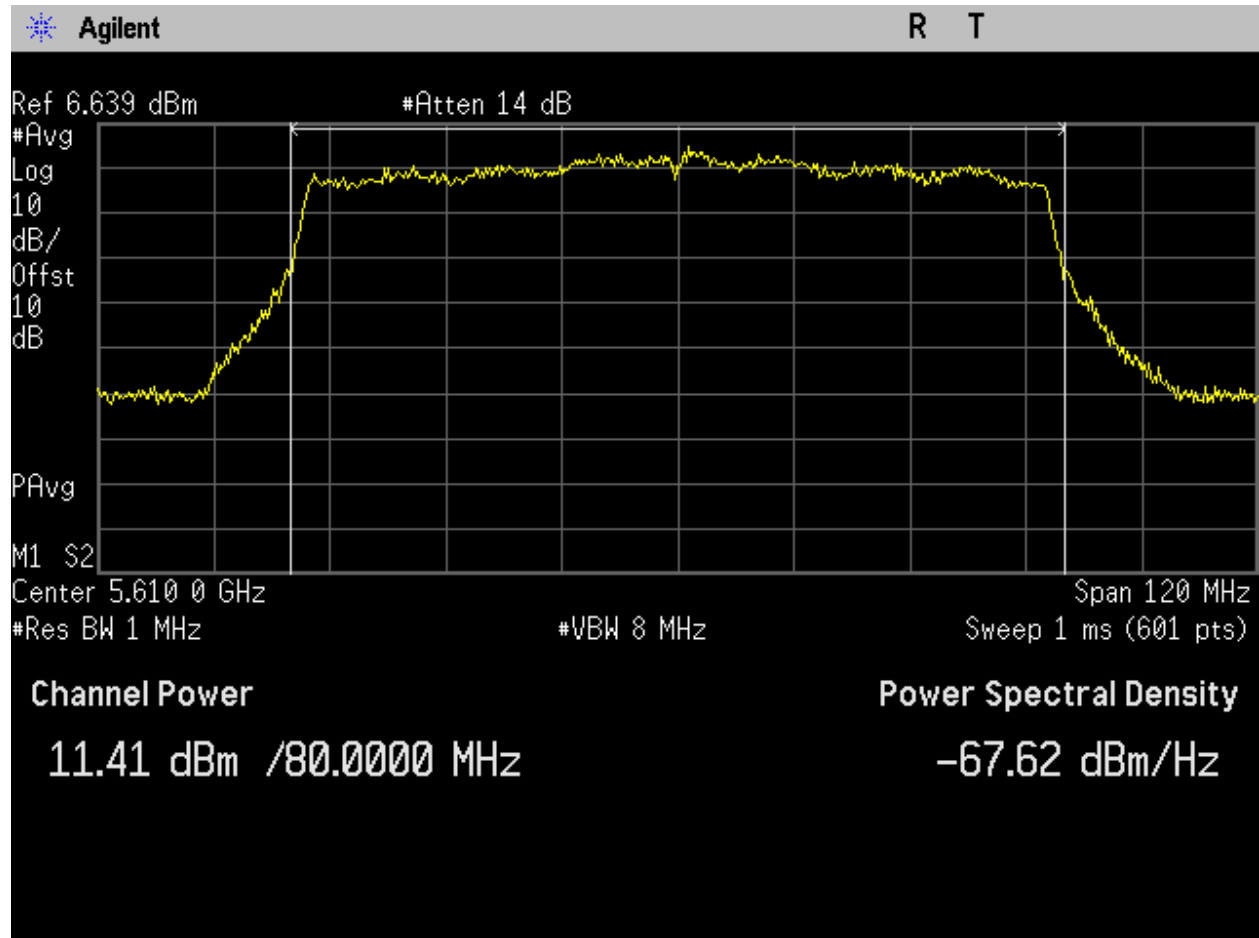


Figure 284: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Output Power_Port 2.

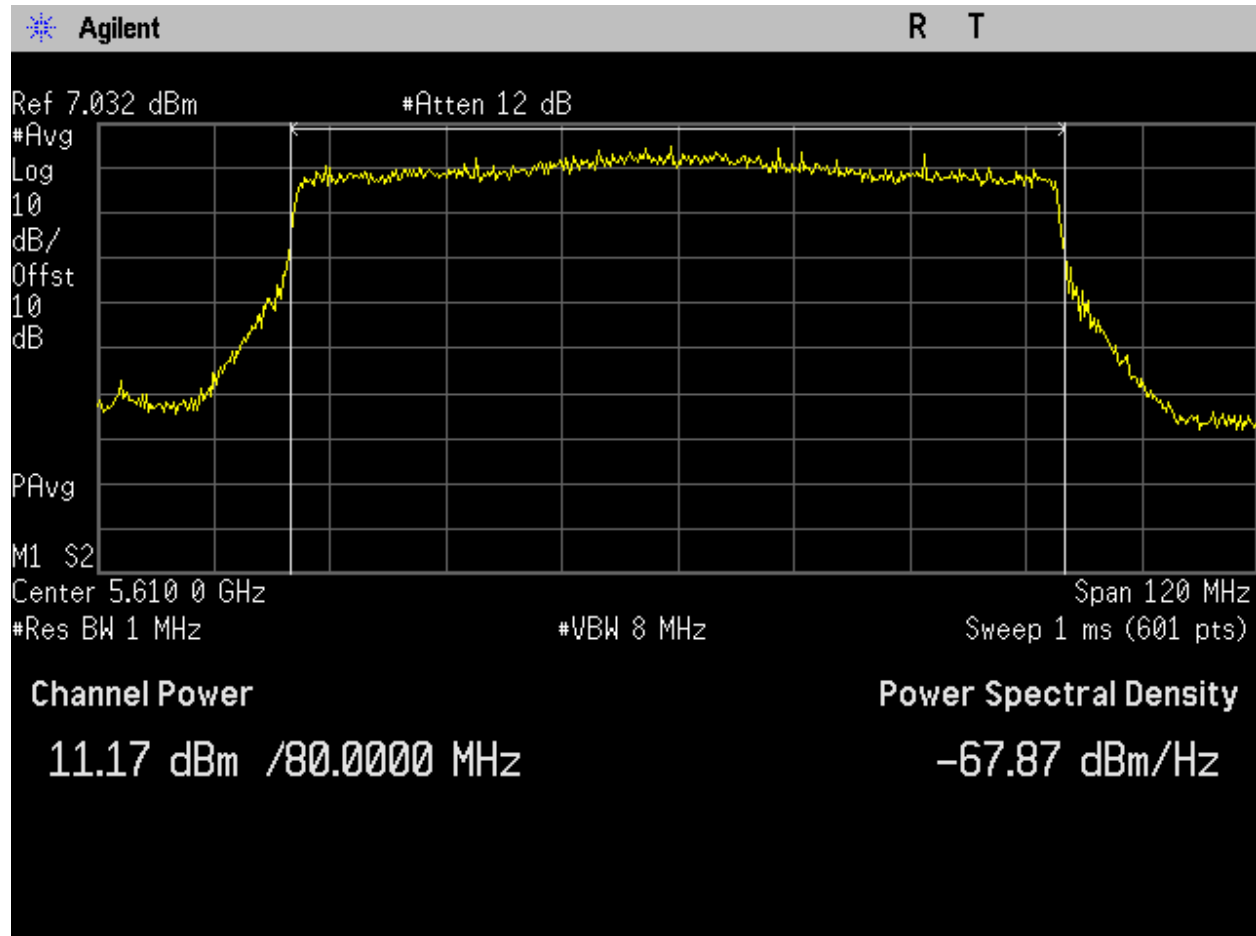


Figure 285: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Output Power_Port 1.

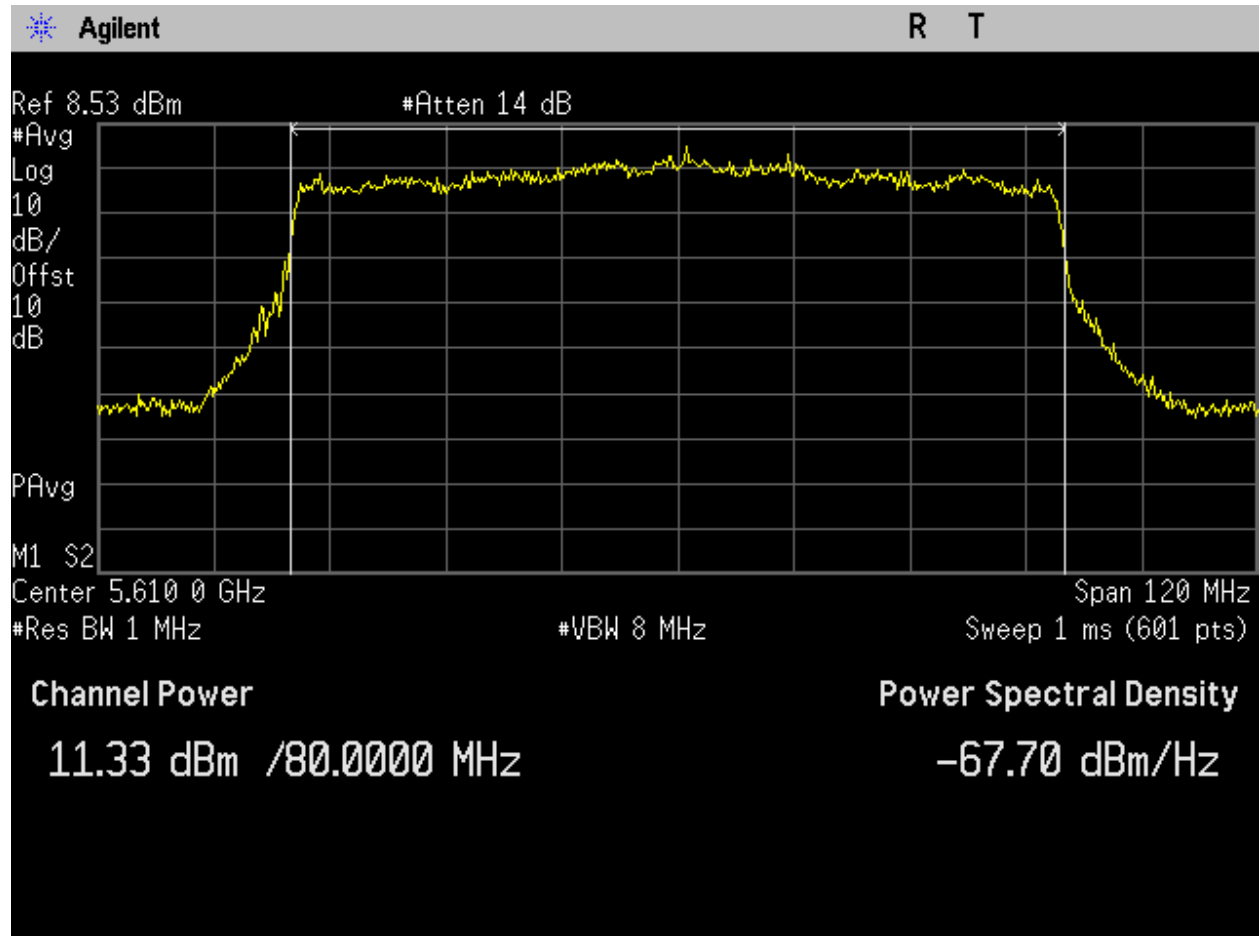


Figure 286: U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Output Power_Port 2.

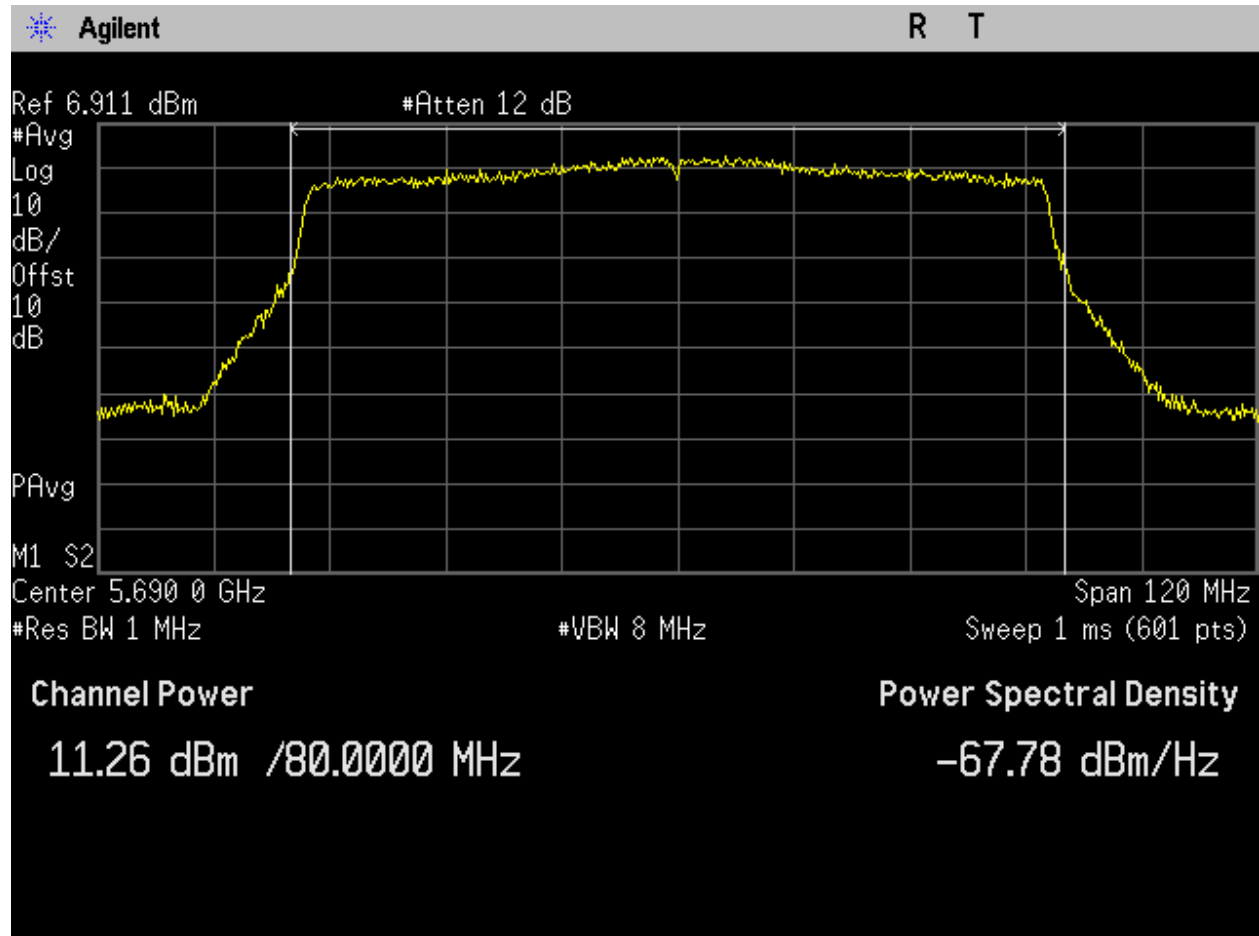


Figure 287: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Output Power_Port 1.

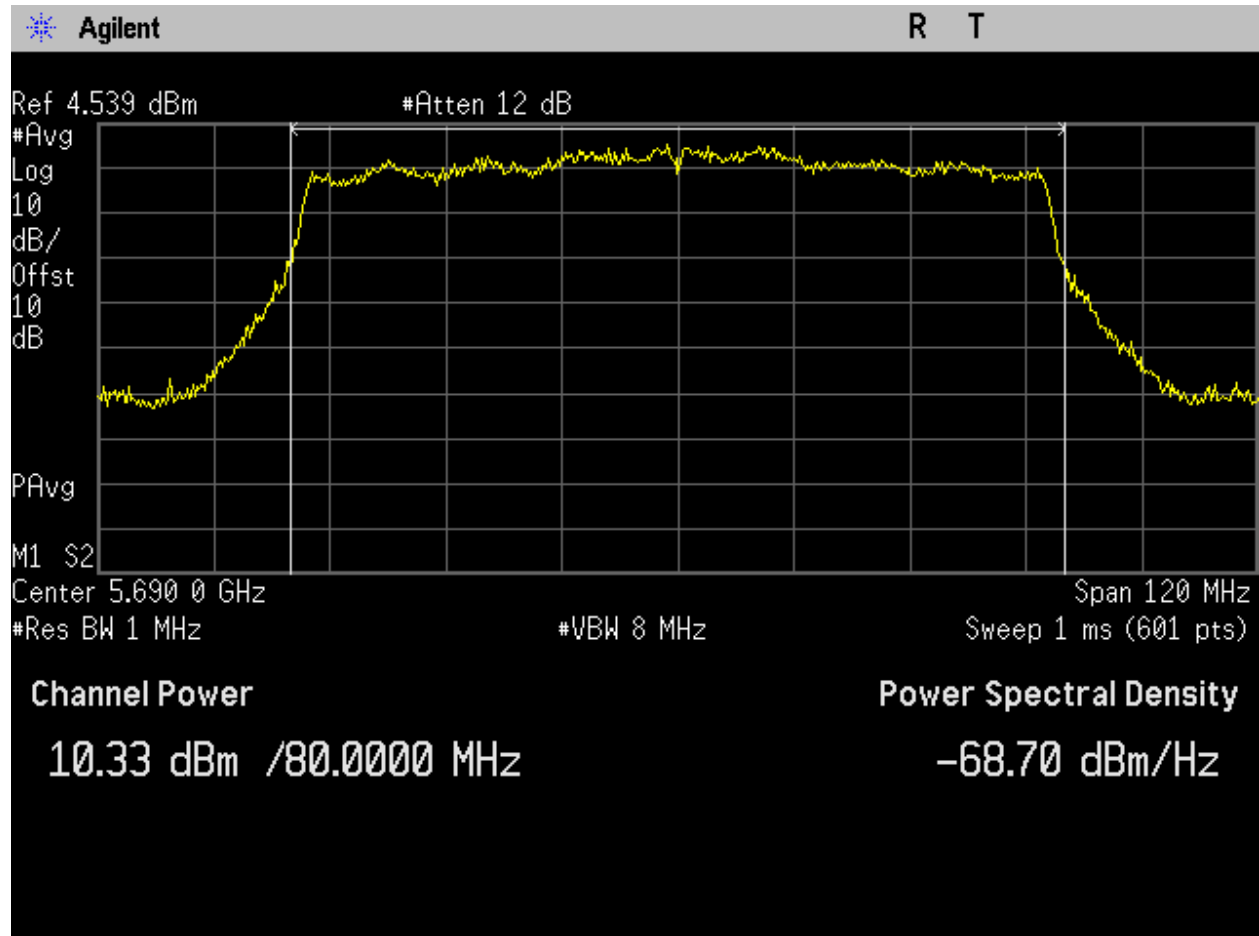


Figure 288: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Output Power_Port 2.

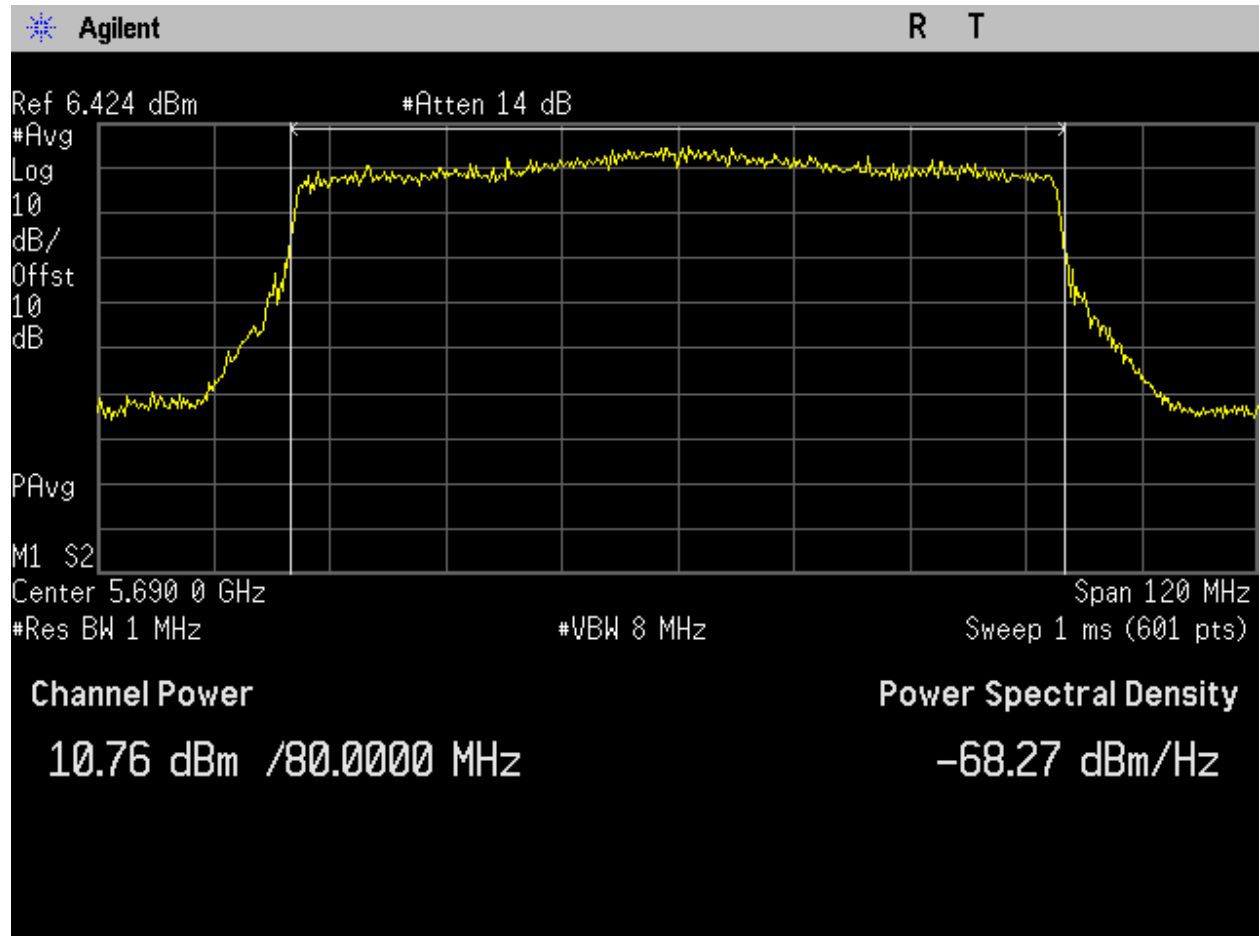


Figure 289: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ax-mode_Output Power_Port 1.

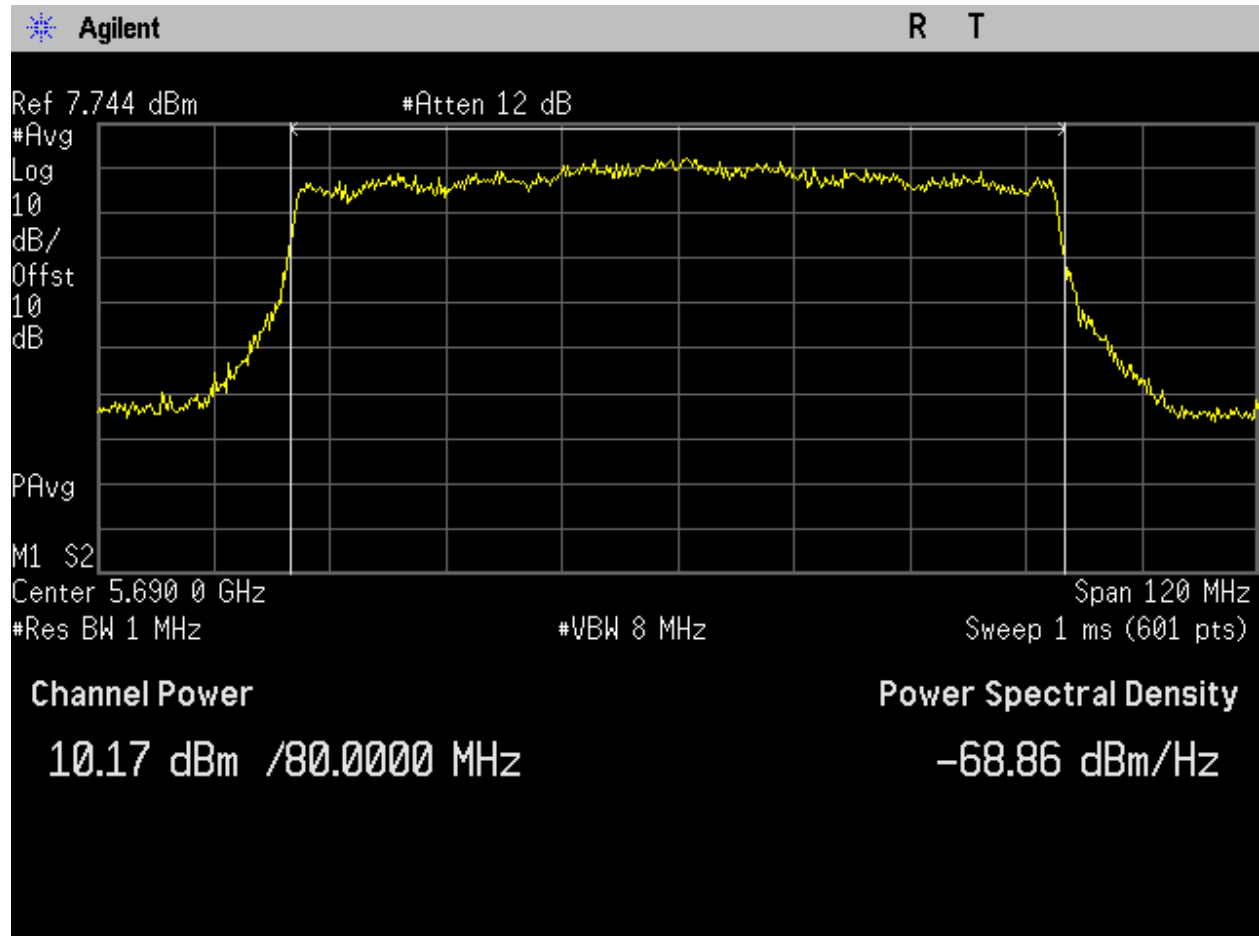


Figure 290: U-NII-2C_5690MHz_High Ch_138_80MHz BW_ax-mode_Output Power_Port 2.

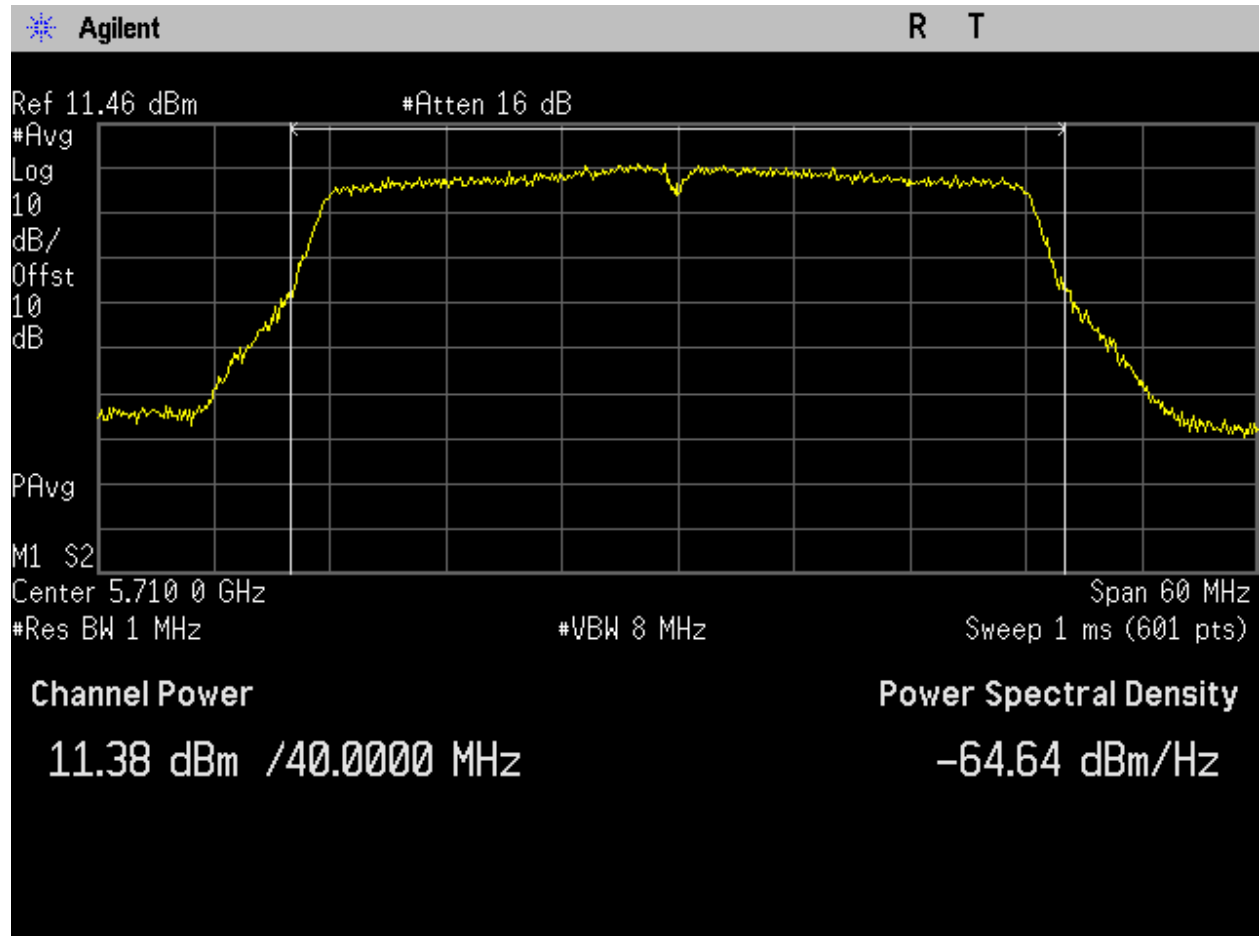


Figure 291: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Output Power_Port 1.

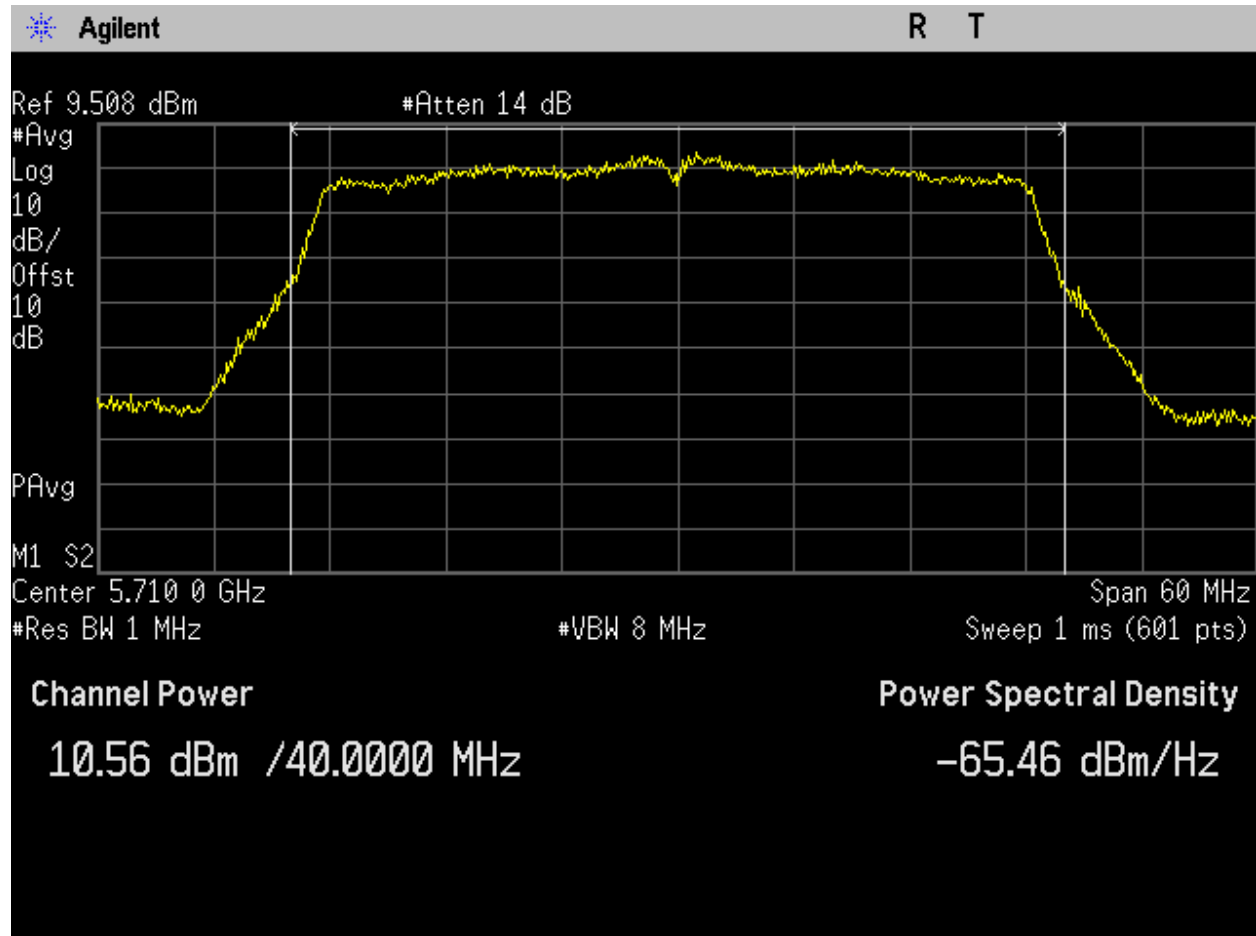


Figure 292: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Output Power_Port 2.

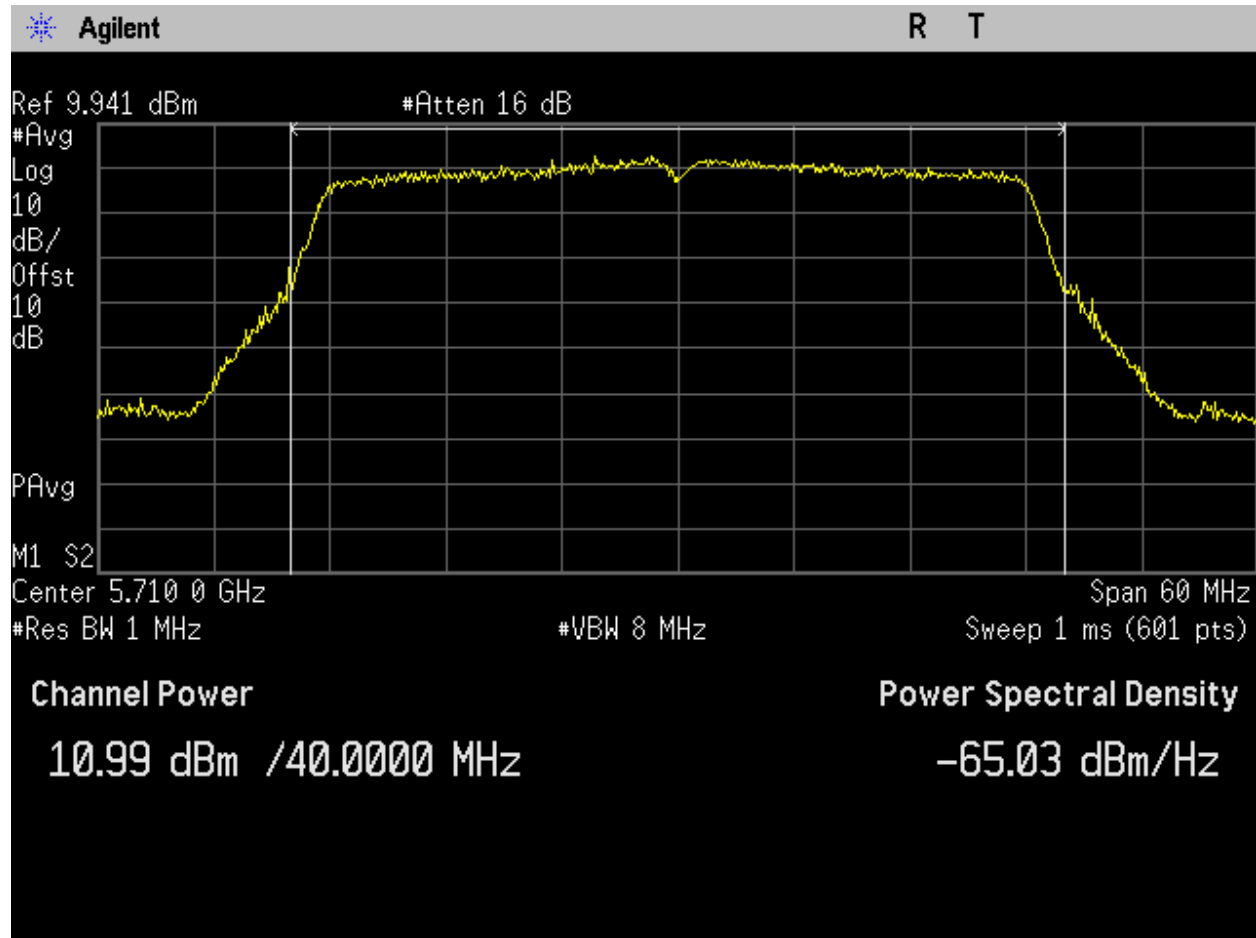


Figure 293: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ax-mode_Output Power_Port 1.

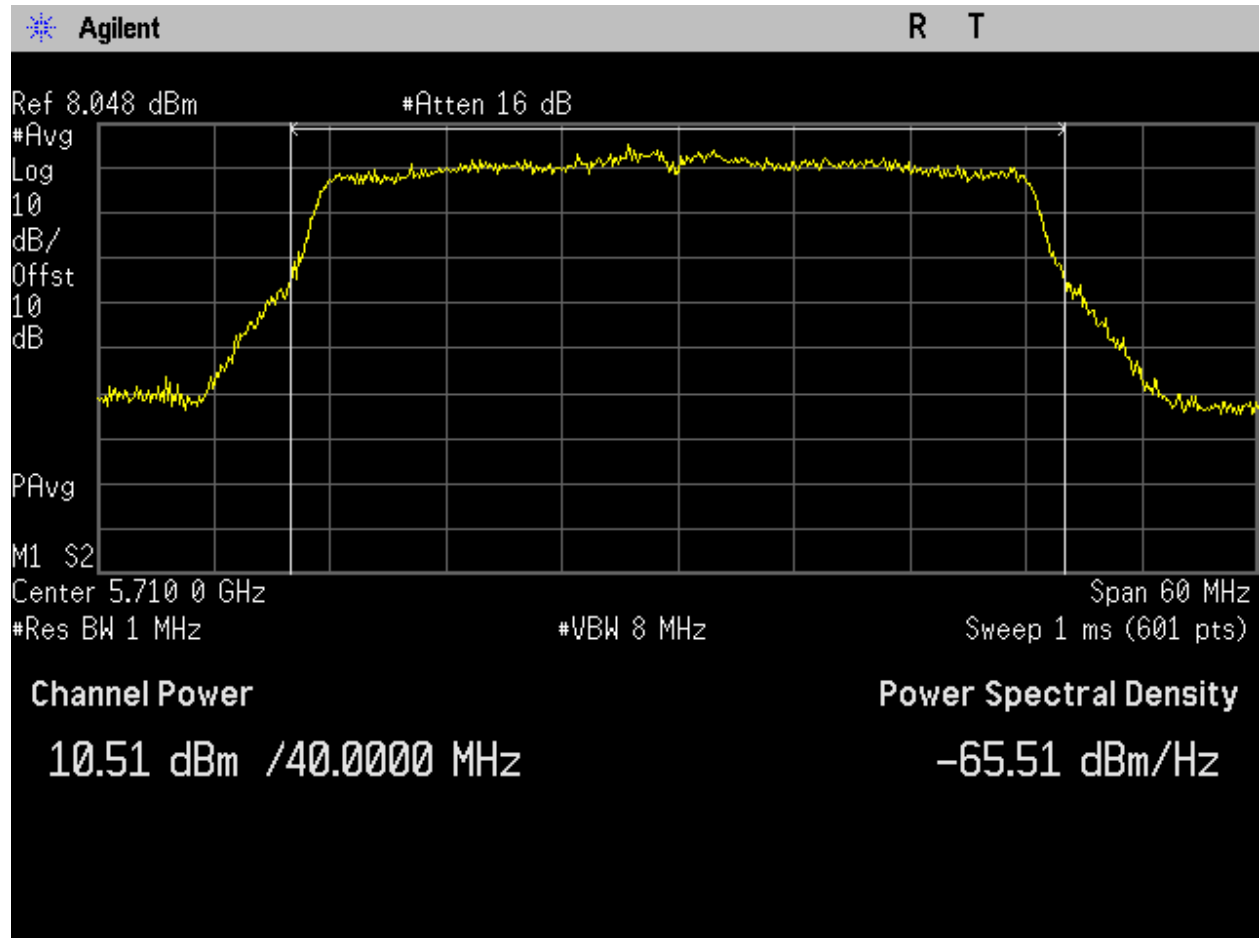


Figure 294: U-NII-2C_5710MHz_High Ch_142_40MHz BW_ax-mode_Output Power_Port 2.

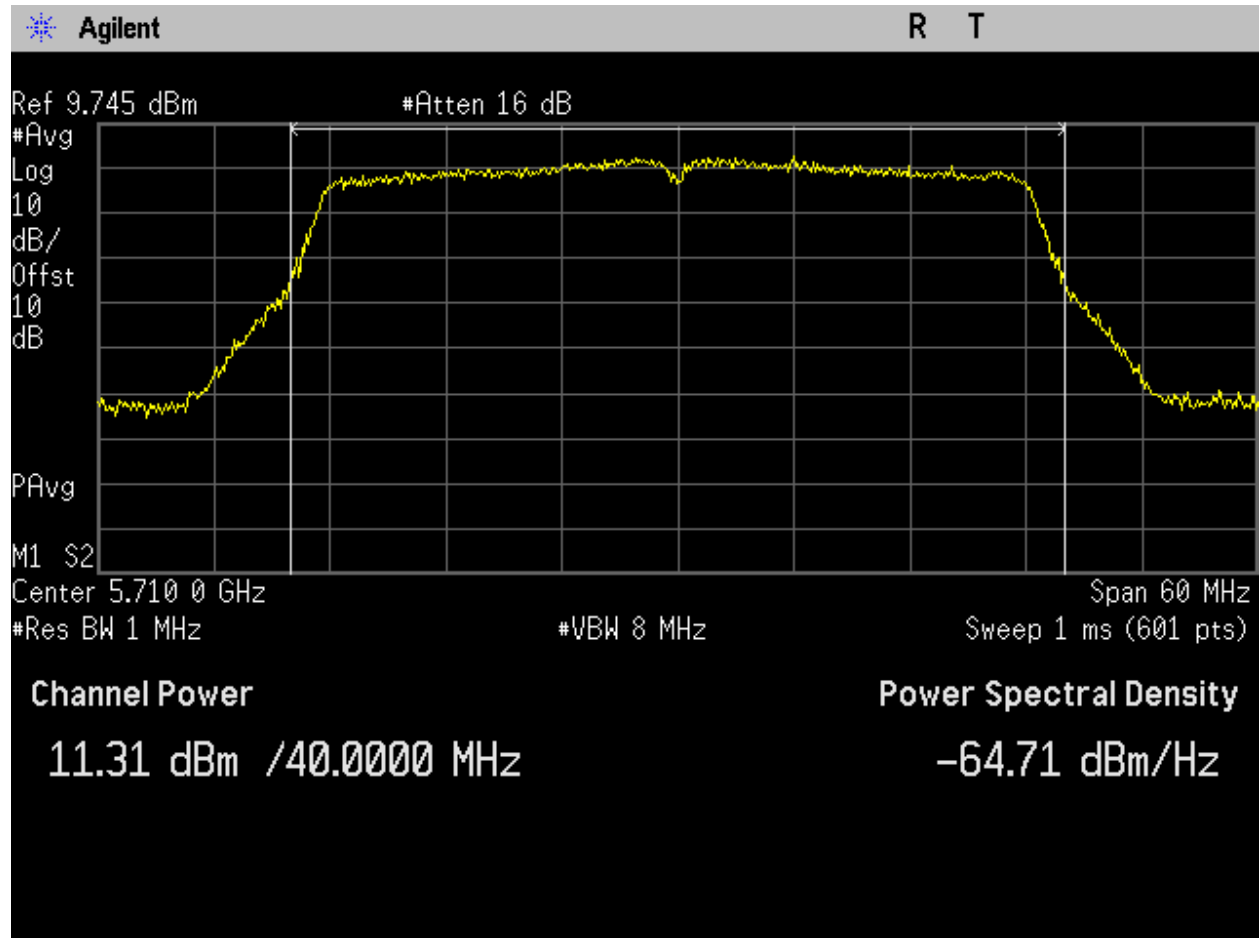


Figure 295: U-NII-2C_5710MHz_High Ch_142_40MHz BW_n-mode_Output Power_Port 1.

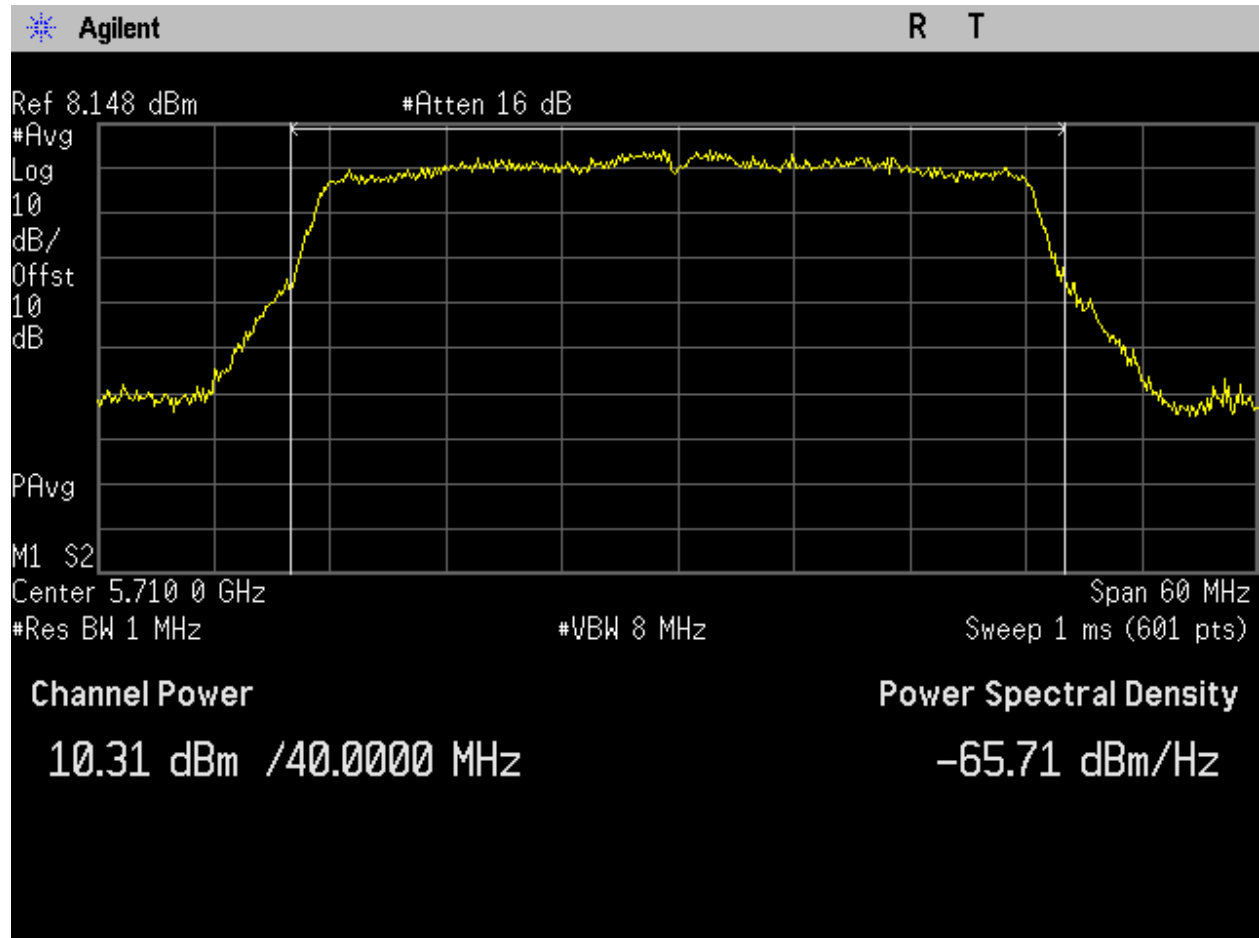


Figure 296: U-NII-2C_5710MHz_High Ch_142_40MHz BW_n-mode_Output Power_Port 2.

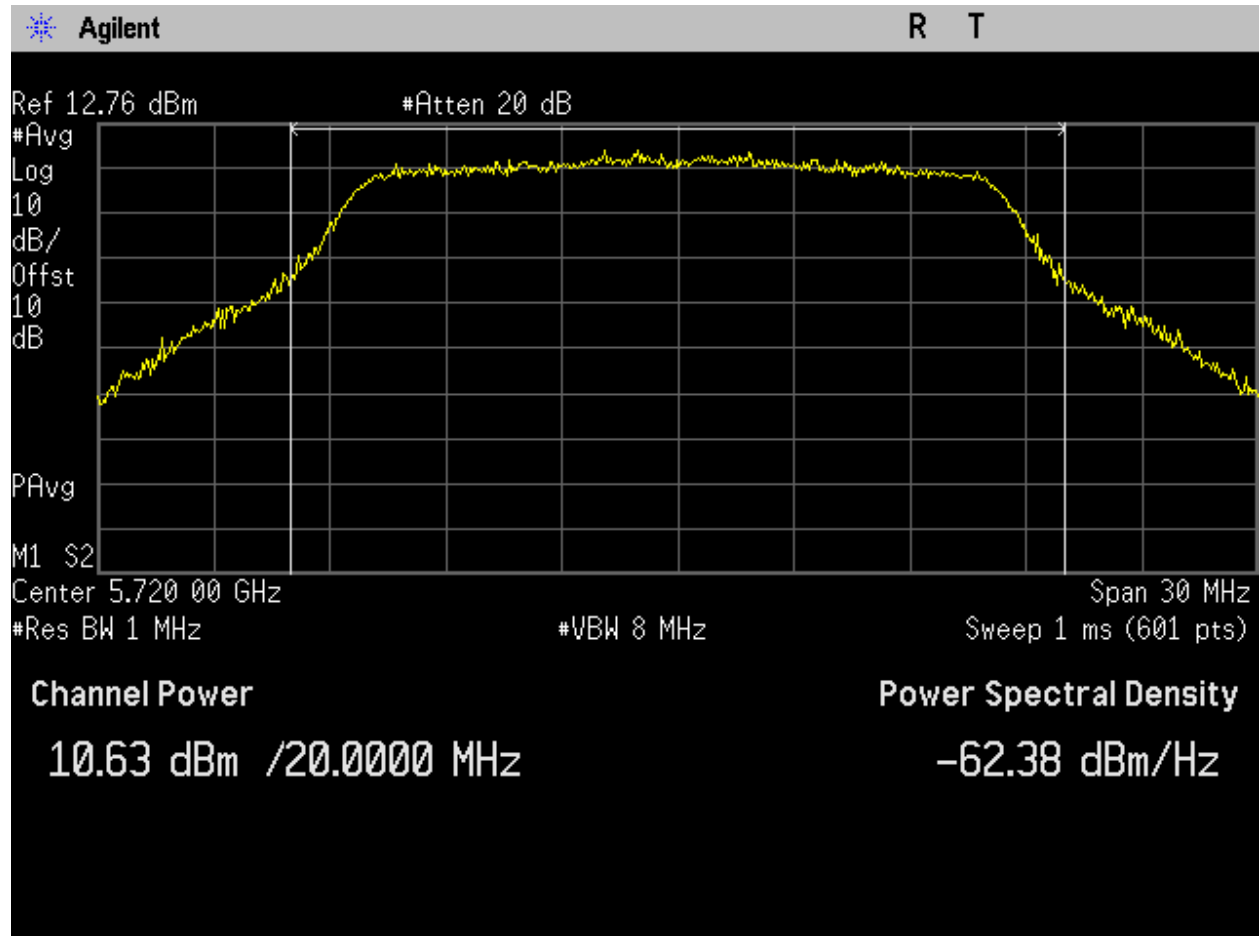


Figure 297: U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Output Power_Port 1.

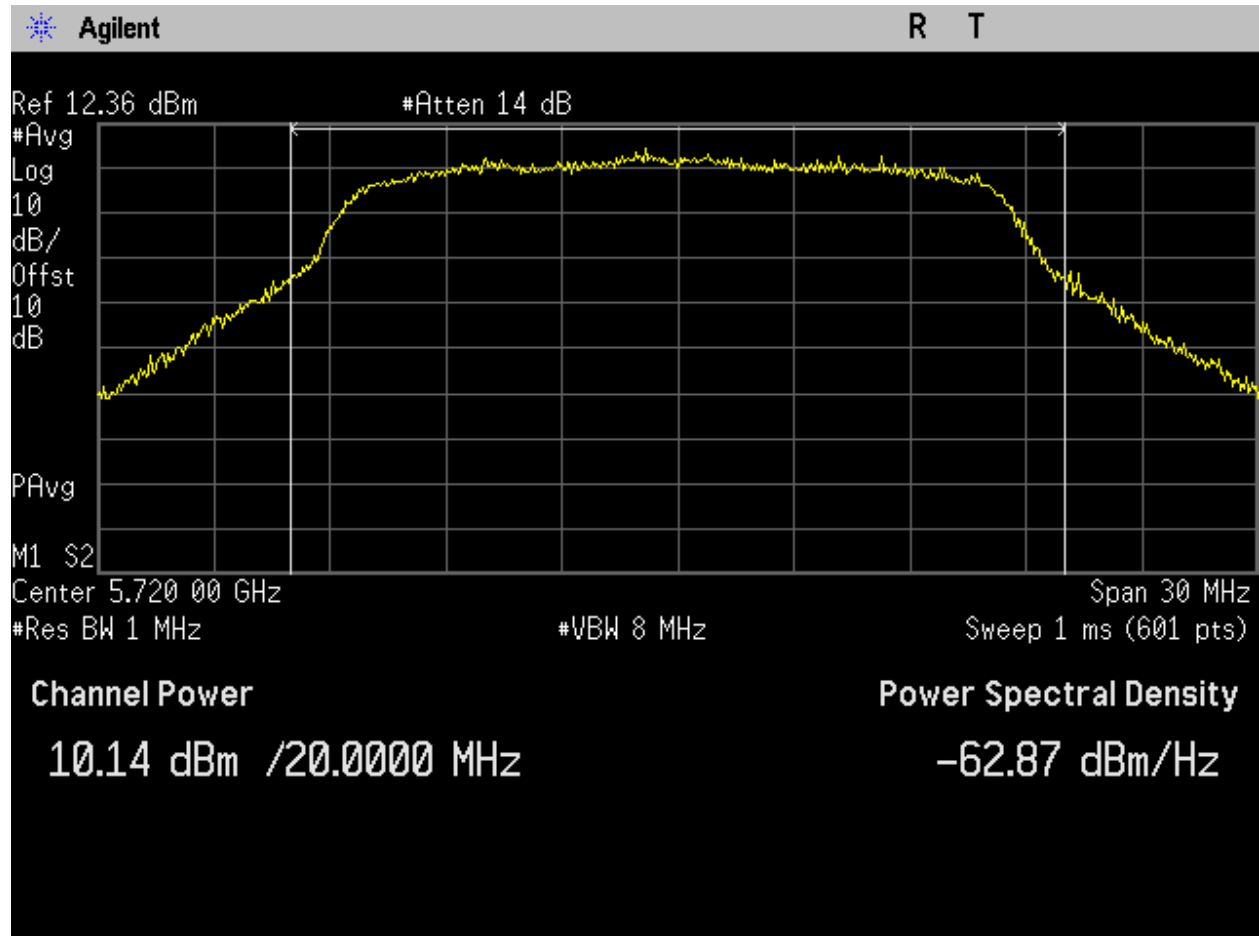


Figure 298: U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Output Power_Port 2.

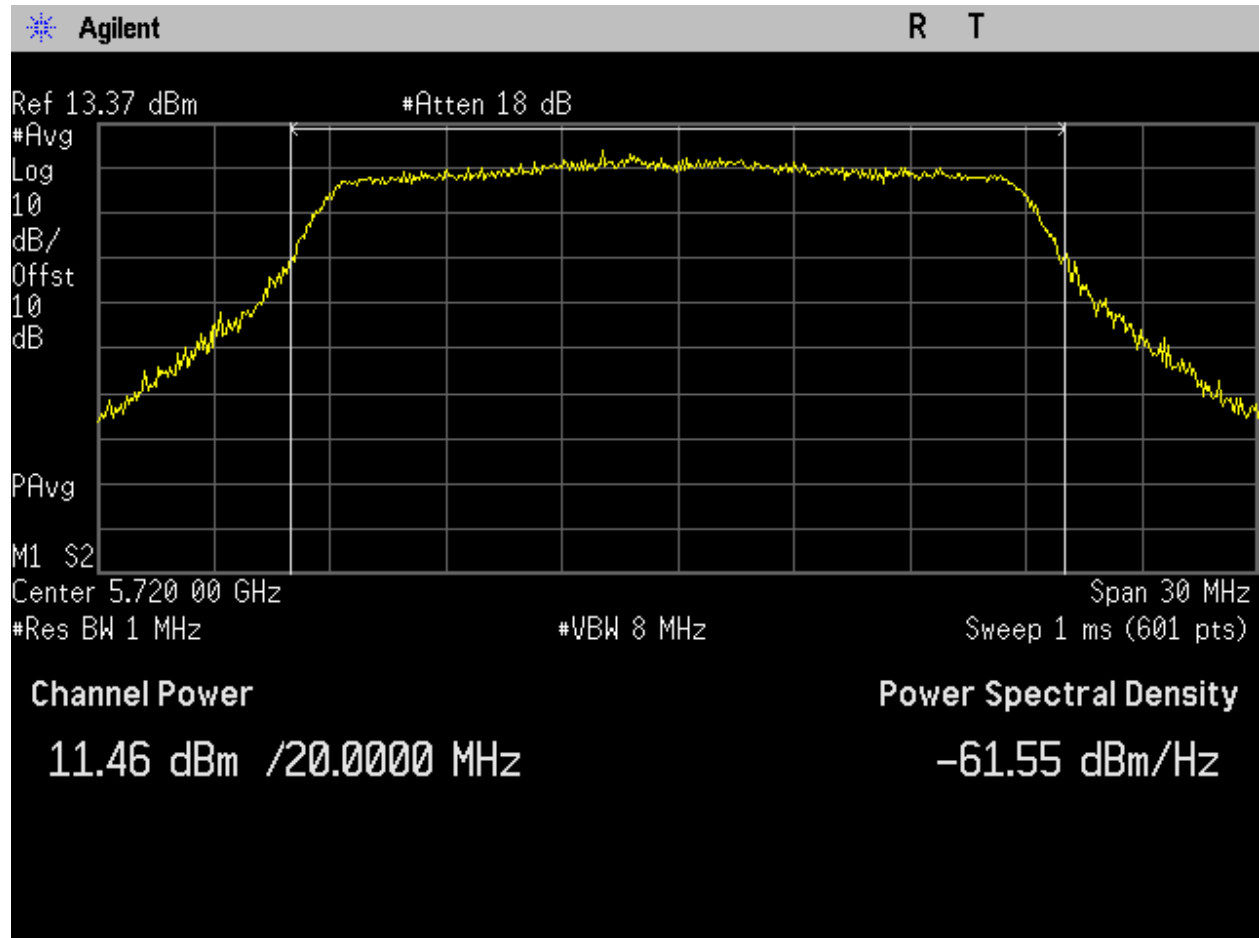


Figure 299: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Output Power_Port 1.

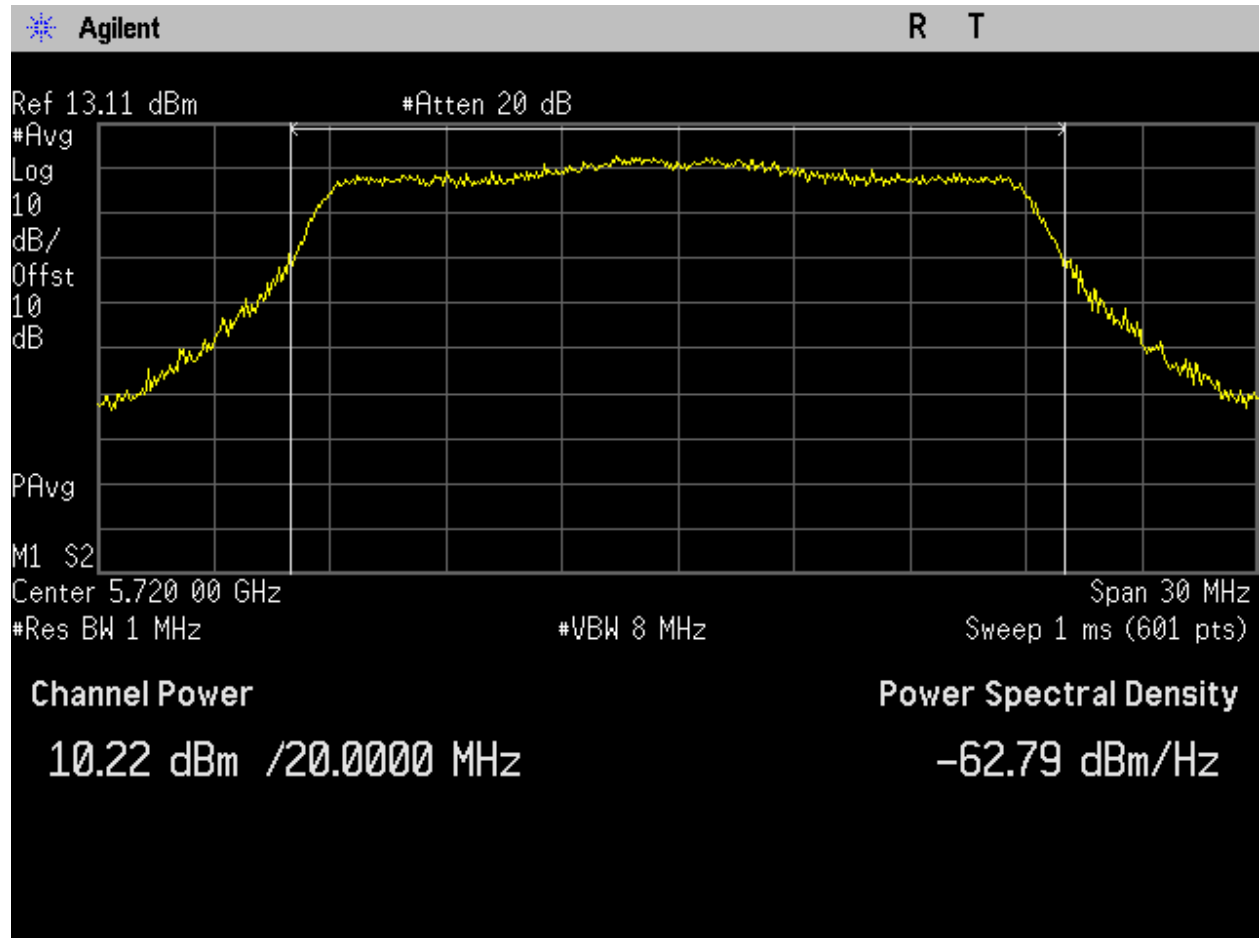


Figure 300: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Output Power_Port 2.

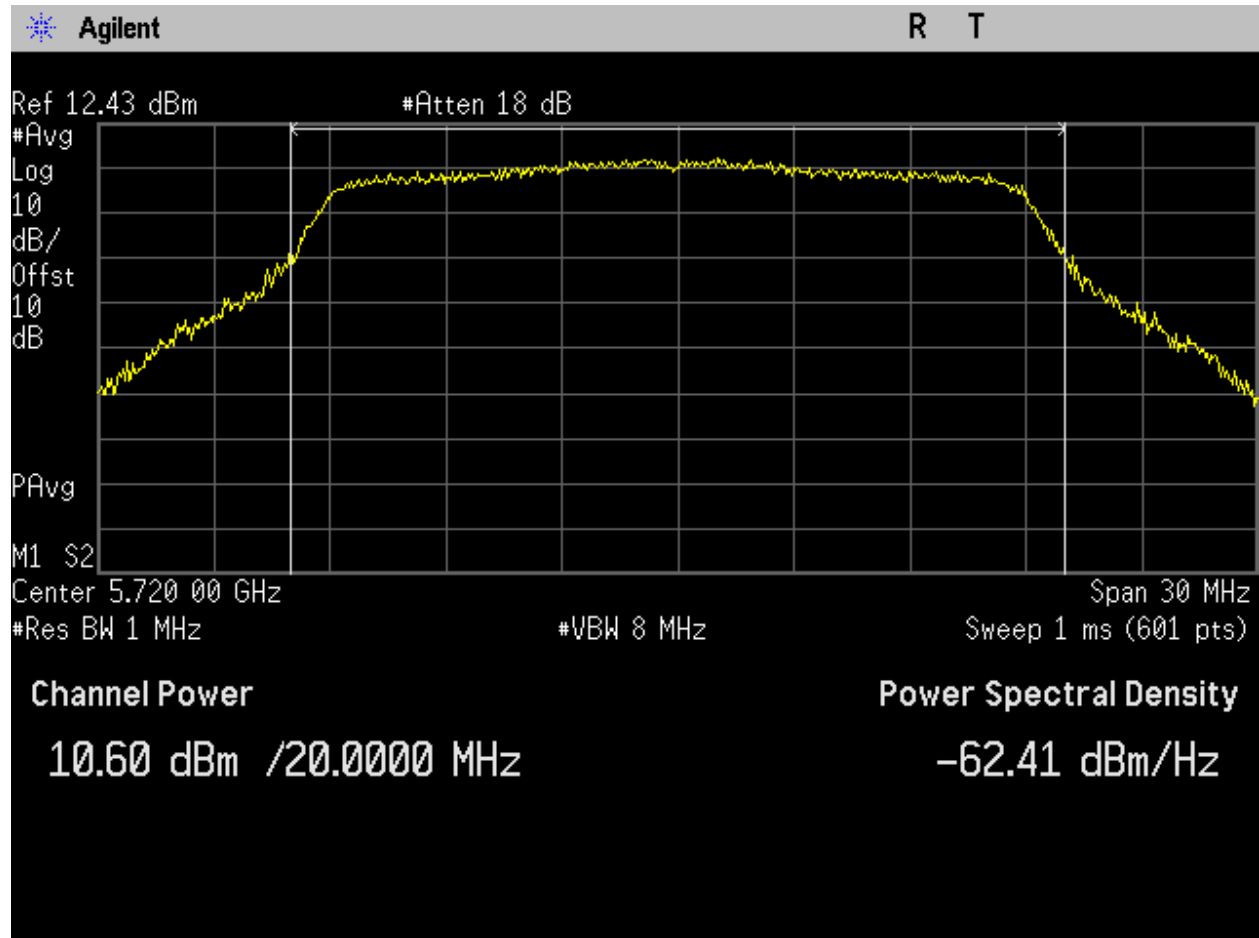


Figure 301: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ax-mode_Output Power_Port 1.

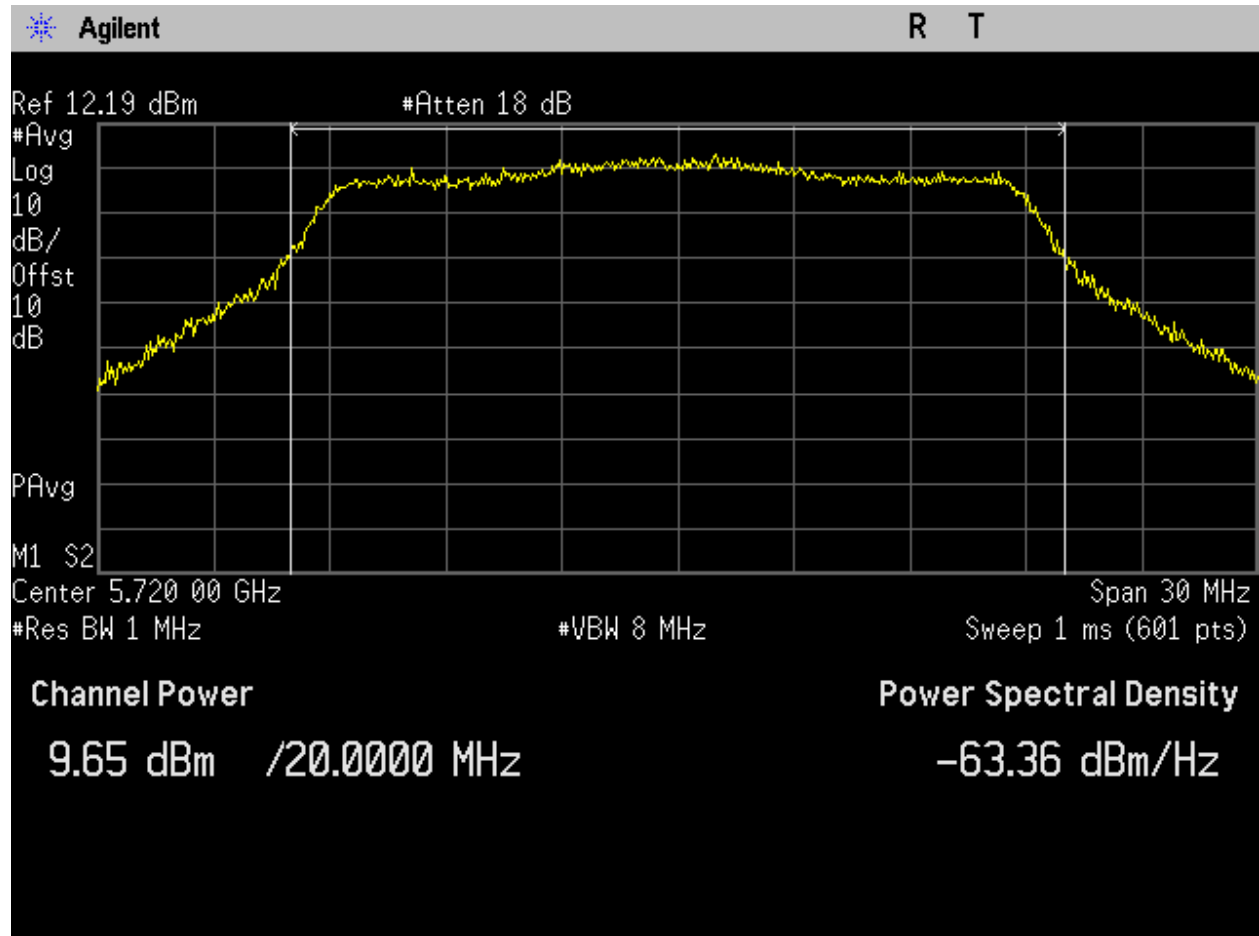


Figure 302: U-NII-2C_5720MHz_High Ch_144_20MHz BW_ax-mode_Output Power_Port 2.

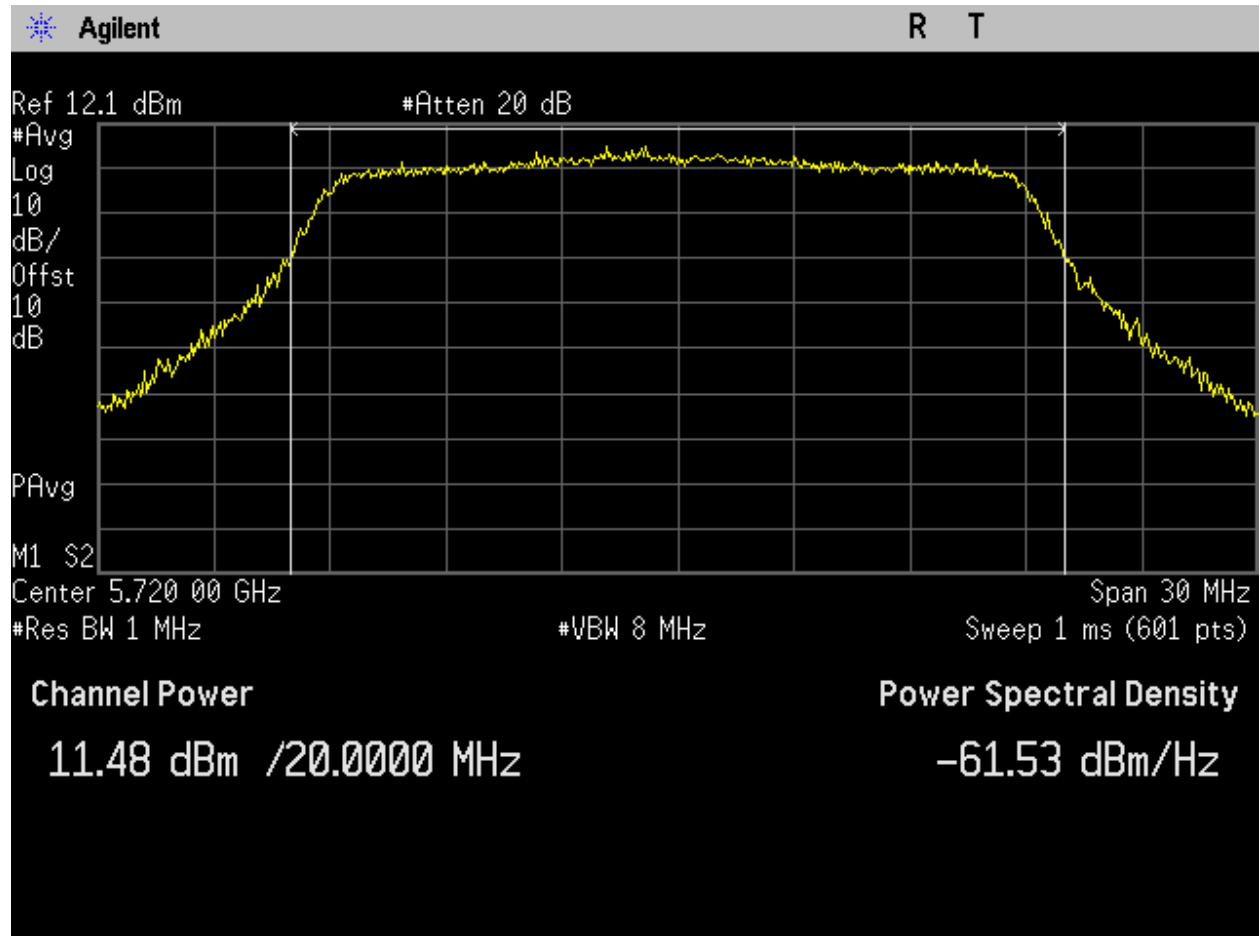


Figure 303: U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Output Power_Port 1.

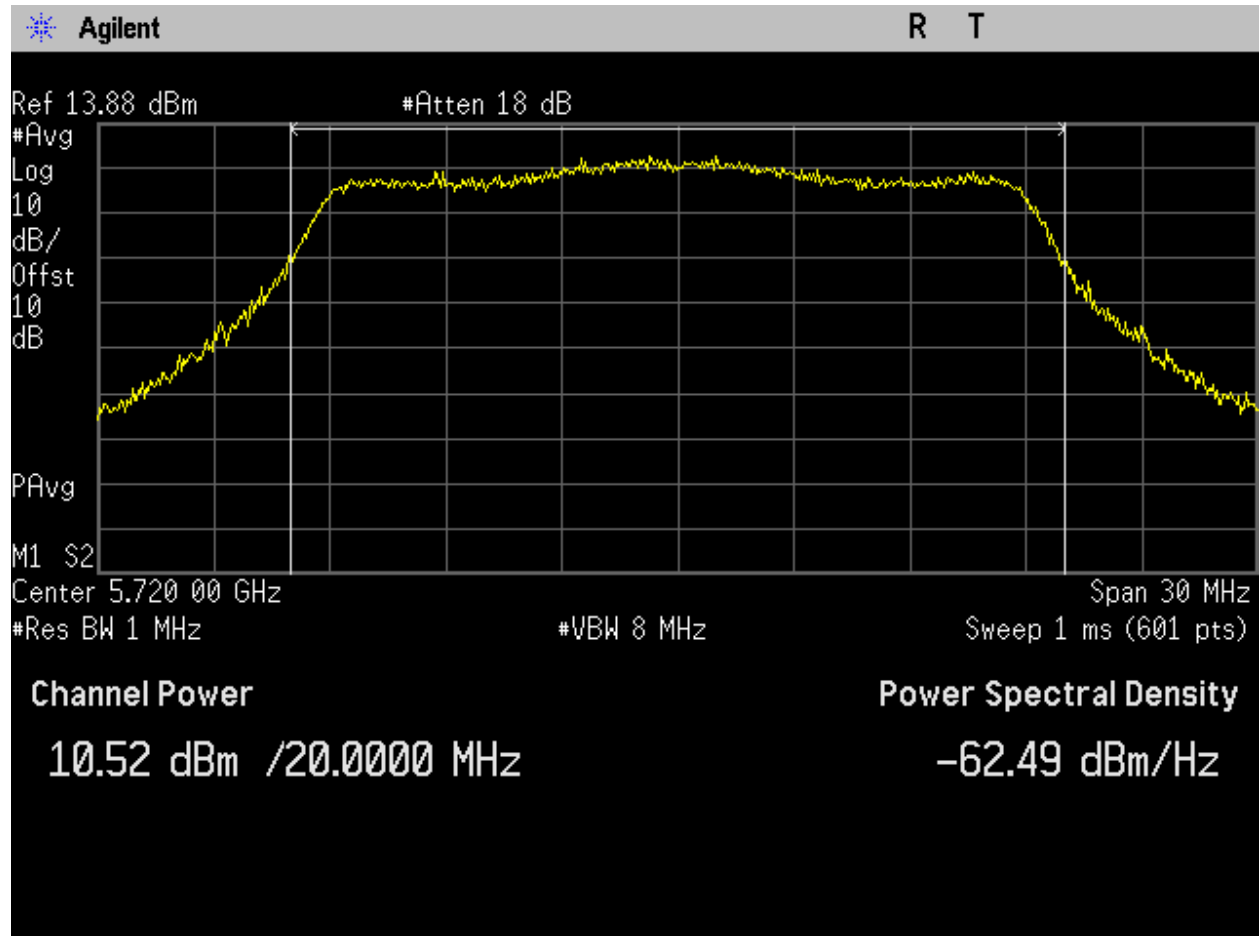


Figure 304: U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Output Power_Port 2.

§15.407(a)(2) Maximum Power Spectral Density

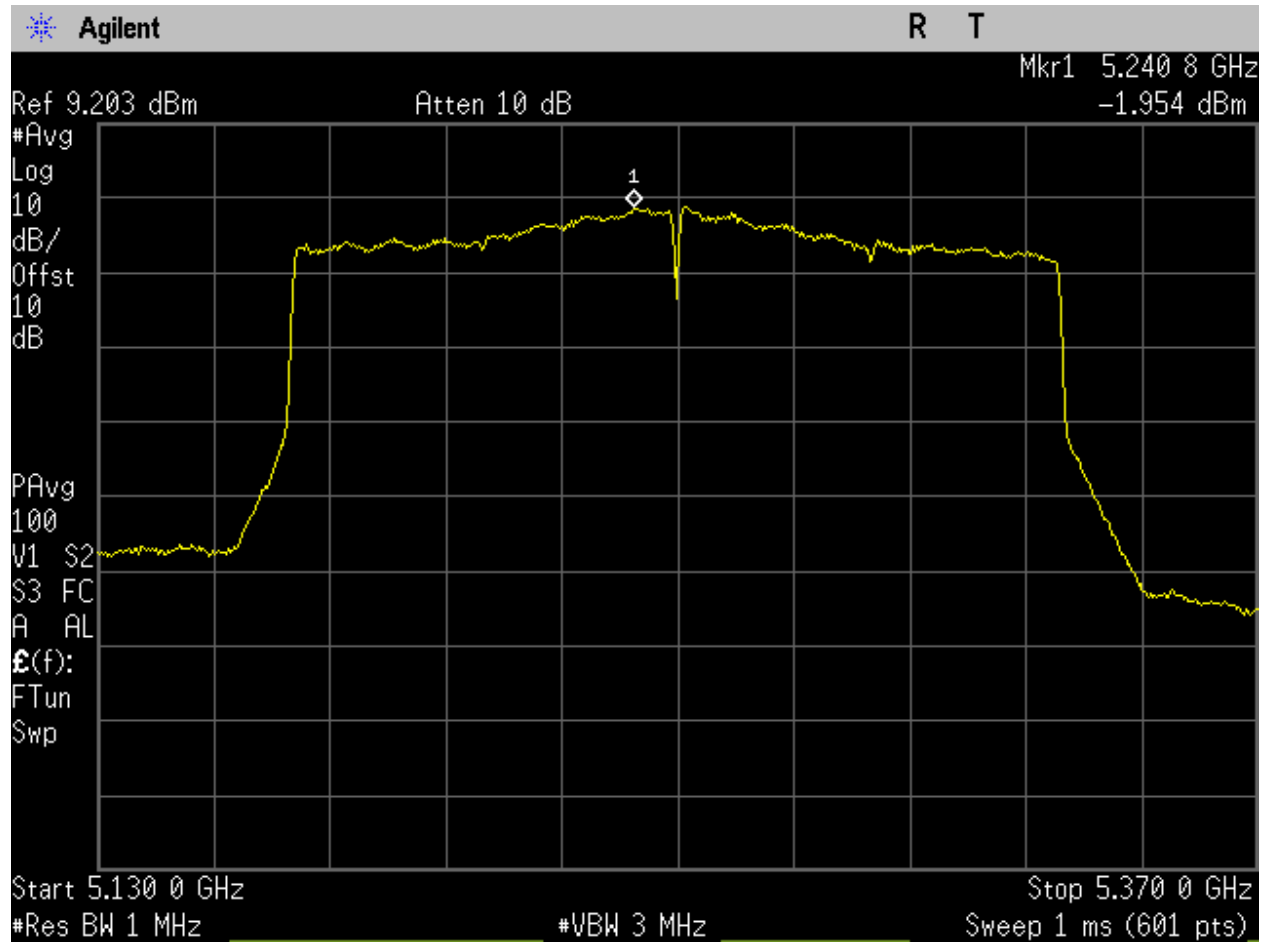


Figure 305: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Spectral Density_Port 1.

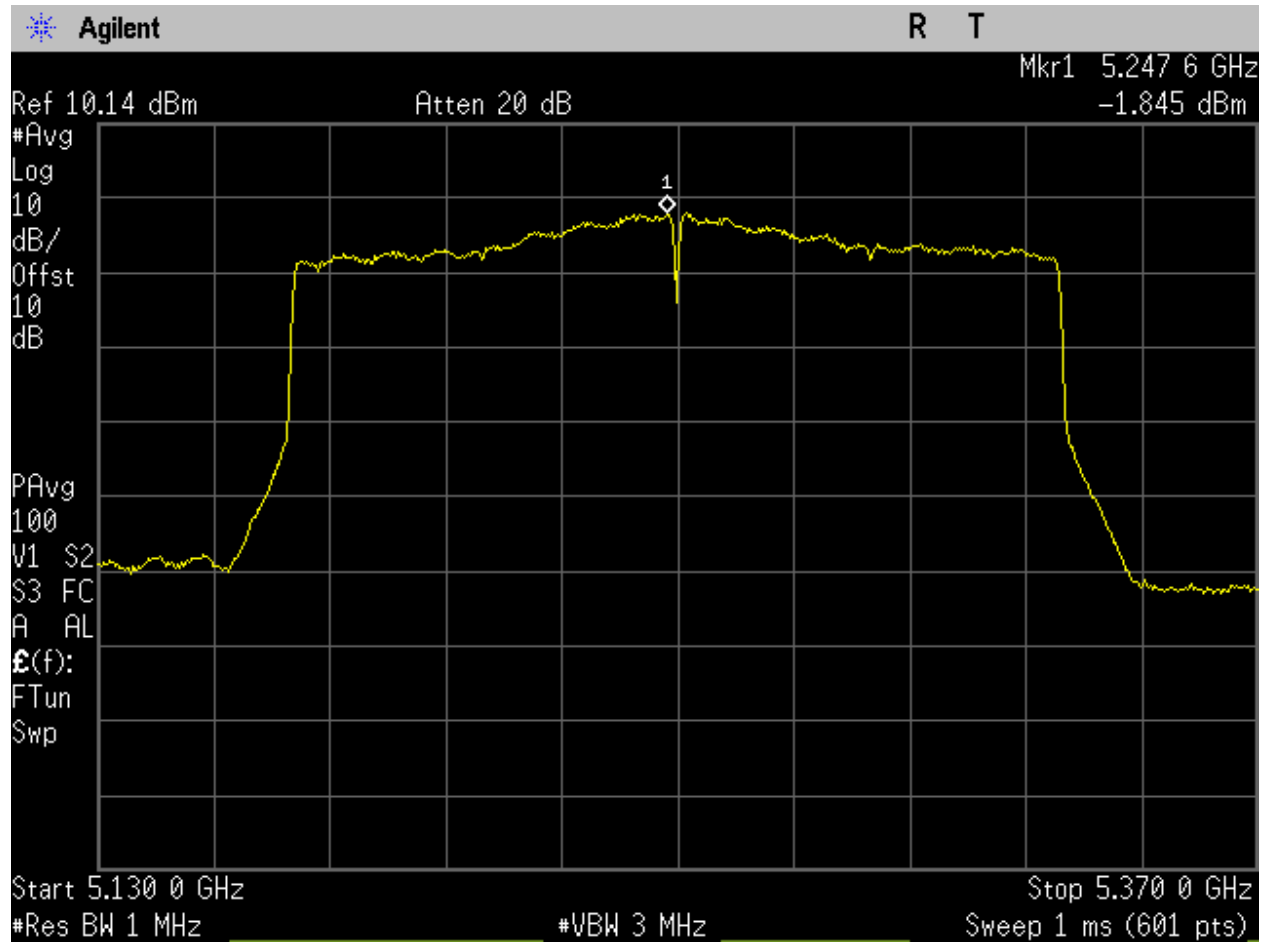


Figure 306: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Spectral Density_Port 2.

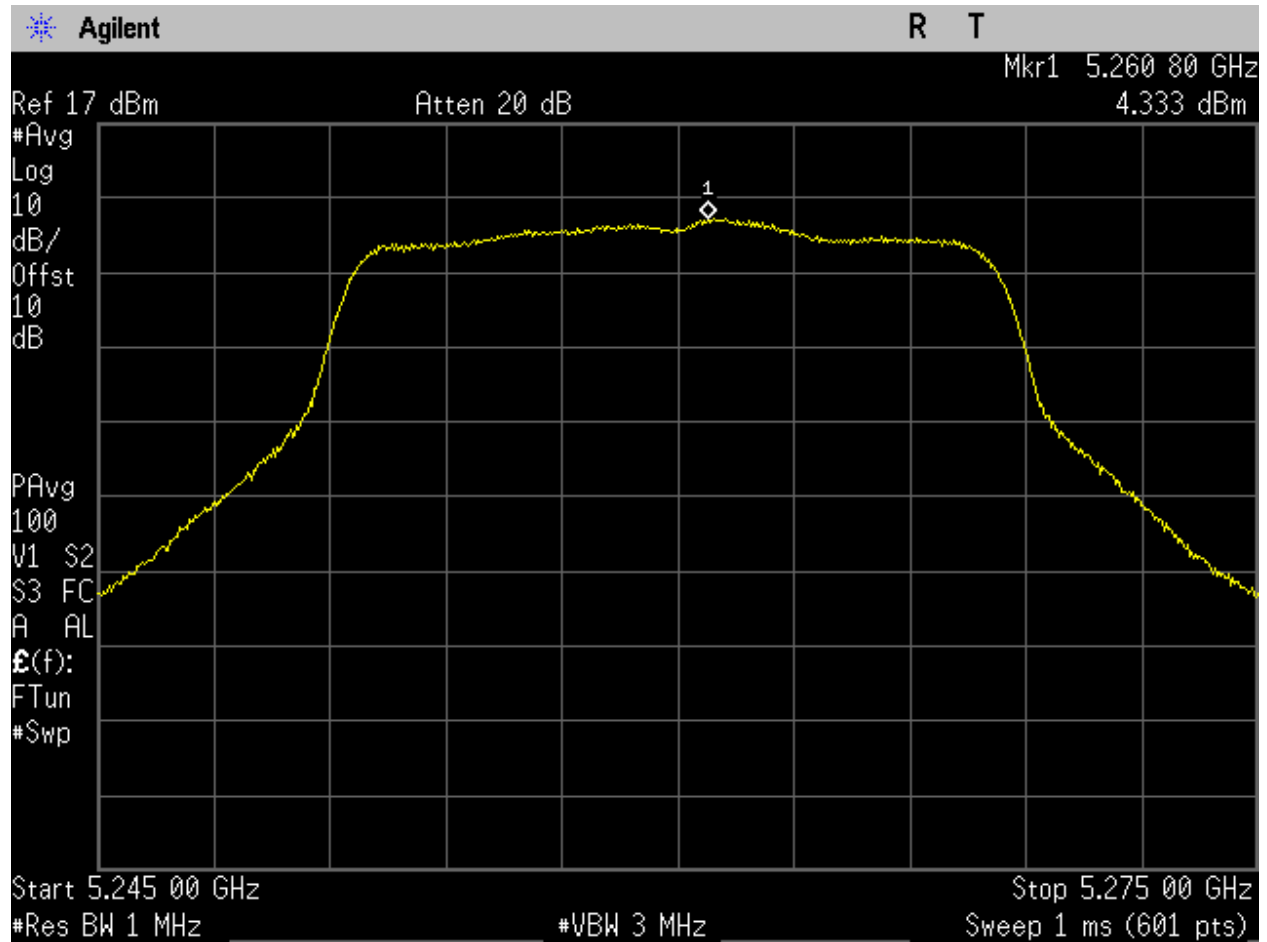


Figure 307: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_Spectral Density_Port 1.

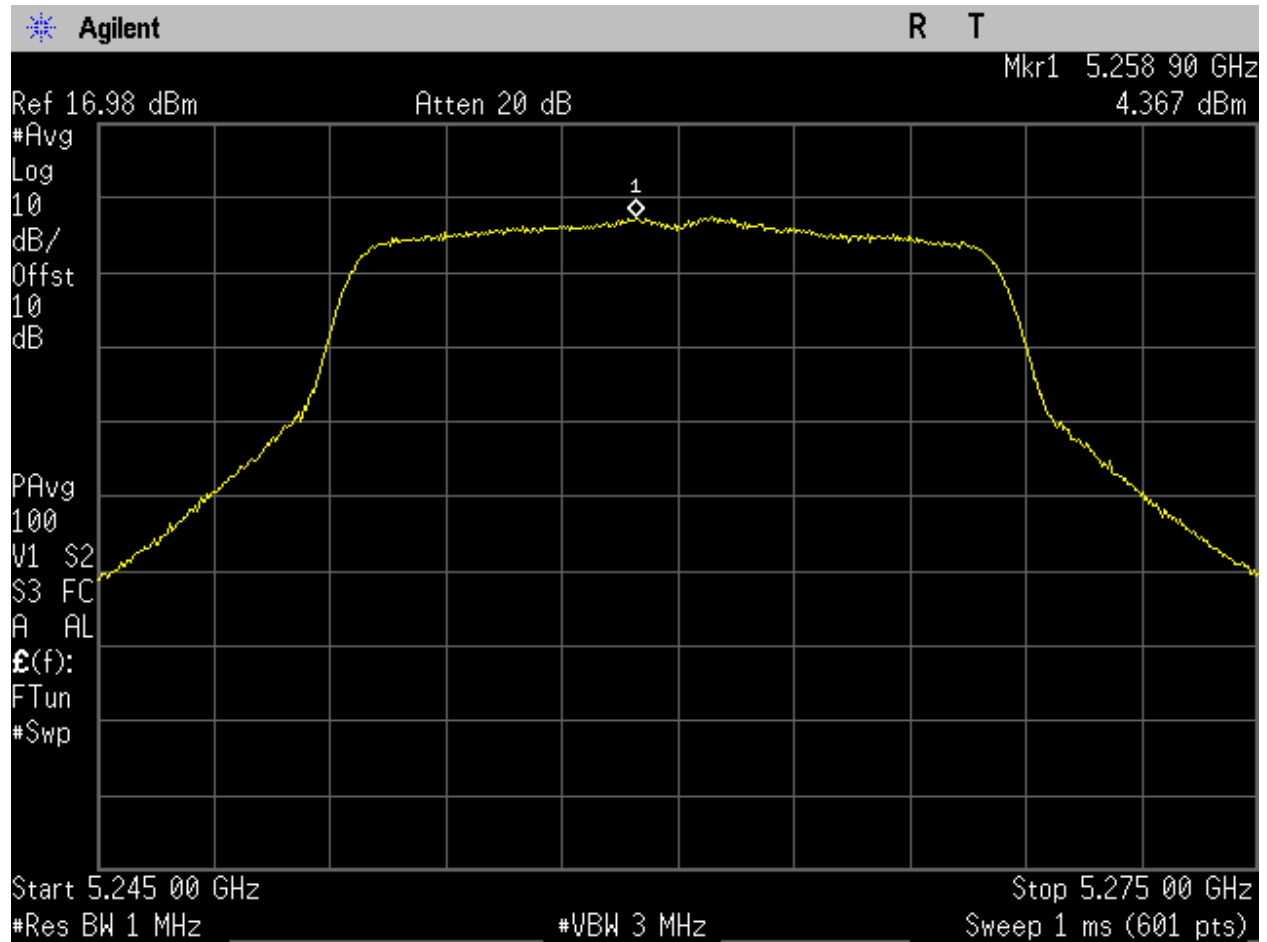


Figure 308: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_Spectral Density_Port 2.

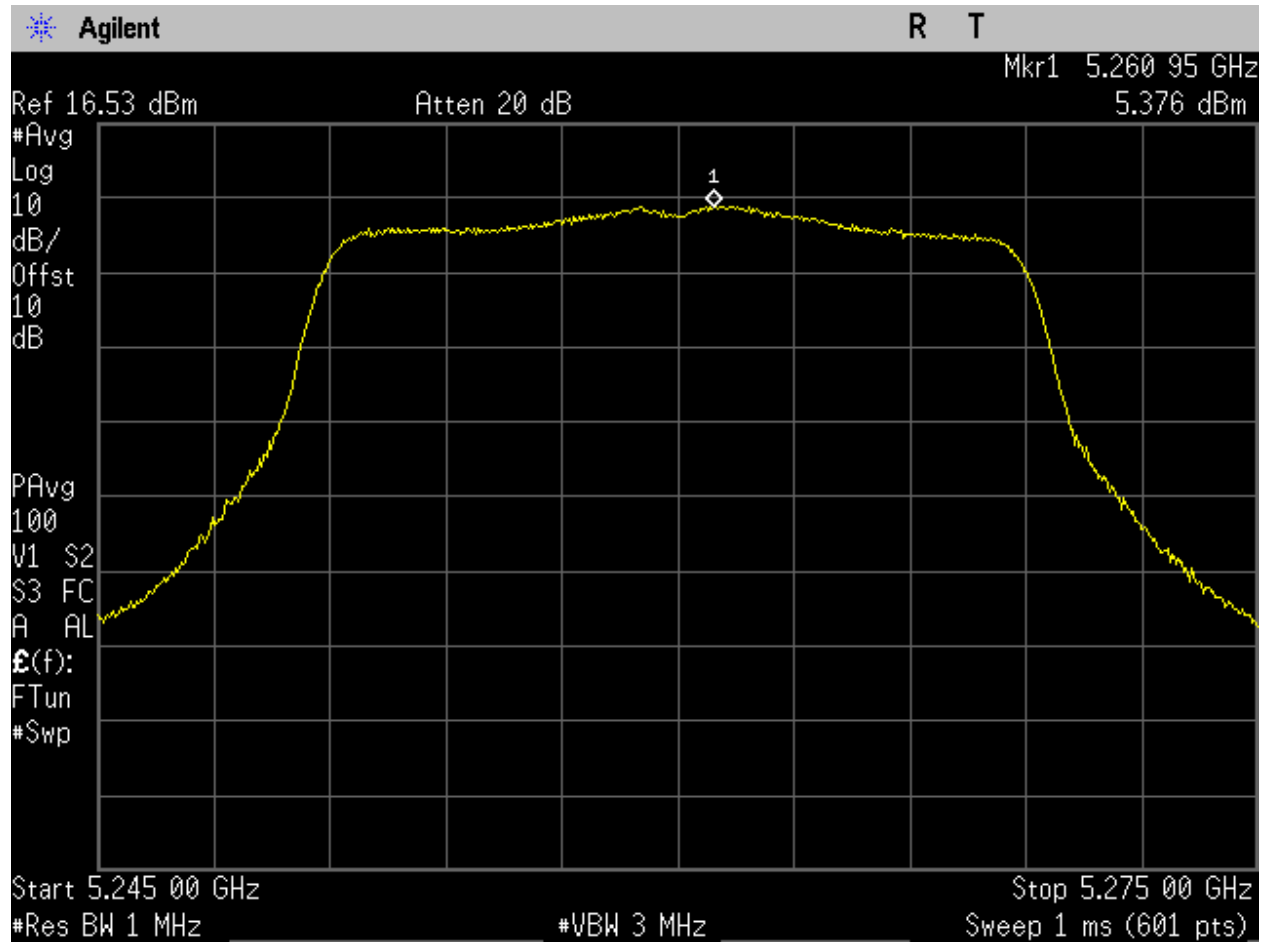


Figure 309: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_Spectral Density_Port 1.

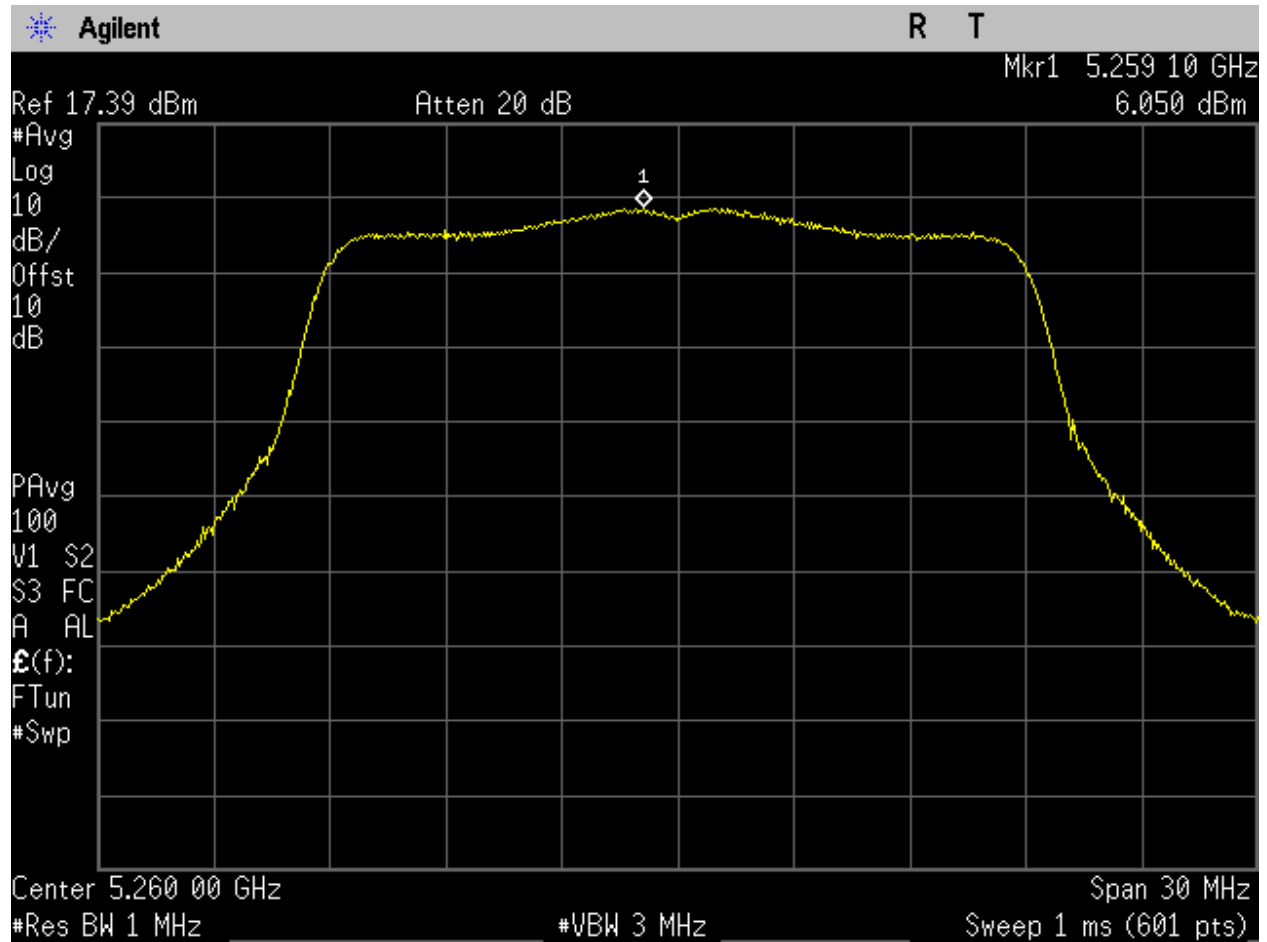


Figure 310: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ac-mode_Spectral Density_Port 2.

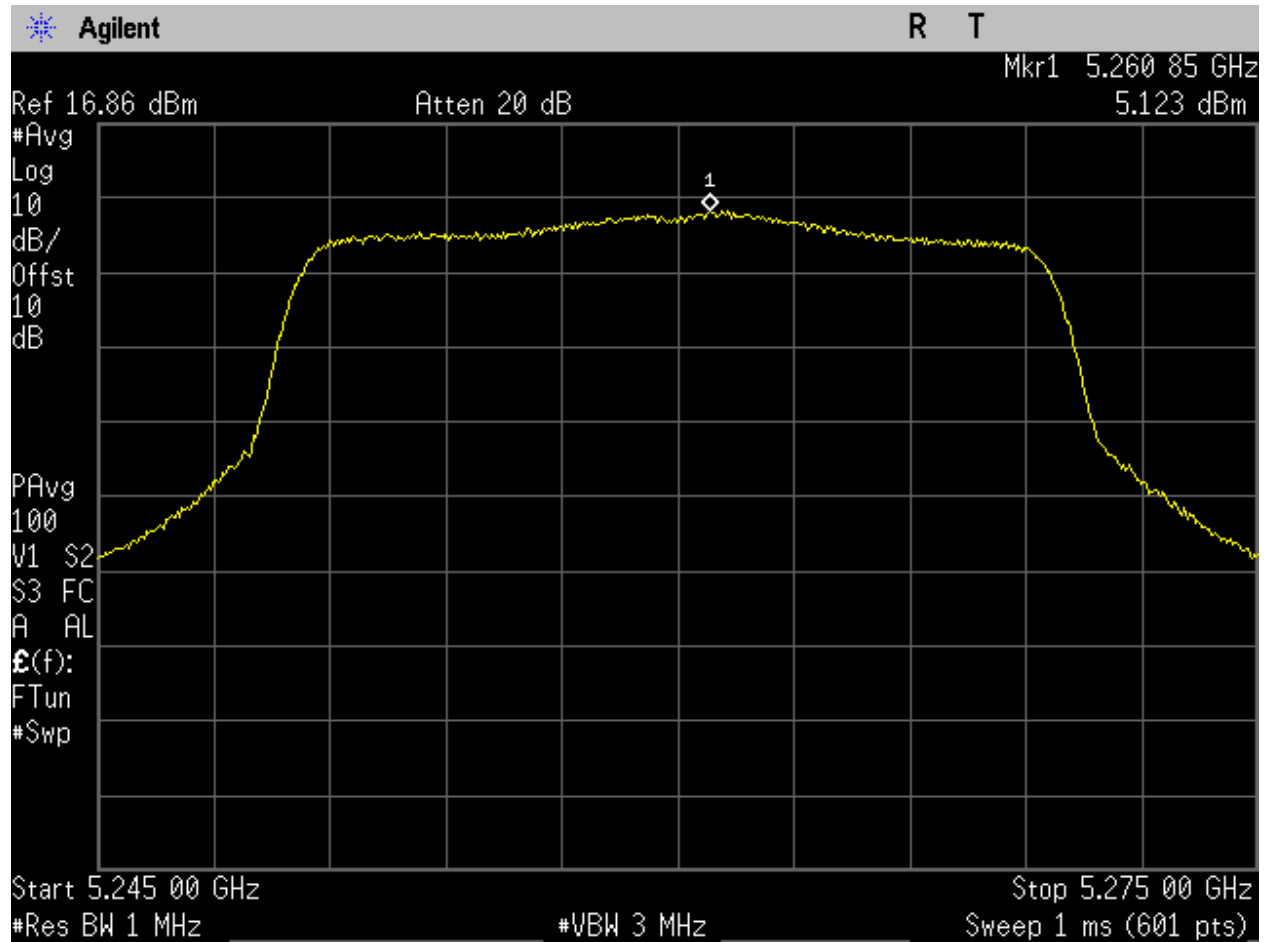


Figure 311: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_Spectral Density_Port 1.

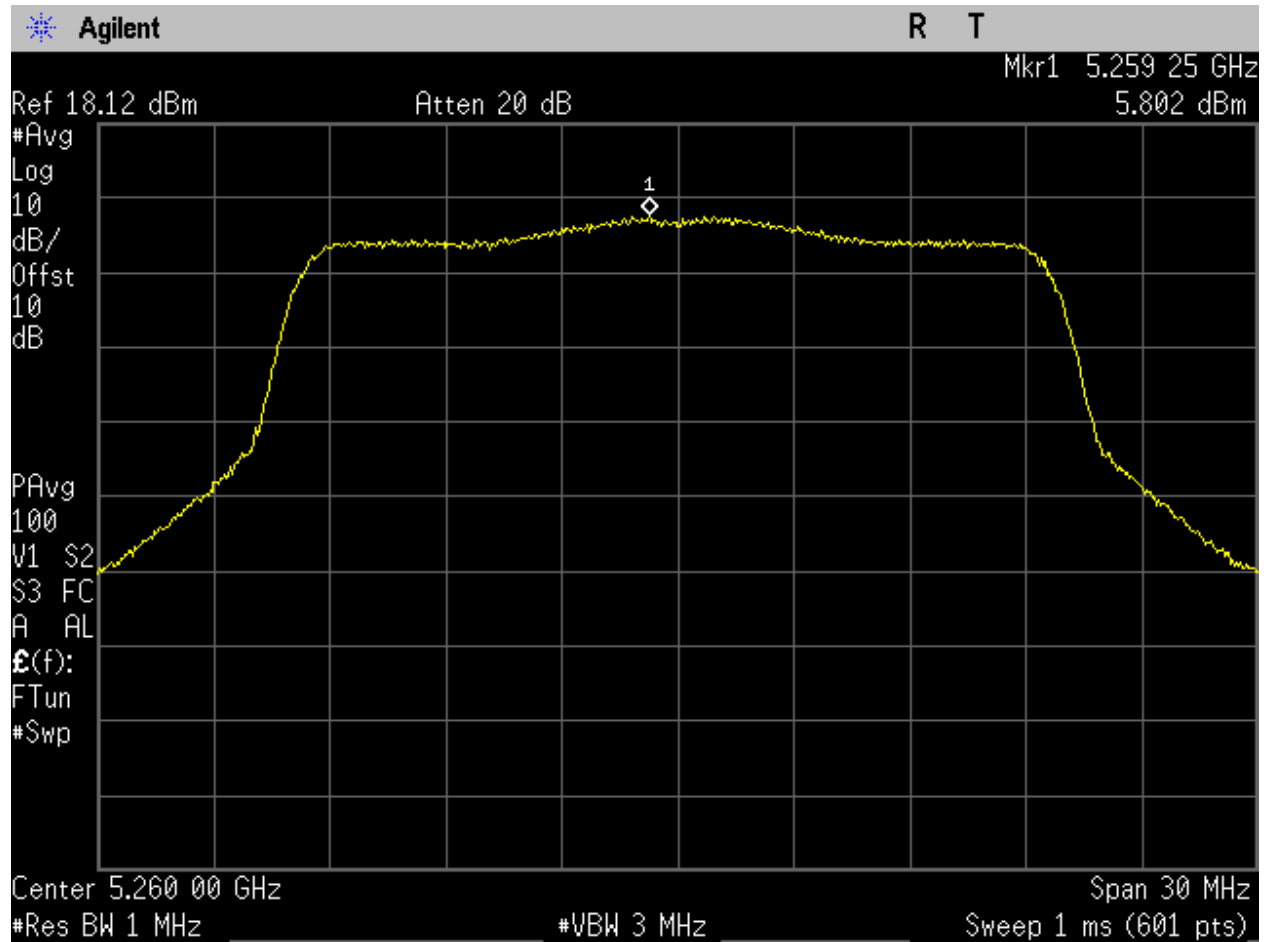


Figure 312: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_ax-mode_Spectral Density_Port 2.

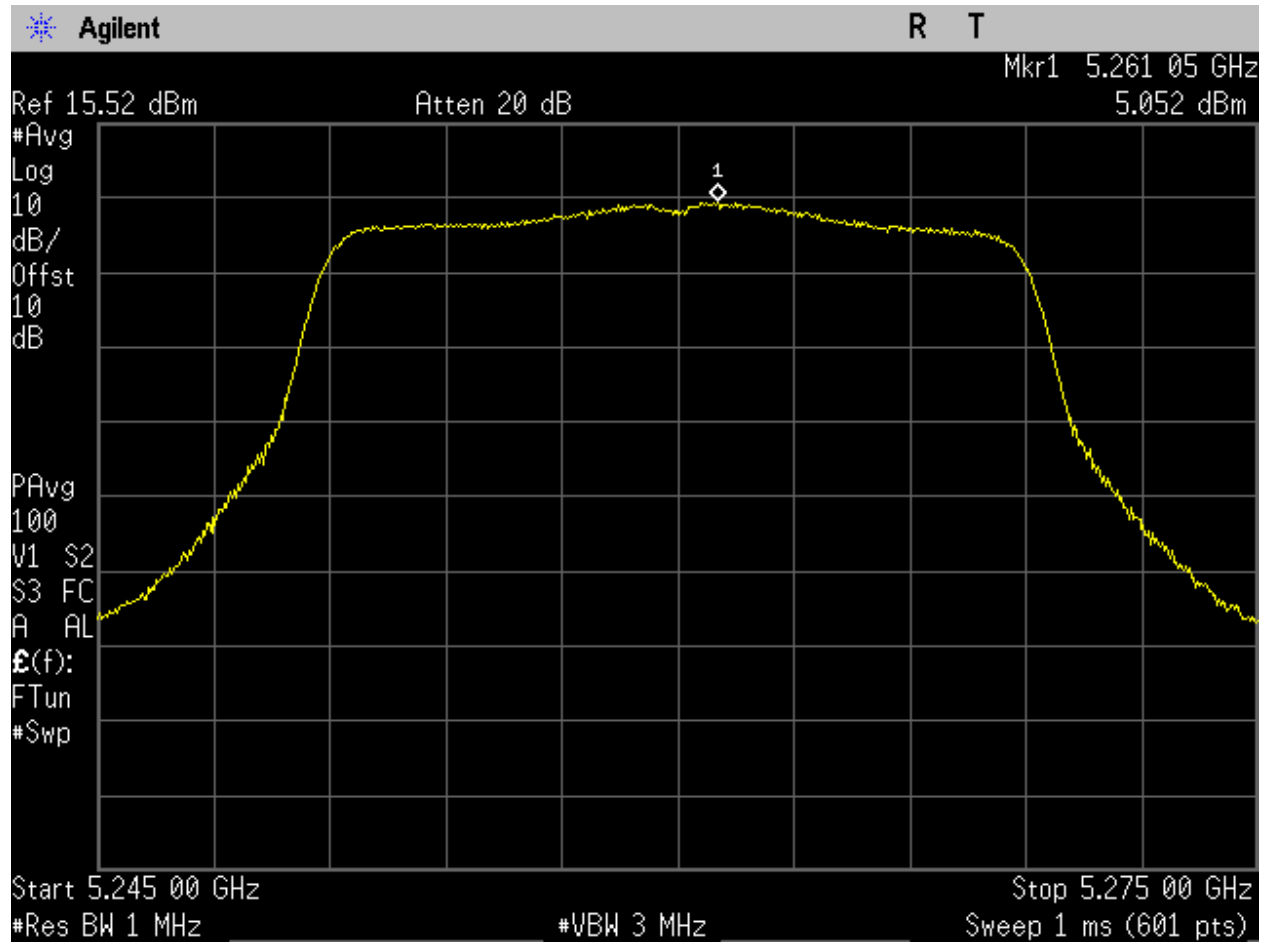


Figure 313: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_Spectral Density_Port 1.

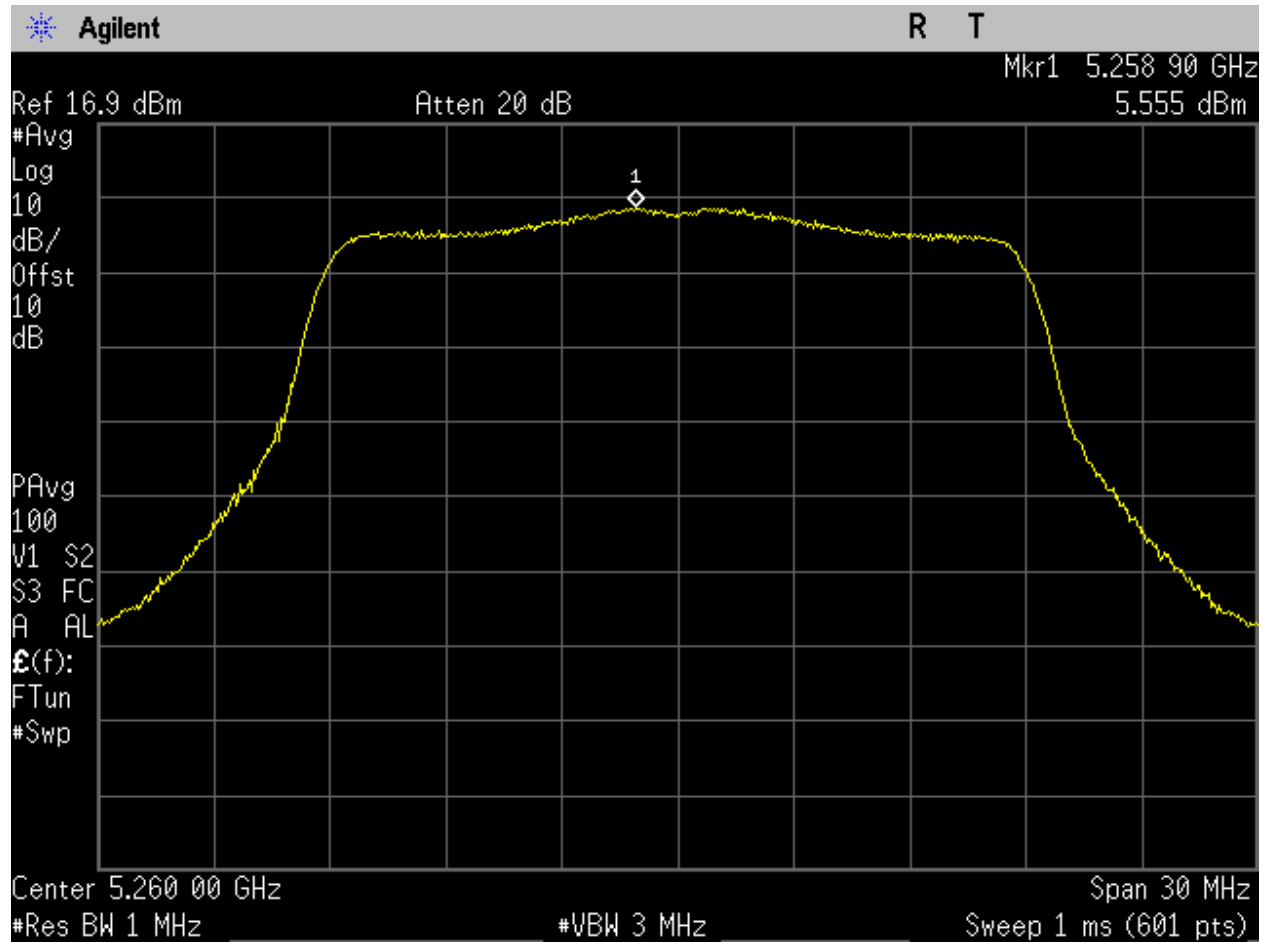


Figure 314: U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_n-mode_Spectral Density_Port 2.

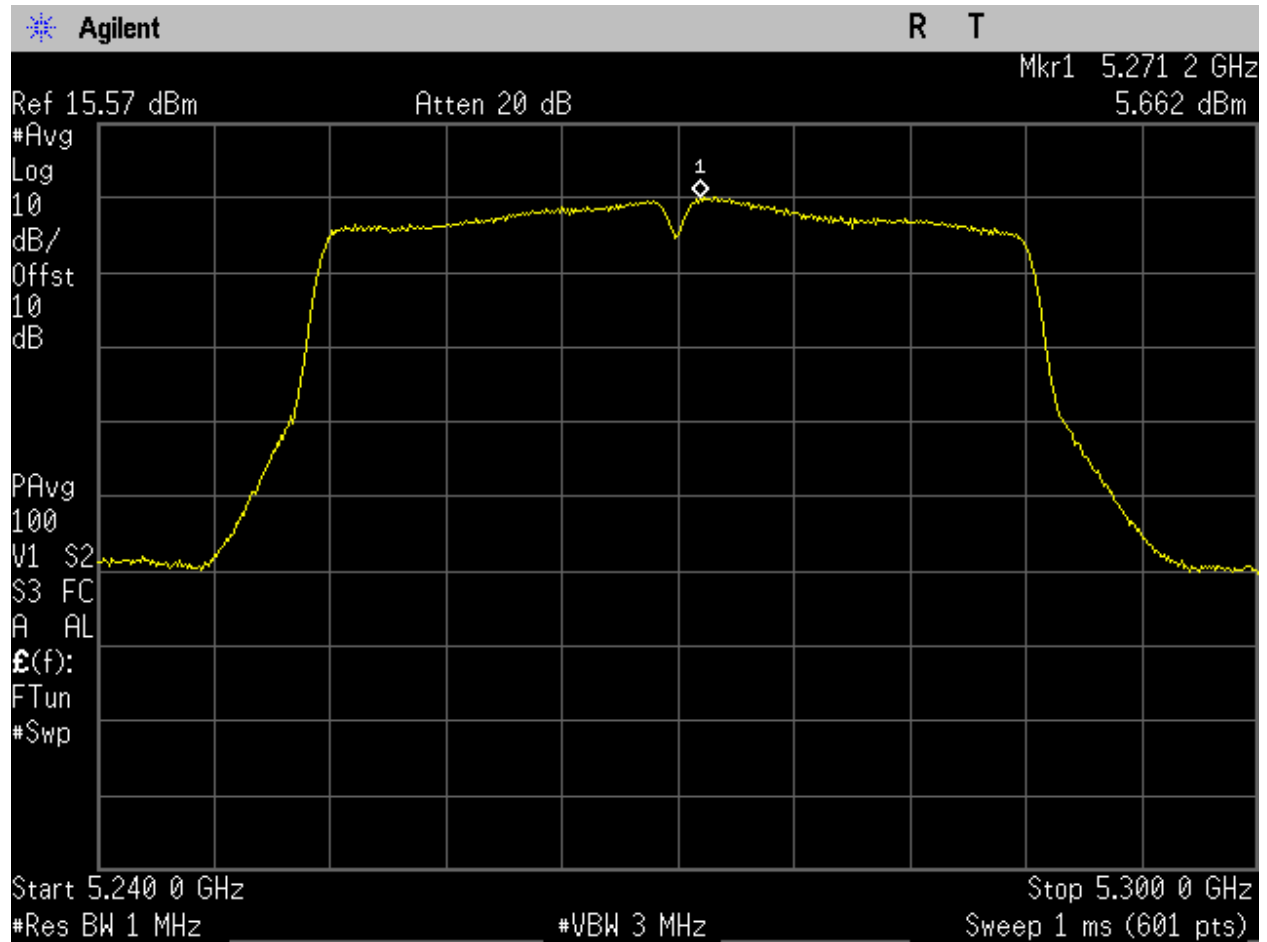


Figure 315: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Spectral Density_Port 1.

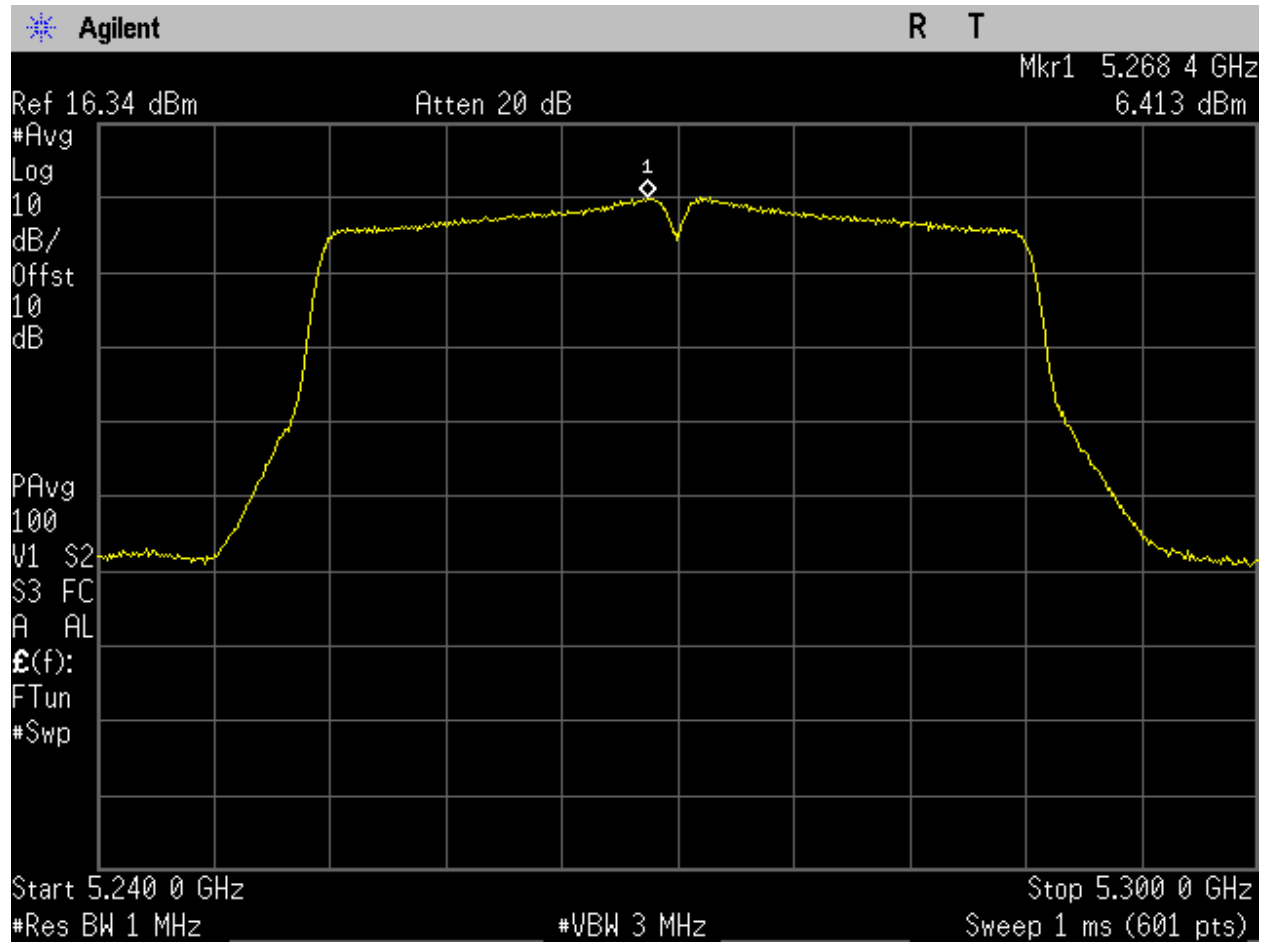


Figure 316: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ac-mode_Spectral Density_Port 2.

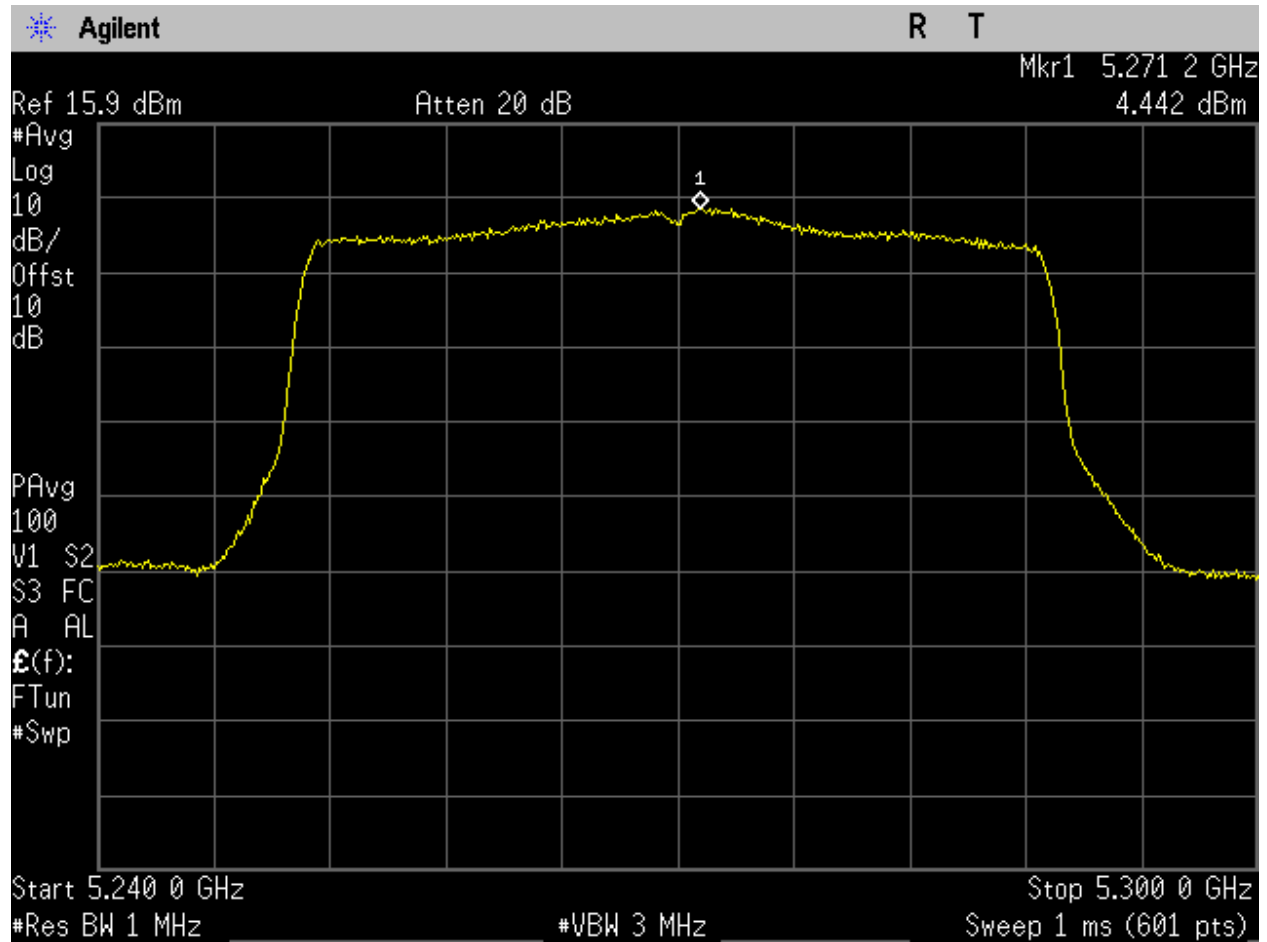


Figure 317: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Spectral Density_Port 1.

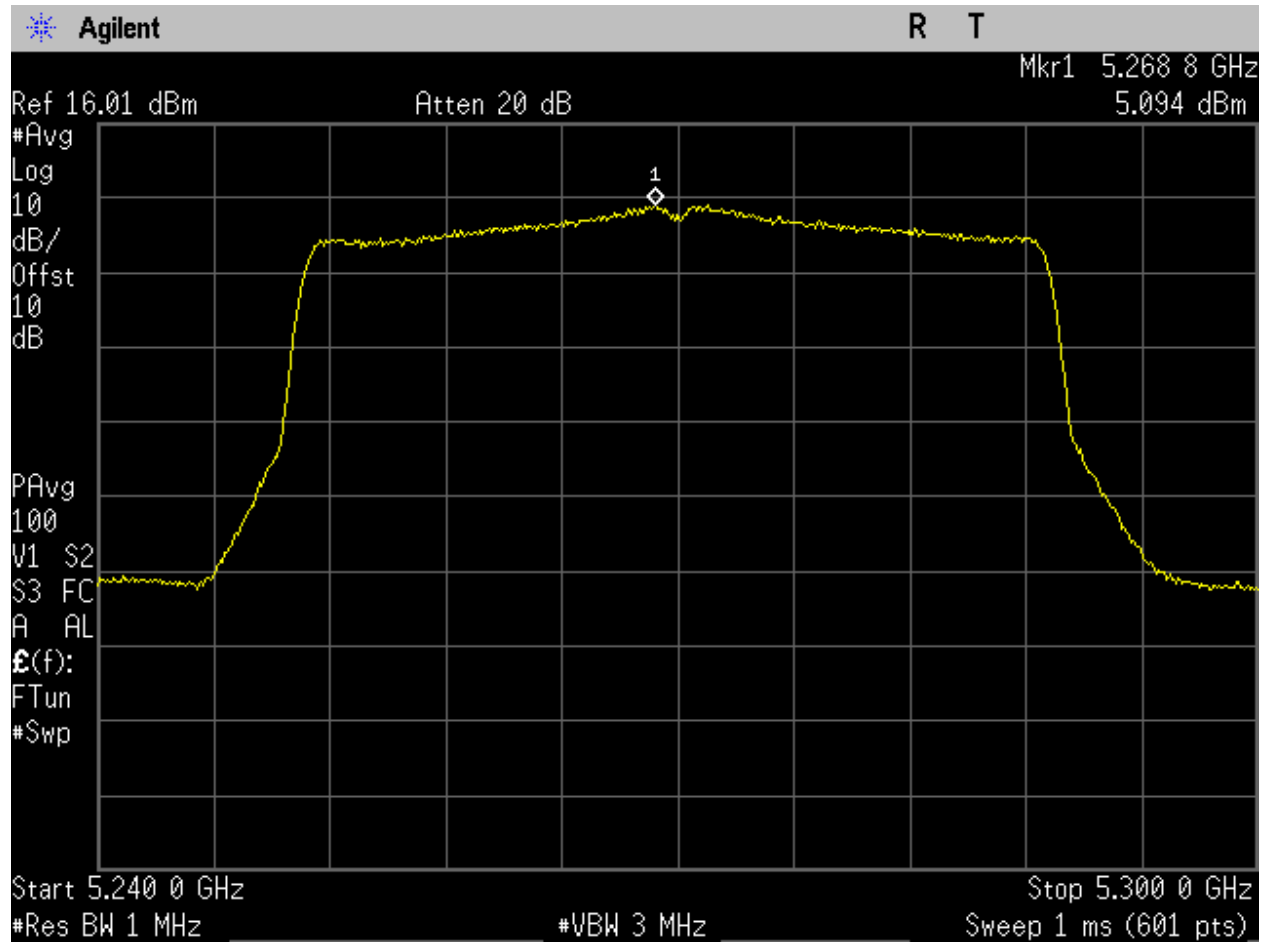


Figure 318: U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_ax-mode_Spectral Density_Port 2.

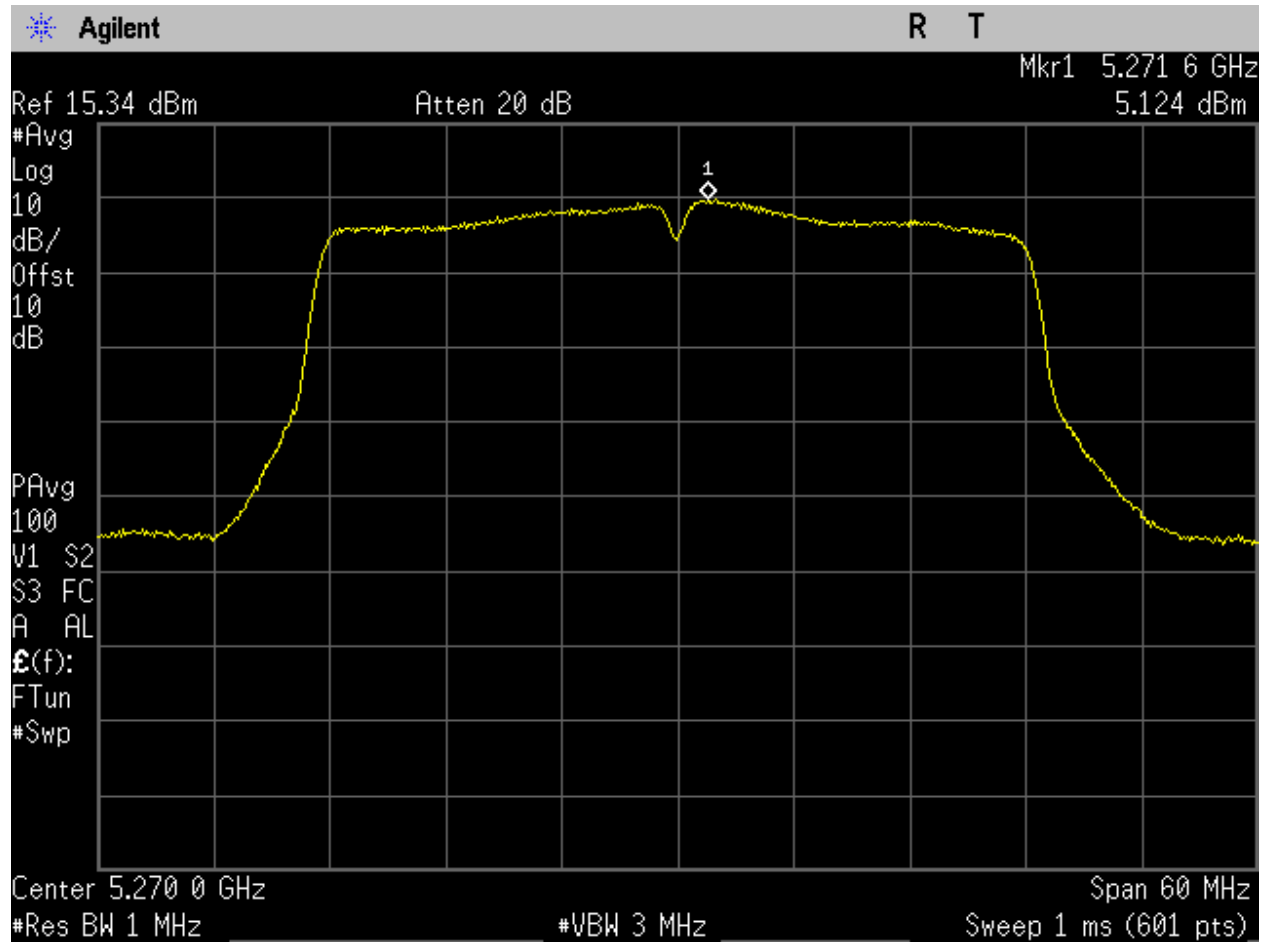


Figure 319: U-NII-2A_5270MHz_low_Mid_Ch_54_40MHz BW_n-mode_Spectral Density_Port 1.

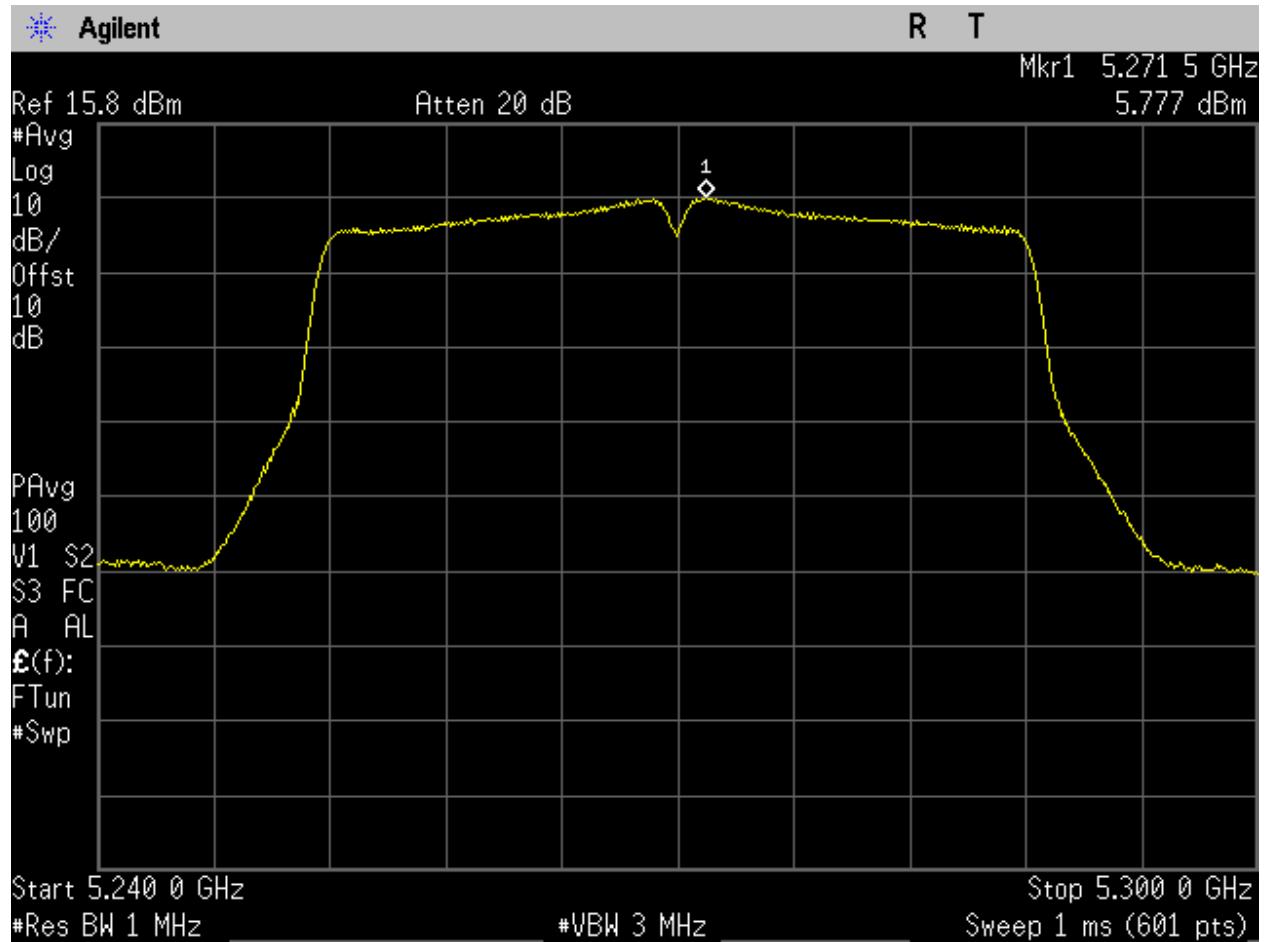


Figure 320: U-NII-2A_5270MHz_low_Mid_Ch_54_40MHz BW_n-mode_Spectral Density_Port 2.

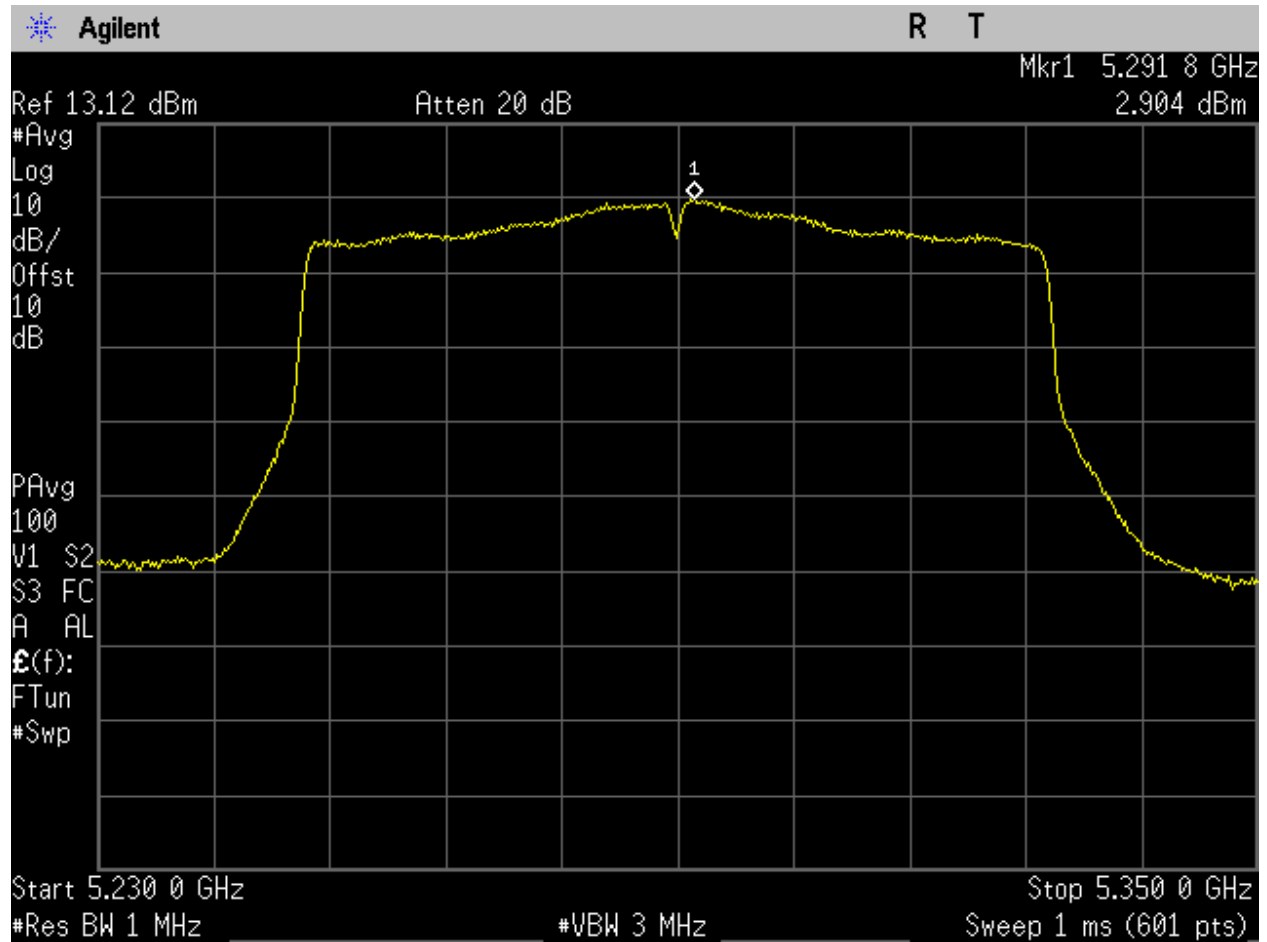


Figure 321: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Spectral Density_Port 1.

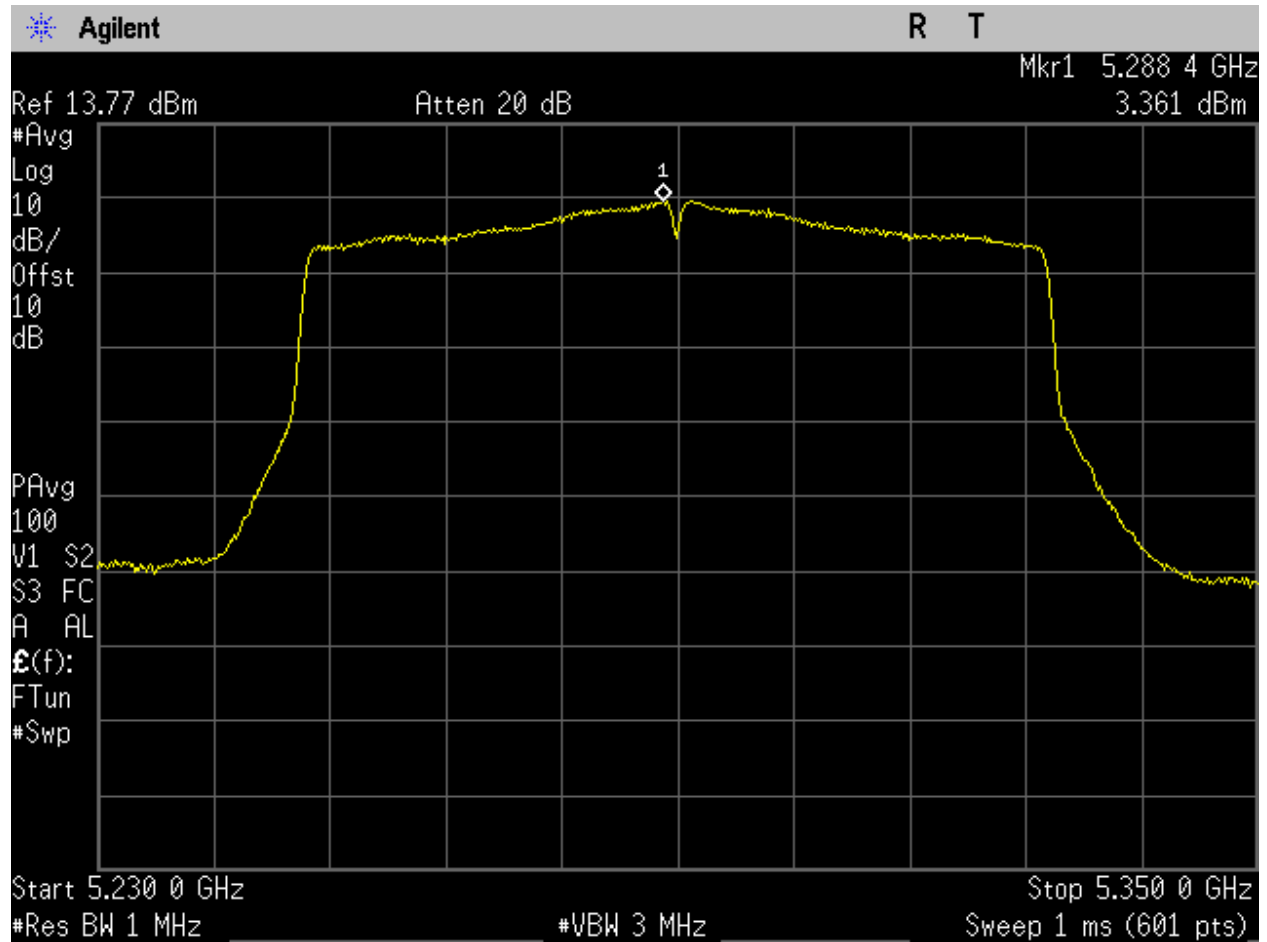


Figure 322: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ac-mode_Spectral Density_Port 2.

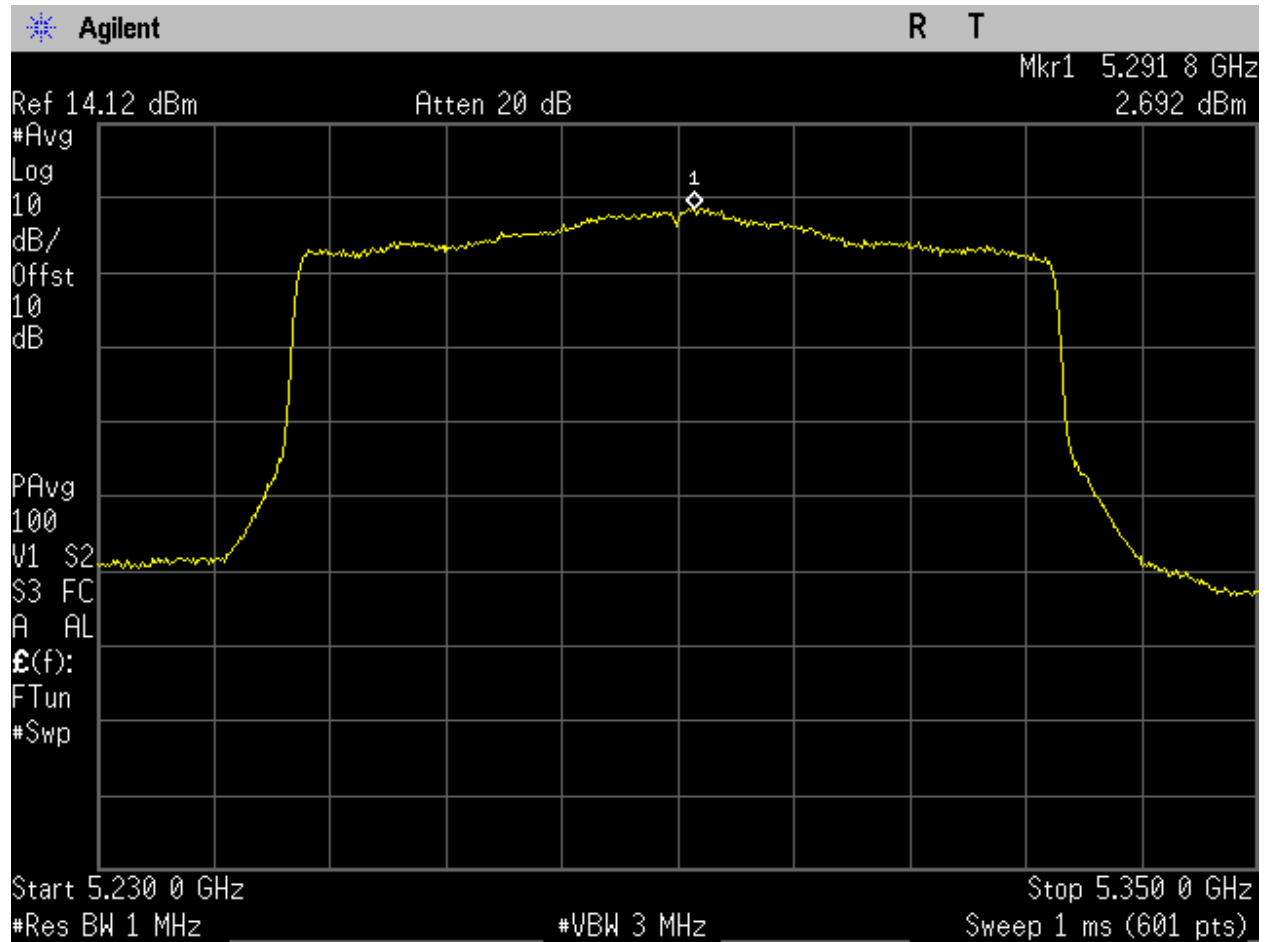


Figure 323: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Spectral Density_Port 1.

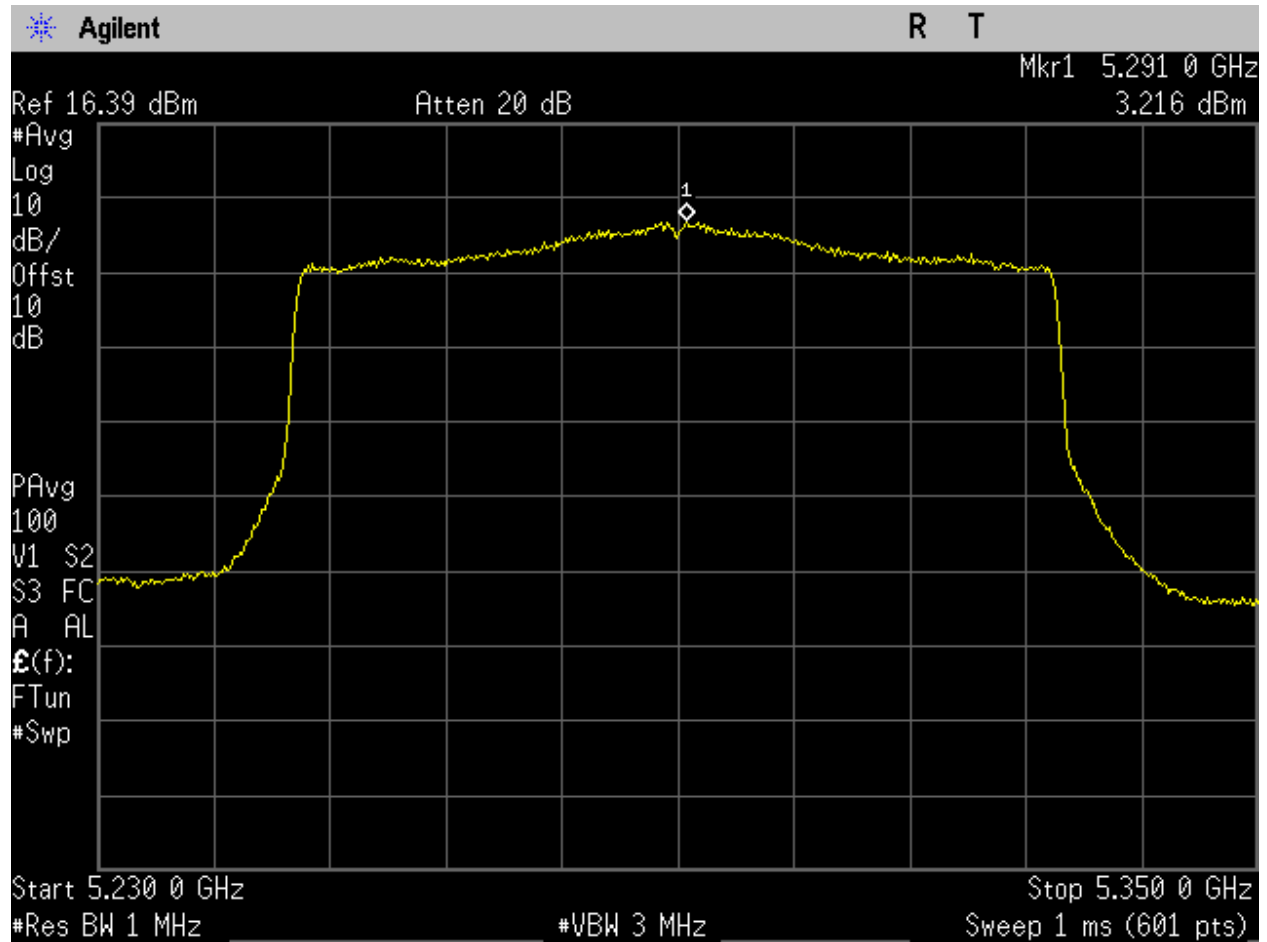


Figure 324: U-NII-2A_5290MHz_High_Mid Ch_58_80MHz BW_ax-mode_Spectral Density_Port 2.

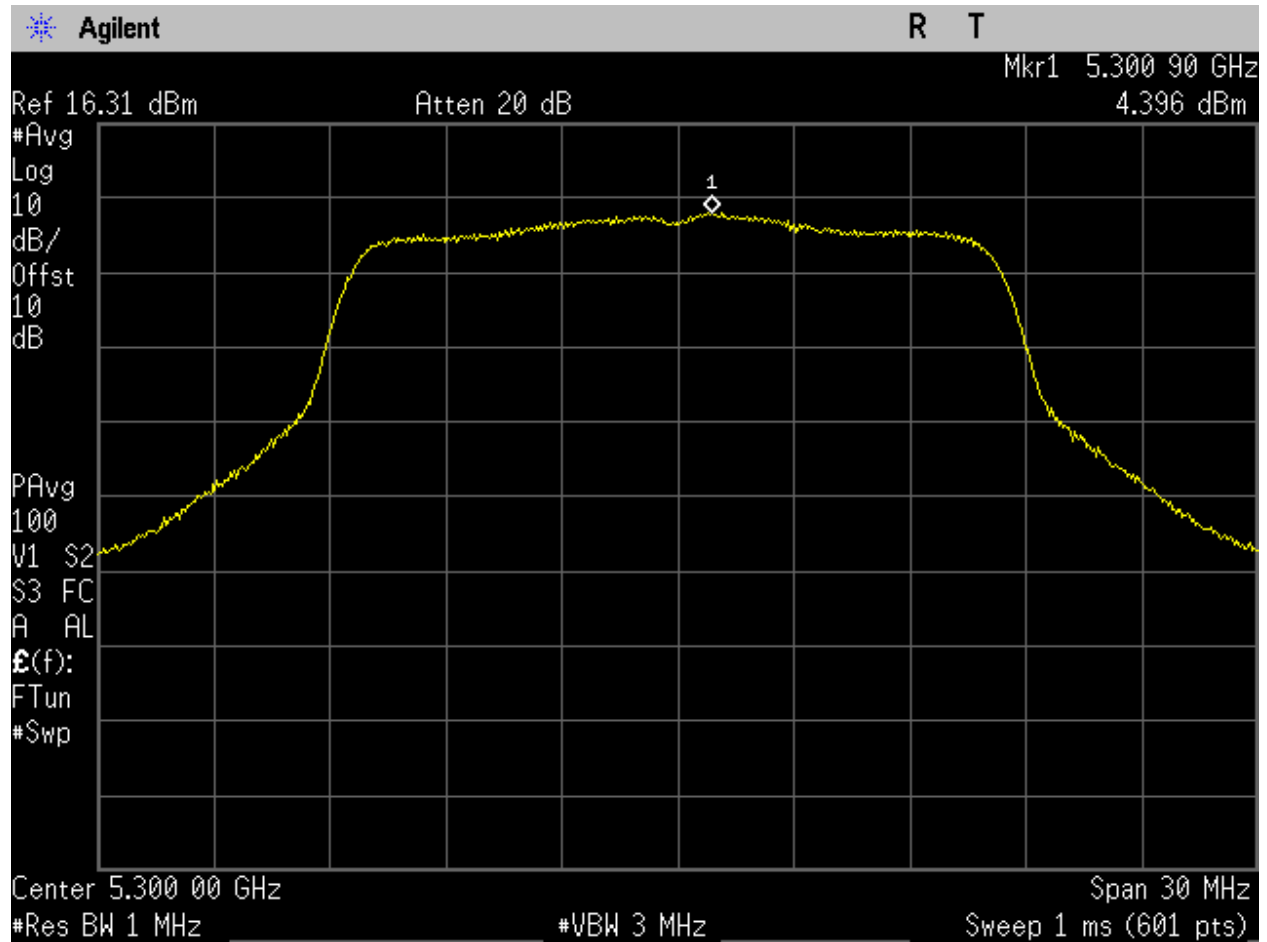


Figure 325: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Spectral Density_Port 1.

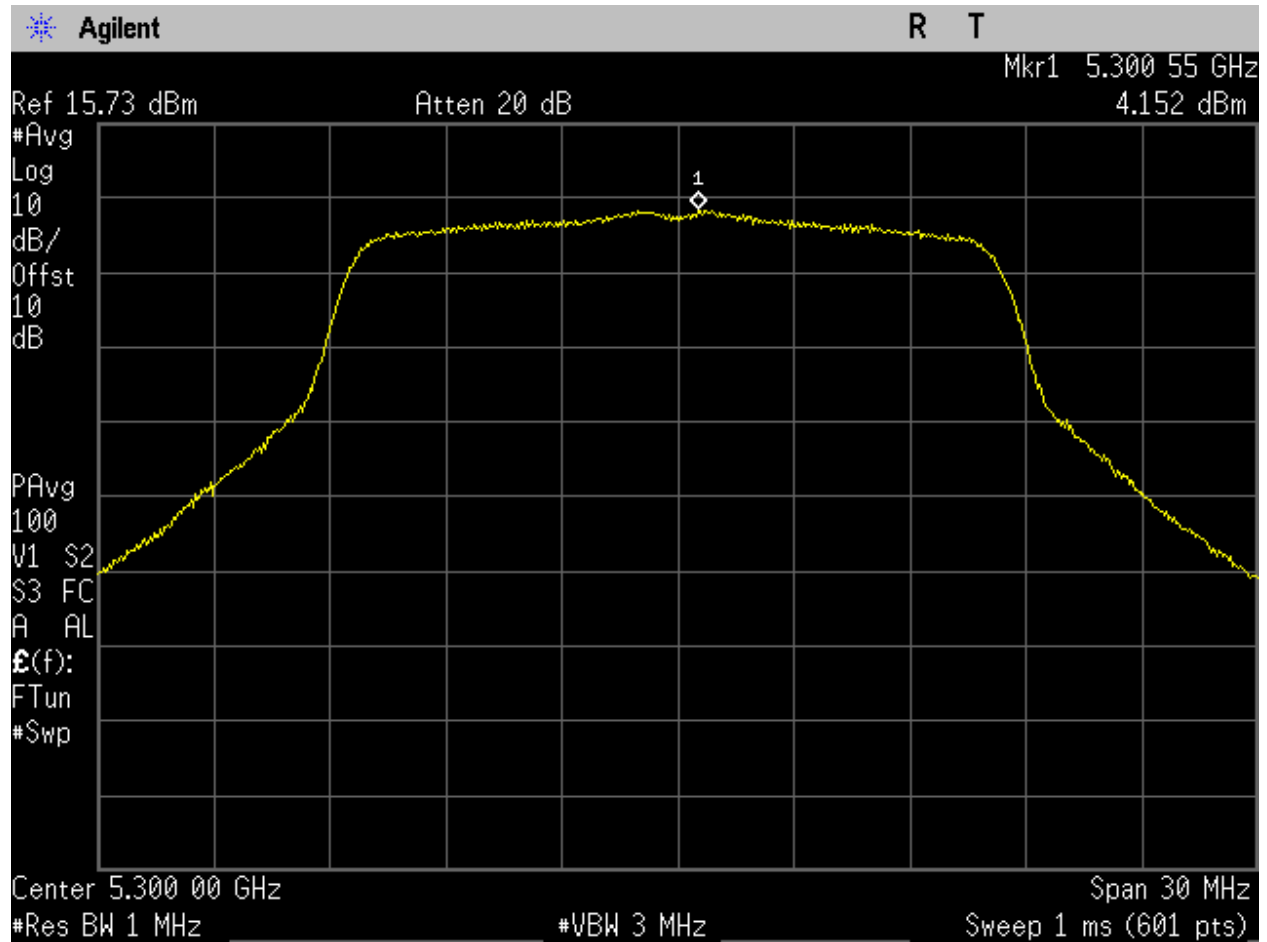


Figure 326: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_a-mode_Spectral Density_Port 2.

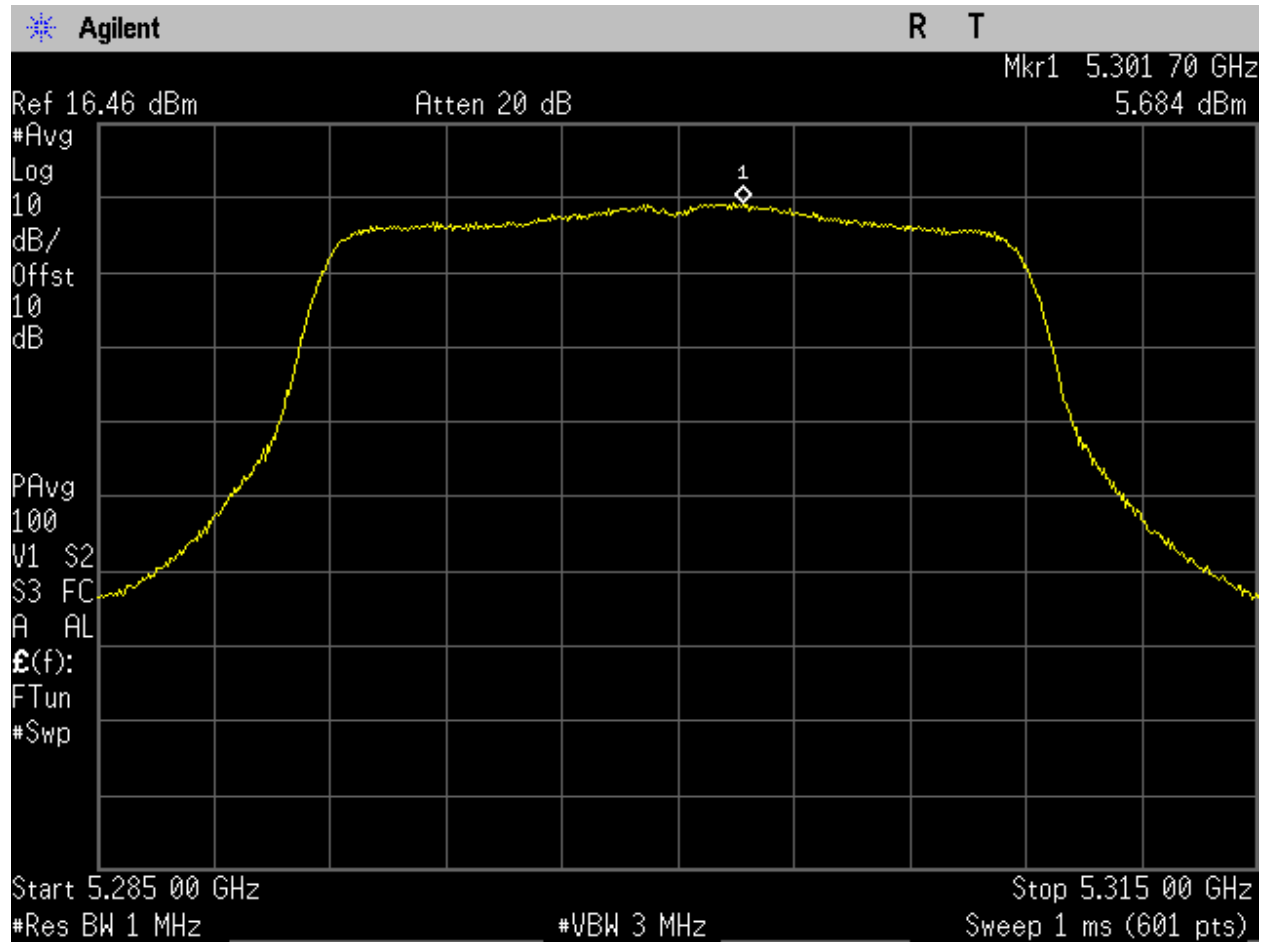


Figure 327: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Spectral Density_Port 1.

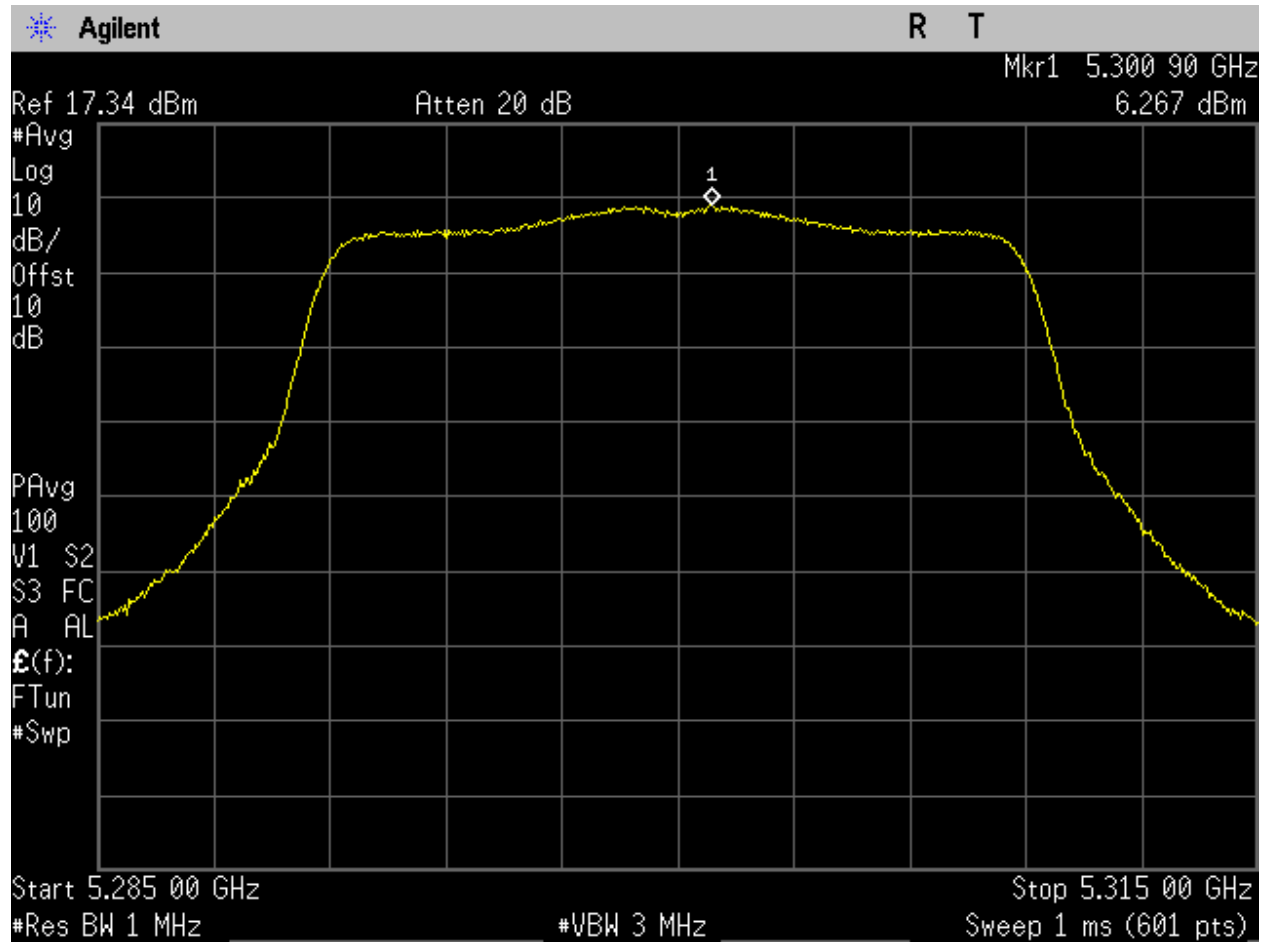


Figure 328: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ac-mode_Spectral Density_Port 2.

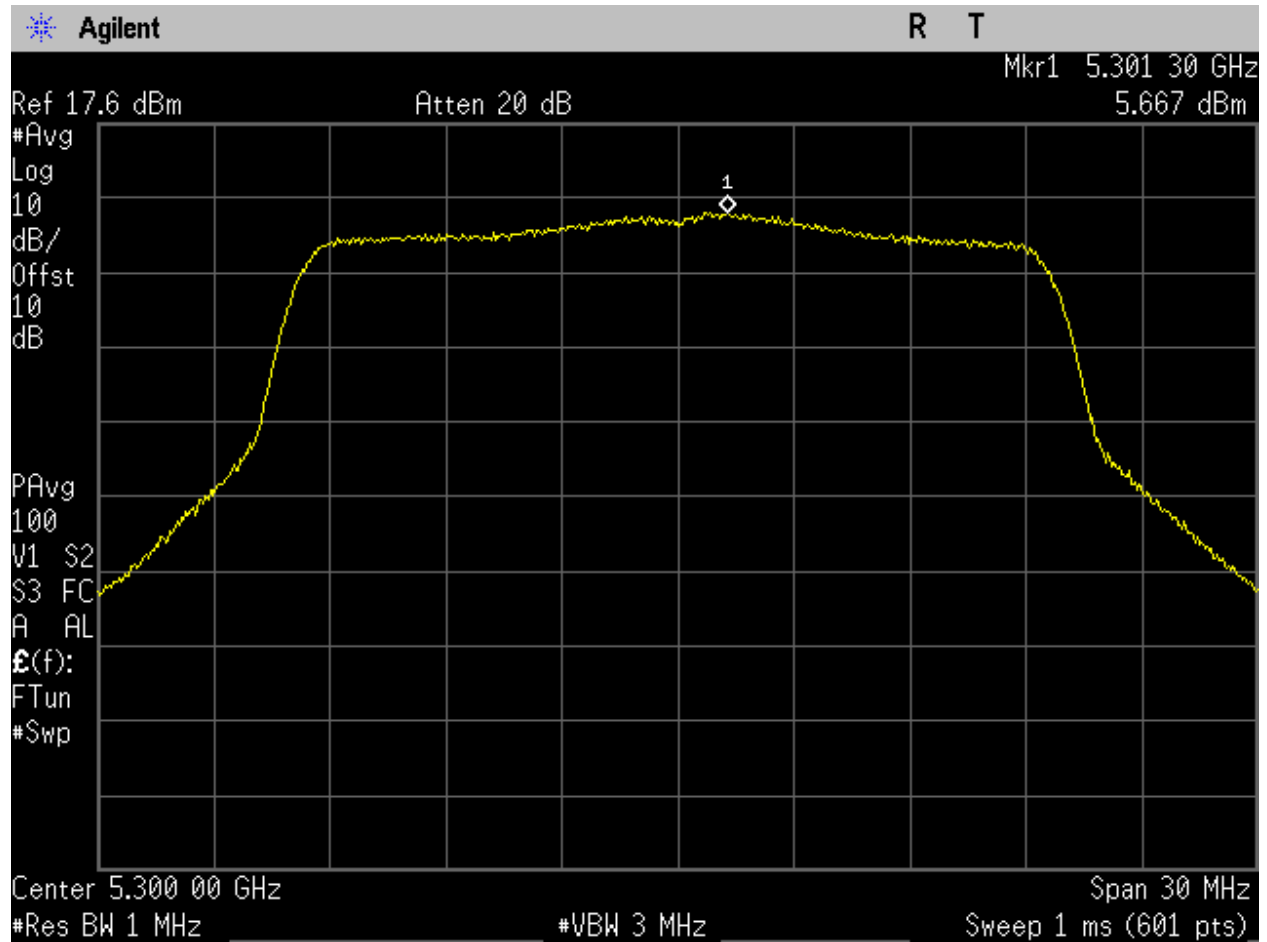


Figure 329: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_Spectral Density_Port 1.

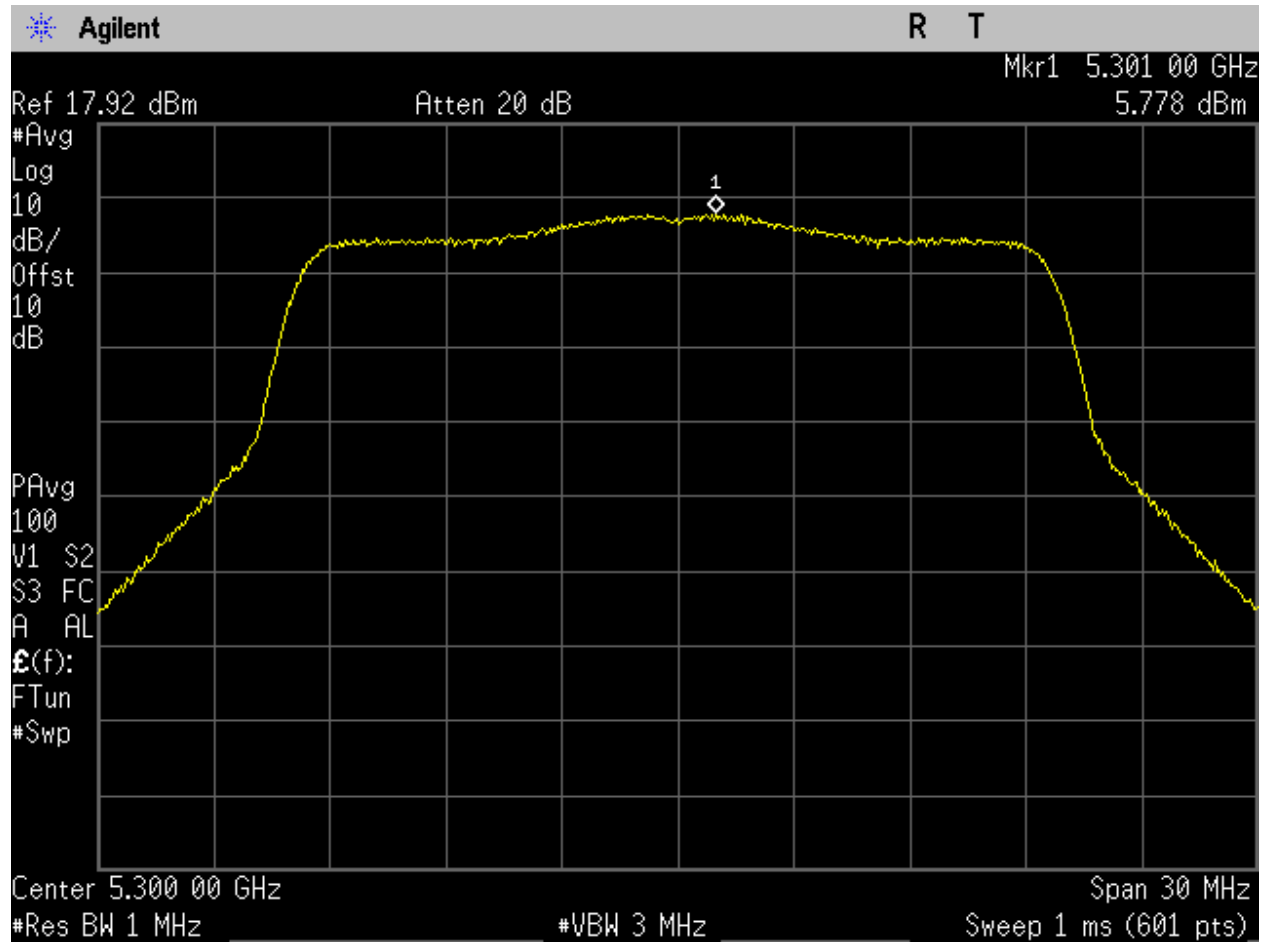


Figure 330: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_ax-mode_Spectral Density_Port 2.

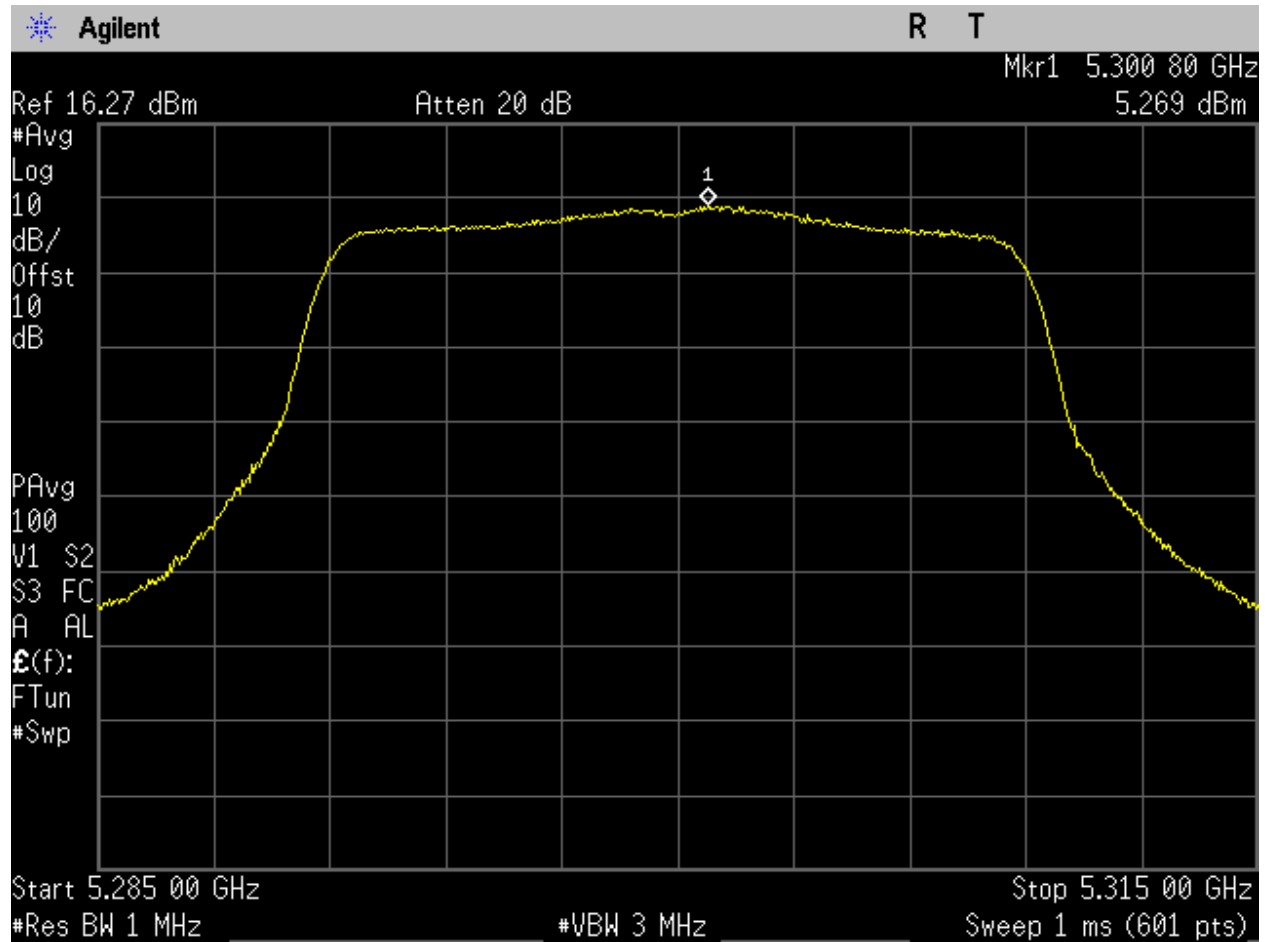


Figure 331: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Spectral Density_Port 1.

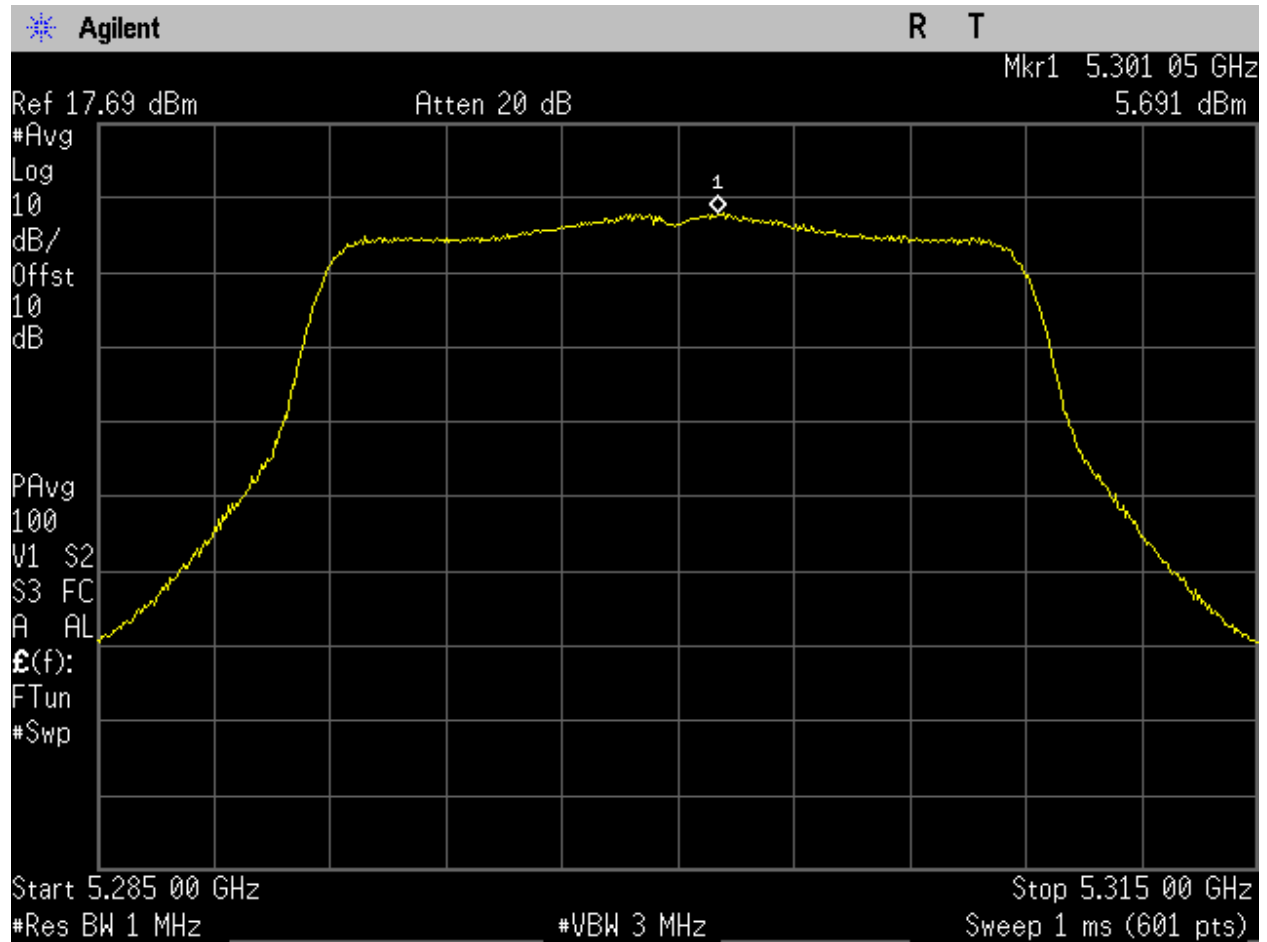


Figure 332: U-NII-2A_5300MHz_high_Mid Ch_60_20MHz BW_n-mode_Spectral Density_Port 2.

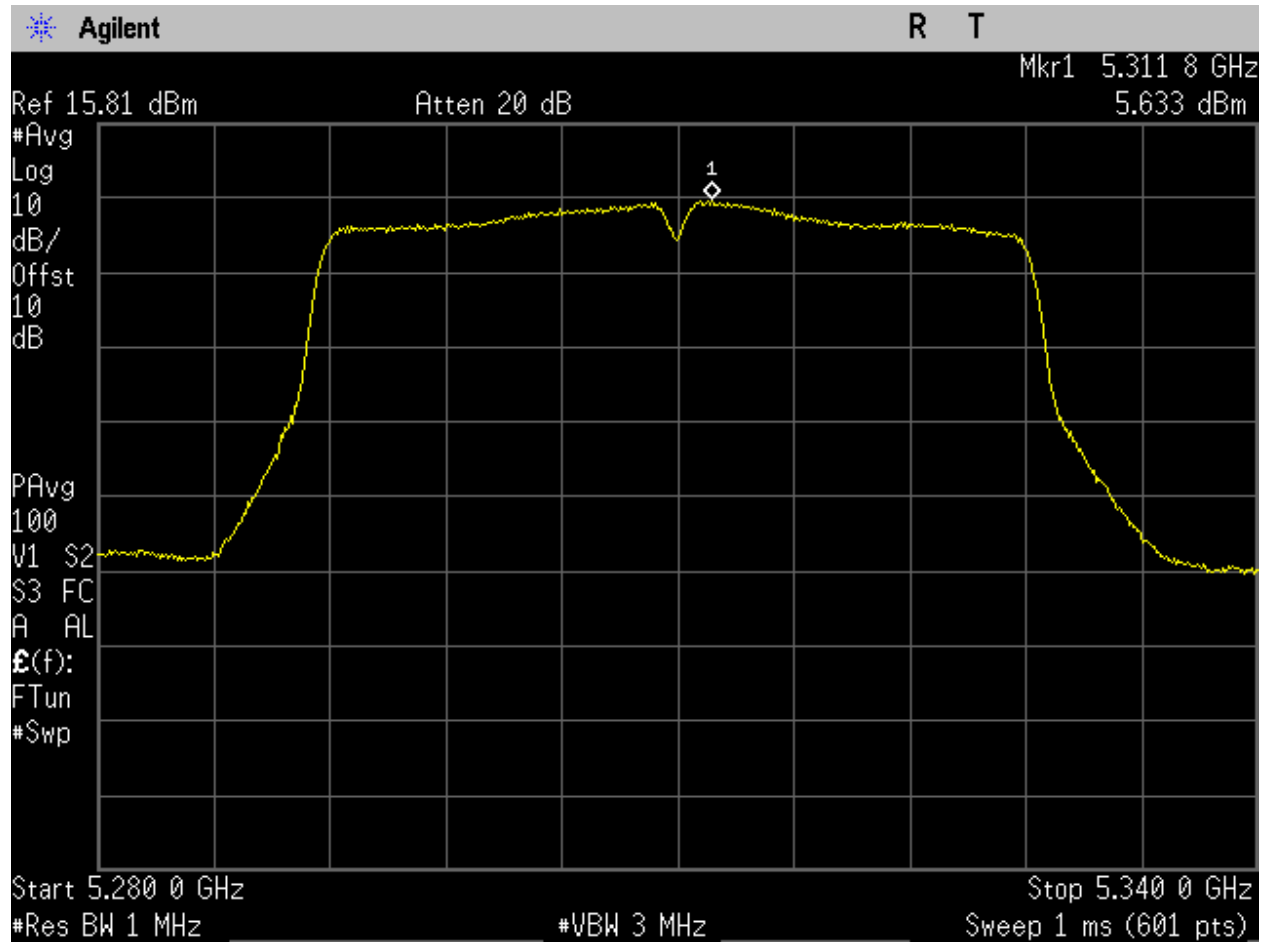


Figure 333: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_Spectral Density_Port 1.

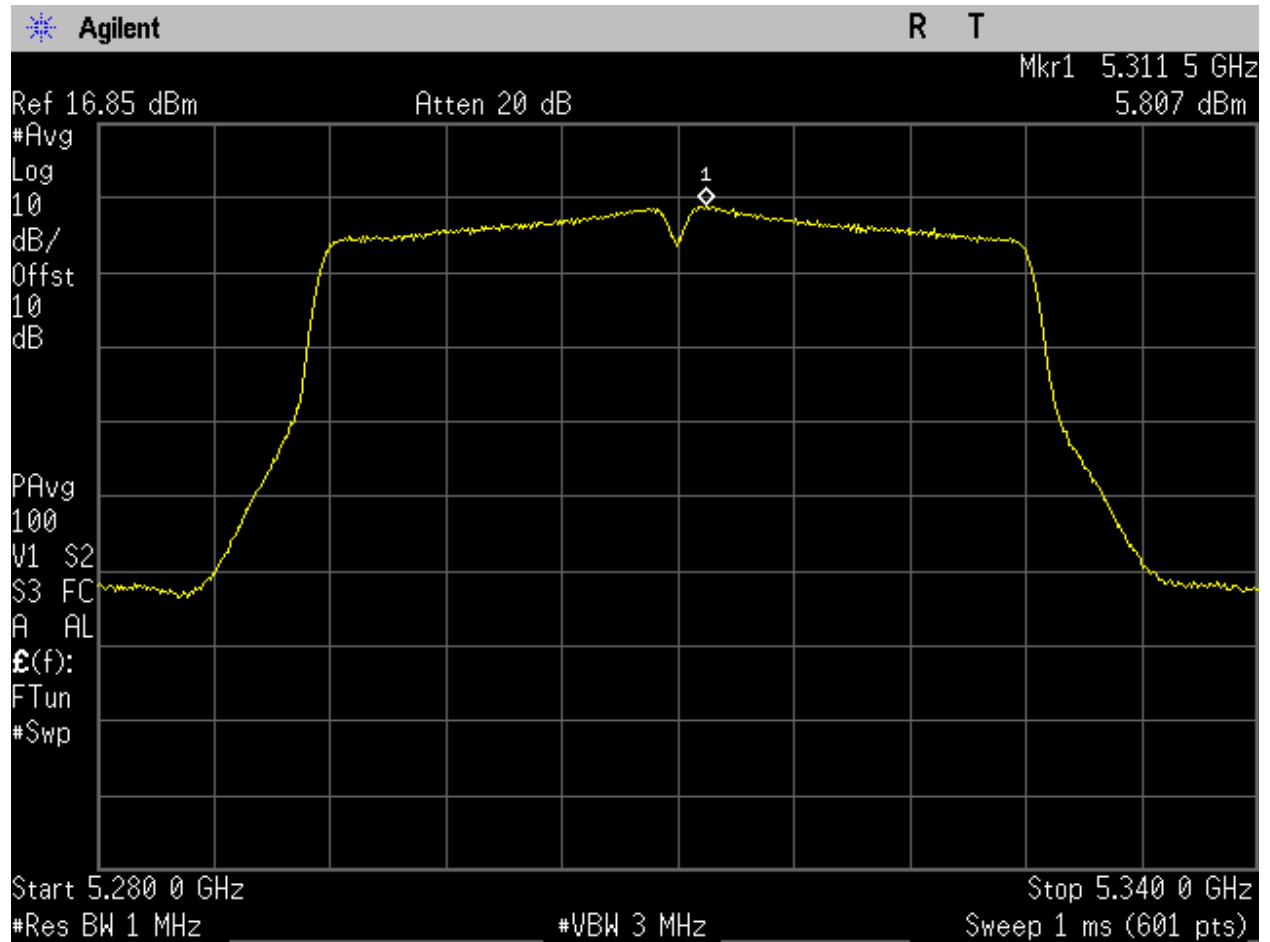


Figure 334: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ac-mode_Spectral Density_Port 2.

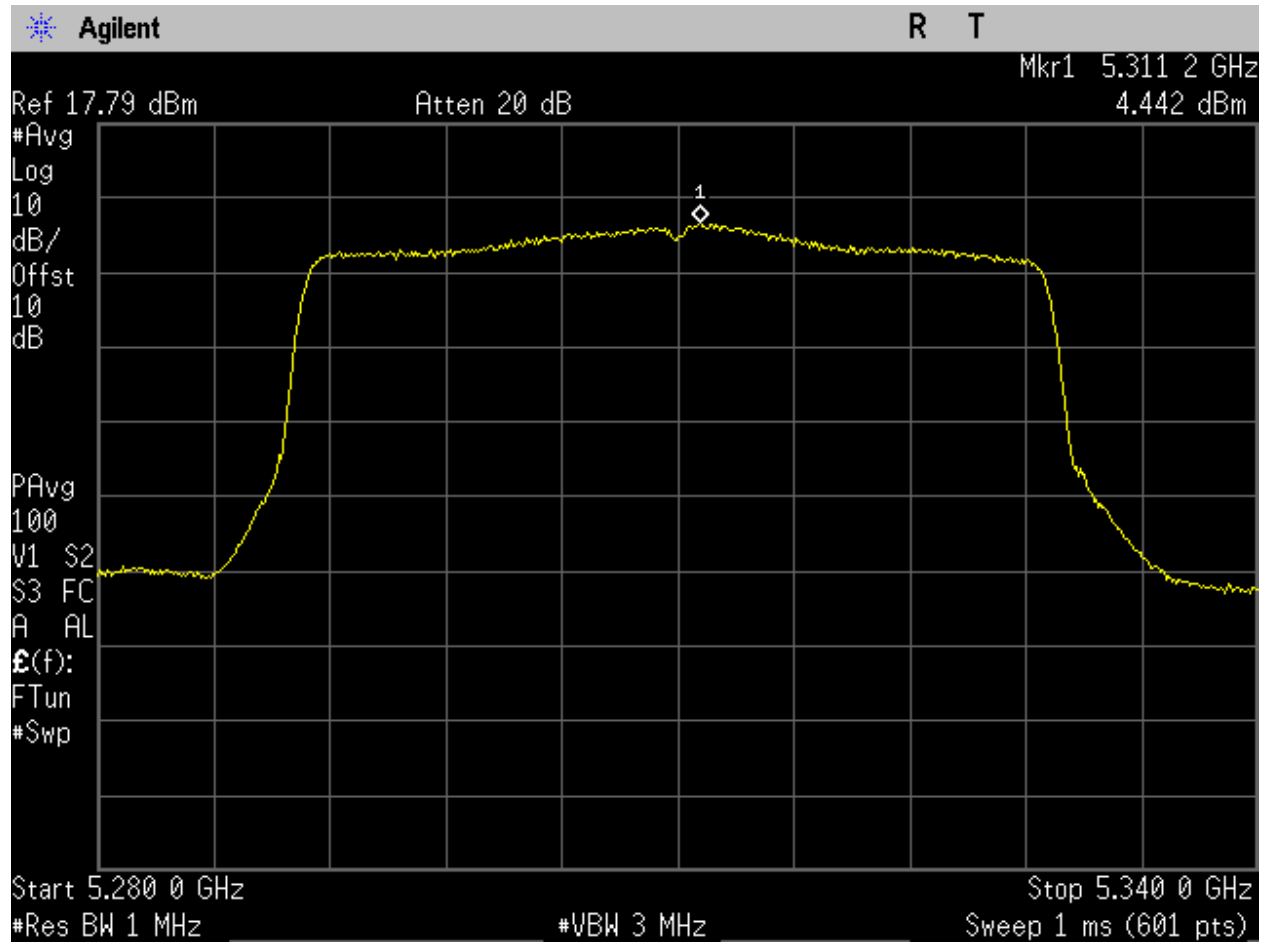


Figure 335: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_Spectral Density_Port 1.

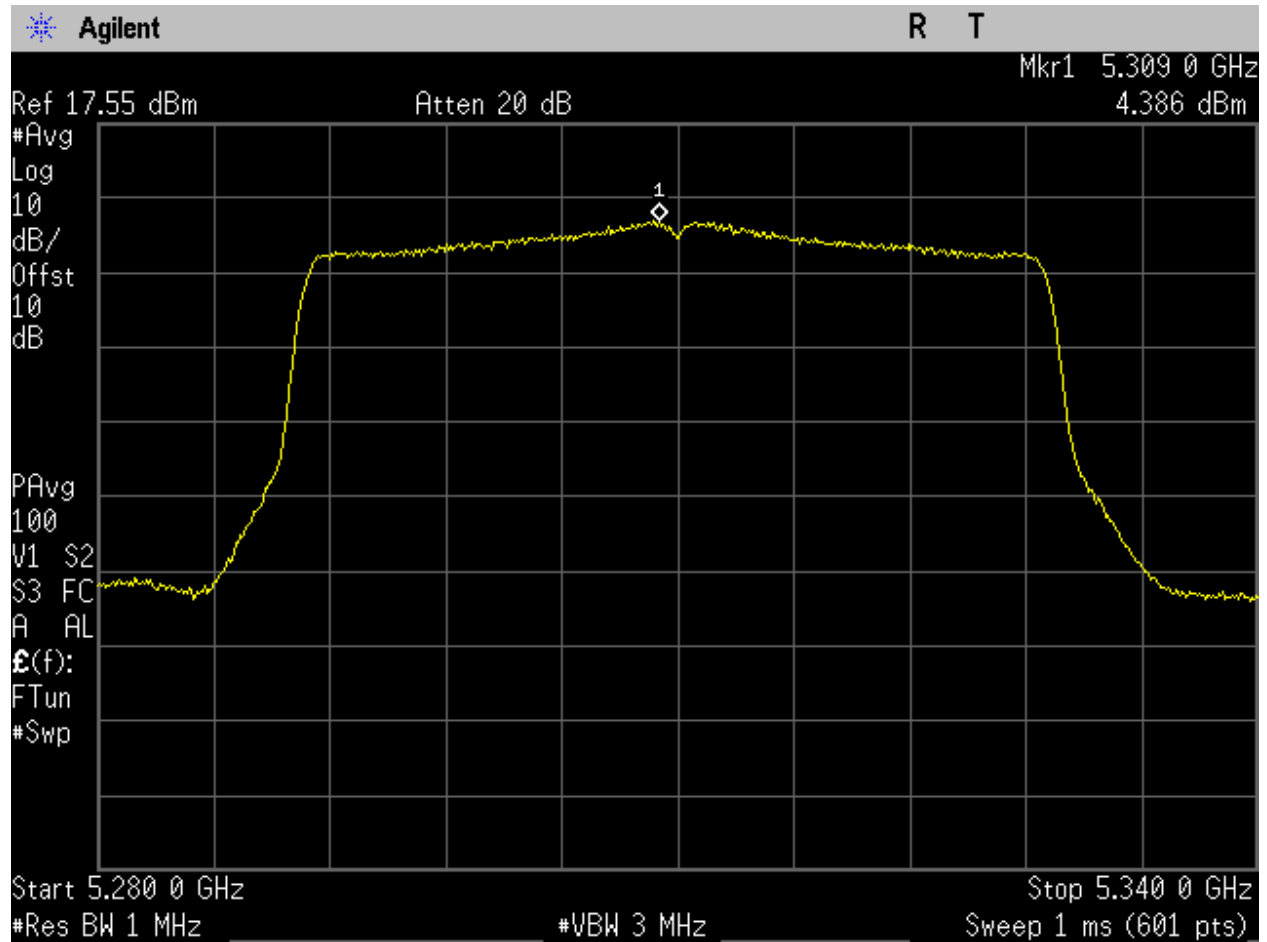


Figure 336: U-NII-2A_5310MHz_High Ch_62_40MHz BW_ax-mode_Spectral Density_Port 2.

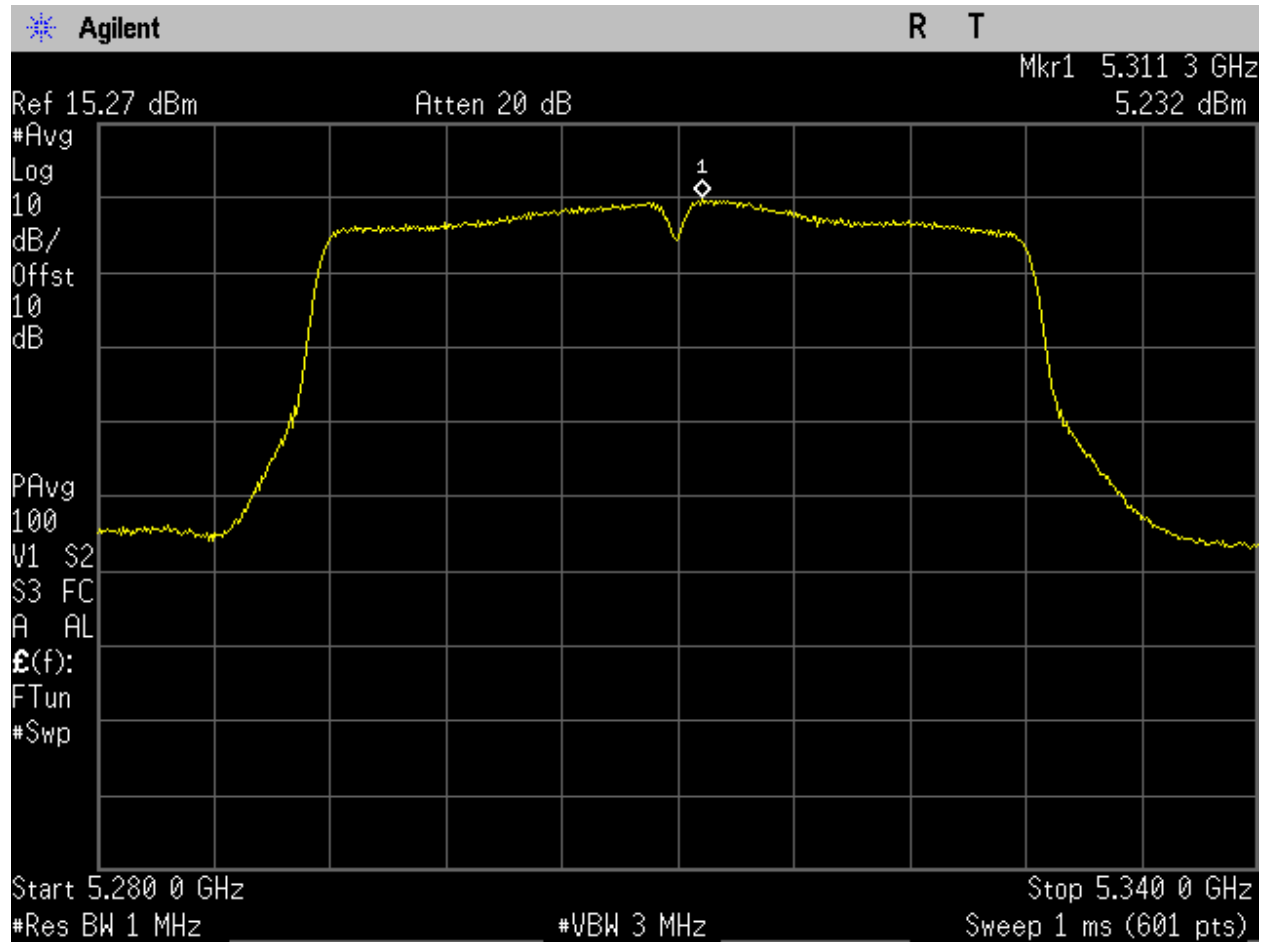


Figure 337: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Spectral Density_Port 1.

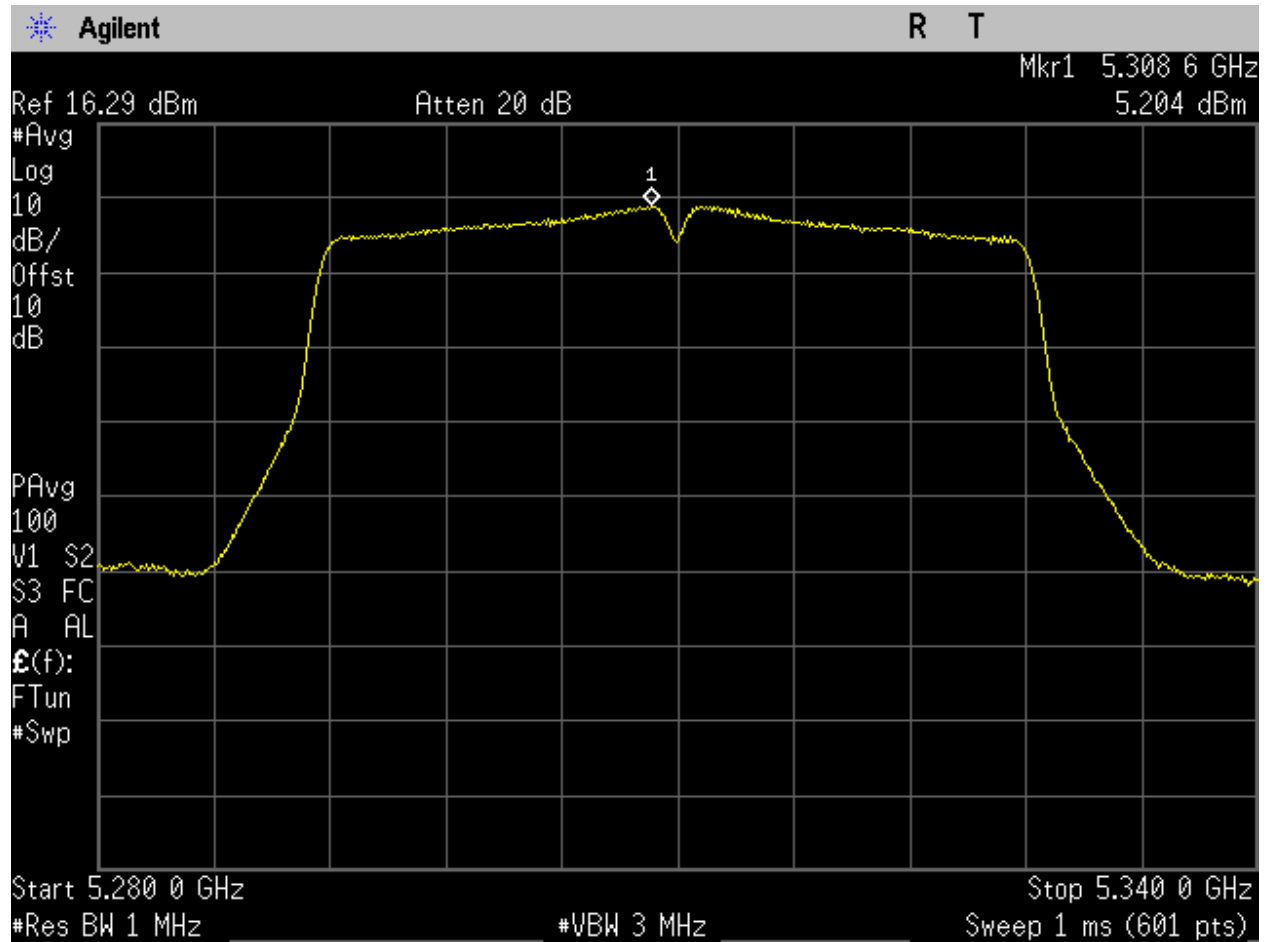


Figure 338: U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Spectral Density_Port 2.

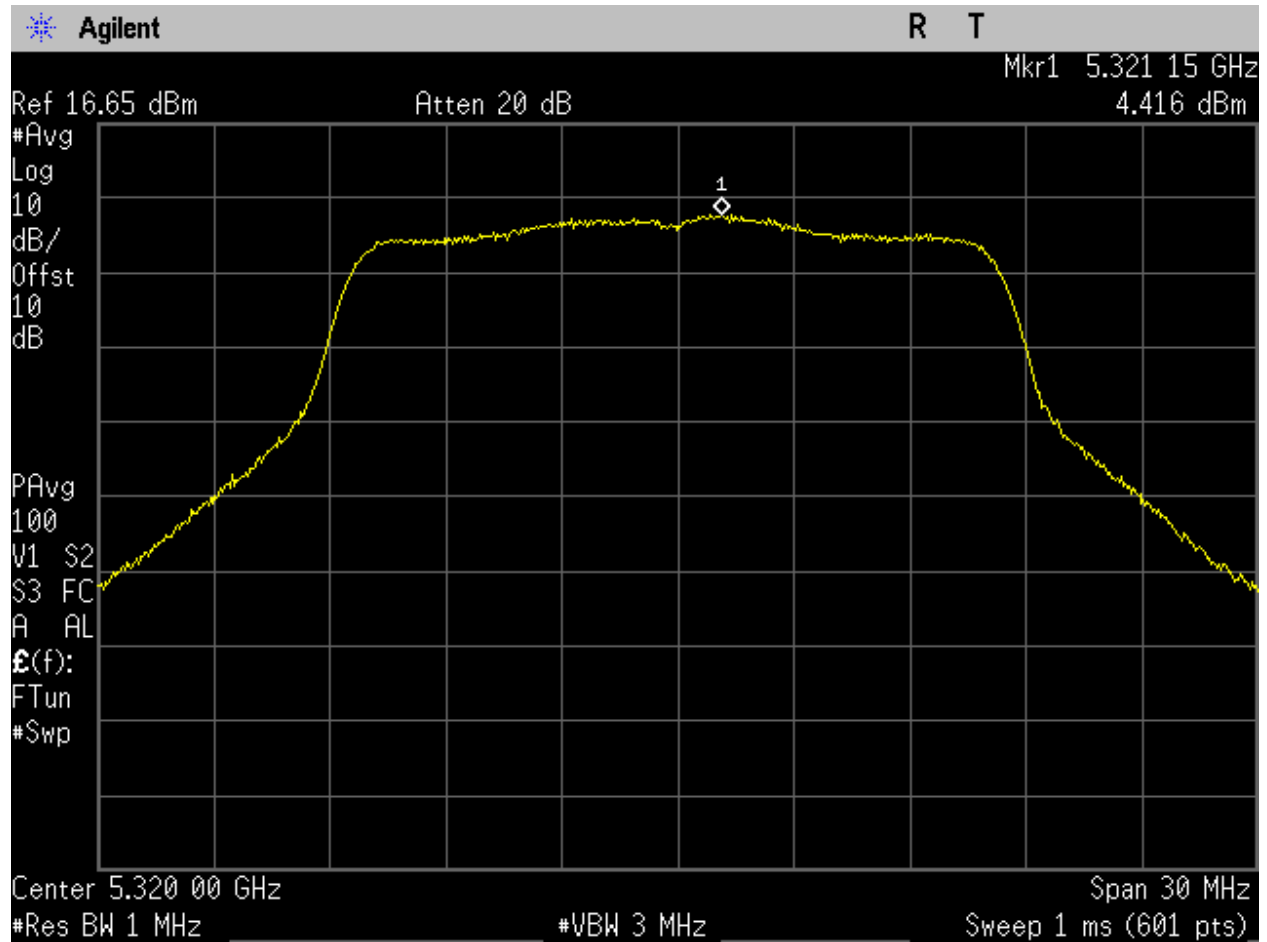


Figure 339: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_Spectral Density_Port 1.

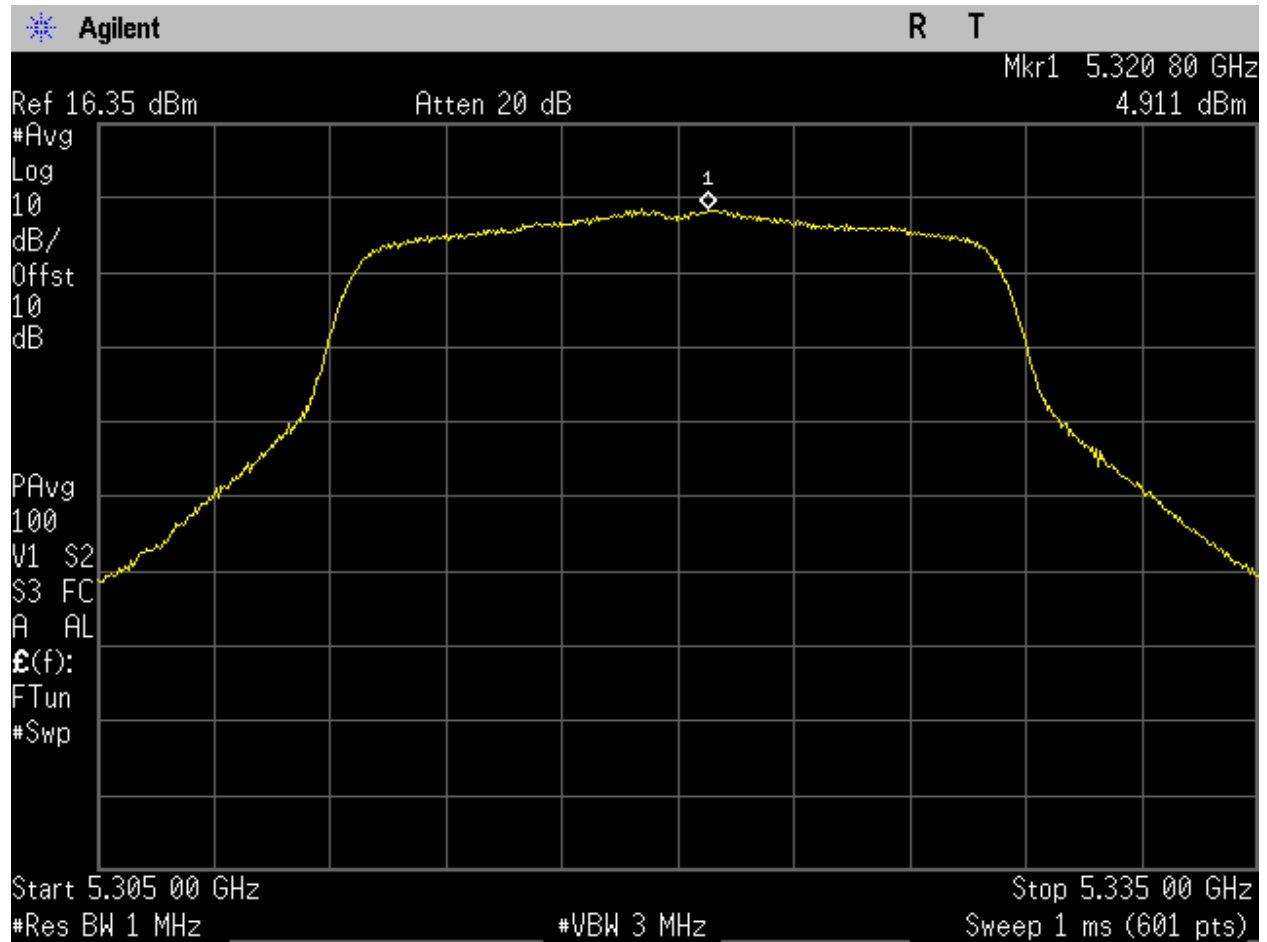


Figure 340: U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_Spectral Density_Port 2.

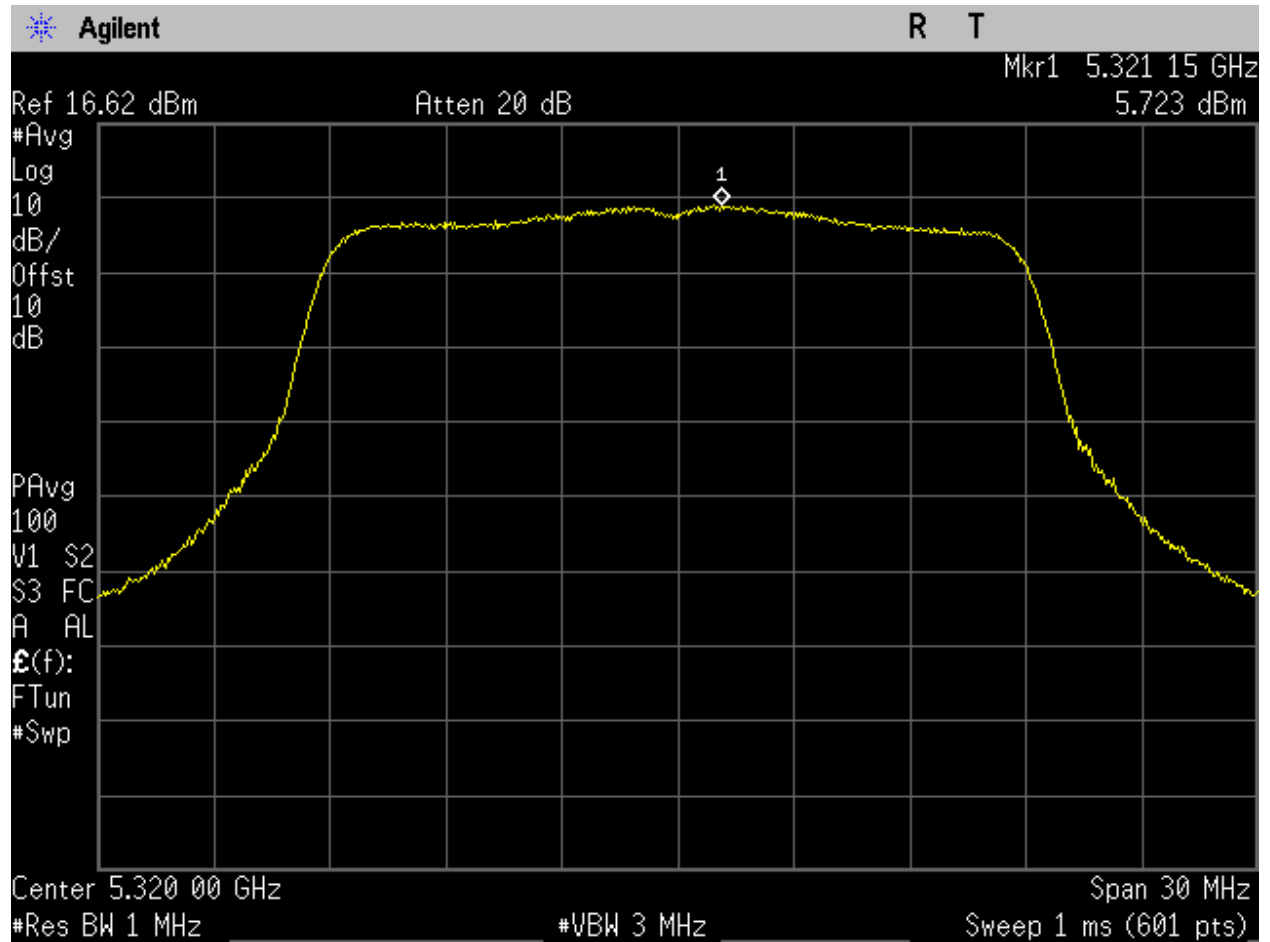


Figure 341: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_Spectral Density_Port 1.

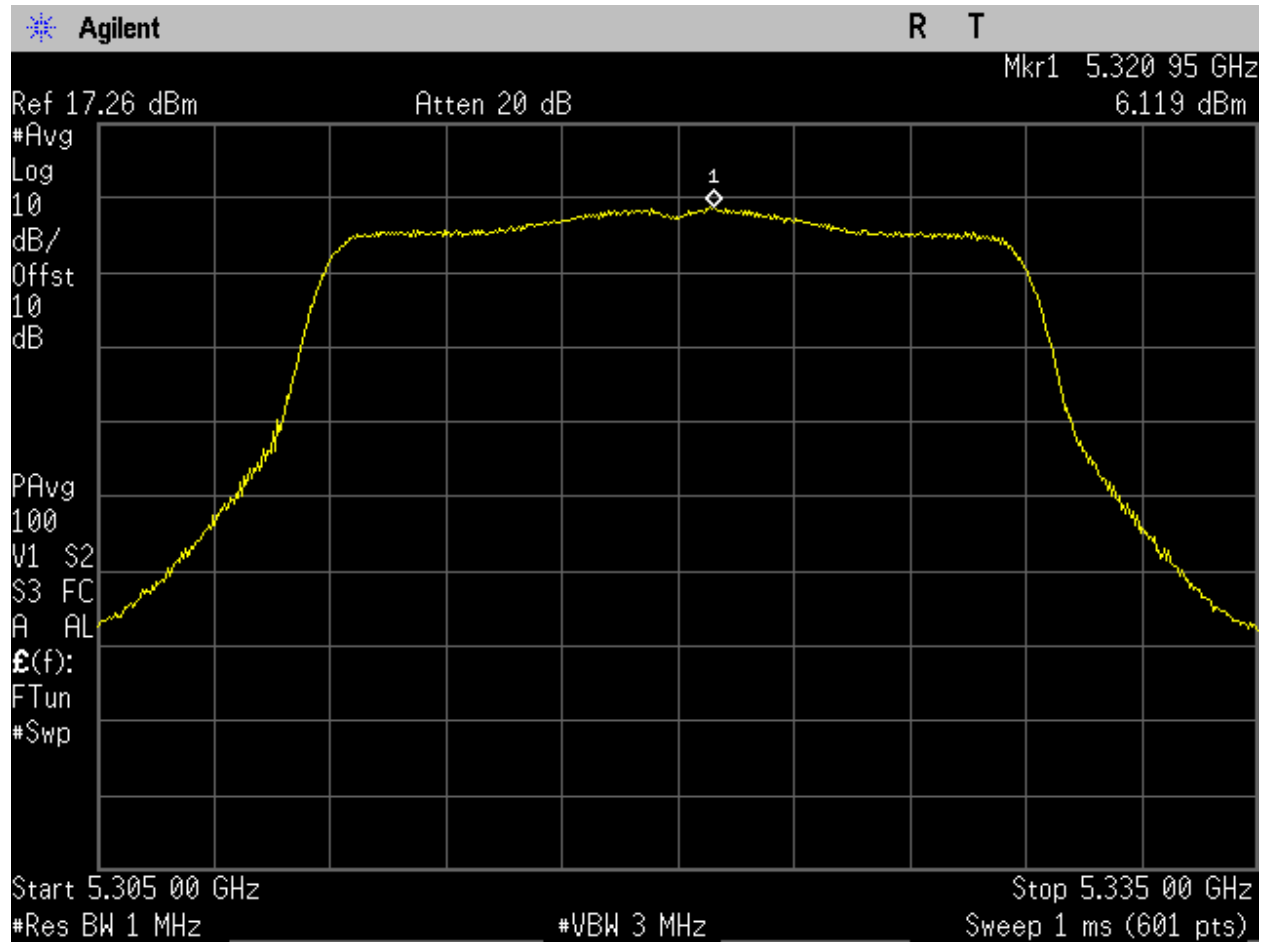


Figure 342: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ac-mode_Spectral Density_Port 2.

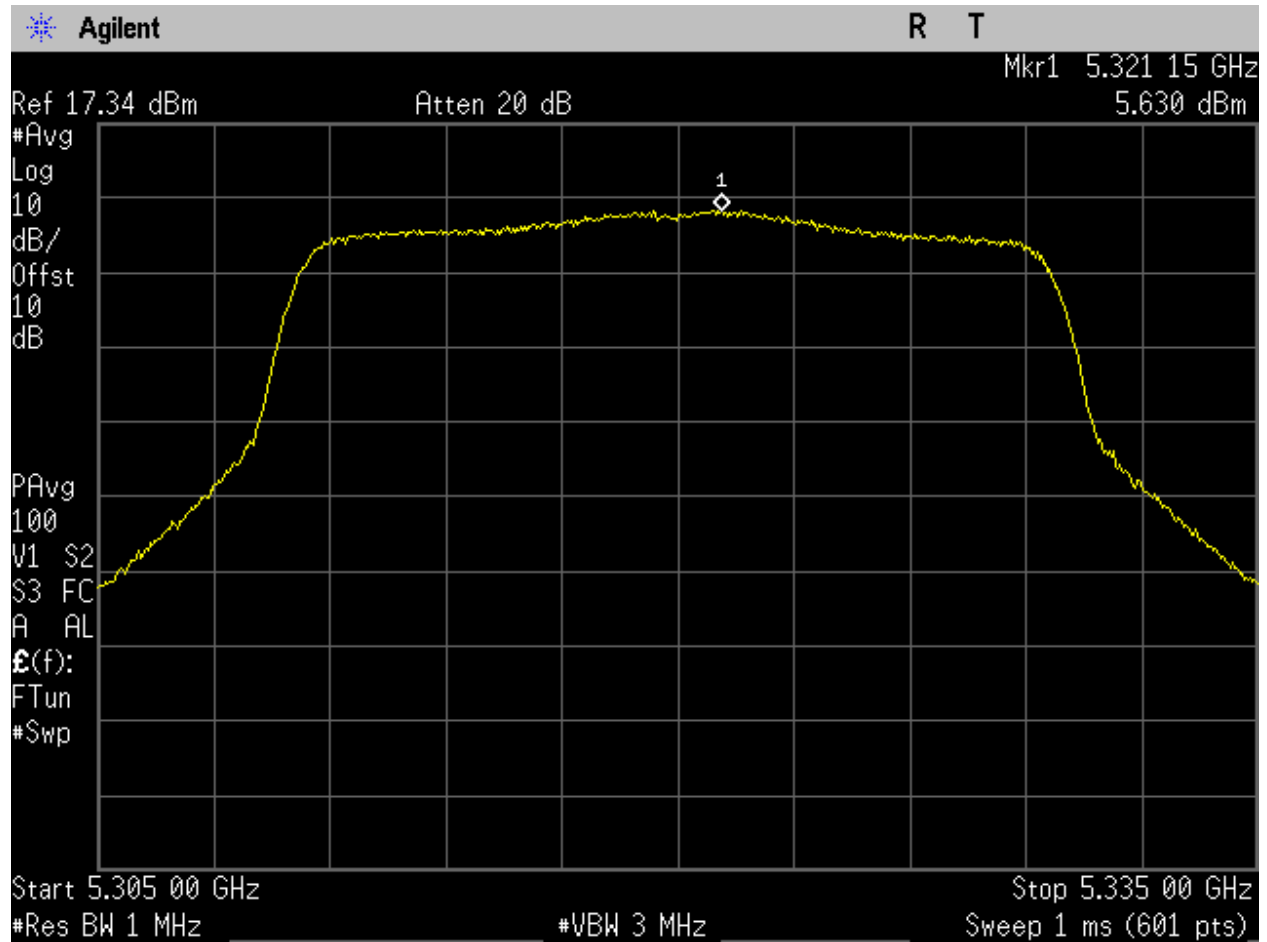


Figure 343: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_Spectral Density_Port 1.

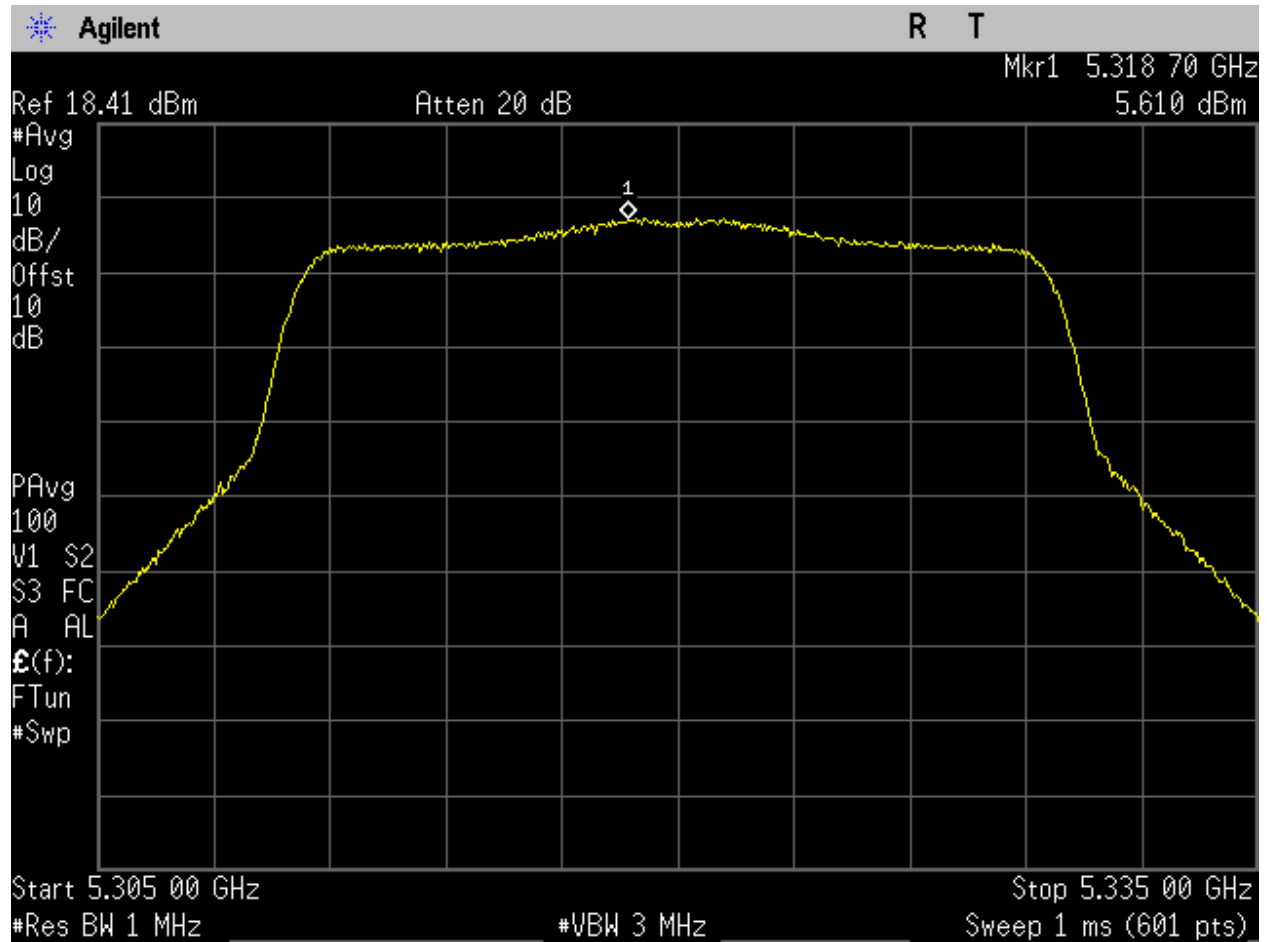


Figure 344: U-NII-2A_5320MHz_High Ch_64_20MHz BW_ax-mode_Spectral Density_Port 2.

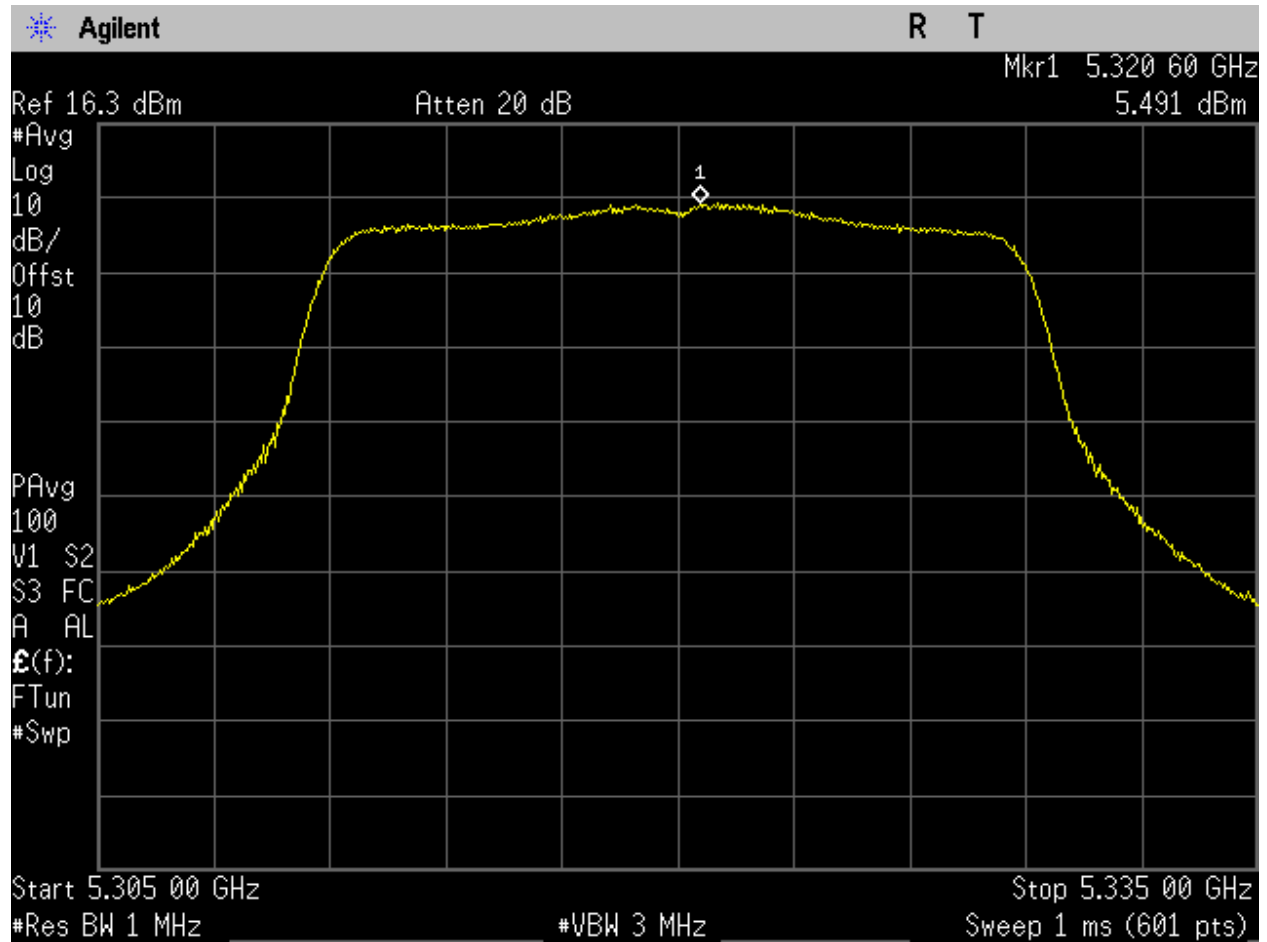


Figure 345: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Spectral Density_Port 1.

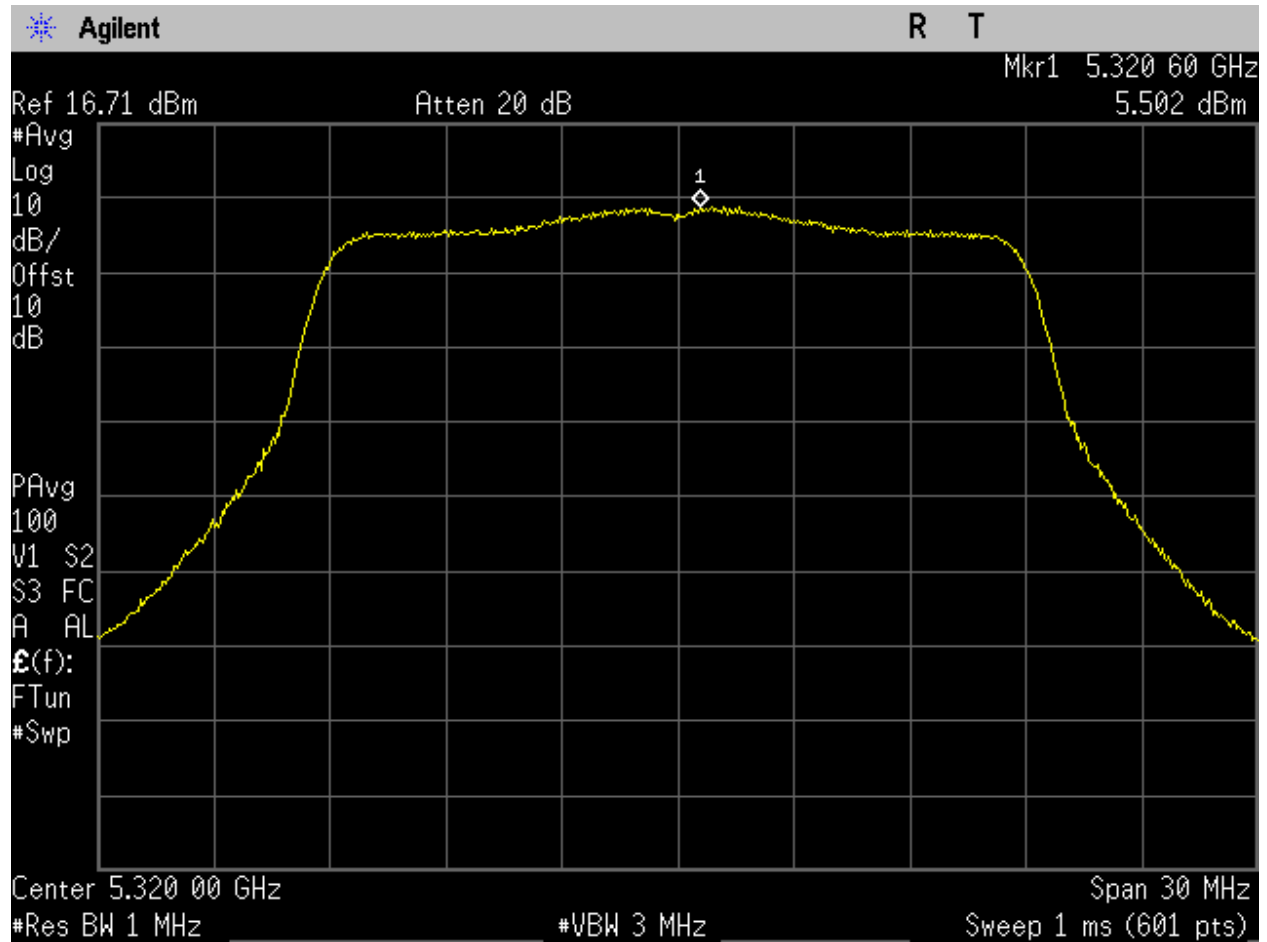


Figure 346: U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Spectral Density_Port 2.

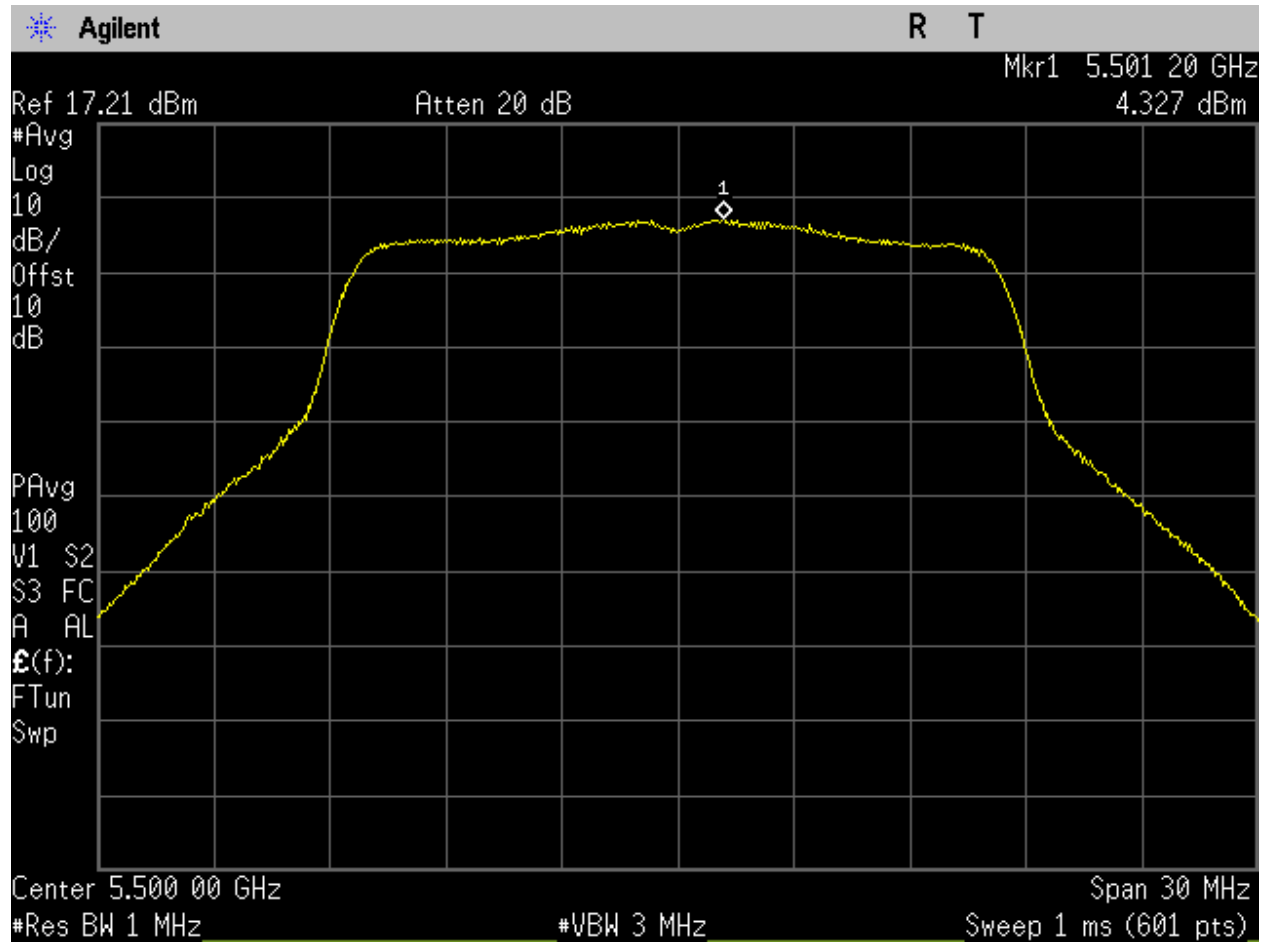


Figure 347: U-NII-2C_5500MHz_Low Ch_100_20MHz BW_a-mode_Spectral Density_Port 1.