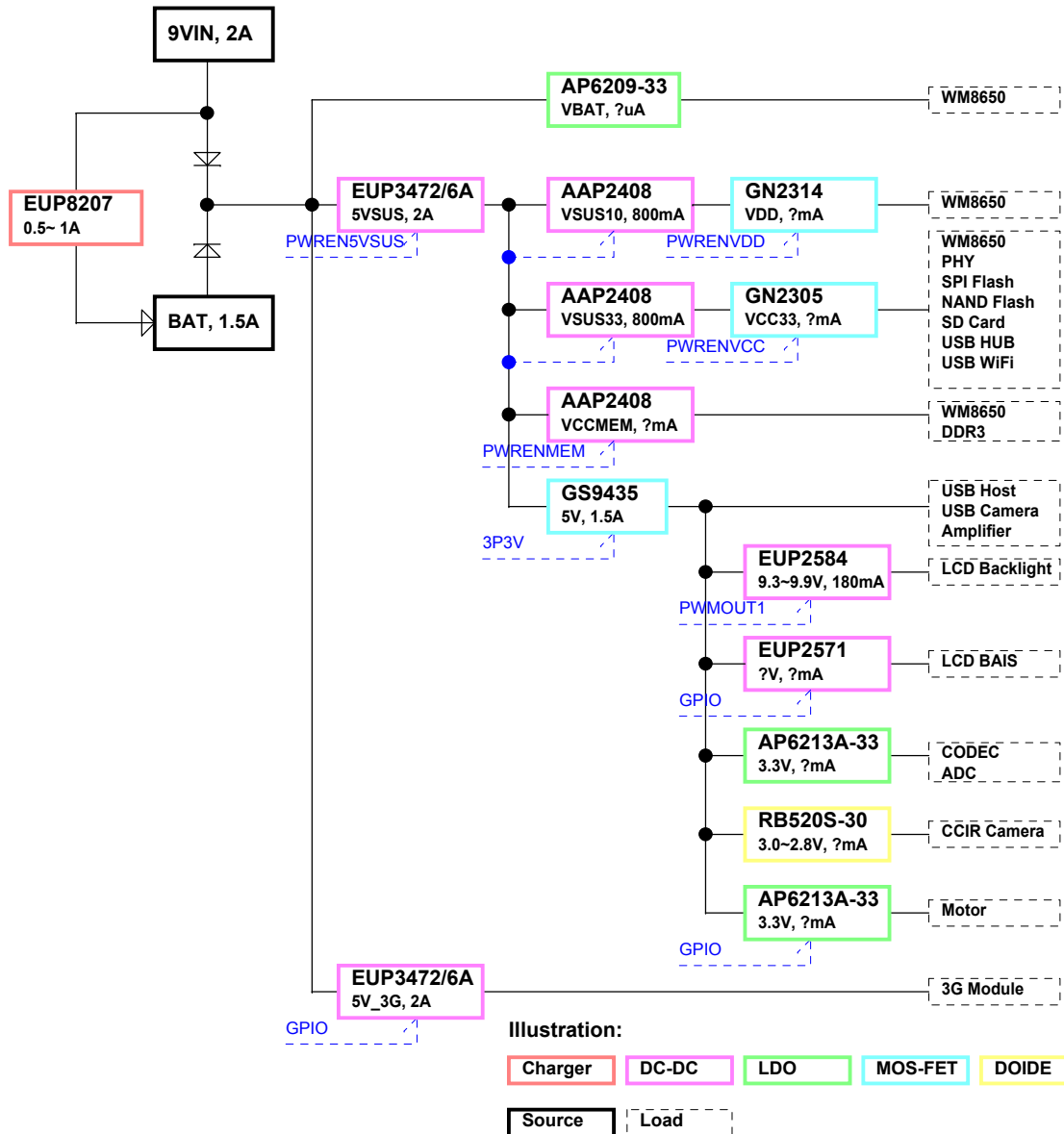


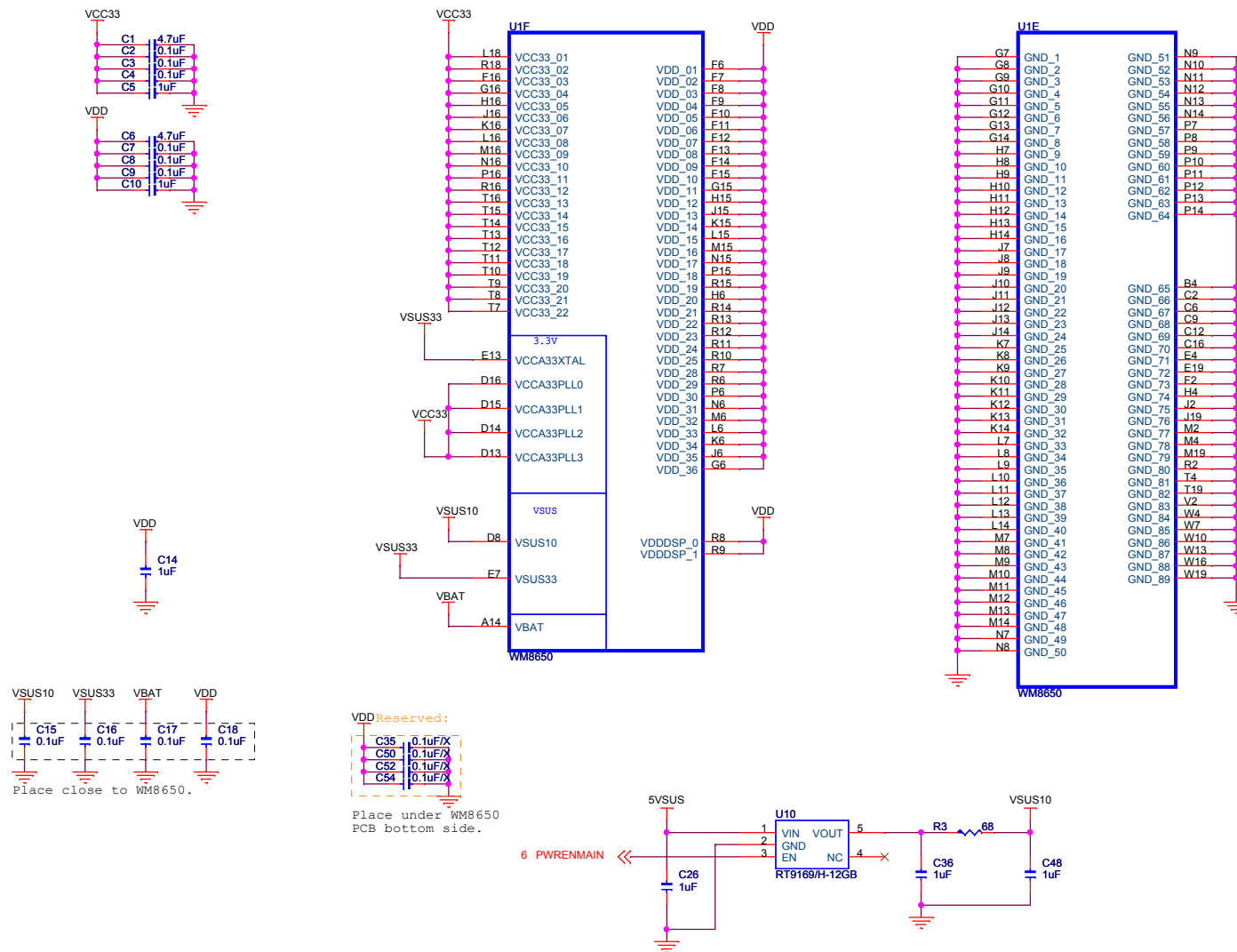
Power Tree:



TITLE	PAGE
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WM8650 DDR3/DRAM	05
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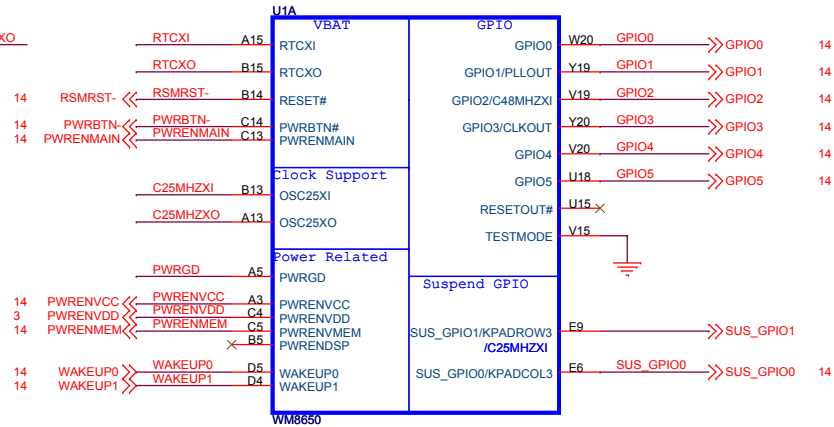
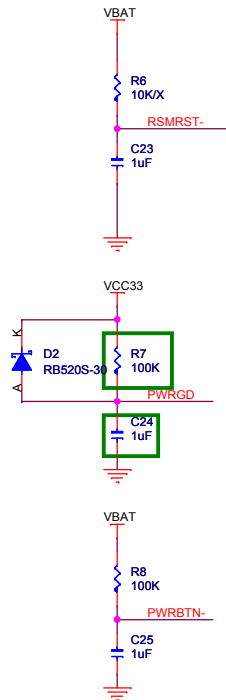
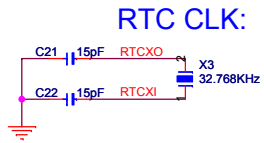
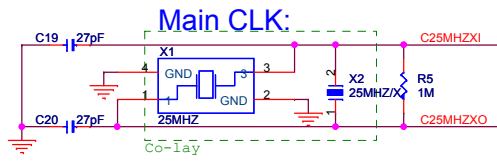
WM8650: Power / Ground

PART: E+F



WM8650: CLOCK/RESET/PMC/GPIO

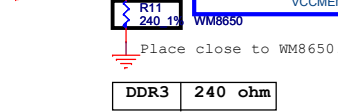
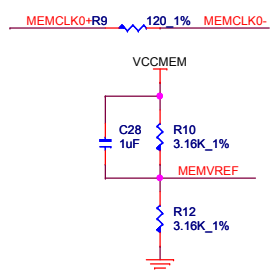
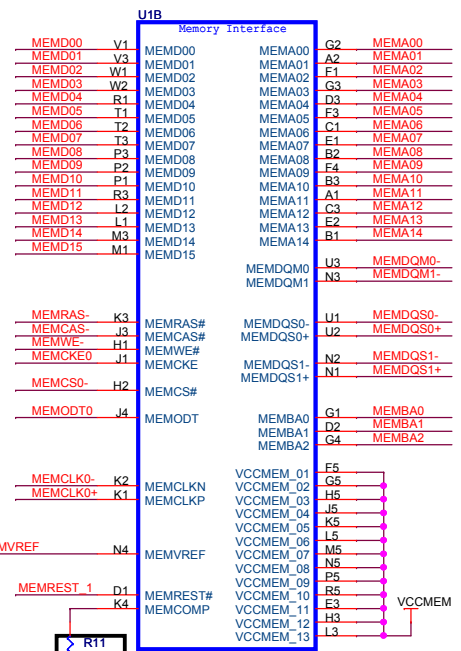
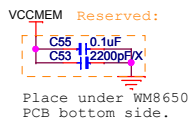
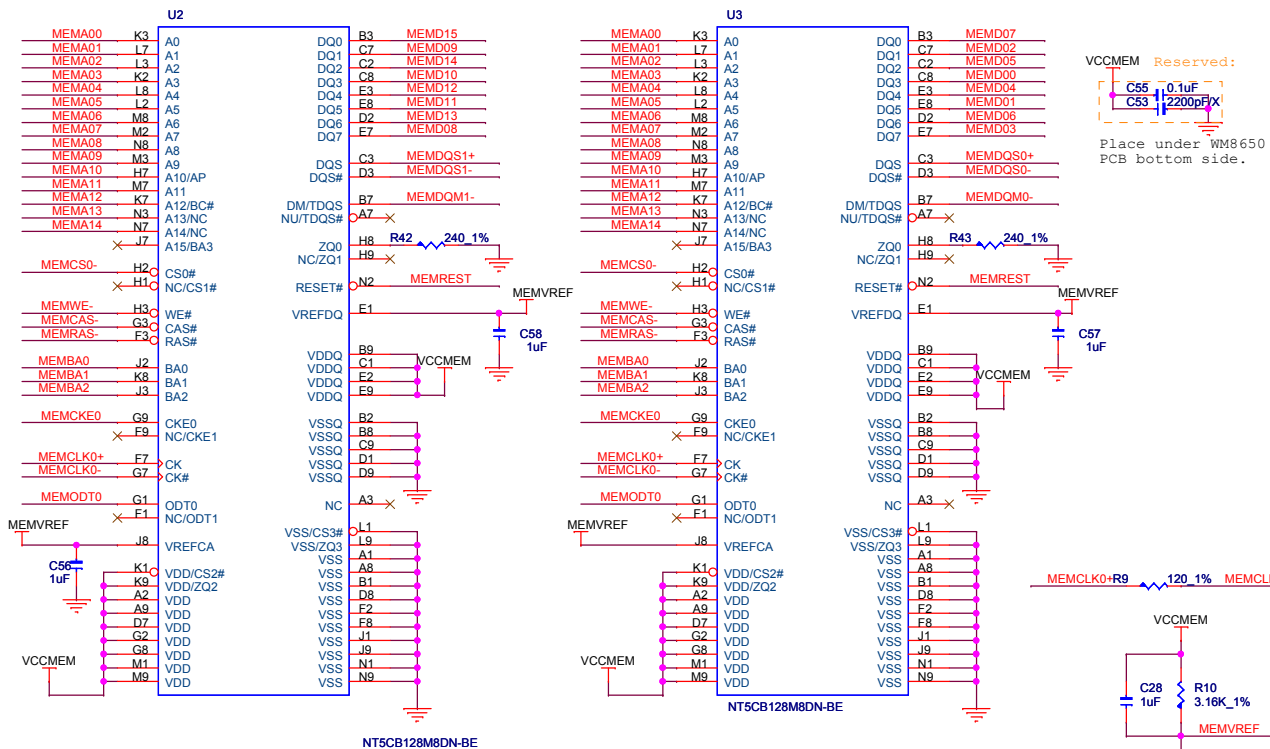
PART:A



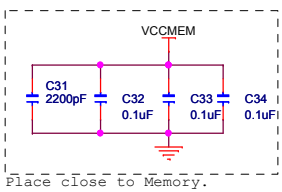
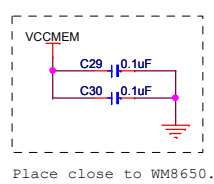
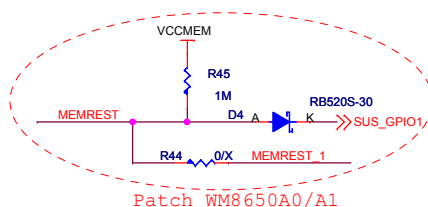
WM8650: DDR2 SDRAM

PART: B

DDR3_1Gb_8bit x 2



DDR3 240 ohm



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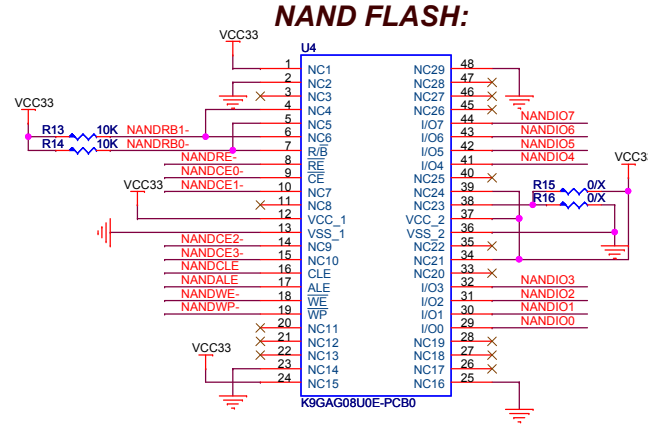
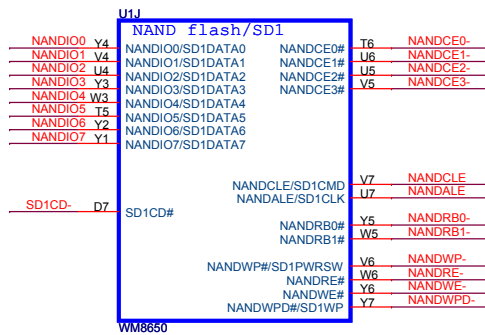
Title: **WM8650: DDR3**

Size: Custom Document Number: **WMS8152A** Rev: 1.0

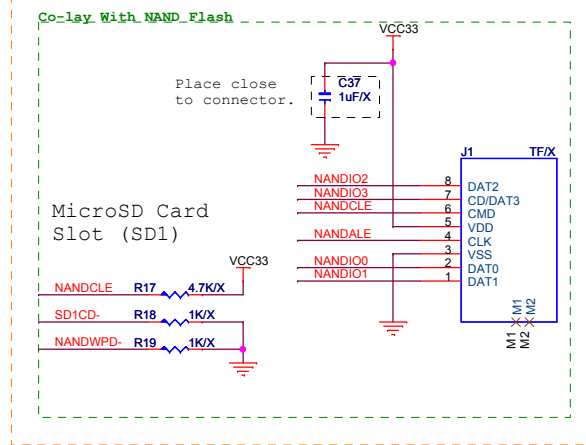
Date: Wednesday, December 29, 2010 Sheet 5 of 15

WM8650: NAND FLASH

PART: J

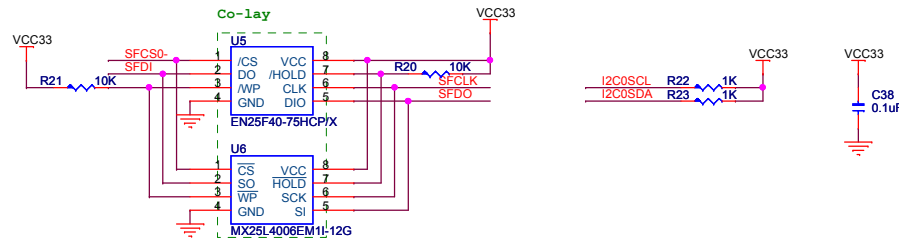
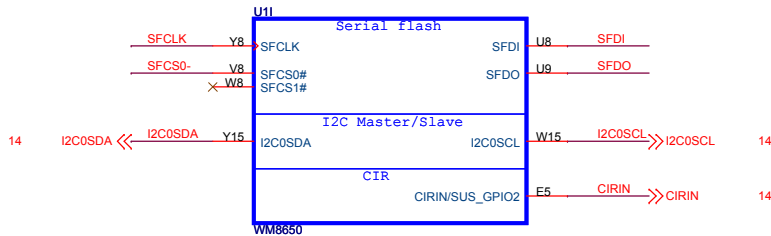


Optional



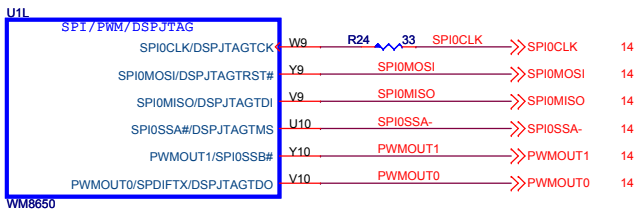
WM8650: SPI FLASH

PART: I



WM8650: SPI + PWM

PART: L



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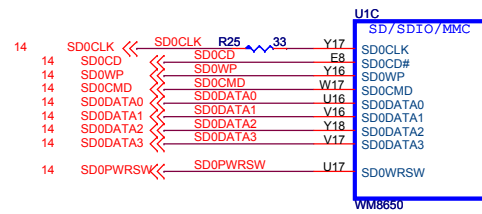
Title: **WM8650: NAND FLASH**


Size: Custom Document Number: **WMS8152A** Rev: 1.0

Date: Tuesday, December 21, 2010 Sheet 6 of 15

WM8650: SD/SDIO


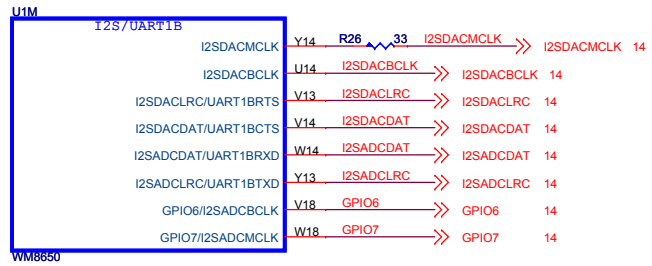
PART: C



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Title: WM8650: SD/SDIO		
Size: Custom	Document Number: WMS8152A	Rev: 1.0
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WM8650: I2S

PART: M

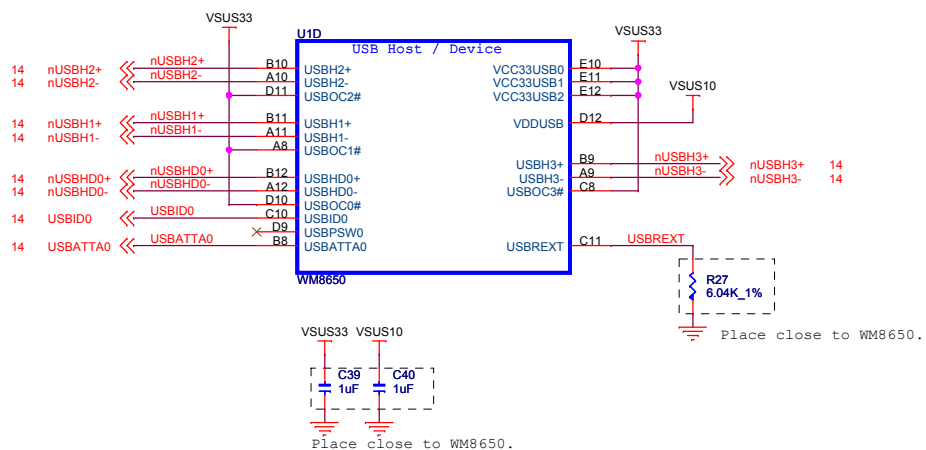


WonderMedia Technologies, Inc.

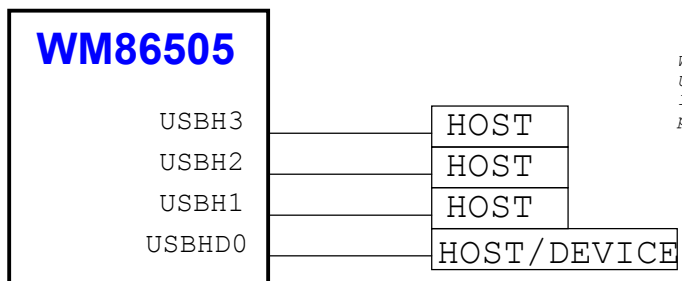
Title		WM8650: I2S
Size	Document Number	Rev
Custom	WMS8152A	1.0
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WM8505: USB (HOST+DEVICE)


PART: D



(USBH3) to
 (USBH2) to
 (USBH1) to
 (USBHD0) to USB mini-AB connector.

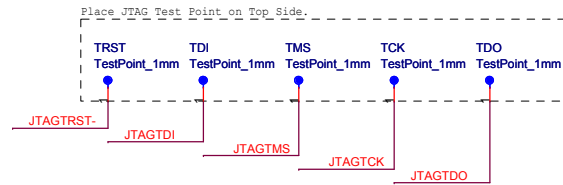
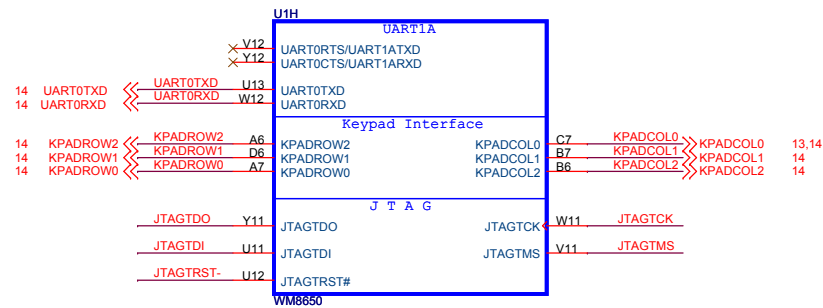


Wakeup signal (USBATTA) is asserted from external USB-Host only.
 If WM8650 at USB-Host mode, should ignore USBATTA pin

 WonderMedia Technologies, Inc.		
Title		
WM8650: USB (H/D)		
Size	Document Number	Rev
Custom	WMS8152A	1.0
Date:	Tuesday, December 21, 2010	Sheet 9 of 15

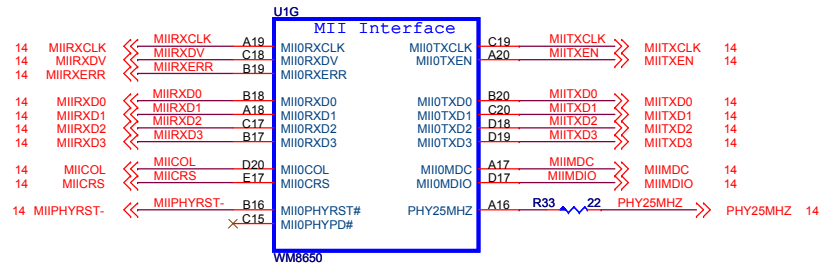
WM8650: UART/KEYPAD/JTAG

PART: H



WM8650: MII

PART: G



WonderMedia Technologies, Inc.

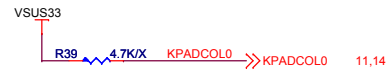
Title: **WM8650: MII**

Size	Document Number	Rev
Custom	WMS8152A	1.0

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Strapping Option

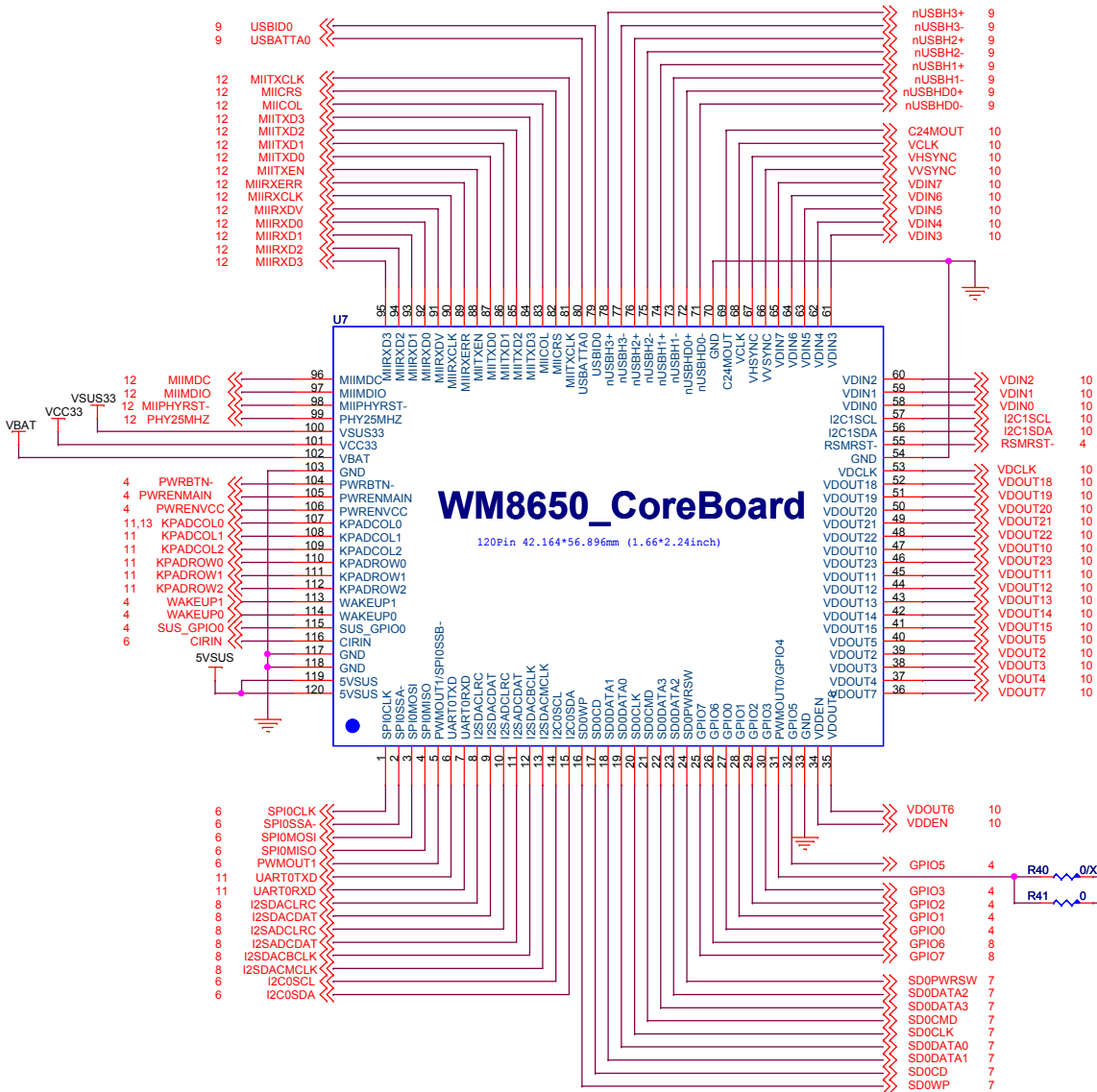
Pin	Internal Pull up/down	Default	Description
KPADCOL0	pull down	0	Inert PWRBTN# 0:debounced for a few cycles 1:kept 2s to trigger



WARNING:

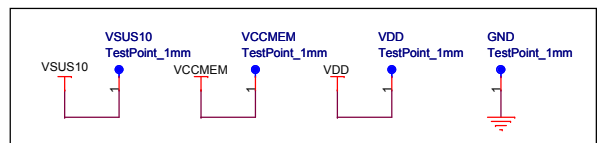
ALL others strappings not listed above recommend to use internal settings as default. Otherwise un-expected results may encounter.

 WonderMedia Technologies, Inc.		
Title Strapping Option		
Size Custom	Document Number WMS8152A	Rev 1.0
Date: Tuesday, December 21, 2010		Sheet 13 of 15



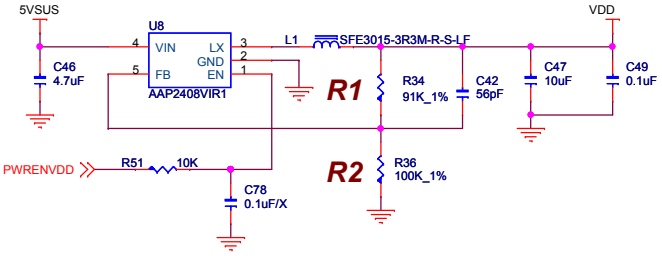
WAKEUP0	BAT_LOW Detector
WAKEUP1	DCIN_DET
PWMOUT0	LCD_BL_DIM
PWMOUT1	
GPIO0	
GPIO1	MOTOR_SHAKE
GPIO2	Touch_INT
GPIO3	G-Sensor_INT
GPIO4	
GPIO5	Charger_STAT
GPIO6	Wifi_POWER_DN
GPIO7	CAMERA_POWER_DN
SUS_GPIO0	MUTE_GPIO
I2C0	G-Sensor / I2S / VDOUT
I2C1	VDIN / Touch Panel
SPI0	
KPADCOL0	- Volume + Key
KPADCOL1	- Volume - Key
KPADROW0	- BACK Key
KPADROW1	- Menu Key

Top Side:



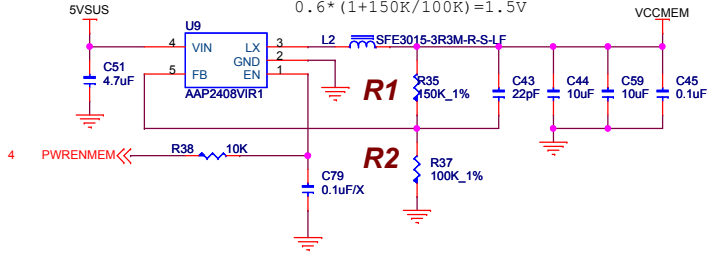
$$V_{out} = 0.6 * (1 + R1/R2)$$

$$0.6 * (1 + 91K/100K) = 1.146V$$



$$V_{out} = 0.6 * (1 + R1/R2)$$

$$0.6 * (1 + 150K/100K) = 1.5V$$



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Core Board Package

Title	Core Board Package		Rev
Size	Document Number		1.0
Customer	WMS8152A		
Date	Monday, December 27, 2010	Sheet	14 of 15

Version History

Version	Modify Content	Responsibility	Date
WMS8152A	Initial released.	Sunday Liang	2010/12/21

Power Sequence:

