
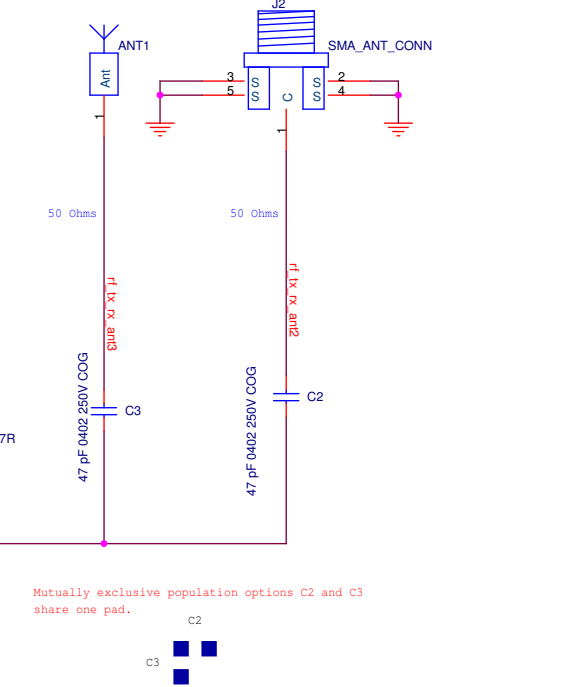
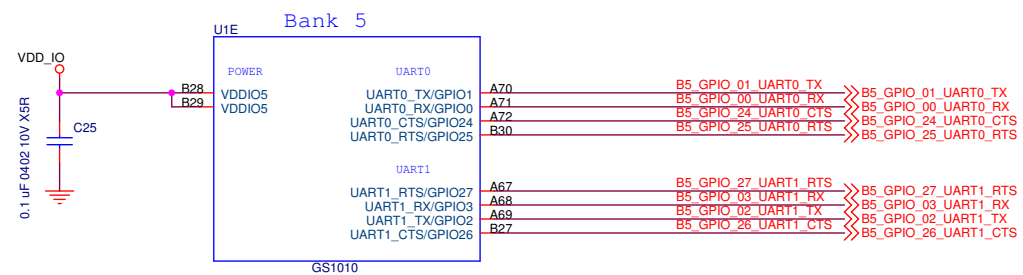
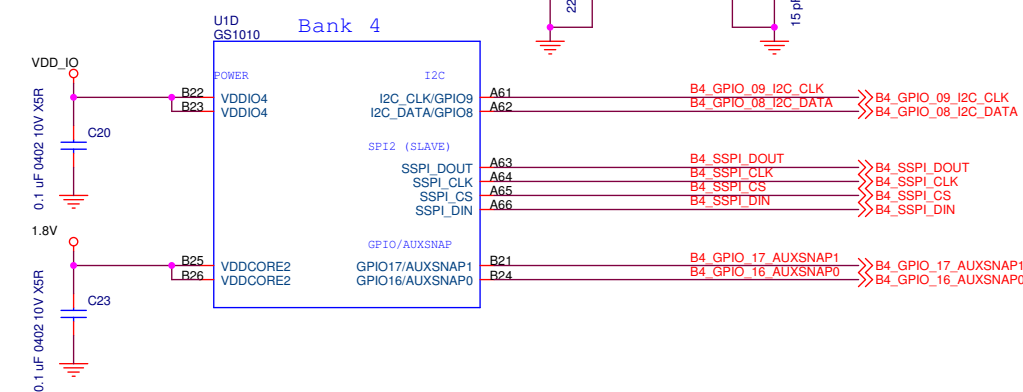
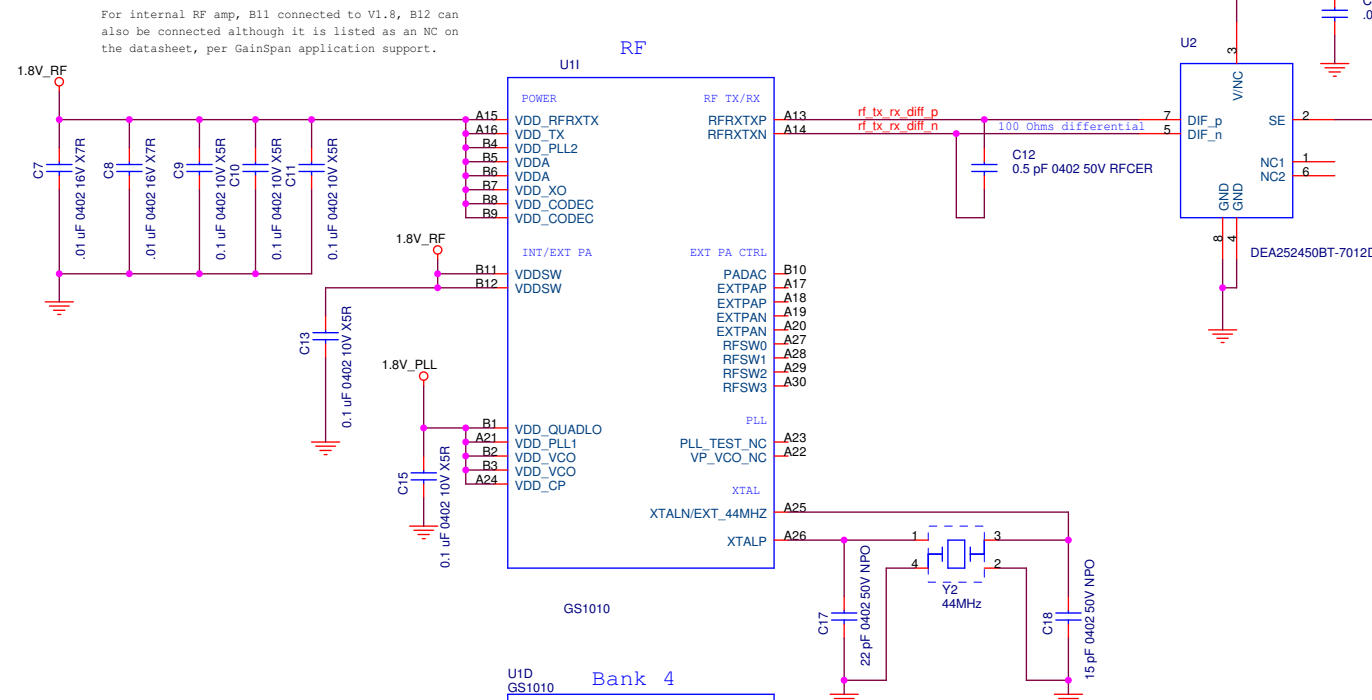
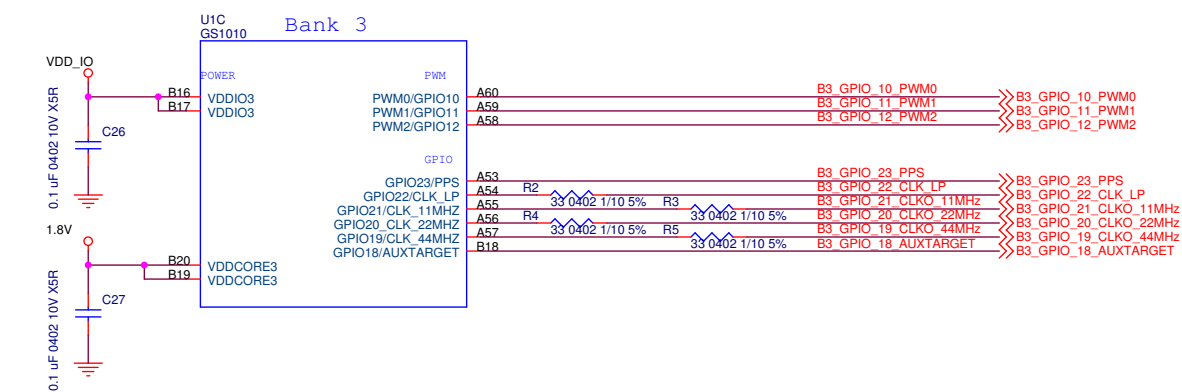
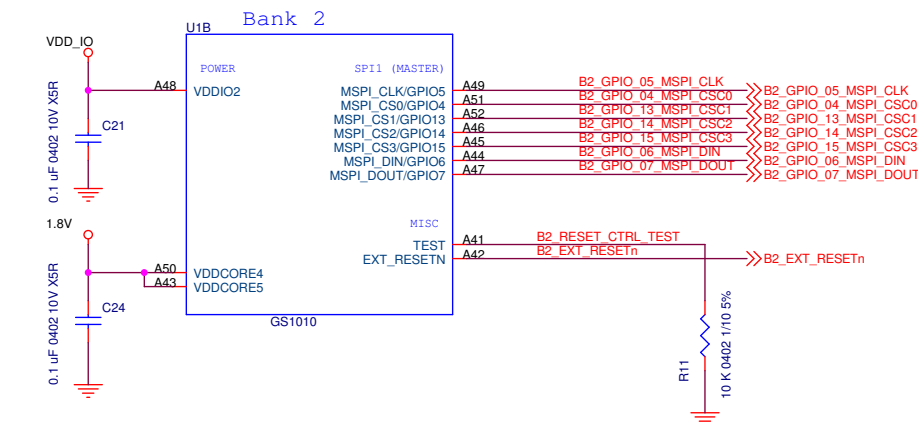
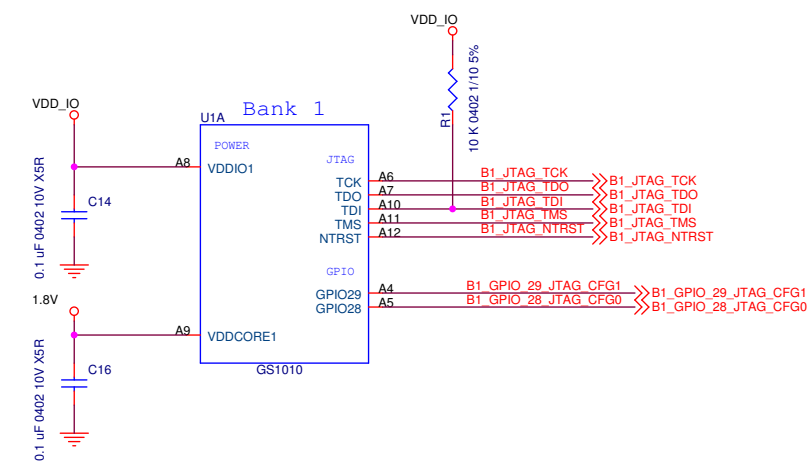
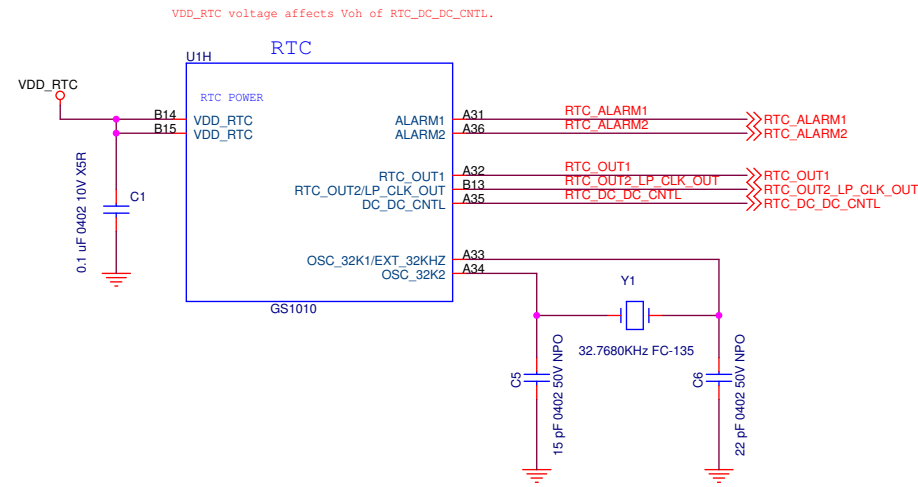


Revision History			
REV	Description	ENG	Date
A	Initial Release	MSM	04/16/2009
A.01	Fixed net names for B2_GPIO_13_MSP1_CSCL, B2_GPIO_06_MSP1_DIN A44, B2_GPIO_07_MSP1_DOUT	MSM	05/11/2009
A.02	Updated notes on C28, C2, and C3 to reflect actual reference designator after REFD resequencing.	MSM	05/22/2009
A.03	Mirrored symbol and added mounting hole graphics to physically match the 0544770808 connector.	MSM	06/15/2009
A.04	Changed L2, L3, and L4 to 0 Ohm resistor per GainSpan recommendation change.	MSM	07/12/2009

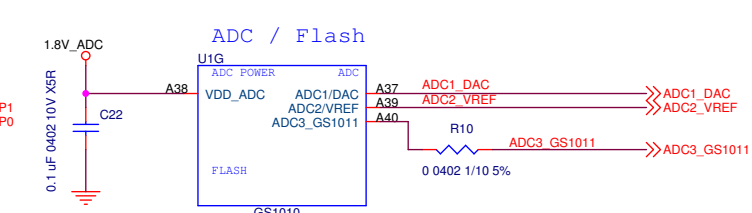
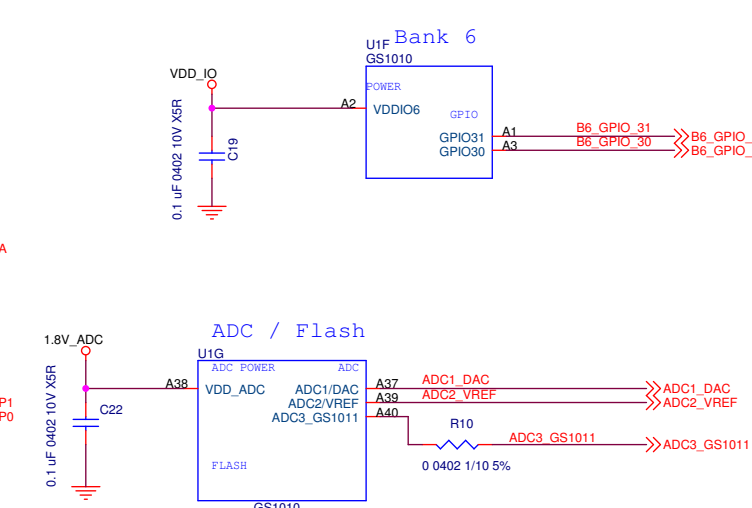
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Page	Description
2	GS1010
3	Connector and Power Supply

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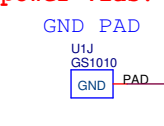


For TPS62260DRVT: R6: 360K 1%, R9: 180K 1%, C42: 22pF, R7: DNS

For TPS62261DRVT: R6: 0, R9: DNS, C42: DNS, R7: DNS



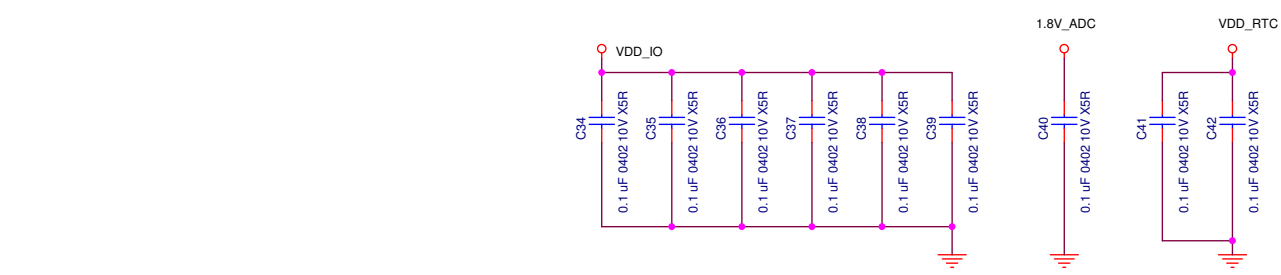
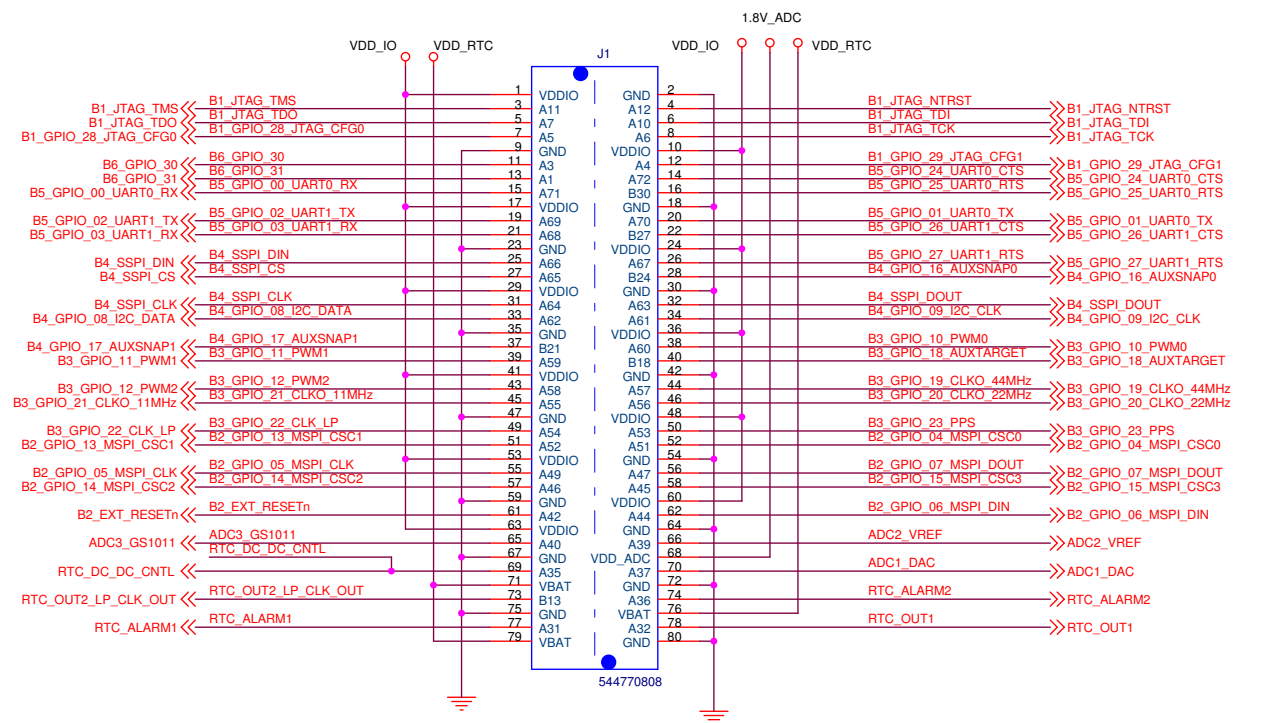
Connection from this pad to the plane must be beefy! Either big power via, or several smaller power vias.



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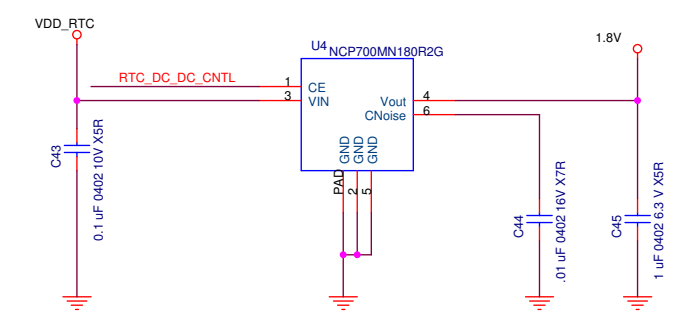
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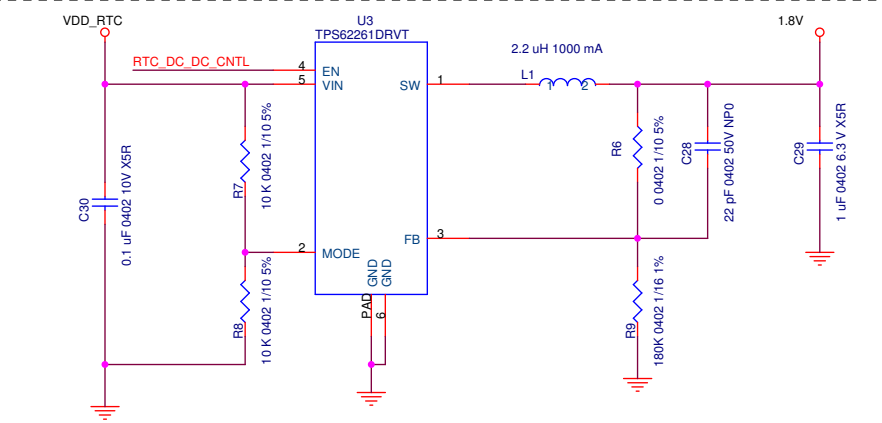
Place these capacitors near J1.

Linear Regulator option: In lieu of switching regulator

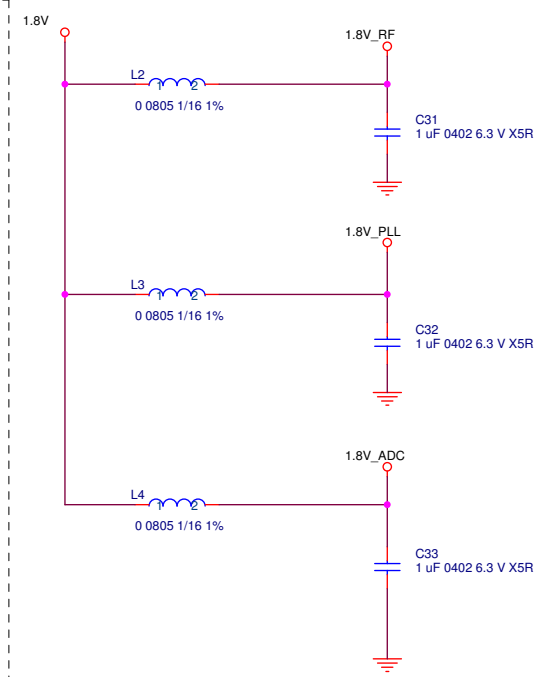


Connect PAD to large copper area. The large copper area will not have solder mask, but there will be a thin solder mask barrier between the PAD and the large copper area. This large copper area will serve to dissipate heat. This linear regulator circuit to be placed on the solder side of the card, and shall not interfere with the VDDIO plane between the GS101 and J1.

Switching Regulator



- | | |
|-------------------|-------------------|
| Adjustable | Fixed |
| For TPS62260DRVT: | For TPS62261DRVT: |
| R6: 360K 1% | R6: 0 |
| R9: 180K 1% | R9: DNS |
| C28: 22pF | C28: DNS |
| R7: DNS | R7: DNS |



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