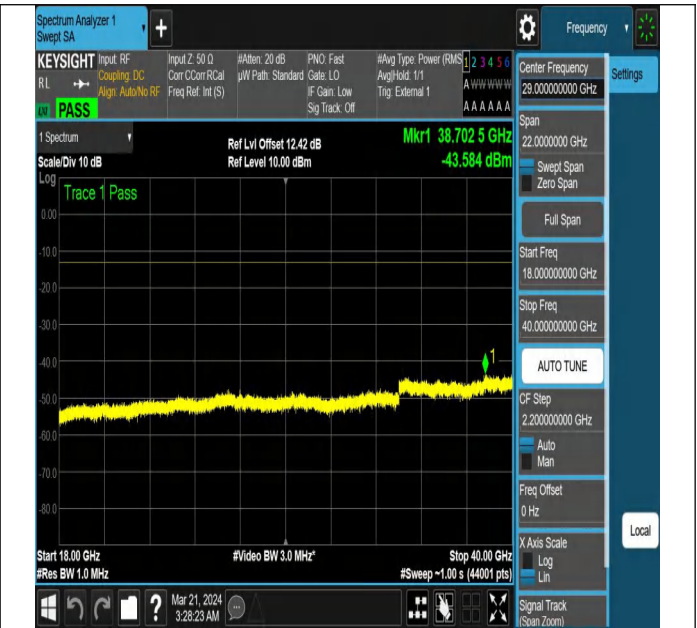
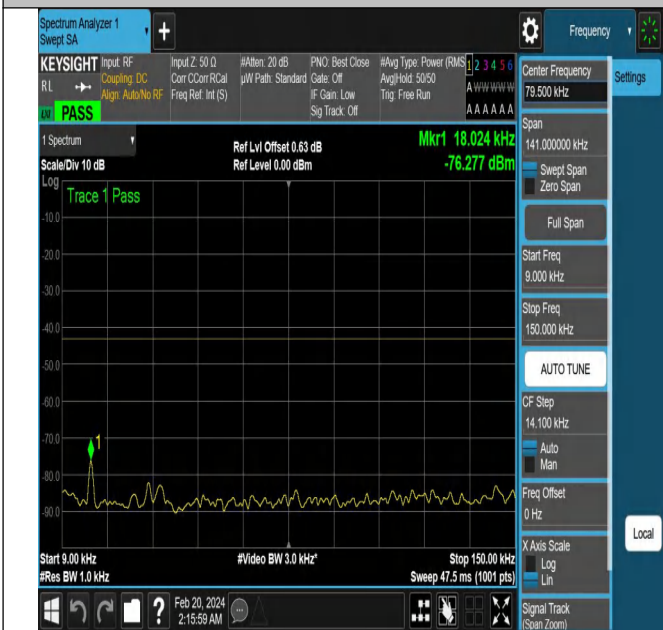


NTNV-N78-3700-3800-PC2-30-100-M-DFT-PI2BPSK-Edge\_1  
 RB\_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-M-DFT-PI2BPSK-Edge\_1  
 RB\_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-M-DFT-PI2BPSK-Edge\_1  
 RB\_Right-Ant1-PASS

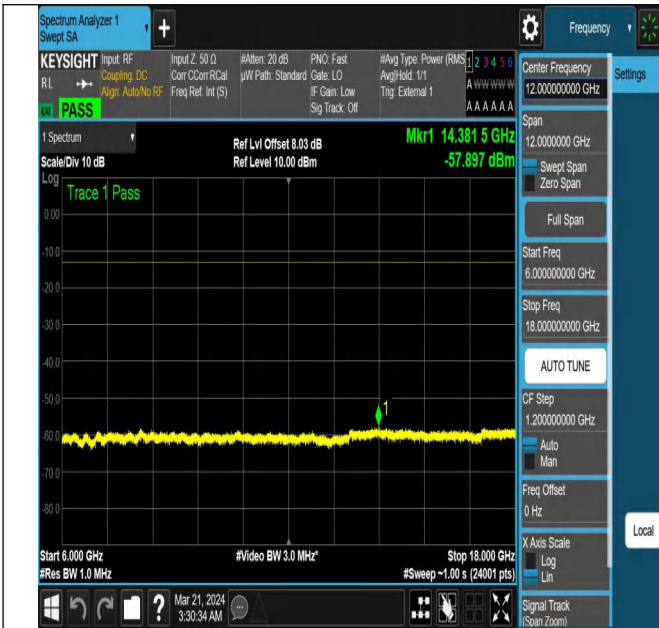


NTNV-N78-3700-3800-PC2-30-100-M-DFT-PI2BPSK-Edge\_1  
 RB\_Right-Ant1-PASS

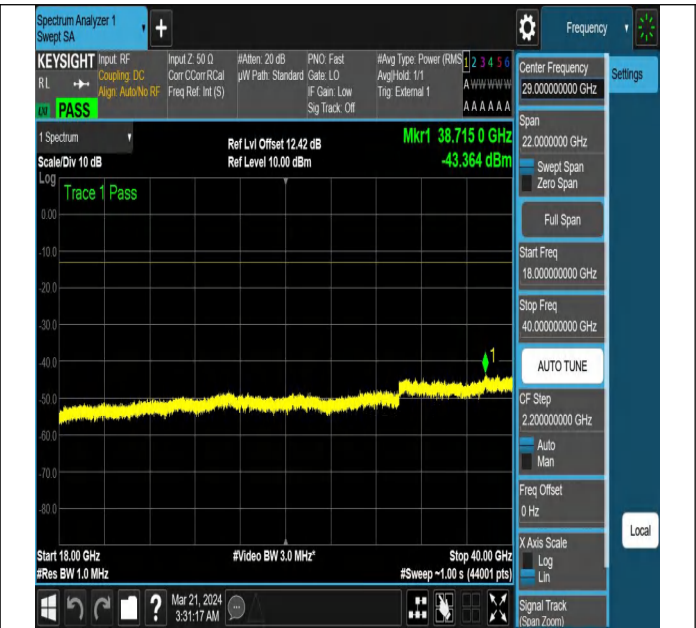




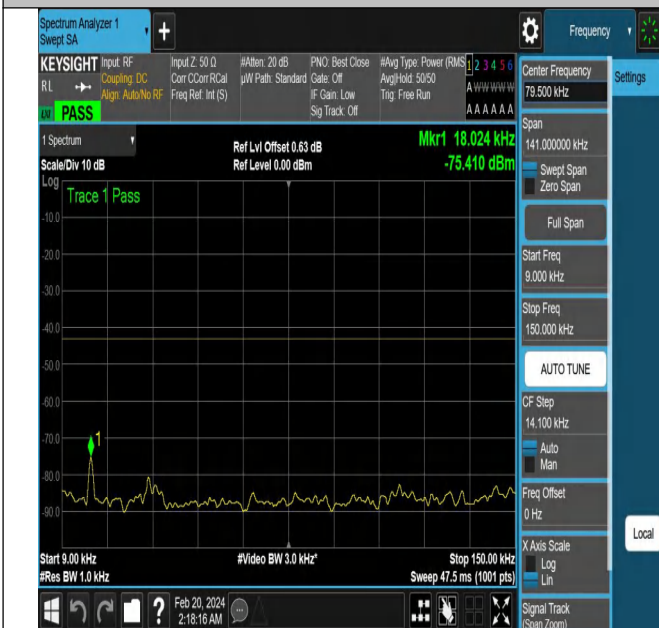




NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK-Edge\_1RB  
\_Left-Ant1-PASS



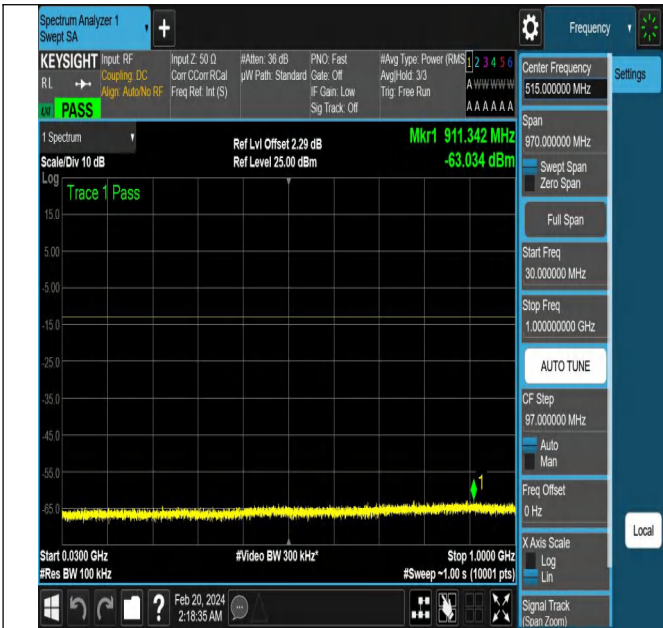
NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK-Edge\_1RB  
\_Left-Ant1-PASS



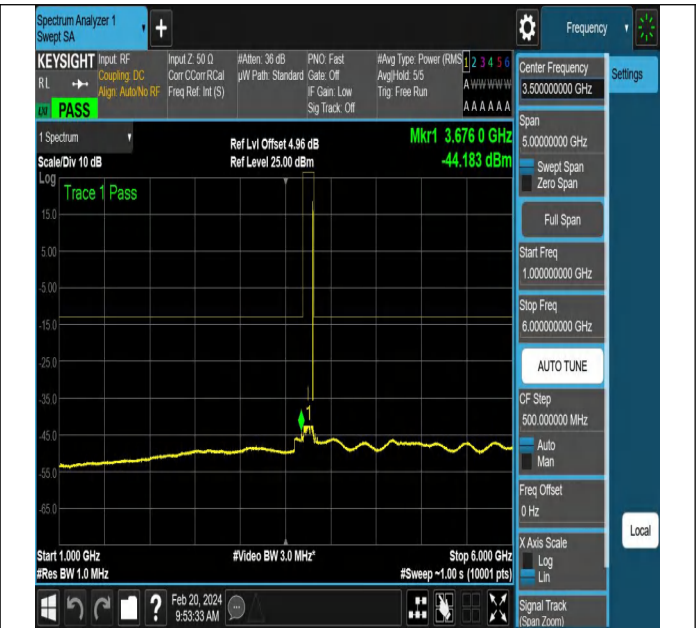
NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



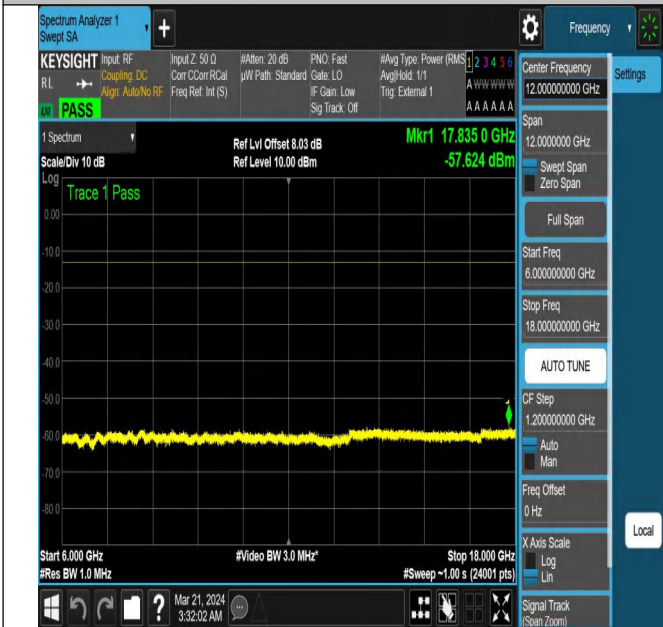
NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



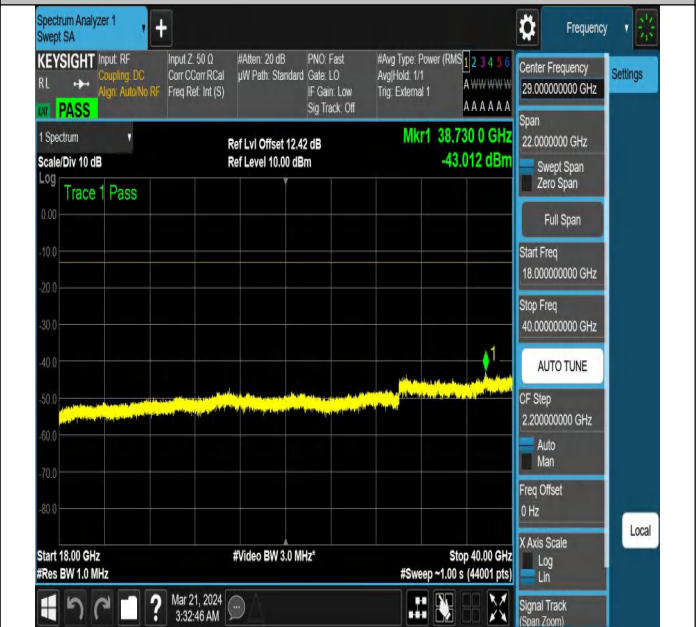
NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



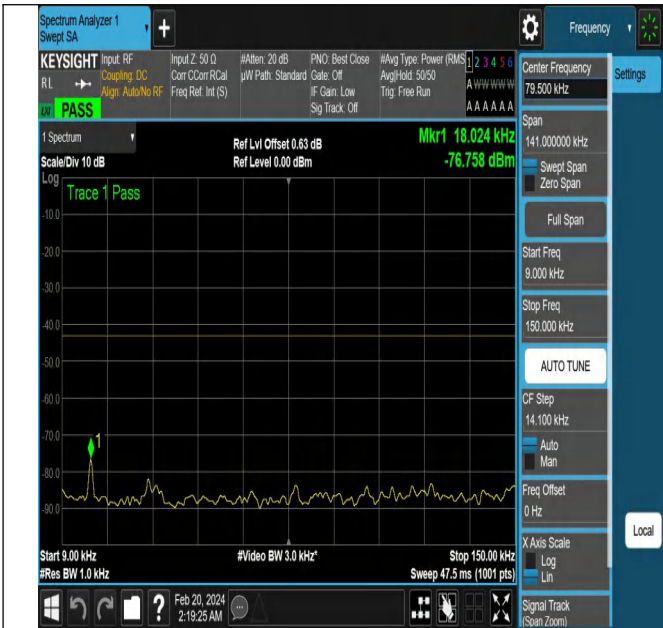
NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



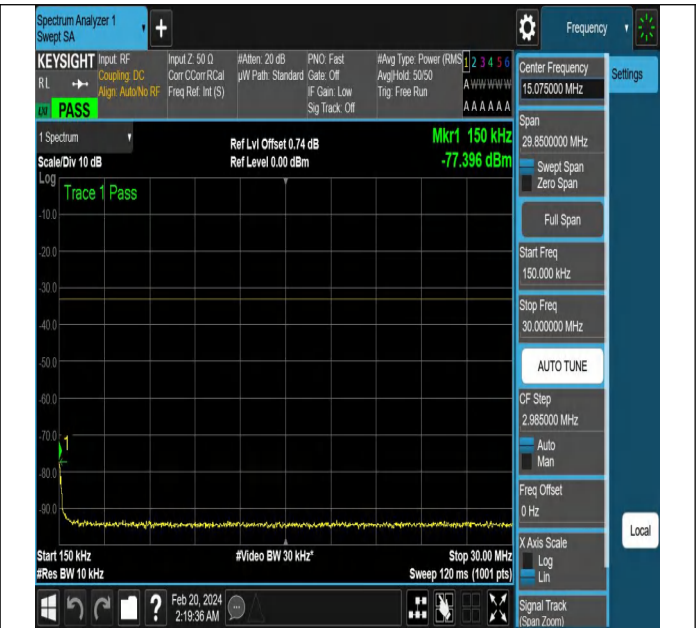
NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-H-DFT-PI2BPSK-Edge\_1  
RB\_Left-Ant1-PASS



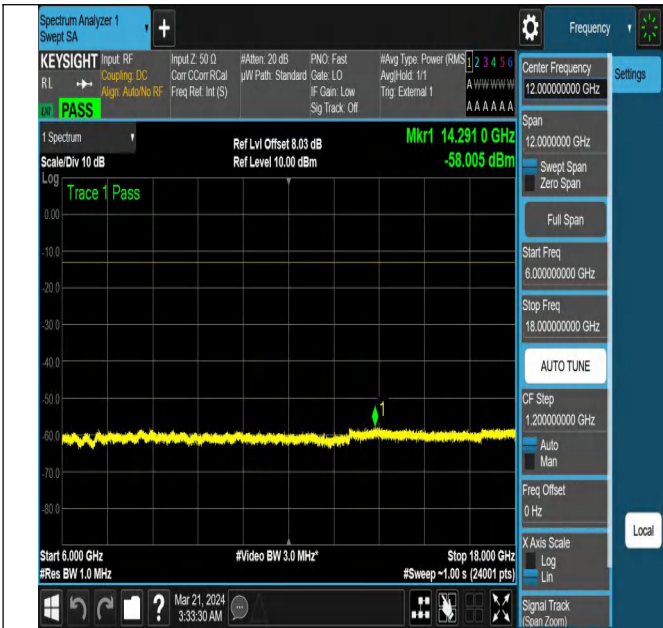
NTNV-N78-3700-3800-PC2-30-100-H-DFT-PI2BPSK-Edge\_1  
RB\_Left-Ant1-PASS



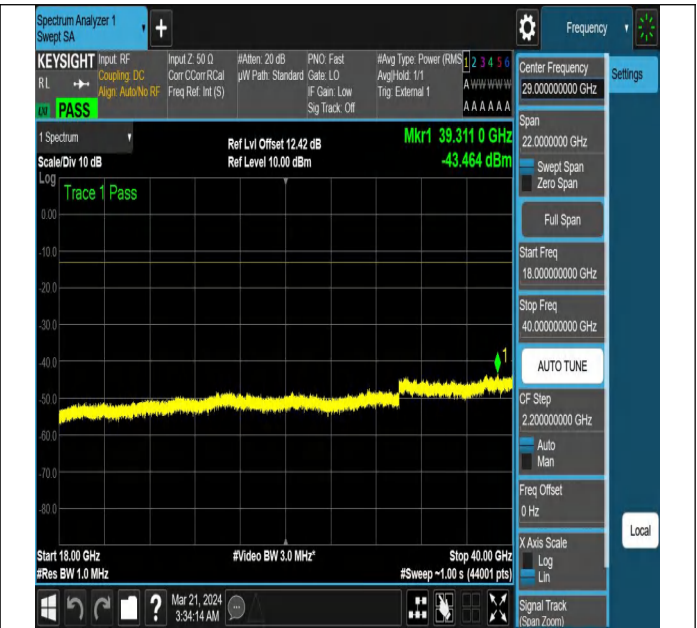
NTNV-N78-3700-3800-PC2-30-100-H-DFT-PI2BPSK-Edge\_1  
RB\_Left-Ant1-PASS



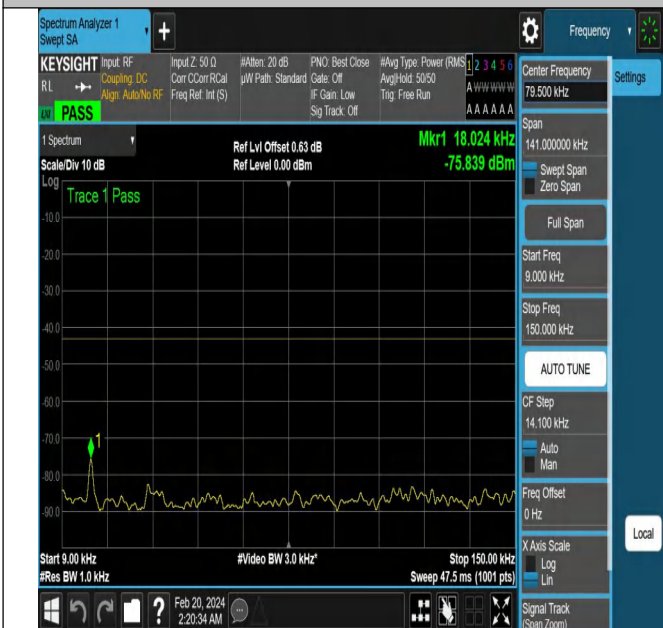
NTNV-N78-3700-3800-PC2-30-100-H-DFT-PI2BPSK-Edge\_1  
RB\_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-H-DFT-PI2BPSK-Edge\_1  
 RB\_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-H-DFT-PI2BPSK-Edge\_1  
 RB\_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-H-DFT-PI2BPSK-Edge\_1  
 RB\_Right-Ant1-PASS



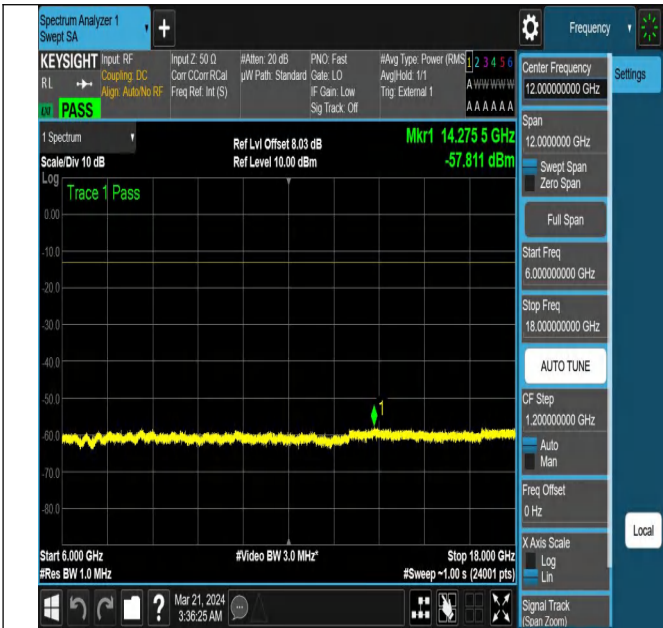
NTNV-N78-3700-3800-PC2-30-100-H-DFT-PI2BPSK-Edge\_1  
 RB\_Right-Ant1-PASS



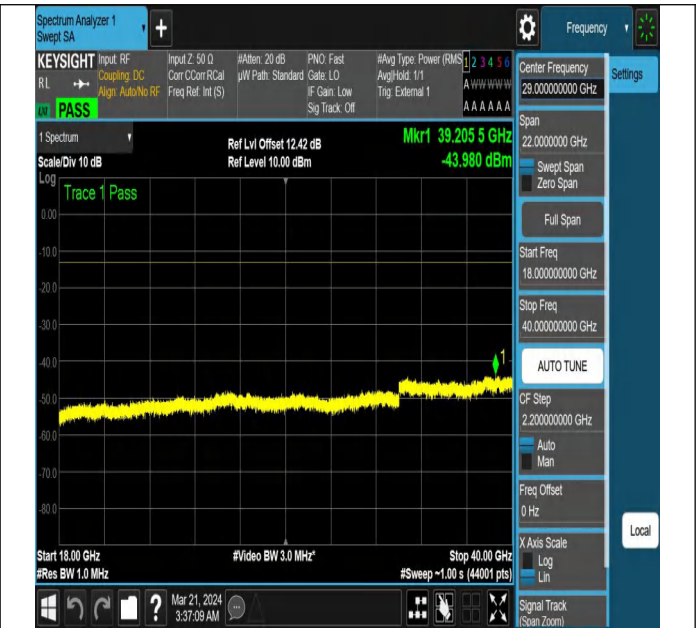




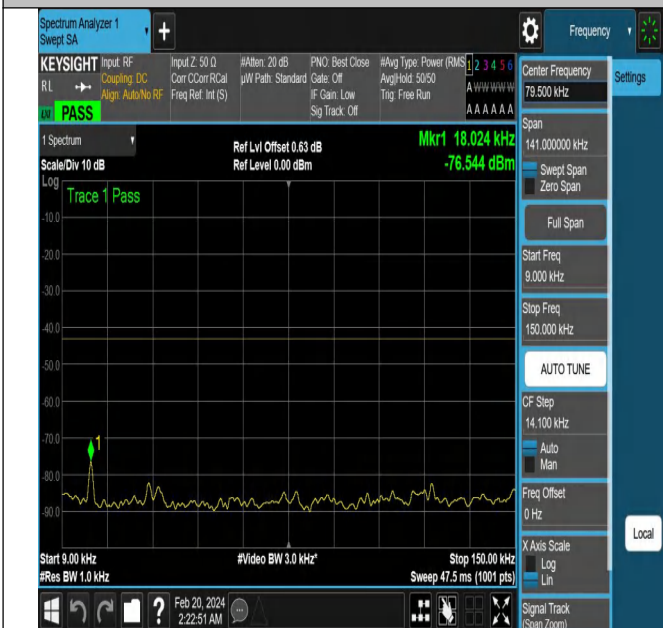




NTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-Edge\_1RB  
\_Left-Ant1-PASS



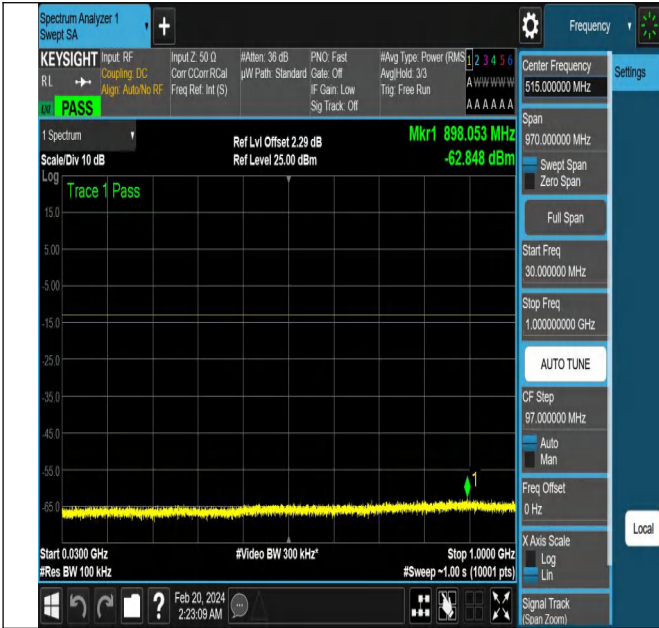
NTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-Edge\_1RB  
\_Left-Ant1-PASS



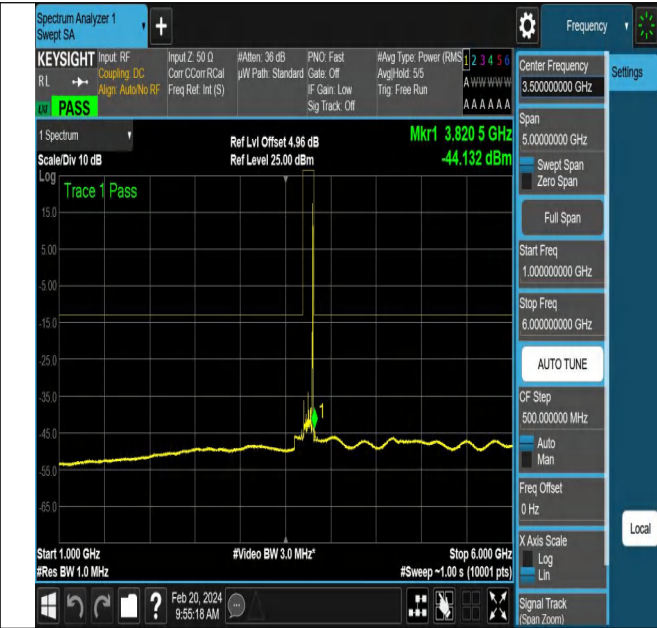
NTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



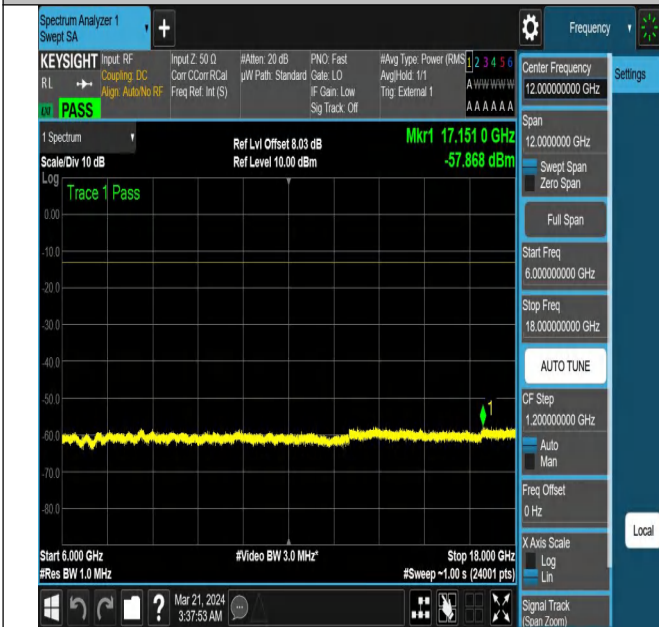
NTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



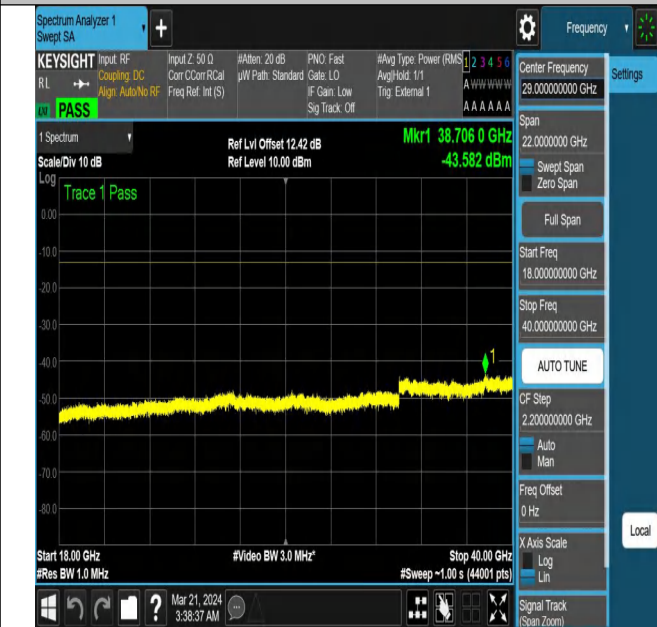
NTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-Edge\_1RB  
\_Right-Ant1-PASS



## Frequency Stability for SA

### Test Result

#### Frequency Error VS. Voltage

Voltage										
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	VL	NT	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	VN	NT	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	VH	NT	-999.000000	-0.266400	PASS

#### Frequency Error VS. Temperature

Voltage										
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	-30	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	-20	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	-10	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	0	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	10	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	20	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	30	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	40	-999.000000	-0.266400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_FuII	NV	50	-999.000000	-0.266400	PASS