

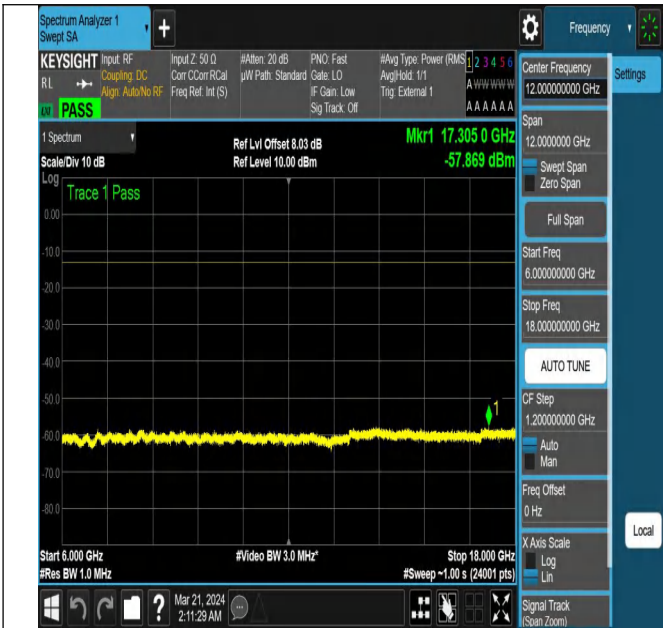
NTNV-N78-3700-3800-PC2-30-50-L-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS

NTNV-N78-3700-3800-PC2-30-50-L-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS

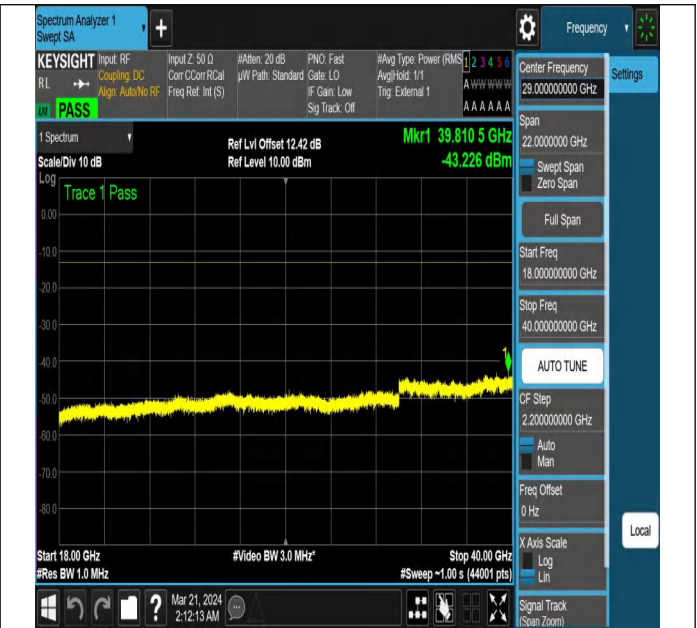


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B_Left-Ant1-PASS

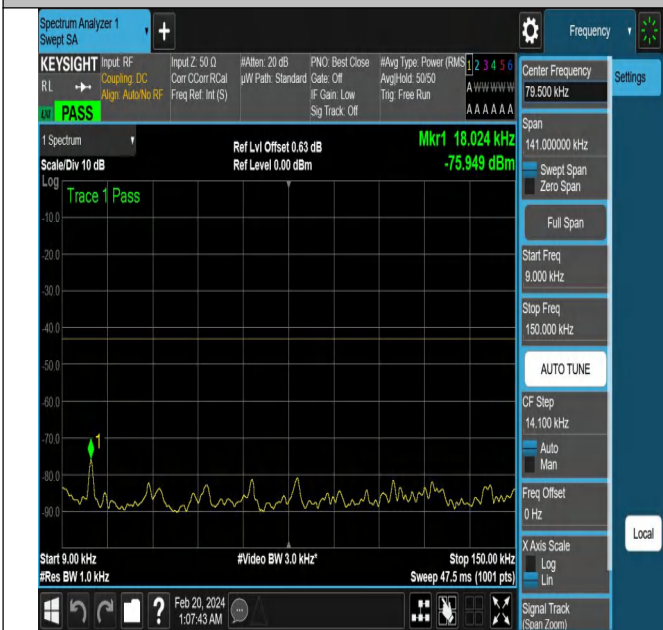
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B_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-L-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS



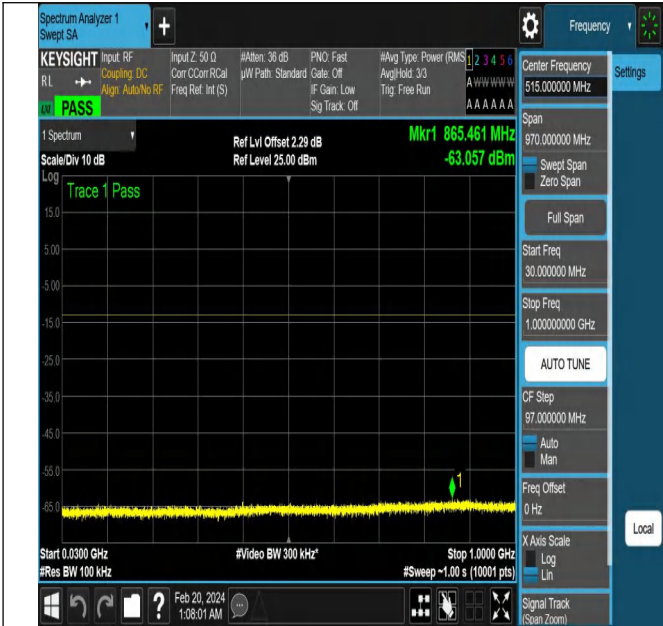
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B_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-L-DFT-PI2BPSK-Edge_1R
B_Right-Ant1-PASS

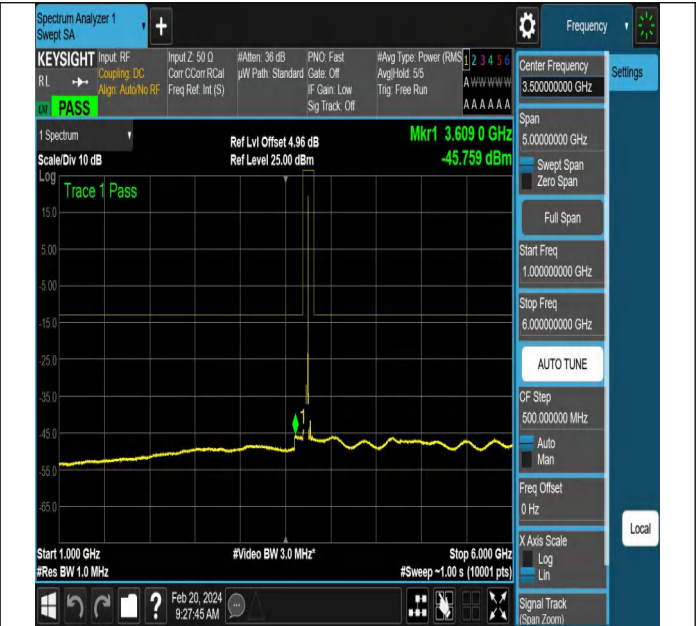


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B_Right-Ant1-PASS



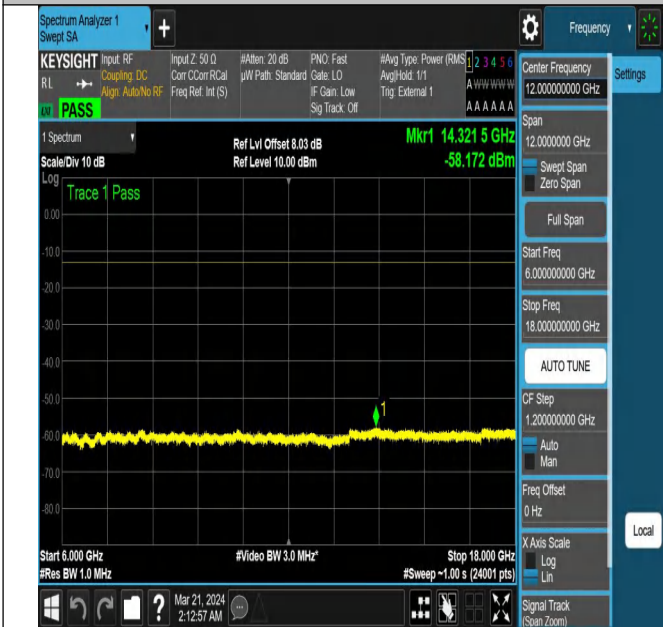
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B_Right-Ant1-PASS



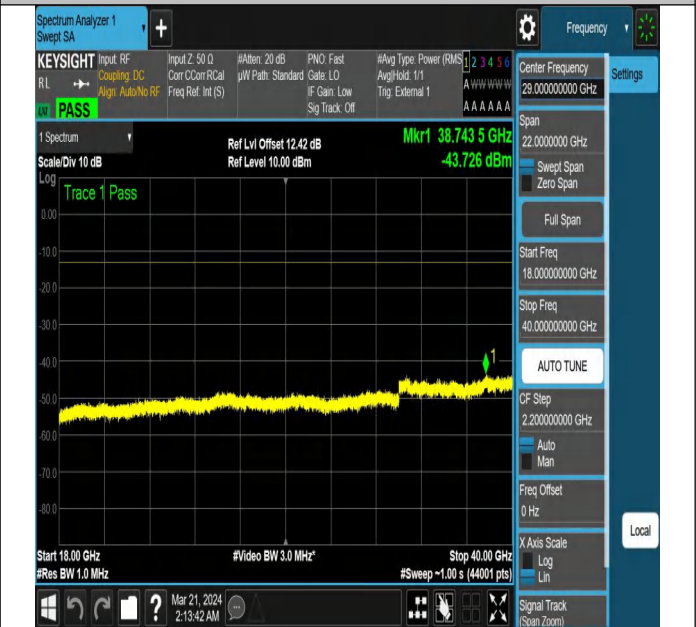
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B_Right-Ant1-PASS



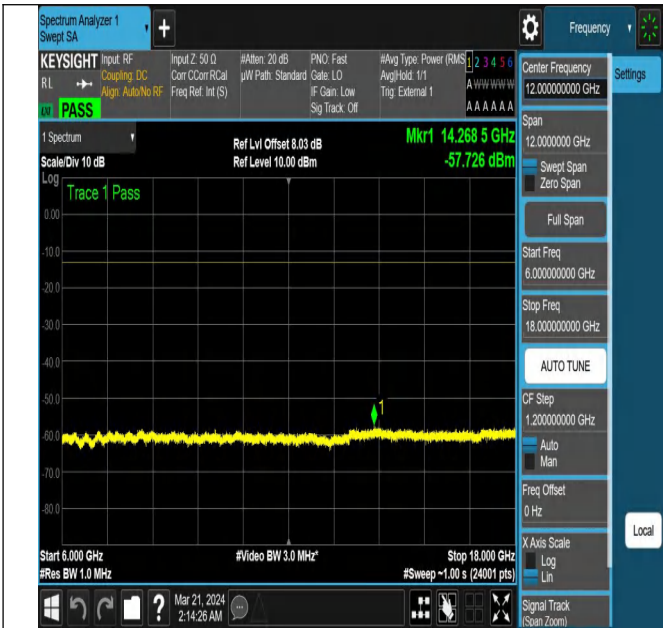
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B_Right-Ant1-PASS

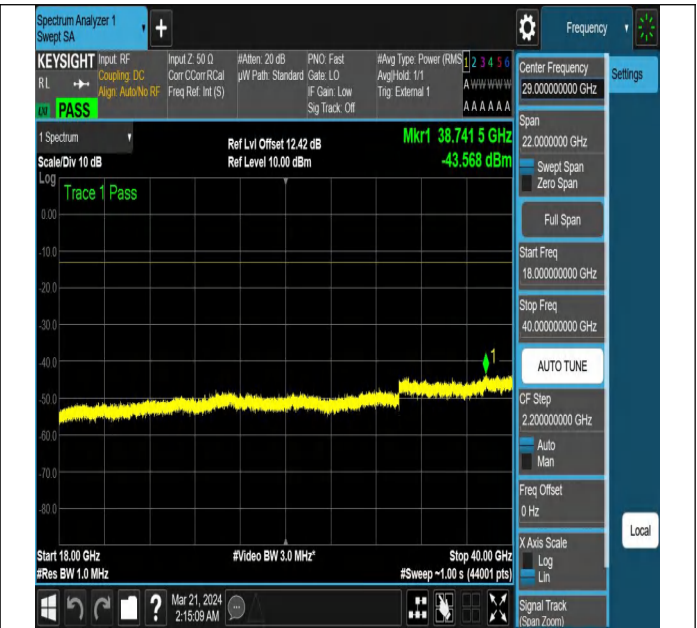


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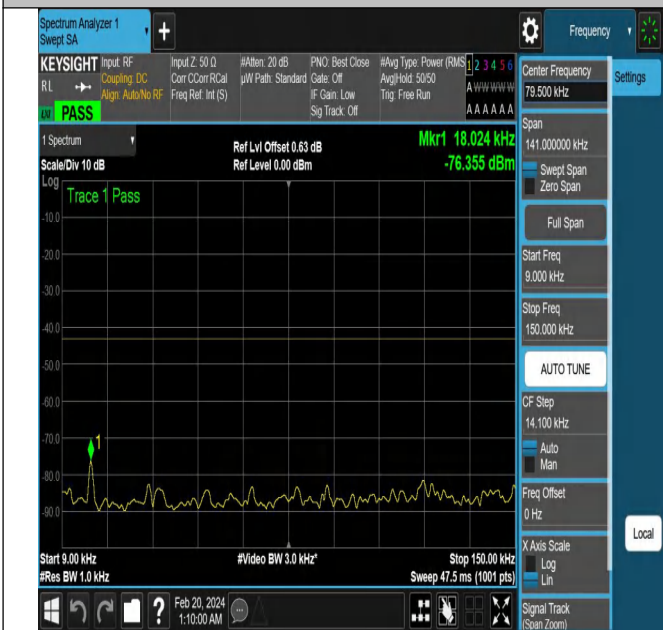
B_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-L-DFT-QPSK-Edge_1RB_L
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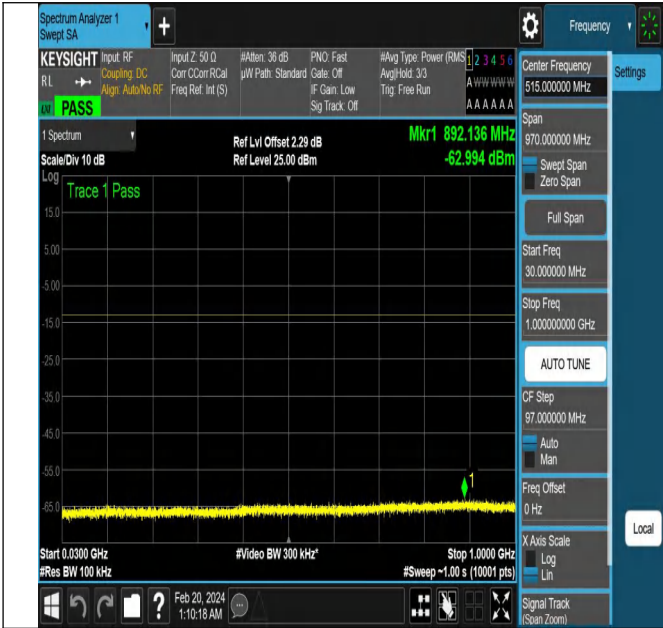
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 eft-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-L-DFT-QPSK-Edge_1RB_
 Right-Ant1-PASS

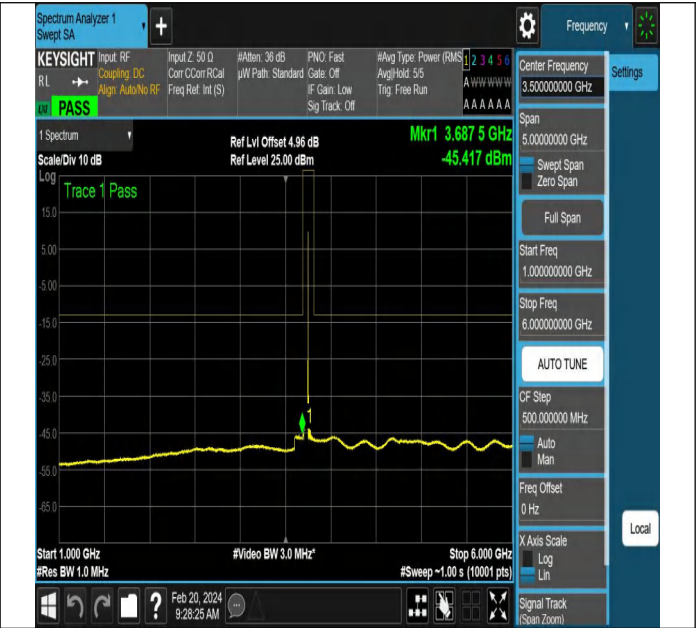


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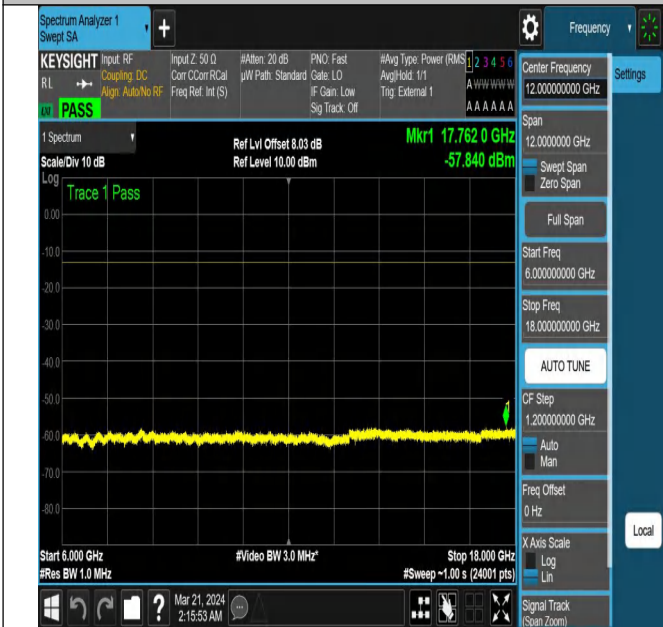
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Right-Ant1-PASS



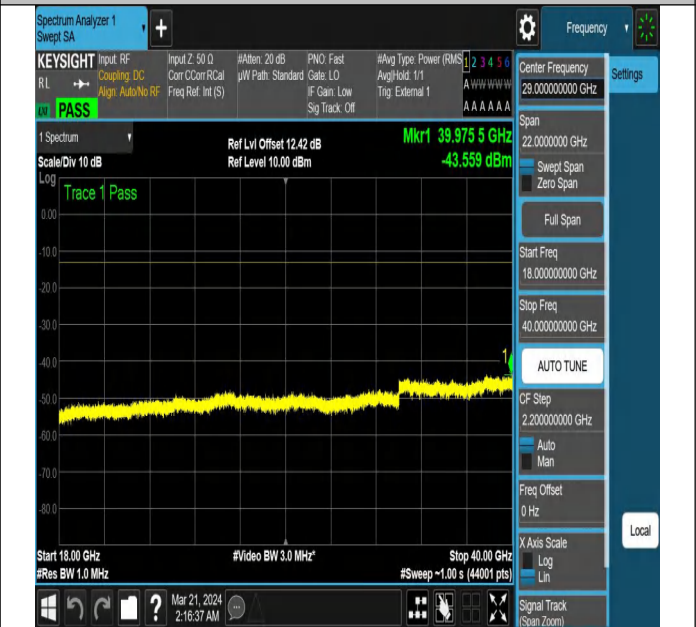
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Right-Ant1-PASS



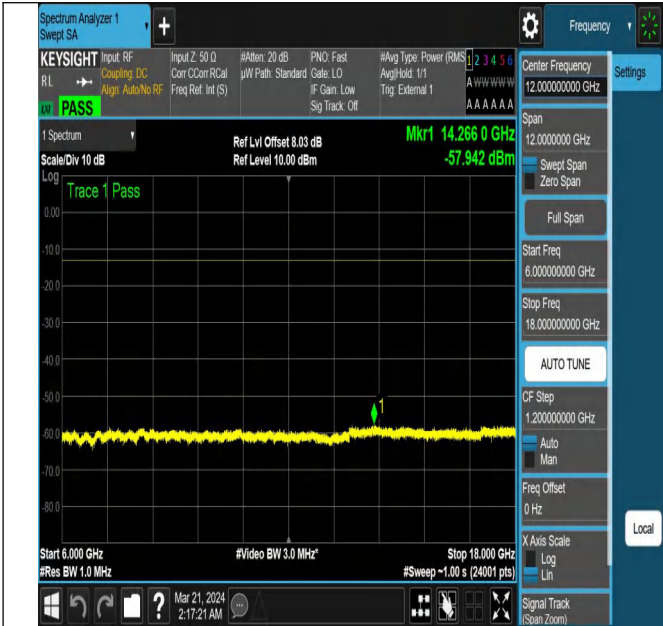
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Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-L-DFT-QPSK-Edge_1RB_

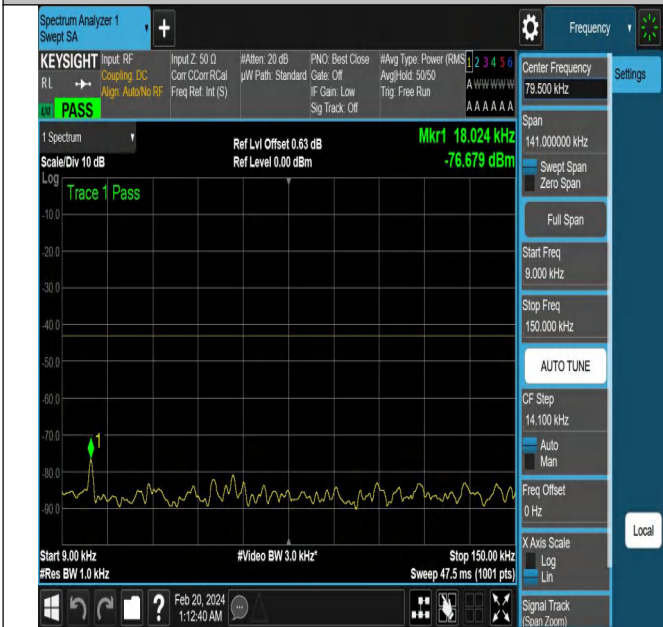
Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-M-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS



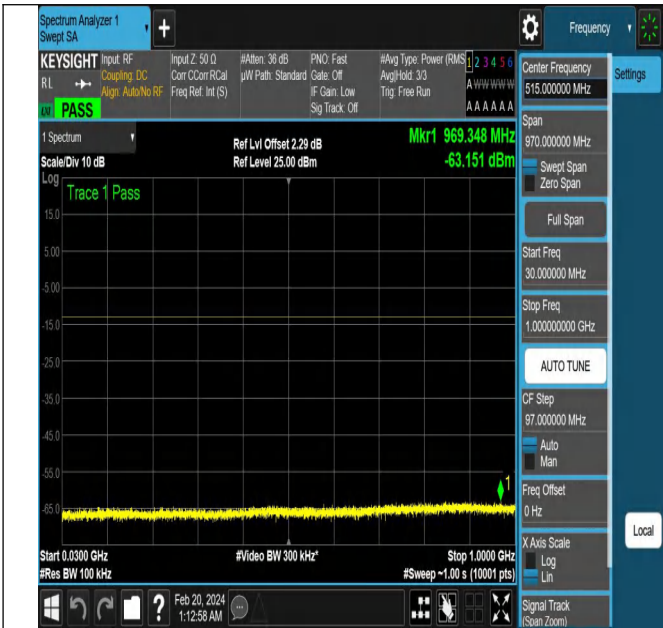
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B_Left-Ant1-PASS



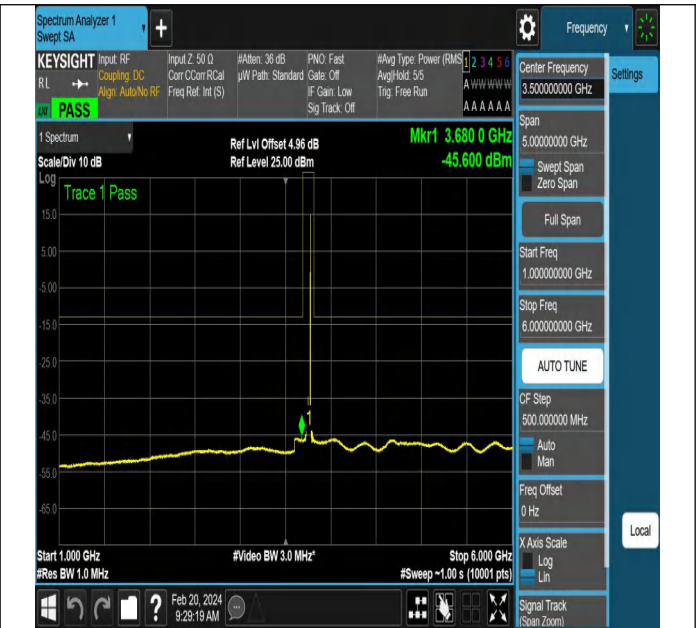
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B_Right-Ant1-PASS



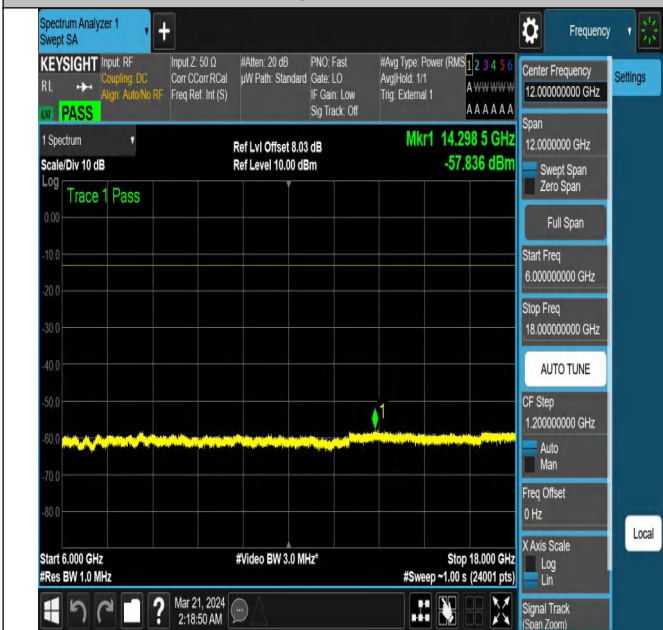
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B_Right-Ant1-PASS



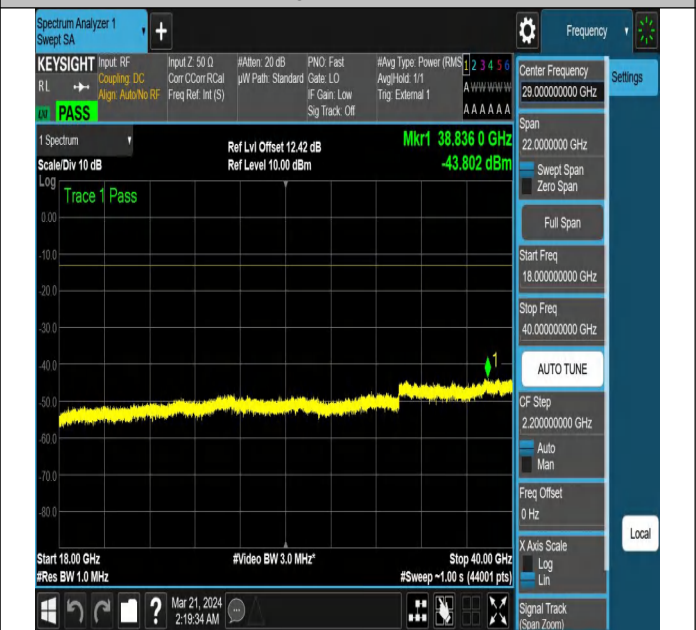
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B_Right-Ant1-PASS



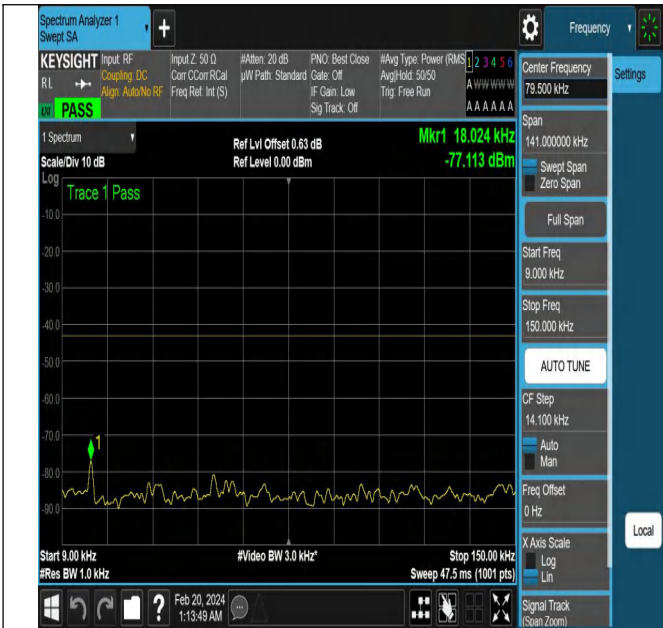
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B_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-M-DFT-PI2BPSK-Edge_1R
B_Right-Ant1-PASS



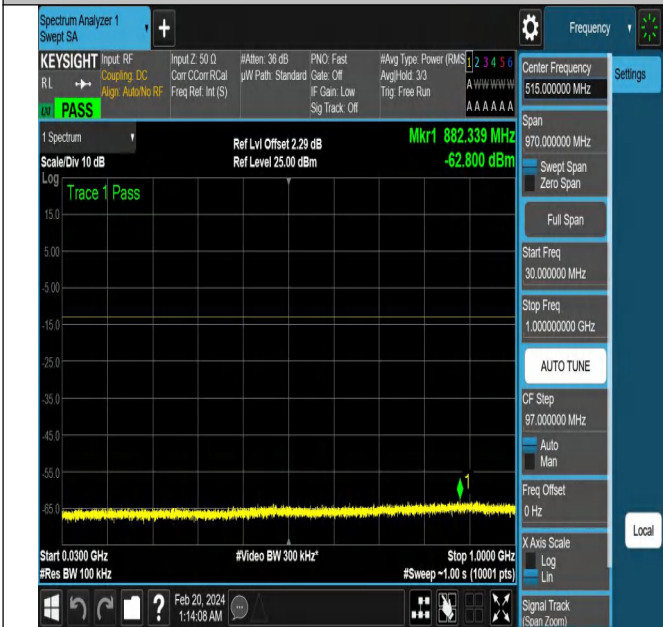
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B_Right-Ant1-PASS



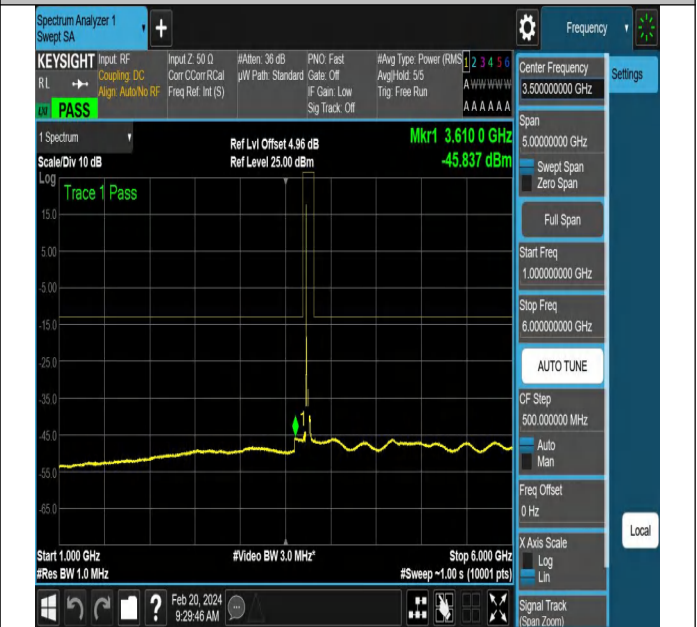
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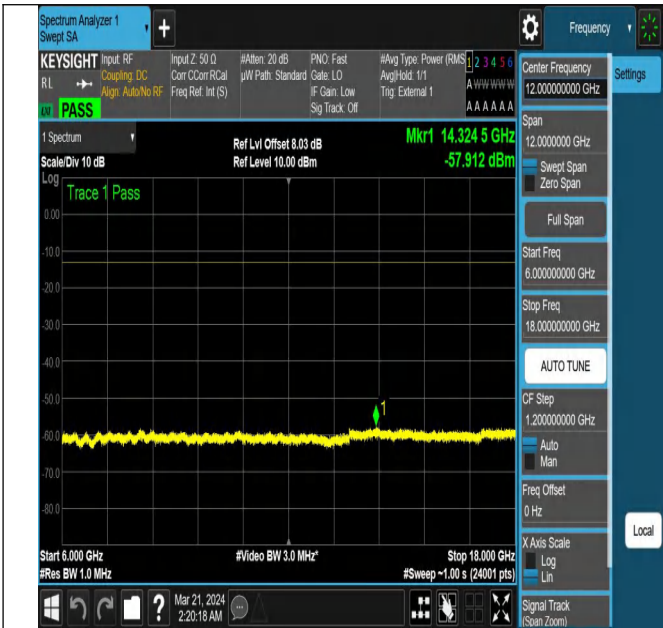
NTNV-N78-3700-3800-PC2-30-50-M-DFT-QPSK-Edge_1RB_ Left-Ant1-PASS



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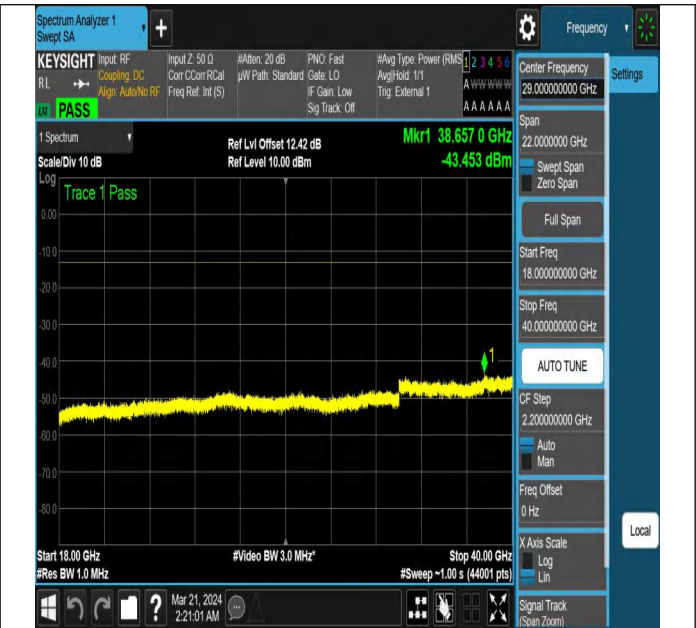


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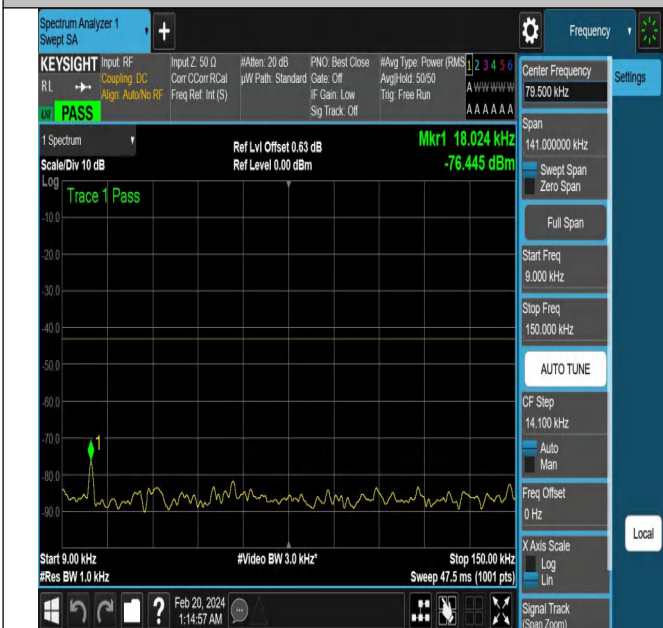
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Left-Ant1-PASS



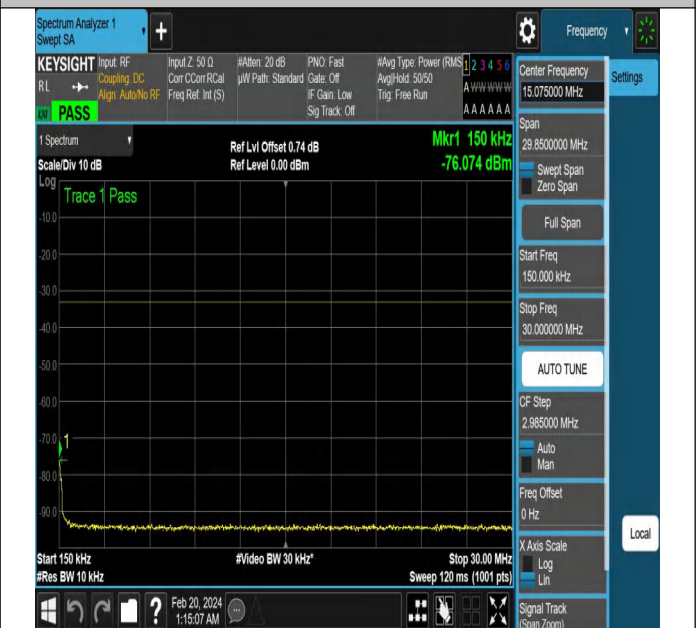
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Left-Ant1-PASS



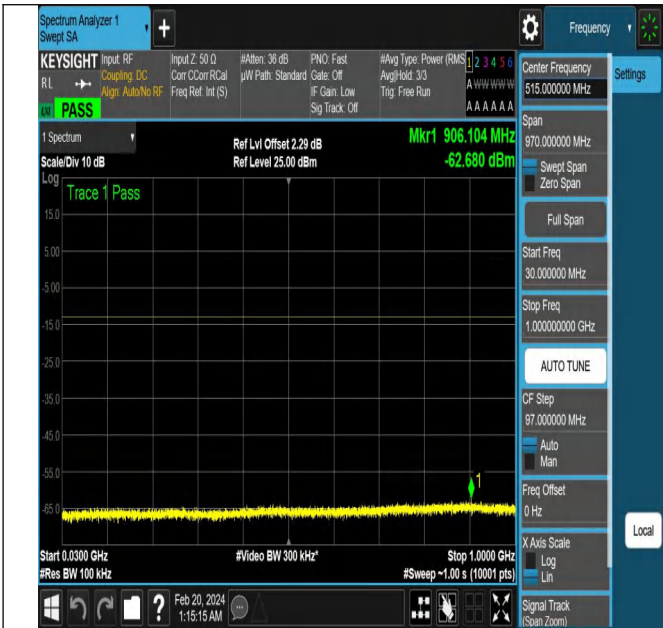
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Right-Ant1-PASS

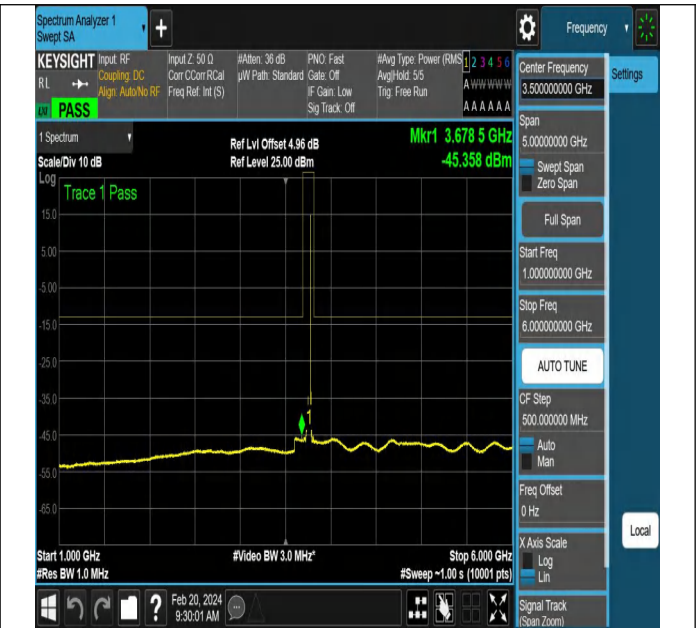


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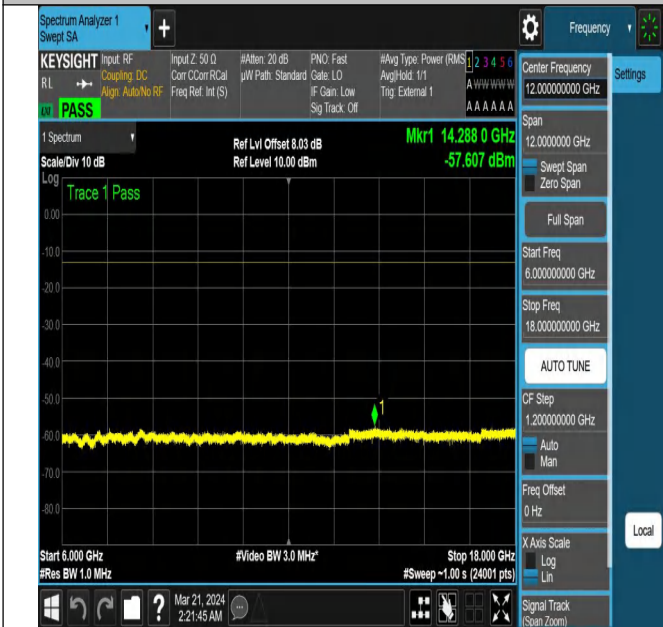
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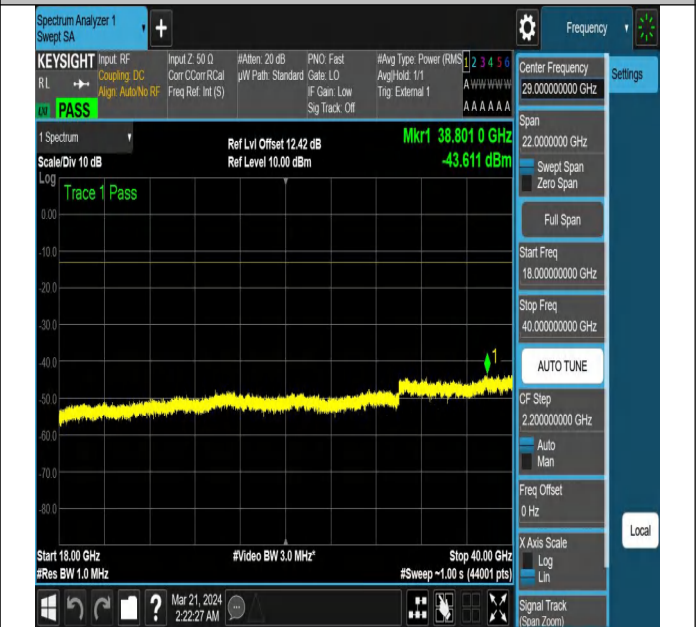
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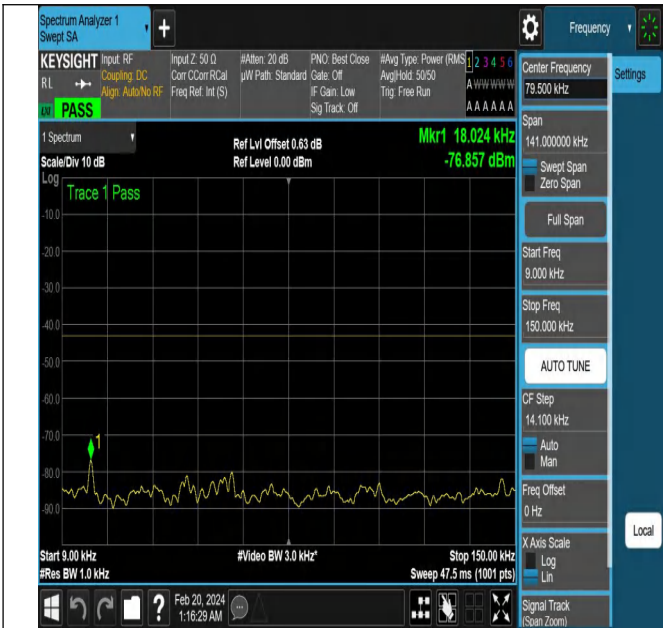
NTNV-N78-3700-3800-PC2-30-50-M-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-M-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-M-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



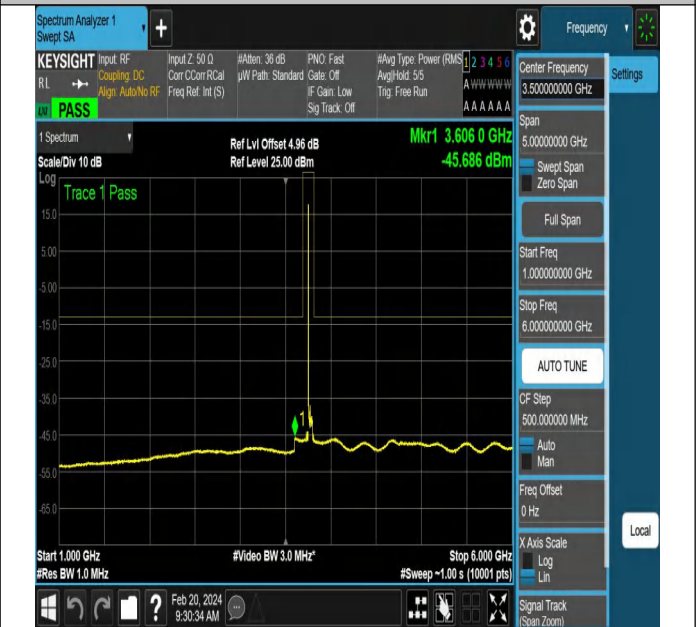
NTNV-N78-3700-3800-PC2-30-50-H-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS



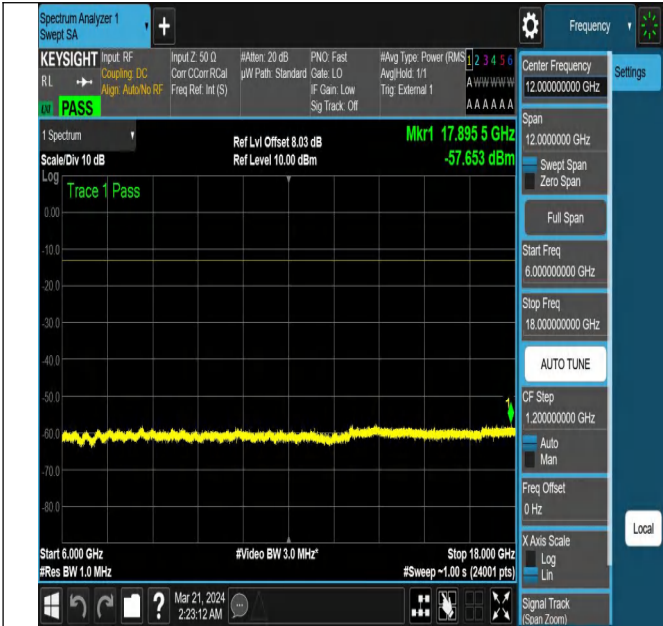
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B_Left-Ant1-PASS



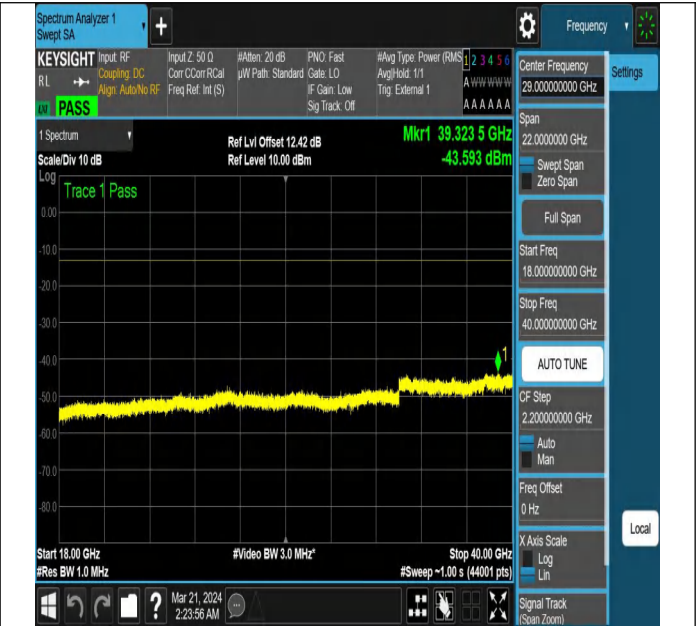
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B_Left-Ant1-PASS



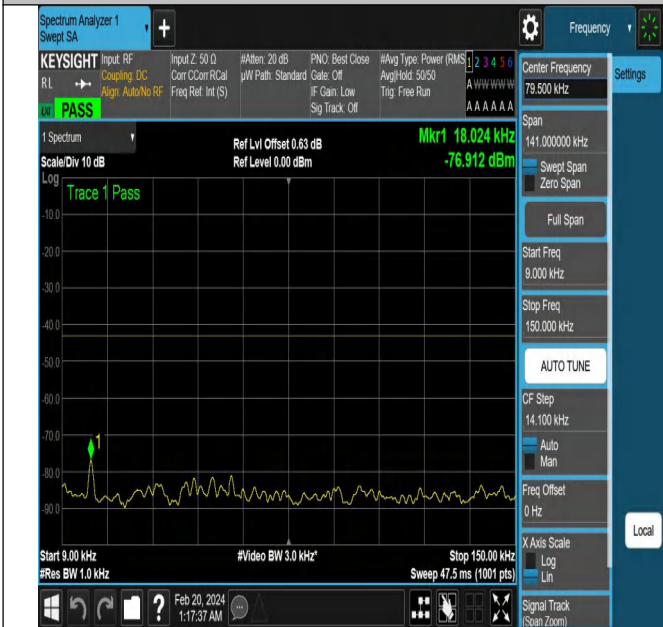
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B_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-H-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS



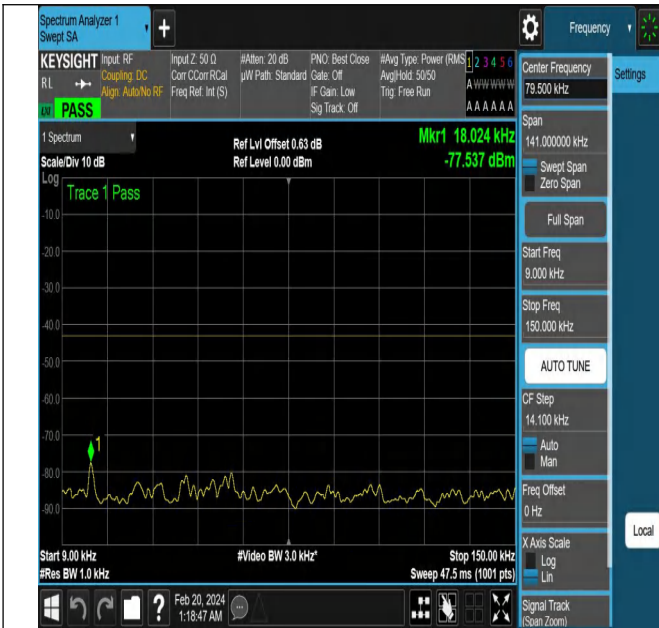
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B_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-H-DFT-PI2BPSK-Edge_1R
B_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-H-DFT-PI2BPSK-Edge_1R
B_Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-H-DFT-QPSK-Edge_1RB_

Left-Ant1-PASS



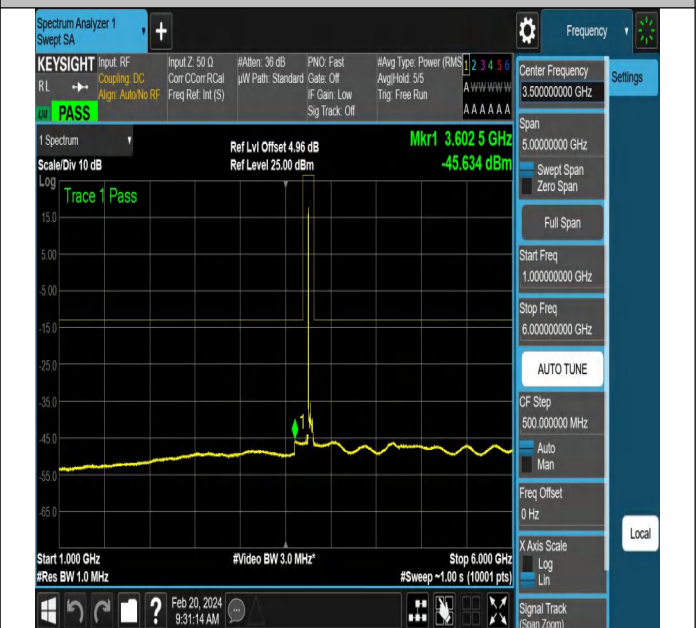
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Left-Ant1-PASS



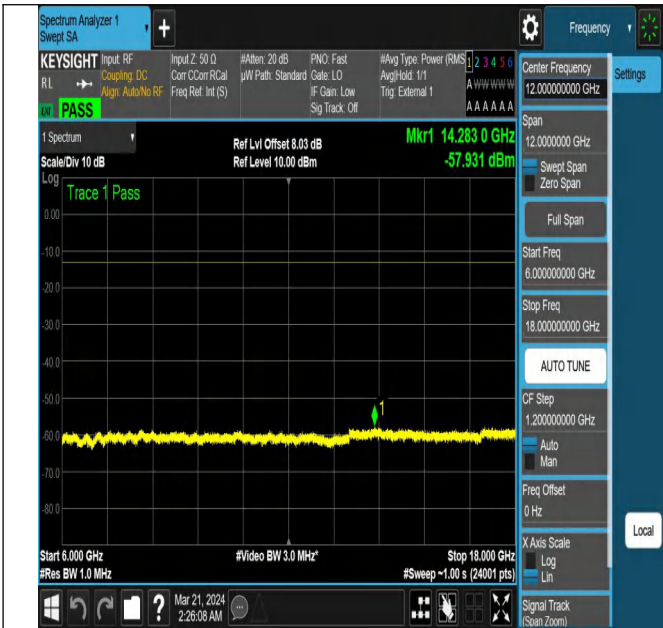
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Left-Ant1-PASS

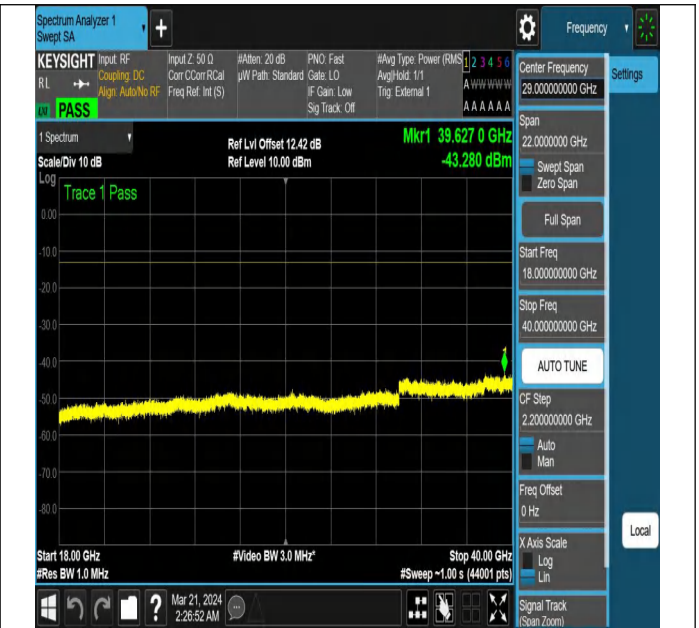


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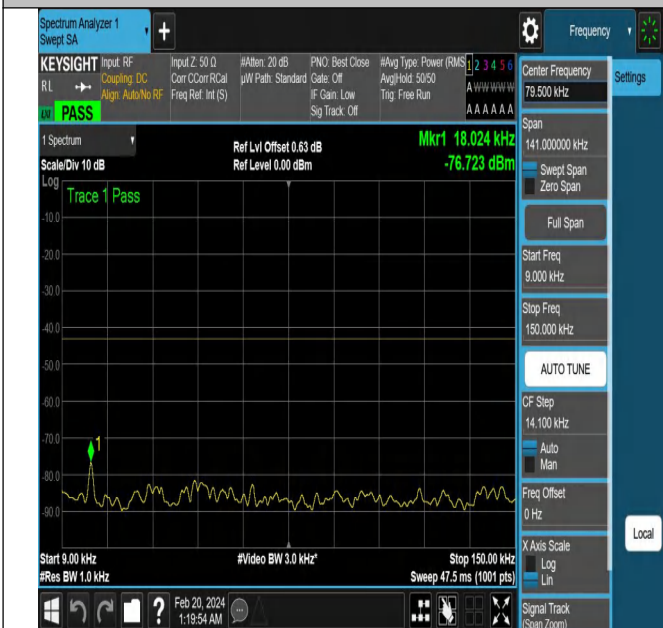
Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-H-DFT-QPSK-Edge_1RB_ Left-Ant1-PASS



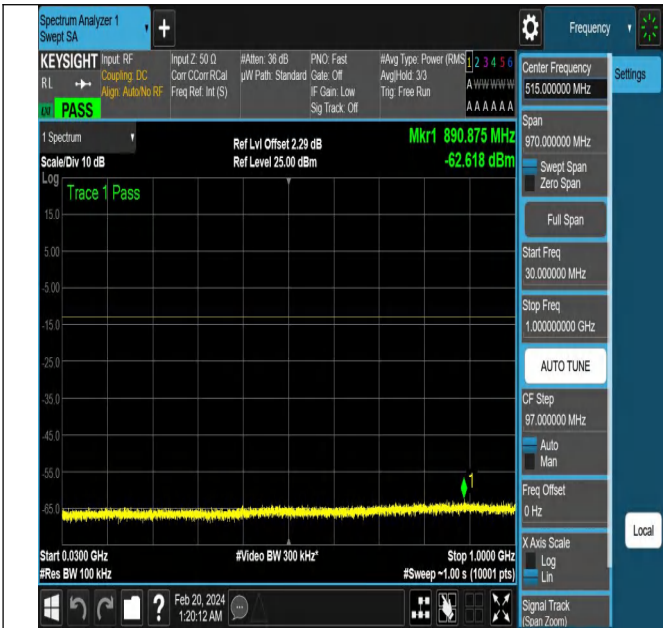
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NTNV-N78-3700-3800-PC2-30-50-H-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



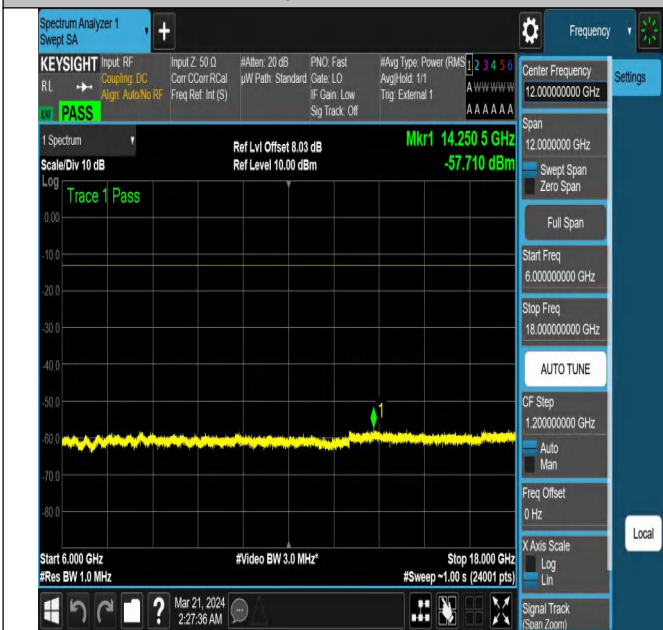
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NTNV-N78-3700-3800-PC2-30-50-H-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



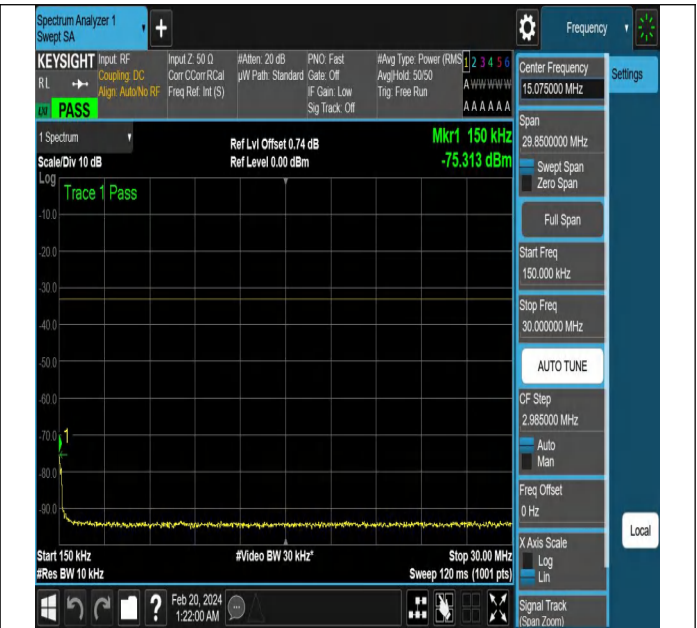
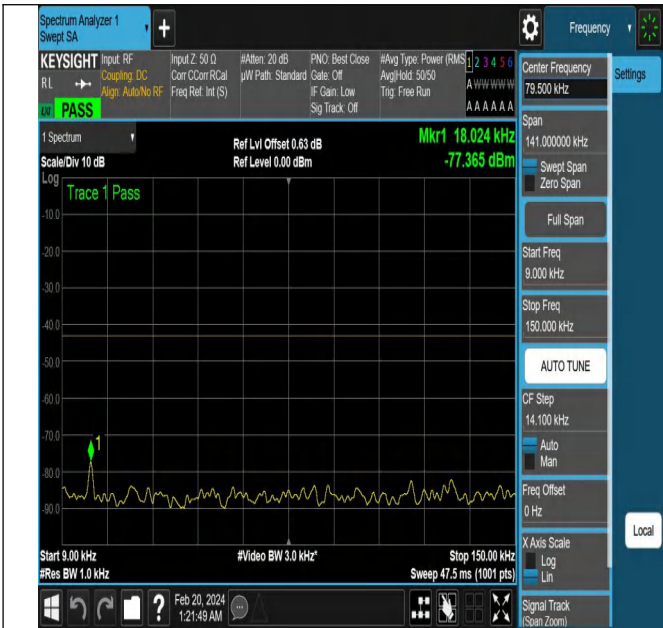
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NTNV-N78-3700-3800-PC2-30-50-H-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-50-H-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-60-L-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS

NTNV-N78-3700-3800-PC2-30-60-L-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS



NTNV-N78-3700-3800-PC2-30-60-L-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS

NTNV-N78-3700-3800-PC2-30-60-L-DFT-PI2BPSK-Edge_1R
B_Left-Ant1-PASS