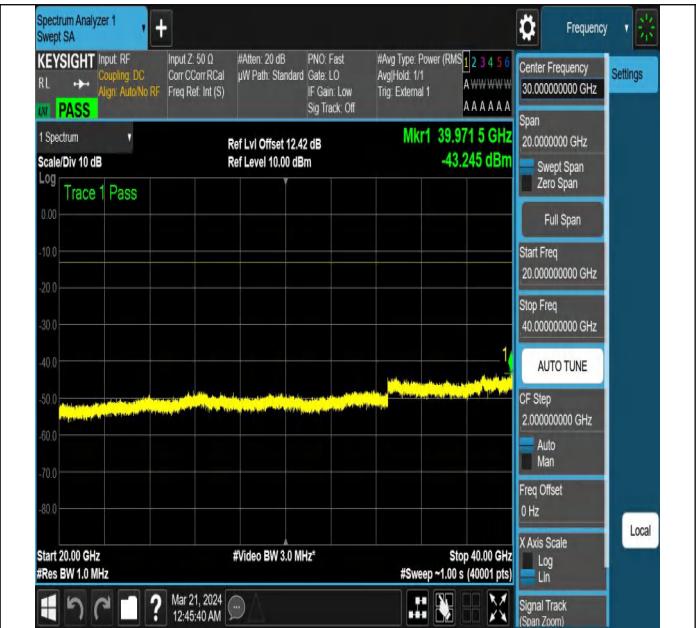
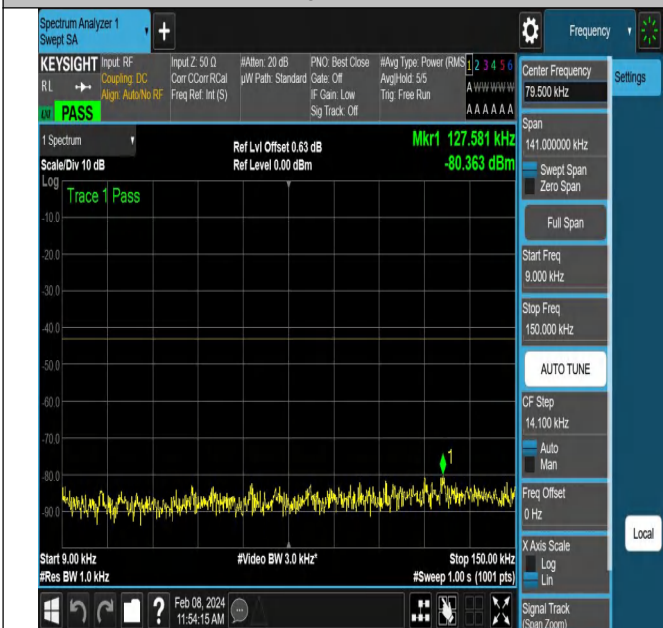


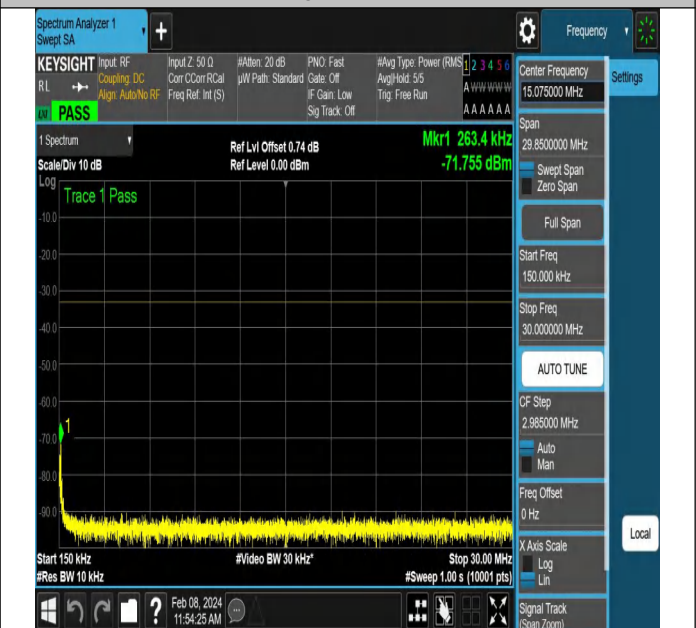
NTNV-N78-3450-3550-PC2-30-100-L-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS



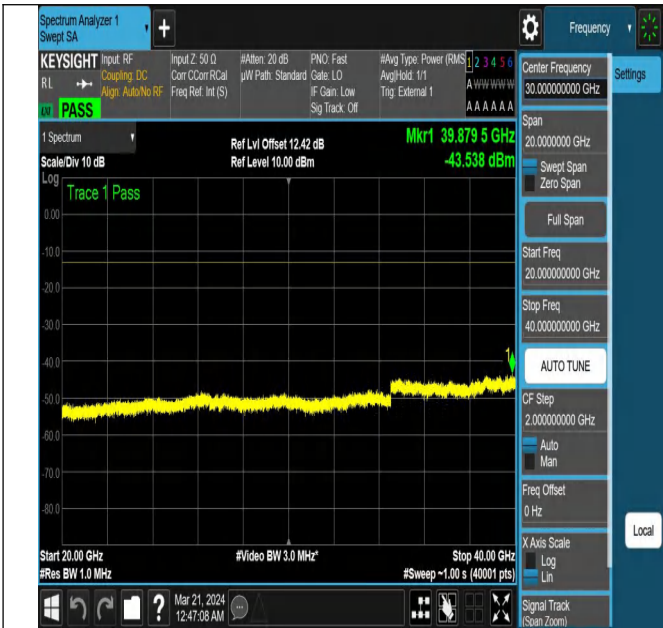
NTNV-N78-3450-3550-PC2-30-100-L-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS



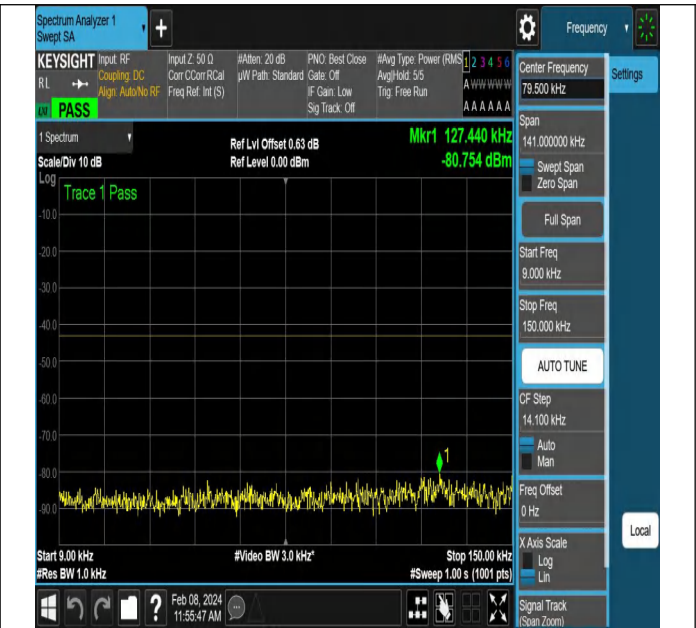
NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_
Left-Ant1-PASS



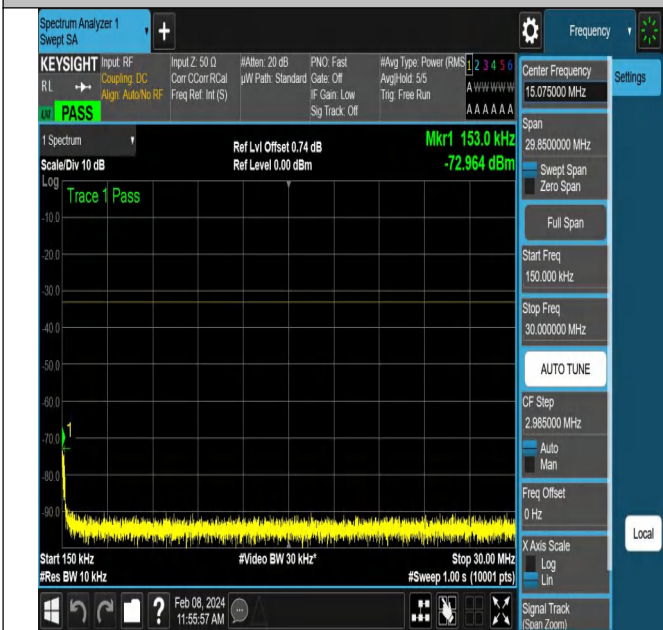
NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_
Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_
Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_
Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_
Right-Ant1-PASS



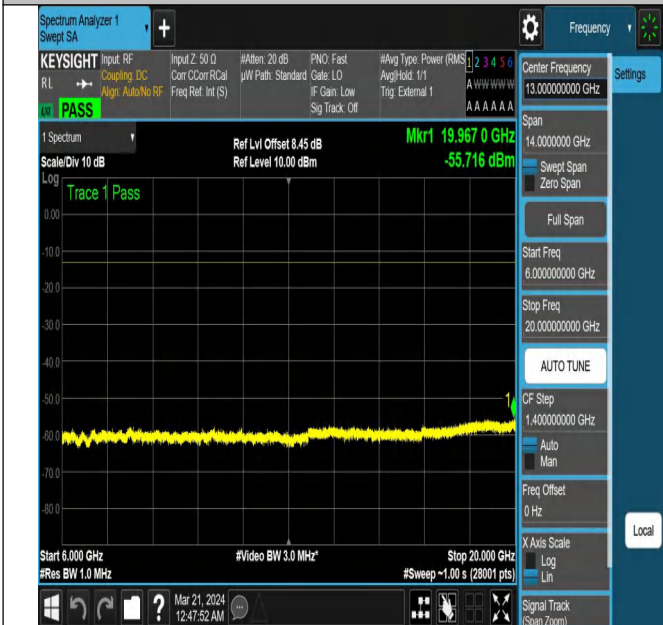
NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_
Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS

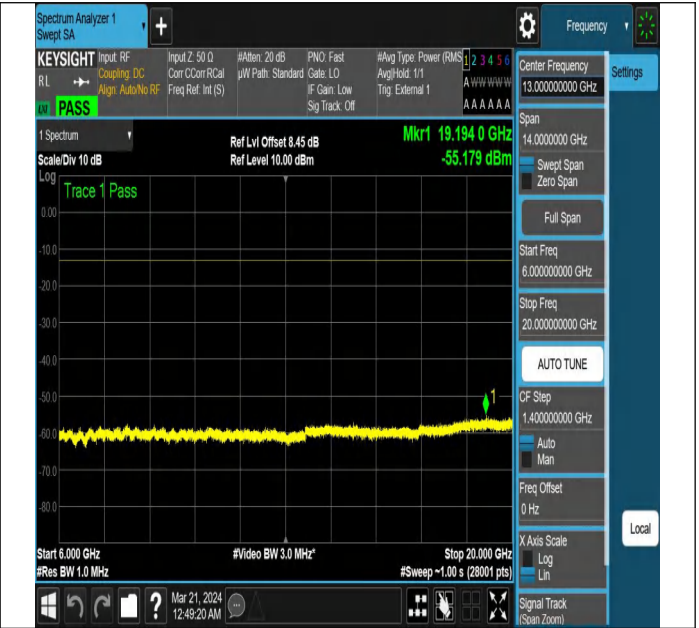


NTNV-N78-3450-3550-PC2-30-100-L-DFT-QPSK-Edge_1RB_ Right-Ant1-PASS



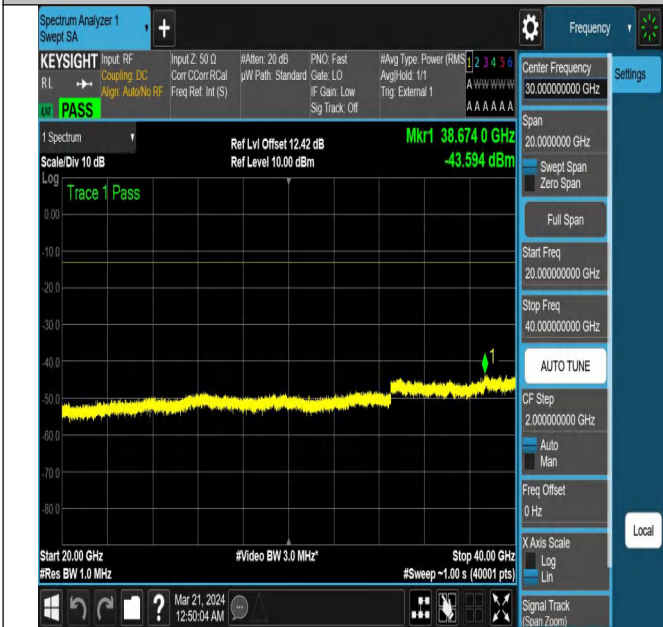
NTNV-N78-3450-3550-PC2-30-100-M-DFT-PI2BPSK-Edge_1

RB_Left-Ant1-PASS



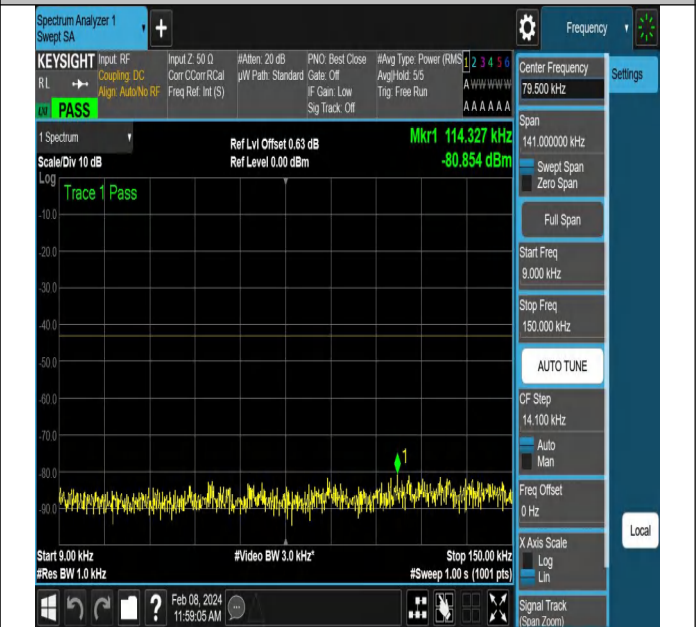
NTNV-N78-3450-3550-PC2-30-100-M-DFT-PI2BPSK-Edge_1

RB_Left-Ant1-PASS



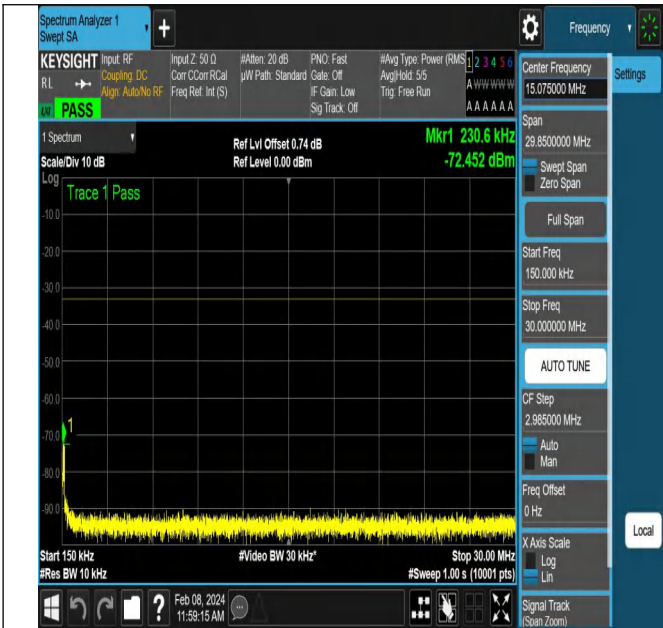
NTNV-N78-3450-3550-PC2-30-100-M-DFT-PI2BPSK-Edge_1

RB_Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-M-DFT-PI2BPSK-Edge_1

RB_Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-M-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-M-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS



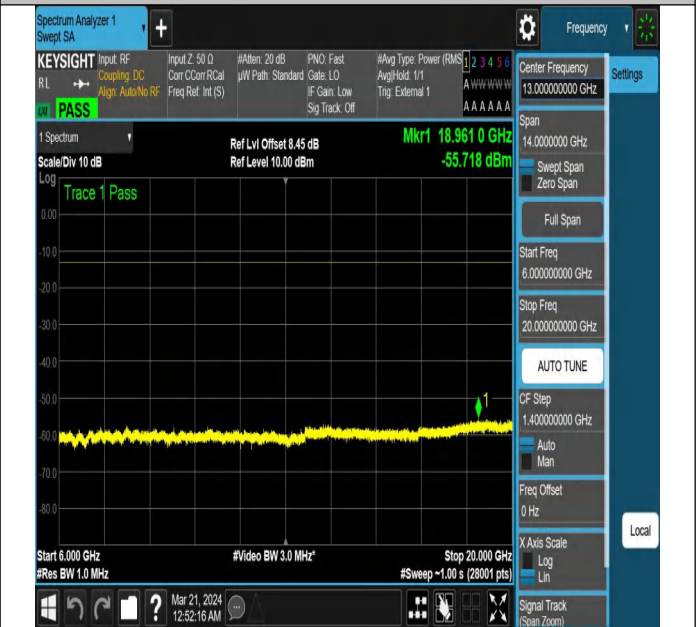
NTNV-N78-3450-3550-PC2-30-100-M-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-M-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS



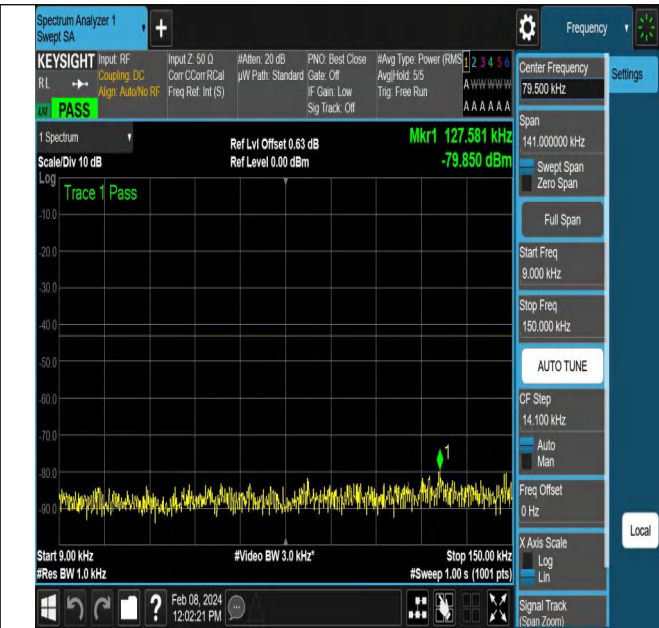
NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS



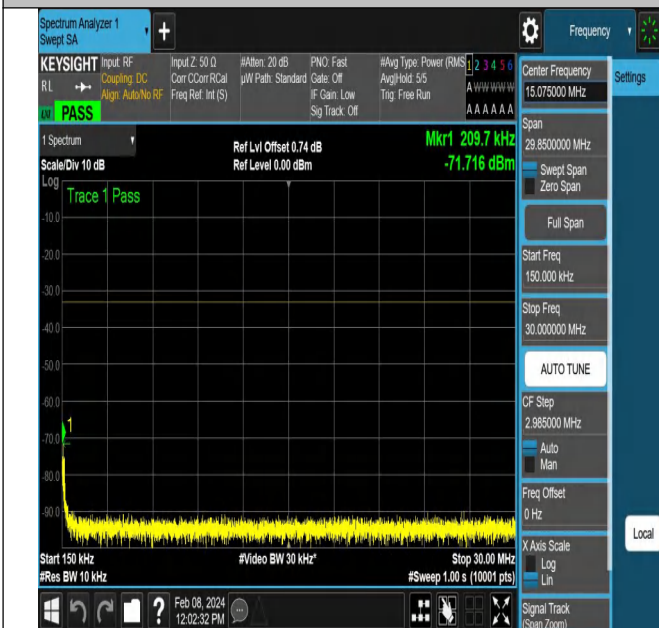
NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS



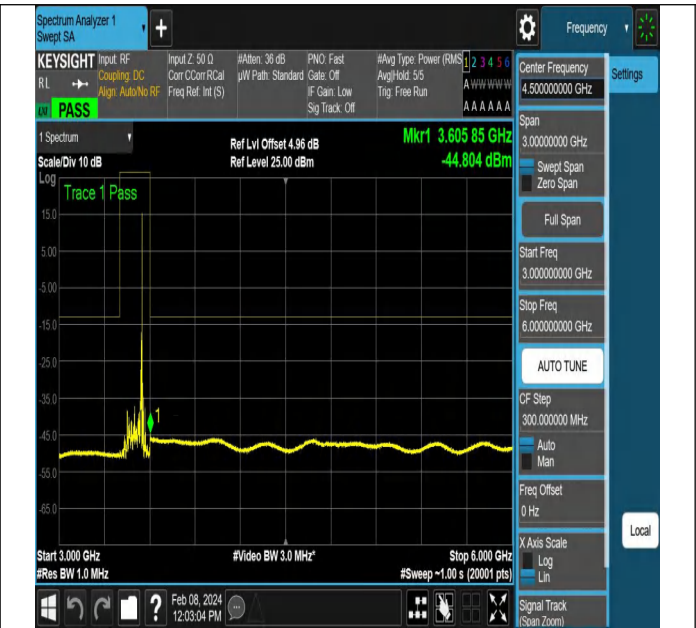
NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
 _Left-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS



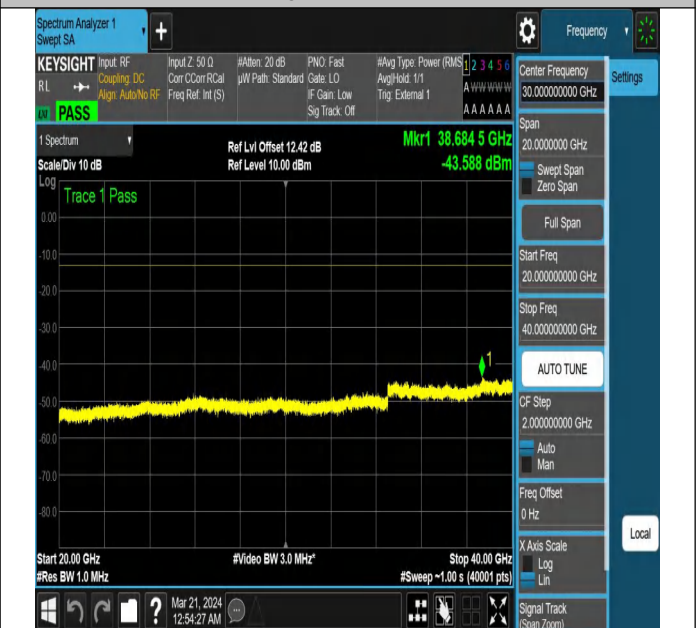
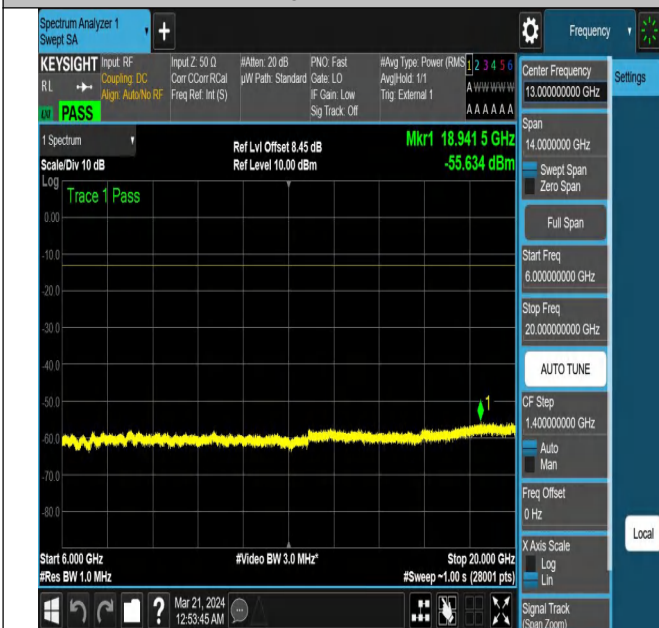
NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS



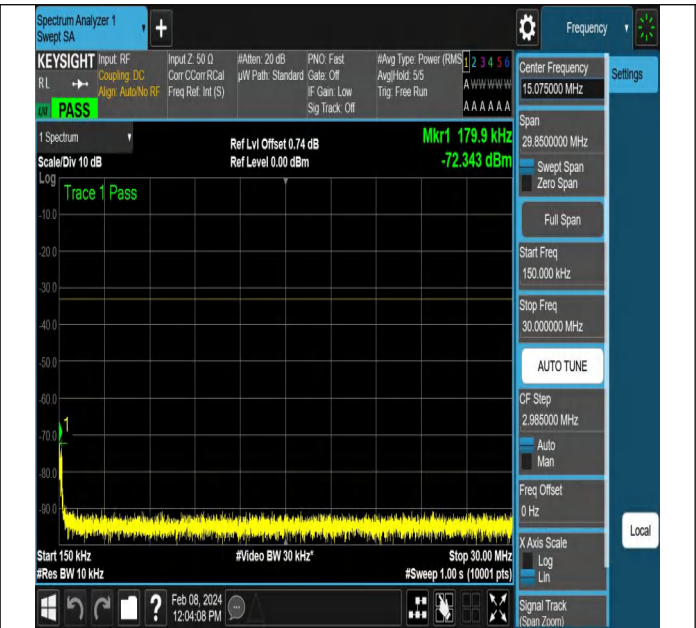
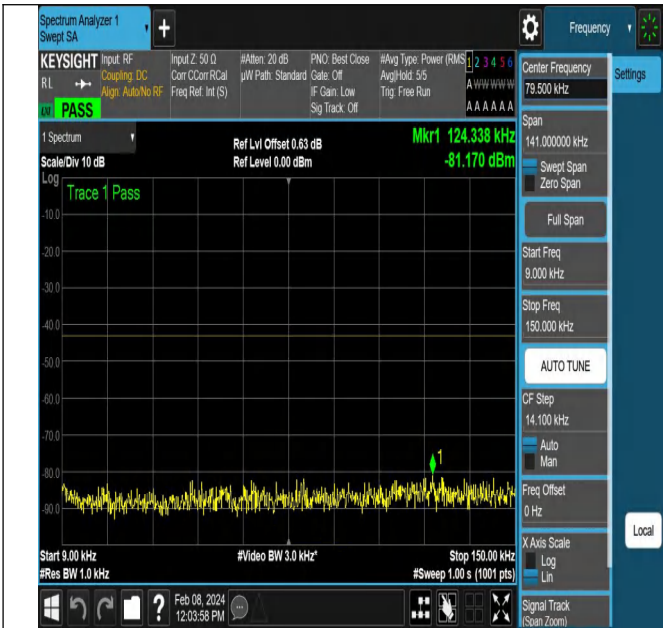
NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-M-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS



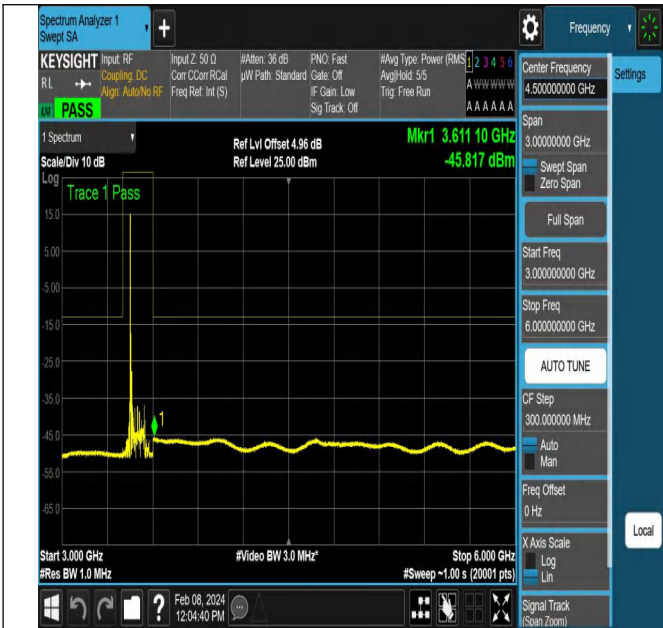
NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
 RB_Left-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
 RB_Left-Ant1-PASS

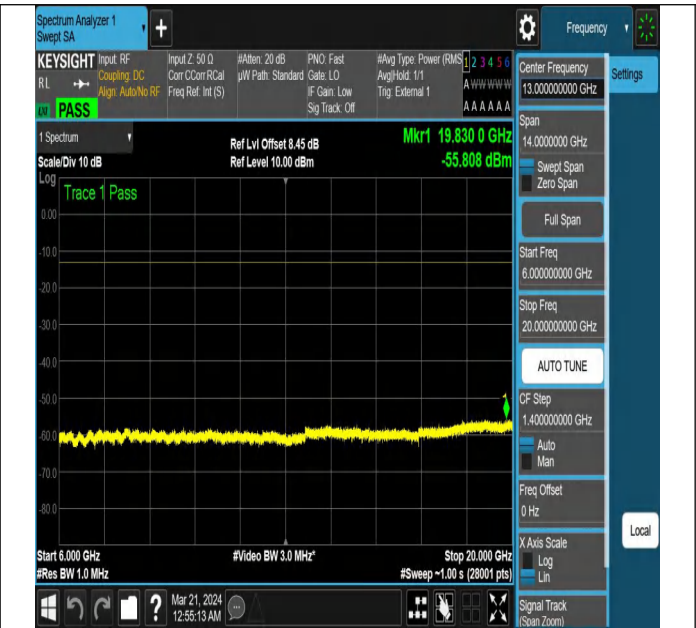


NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
 RB_Left-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
 RB_Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
RB_Left-Ant1-PASS



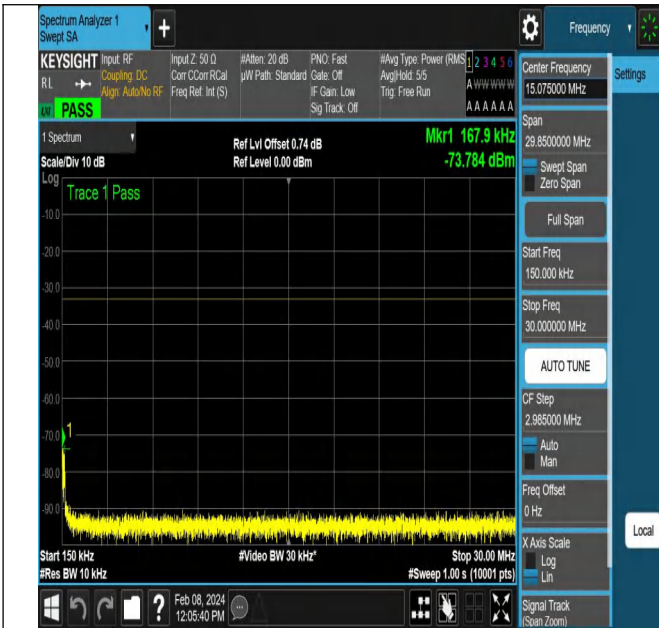
NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
RB_Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
RB_Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS



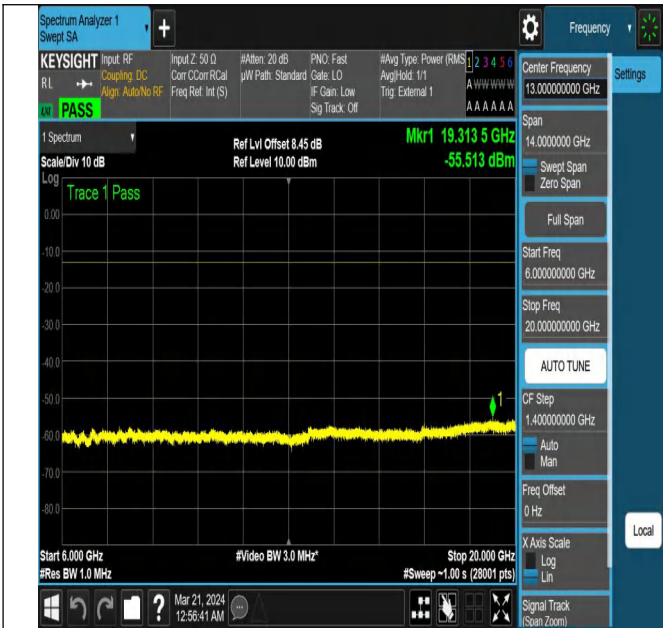
NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
 RB_Right-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
 RB_Right-Ant1-PASS

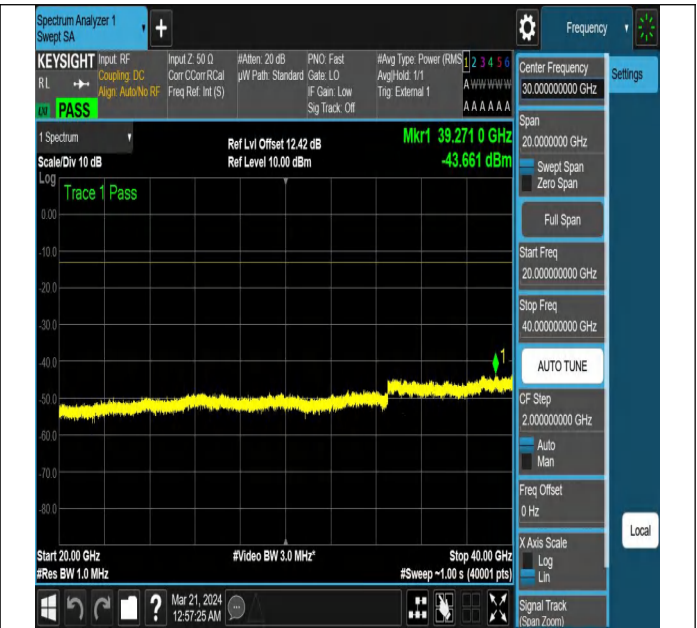


NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
 RB_Right-Ant1-PASS

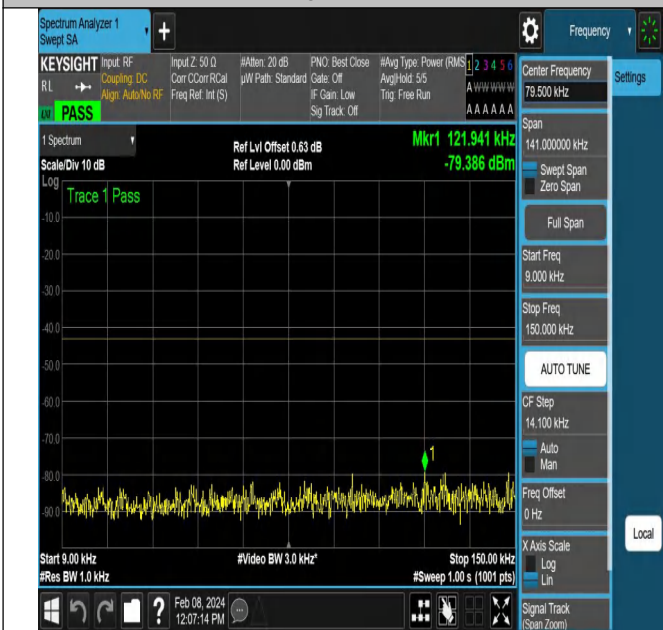
NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
 RB_Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS



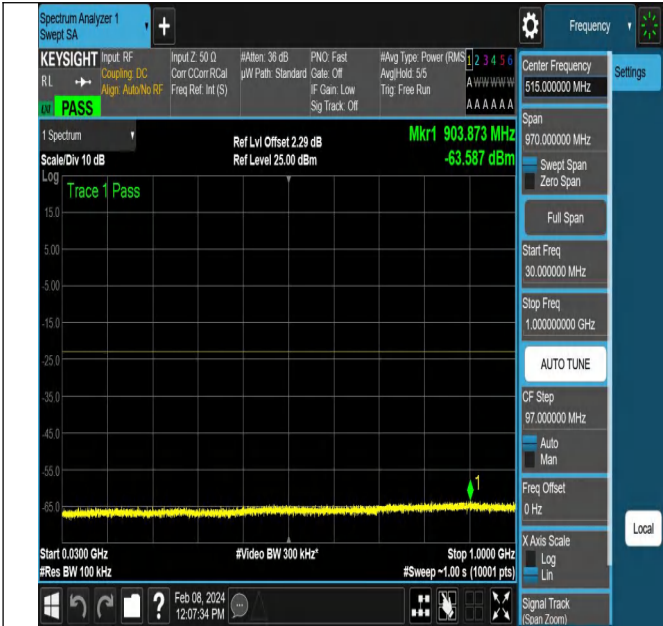
NTNV-N78-3450-3550-PC2-30-100-H-DFT-PI2BPSK-Edge_1
RB_Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS

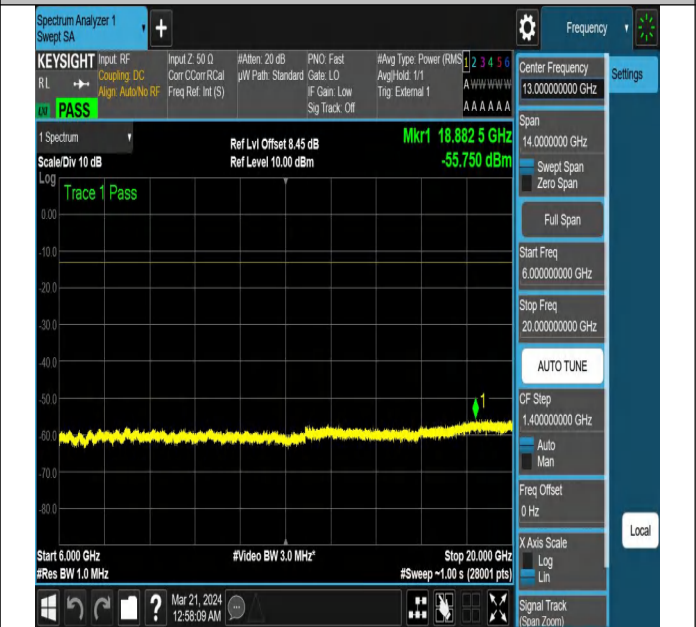
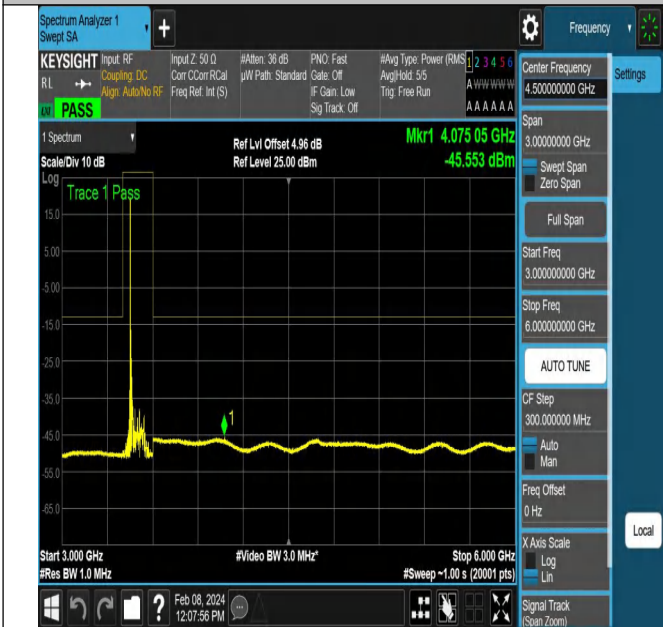


NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS

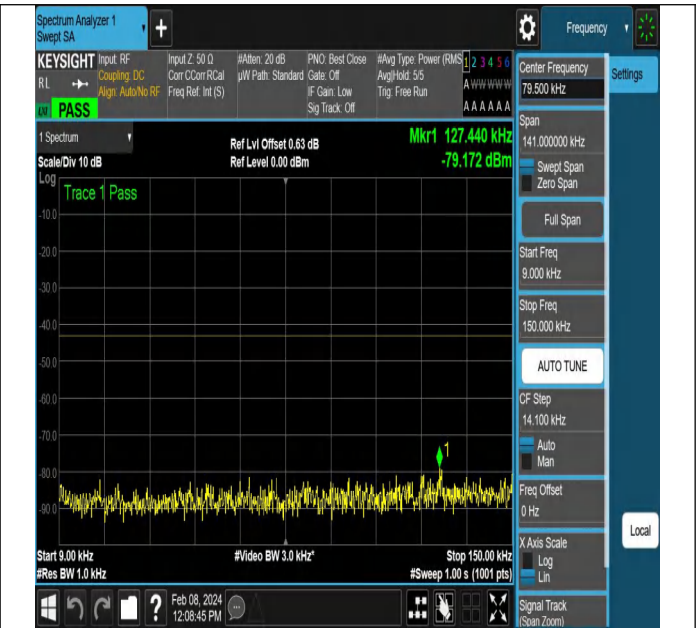


NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS

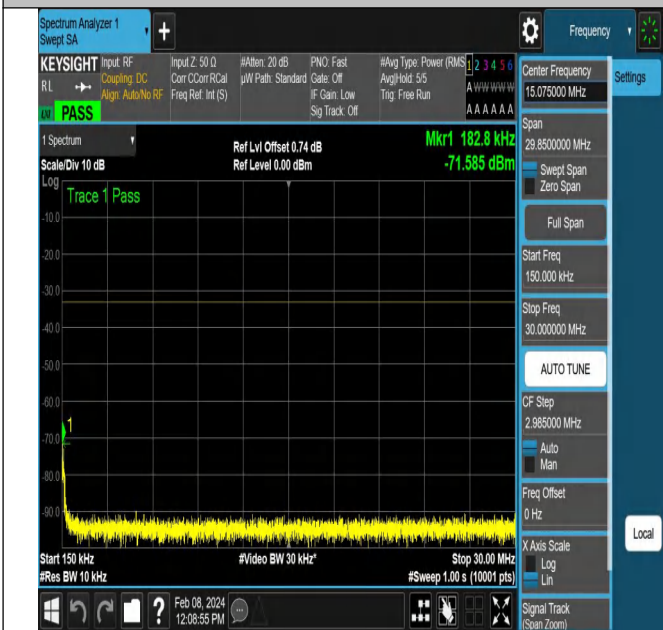
NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Left-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Right-Ant1-PASS

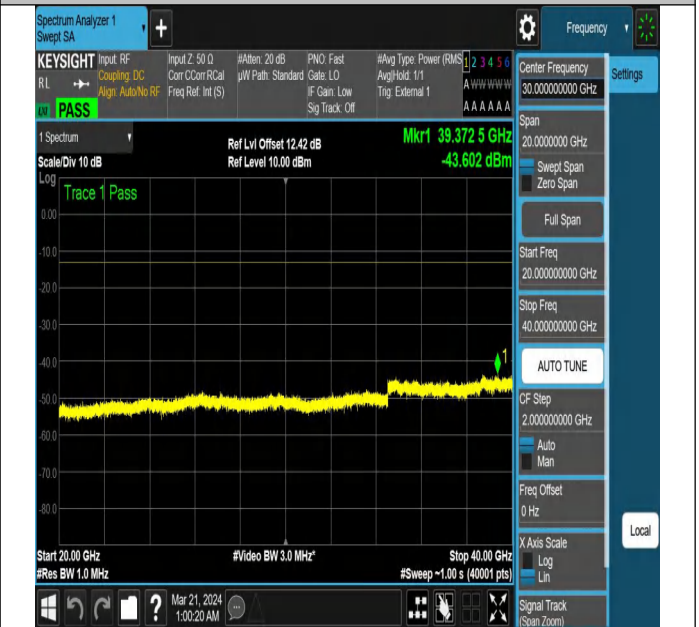
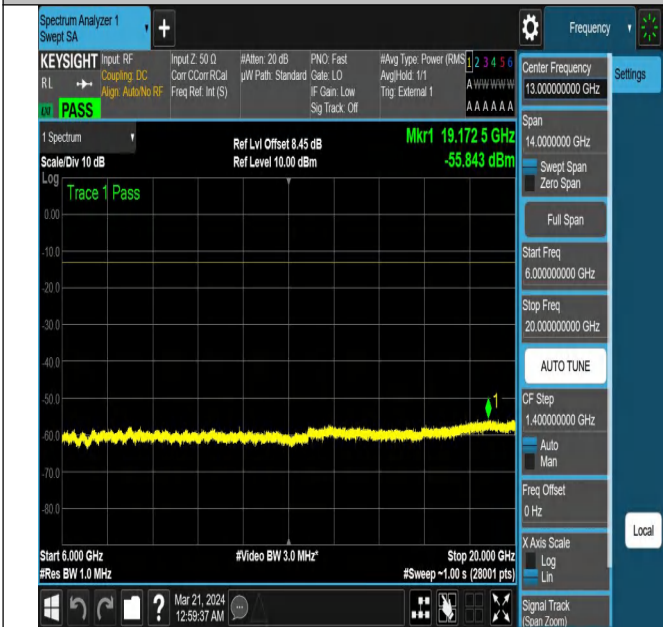


NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
_Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS



NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS

NTNV-N78-3450-3550-PC2-30-100-H-DFT-QPSK-Edge_1RB
 _Right-Ant1-PASS

Frequency Stability for SA

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	VL	NT	-8.590000	-0.002454	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	VN	NT	-11.670000	-0.003334	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	VH	NT	-10.100000	-0.002886	PASS

Frequency Error VS. Temperature

Voltage										
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	-30	-11.090000	-0.003169	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	-20	-9.860000	-0.002817	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	-10	-10.710000	-0.003060	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	0	-10.500000	-0.003000	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	10	-8.010000	-0.002289	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	20	-11.550000	-0.003300	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	30	-9.750000	-0.002786	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	40	-11.050000	-0.003157	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_FuII	NV	50	-7.710000	-0.002203	PASS