

DC_5A_n66A	15	5+10	CP-64QAM	M+H	Edge_1RB_Left	19.14	19.53	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+H	Edge_1RB_Right	18.99	19.38	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+H	Outer_Full	17.81	18.20	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+H	Inner_Full	18.89	19.28	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+H	Edge_1RB_Left	15.29	15.68	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+H	Edge_1RB_Right	15.19	15.58	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+H	Outer_Full	15.79	16.18	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+H	Inner_Full	16.02	16.41	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Left	16.17	16.56	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Right	16.33	16.72	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+L	Outer_Full	16.53	16.92	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+L	Inner_Full	16.47	16.86	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	21.81	22.20	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	21.75	22.14	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+L	Outer_Full	21.91	22.30	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+L	Inner_Full	22.90	23.29	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	21.37	21.76	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	21.55	21.94	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+L	Outer_Full	21.11	21.50	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+L	Inner_Full	21.88	22.27	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Left	20.26	20.65	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Right	20.45	20.84	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+L	Outer_Full	20.57	20.96	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+L	Inner_Full	20.56	20.95	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Left	18.79	19.18	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Right	18.86	19.25	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+L	Outer_Full	18.63	19.02	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+L	Inner_Full	18.58	18.97	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	19.90	20.29	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	20.06	20.45	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	19.01	19.40	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+L	Inner_Full	21.24	21.63	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	19.71	20.10	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	20.11	20.50	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	18.93	19.32	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+L	Inner_Full	21.07	21.46	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+L	Edge_1RB_Left	19.06	19.45	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+L	Edge_1RB_Right	19.19	19.58	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+L	Outer_Full	18.50	18.89	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+L	Inner_Full	19.44	19.83	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+L	Edge_1RB_Left	16.18	16.57	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+L	Edge_1RB_Right	16.40	16.79	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+L	Outer_Full	16.33	16.72	PC3	PASS

DC_5A_n66A	15	5+15	CP-256QAM	M+L	Inner_Full	16.43	16.82	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Left	15.88	16.27	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Right	15.74	16.13	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+M	Outer_Full	16.07	16.46	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+M	Inner_Full	15.98	16.37	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	21.68	22.07	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	21.66	22.05	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+M	Outer_Full	21.51	21.90	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+M	Inner_Full	22.56	22.95	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	21.11	21.50	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	21.03	21.42	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+M	Outer_Full	20.68	21.07	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+M	Inner_Full	21.65	22.04	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Left	20.01	20.40	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Right	19.96	20.35	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+M	Outer_Full	20.11	20.50	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+M	Inner_Full	20.22	20.61	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Left	18.23	18.62	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Right	18.13	18.52	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+M	Outer_Full	18.13	18.52	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+M	Inner_Full	18.16	18.55	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	19.40	19.79	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	19.24	19.63	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	18.69	19.08	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+M	Inner_Full	21.21	21.60	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	20.05	20.44	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	19.82	20.21	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	18.64	19.03	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+M	Inner_Full	20.64	21.03	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+M	Edge_1RB_Left	19.40	19.79	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+M	Edge_1RB_Right	19.37	19.76	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+M	Outer_Full	18.13	18.52	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+M	Inner_Full	19.28	19.67	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+M	Edge_1RB_Left	15.89	16.28	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+M	Edge_1RB_Right	15.74	16.13	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+M	Outer_Full	16.15	16.54	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+M	Inner_Full	16.04	16.43	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Left	15.74	16.13	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Right	15.80	16.19	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+H	Outer_Full	15.79	16.18	PC3	PASS
DC_5A_n66A	15	5+15	DFT-PI2BPSK	M+H	Inner_Full	15.87	16.26	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	21.27	21.66	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	21.33	21.72	PC3	PASS

DC_5A_n66A	15	5+15	DFT-QPSK	M+H	Outer_Full	21.24	21.63	PC3	PASS
DC_5A_n66A	15	5+15	DFT-QPSK	M+H	Inner_Full	22.30	22.69	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	20.56	20.95	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	20.58	20.97	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+H	Outer_Full	20.44	20.83	PC3	PASS
DC_5A_n66A	15	5+15	DFT-16QAM	M+H	Inner_Full	21.23	21.62	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Left	19.96	20.35	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Right	19.92	20.31	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+H	Outer_Full	19.79	20.18	PC3	PASS
DC_5A_n66A	15	5+15	DFT-64QAM	M+H	Inner_Full	19.99	20.38	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Left	17.75	18.14	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Right	17.65	18.04	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+H	Outer_Full	17.87	18.26	PC3	PASS
DC_5A_n66A	15	5+15	DFT-256QAM	M+H	Inner_Full	17.82	18.21	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	19.06	19.45	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	19.10	19.49	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	18.33	18.72	PC3	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+H	Inner_Full	20.92	21.31	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	19.30	19.69	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	19.31	19.70	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	18.31	18.70	PC3	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+H	Inner_Full	20.48	20.87	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+H	Edge_1RB_Left	18.20	18.59	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+H	Edge_1RB_Right	18.26	18.65	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+H	Outer_Full	17.82	18.21	PC3	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+H	Inner_Full	19.02	19.41	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+H	Edge_1RB_Left	15.74	16.13	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+H	Edge_1RB_Right	15.84	16.23	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+H	Outer_Full	15.79	16.18	PC3	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+H	Inner_Full	15.89	16.28	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	16.28	16.67	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	16.39	16.78	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	16.51	16.90	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+L	Inner_Full	16.51	16.90	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.65	22.04	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.63	22.02	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+L	Outer_Full	21.94	22.33	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+L	Inner_Full	22.93	23.32	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	20.96	21.35	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.12	21.51	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+L	Outer_Full	21.09	21.48	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+L	Inner_Full	21.96	22.35	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.46	20.85	PC3	PASS

DC_5A_n66A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	20.54	20.93	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+L	Outer_Full	20.62	21.01	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+L	Inner_Full	20.60	20.99	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	18.43	18.82	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	18.46	18.85	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+L	Outer_Full	18.59	18.98	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+L	Inner_Full	18.60	18.99	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	19.91	20.30	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	19.88	20.27	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	19.88	20.27	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+L	Inner_Full	21.49	21.88	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	19.91	20.30	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	19.93	20.32	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	19.07	19.46	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+L	Inner_Full	20.99	21.38	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	19.27	19.66	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	19.59	19.98	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+L	Outer_Full	18.60	18.99	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+L	Inner_Full	19.66	20.05	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	16.26	16.65	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	16.36	16.75	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+L	Outer_Full	16.58	16.97	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+L	Inner_Full	16.54	16.93	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	16.07	16.46	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	15.89	16.28	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	16.06	16.45	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+M	Inner_Full	16.14	16.53	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.63	22.02	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	21.43	21.82	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+M	Outer_Full	21.44	21.83	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+M	Inner_Full	22.59	22.98	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.07	21.46	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.92	21.31	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+M	Outer_Full	20.61	21.00	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+M	Inner_Full	21.55	21.94	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	20.65	21.04	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	20.57	20.96	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+M	Outer_Full	20.06	20.45	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+M	Inner_Full	20.24	20.63	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	18.13	18.52	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	17.96	18.35	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+M	Outer_Full	18.02	18.41	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+M	Inner_Full	18.20	18.59	PC3	PASS

DC_5A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	20.19	20.58	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	19.97	20.36	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	19.97	20.36	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+M	Inner_Full	21.11	21.50	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	19.88	20.27	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	19.75	20.14	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	18.45	18.84	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+M	Inner_Full	20.66	21.05	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	19.42	19.81	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	19.36	19.75	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+M	Outer_Full	17.98	18.37	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+M	Inner_Full	19.24	19.63	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	16.08	16.47	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	15.89	16.28	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+M	Outer_Full	16.01	16.40	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+M	Inner_Full	16.12	16.51	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	15.97	16.36	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	15.78	16.17	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	15.81	16.20	PC3	PASS
DC_5A_n66A	15	5+20	DFT-PI2BPSK	M+H	Inner_Full	15.86	16.25	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	21.31	21.70	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.18	21.57	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+H	Outer_Full	21.20	21.59	PC3	PASS
DC_5A_n66A	15	5+20	DFT-QPSK	M+H	Inner_Full	22.35	22.74	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	20.53	20.92	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	20.39	20.78	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+H	Outer_Full	20.33	20.72	PC3	PASS
DC_5A_n66A	15	5+20	DFT-16QAM	M+H	Inner_Full	21.33	21.72	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	19.88	20.27	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	19.85	20.24	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+H	Outer_Full	19.94	20.33	PC3	PASS
DC_5A_n66A	15	5+20	DFT-64QAM	M+H	Inner_Full	19.93	20.32	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.01	18.40	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	17.85	18.24	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+H	Outer_Full	17.85	18.24	PC3	PASS
DC_5A_n66A	15	5+20	DFT-256QAM	M+H	Inner_Full	17.92	18.31	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	19.41	19.80	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	19.24	19.63	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	19.24	19.63	PC3	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+H	Inner_Full	20.94	21.33	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	19.73	20.12	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	19.48	19.87	PC3	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	18.44	18.83	PC3	PASS

DC_5A_n66A	15	5+20	CP-16QAM	M+H	Inner_Full	20.37	20.76	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	19.05	19.44	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	18.89	19.28	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+H	Outer_Full	17.86	18.25	PC3	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+H	Inner_Full	18.91	19.30	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	15.98	16.37	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	15.85	16.24	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+H	Outer_Full	15.78	16.17	PC3	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+H	Inner_Full	15.79	16.18	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.31	21.70	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	20.96	21.35	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+L	Outer_Full	21.85	22.24	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+L	Inner_Full	22.81	23.20	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.37	21.76	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.06	21.45	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+L	Outer_Full	21.89	22.28	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+L	Inner_Full	22.78	23.17	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+L	Edge_1RB_Left	20.65	21.04	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.23	20.62	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+L	Outer_Full	20.96	21.35	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+L	Inner_Full	21.80	22.19	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+L	Edge_1RB_Left	19.86	20.25	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+L	Edge_1RB_Right	19.45	19.84	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+L	Outer_Full	20.42	20.81	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+L	Inner_Full	20.36	20.75	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+L	Edge_1RB_Left	18.22	18.61	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+L	Edge_1RB_Right	17.87	18.26	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+L	Outer_Full	18.48	18.87	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+L	Inner_Full	18.38	18.77	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+L	Edge_1RB_Left	19.64	20.03	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+L	Edge_1RB_Right	19.31	19.70	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+L	Outer_Full	18.93	19.32	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+L	Inner_Full	21.39	21.78	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+L	Edge_1RB_Left	19.52	19.91	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+L	Edge_1RB_Right	19.32	19.71	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+L	Outer_Full	19.02	19.41	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+L	Inner_Full	20.95	21.34	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+L	Edge_1RB_Left	18.86	19.25	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+L	Edge_1RB_Right	18.56	18.95	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+L	Outer_Full	18.49	18.88	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+L	Inner_Full	19.39	19.78	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+L	Edge_1RB_Left	16.26	16.65	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+L	Edge_1RB_Right	15.93	16.32	PC3	PASS

DC_5A_n66A	15	5+40	CP-256QAM	M+L	Outer_Full	16.40	16.79	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+L	Inner_Full	16.39	16.78	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Left	16.19	16.58	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Right	15.74	16.13	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+M	Outer_Full	15.73	16.12	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+M	Inner_Full	16.10	16.49	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.27	21.66	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+M	Edge_1RB_Right	20.87	21.26	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+M	Outer_Full	21.18	21.57	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+M	Inner_Full	22.50	22.89	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.79	21.18	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.36	20.75	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+M	Outer_Full	20.30	20.69	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+M	Inner_Full	21.49	21.88	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+M	Edge_1RB_Left	19.94	20.33	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+M	Edge_1RB_Right	19.53	19.92	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+M	Outer_Full	19.78	20.17	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+M	Inner_Full	20.17	20.56	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+M	Edge_1RB_Left	18.16	18.55	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+M	Edge_1RB_Right	17.72	18.11	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+M	Outer_Full	17.86	18.25	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+M	Inner_Full	18.11	18.50	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+M	Edge_1RB_Left	19.56	19.95	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+M	Edge_1RB_Right	19.13	19.52	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+M	Outer_Full	18.27	18.66	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+M	Inner_Full	21.14	21.53	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+M	Edge_1RB_Left	19.66	20.05	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+M	Edge_1RB_Right	19.17	19.56	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+M	Outer_Full	18.28	18.67	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+M	Inner_Full	20.69	21.08	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+M	Edge_1RB_Left	18.81	19.20	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+M	Edge_1RB_Right	18.38	18.77	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+M	Outer_Full	17.77	18.16	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+M	Inner_Full	19.13	19.52	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+M	Edge_1RB_Left	16.19	16.58	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+M	Edge_1RB_Right	15.79	16.18	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+M	Outer_Full	15.83	16.22	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+M	Inner_Full	16.06	16.45	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	15.77	16.16	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	15.43	15.82	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+H	Outer_Full	16.07	16.46	PC3	PASS
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+H	Inner_Full	15.92	16.31	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.04	21.43	PC3	PASS

DC_5A_n66A	15	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.04	21.43	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+H	Outer_Full	21.55	21.94	PC3	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+H	Inner_Full	22.47	22.86	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.40	20.79	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.12	20.51	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+H	Outer_Full	20.58	20.97	PC3	PASS
DC_5A_n66A	15	5+40	DFT-16QAM	M+H	Inner_Full	21.49	21.88	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+H	Edge_1RB_Left	19.66	20.05	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+H	Edge_1RB_Right	19.34	19.73	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+H	Outer_Full	20.21	20.60	PC3	PASS
DC_5A_n66A	15	5+40	DFT-64QAM	M+H	Inner_Full	20.09	20.48	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+H	Edge_1RB_Left	17.88	18.27	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+H	Edge_1RB_Right	17.60	17.99	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+H	Outer_Full	18.19	18.58	PC3	PASS
DC_5A_n66A	15	5+40	DFT-256QAM	M+H	Inner_Full	18.14	18.53	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+H	Edge_1RB_Left	19.21	19.60	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+H	Edge_1RB_Right	18.96	19.35	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+H	Outer_Full	18.61	19.00	PC3	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+H	Inner_Full	21.09	21.48	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+H	Edge_1RB_Left	19.34	19.73	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+H	Edge_1RB_Right	19.04	19.43	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+H	Outer_Full	18.57	18.96	PC3	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+H	Inner_Full	20.54	20.93	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+H	Edge_1RB_Left	18.68	19.07	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+H	Edge_1RB_Right	18.39	18.78	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+H	Outer_Full	18.13	18.52	PC3	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+H	Inner_Full	19.00	19.39	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+H	Edge_1RB_Left	15.79	16.18	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+H	Edge_1RB_Right	15.47	15.86	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+H	Outer_Full	16.08	16.47	PC3	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+H	Inner_Full	16.01	16.40	PC3	PASS

Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

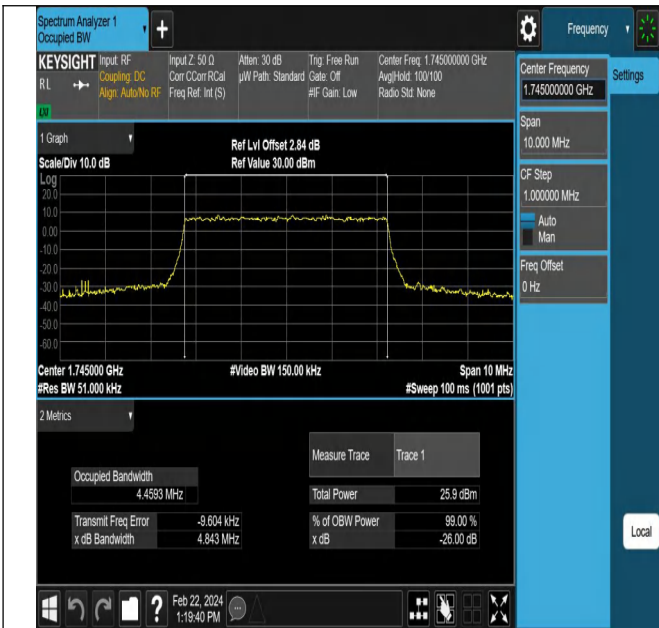
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n66A	15	5+40	DFT-PI2BPSK	M+M	Outer_Full	8.37	≤13	PASS
DC_5A_n66A	15	5+40	DFT-QPSK	M+M	Outer_Full	8.43	≤13	PASS

26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4593	4.843	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4709	4.846	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+M	Outer_Full	4.4561	4.843	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+M	Outer_Full	4.4582	4.865	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2822	9.903	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2748	9.876	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+M	Outer_Full	9.2772	9.901	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+M	Outer_Full	9.2832	9.912	PASS
DC_5A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	14.069	14.78	PASS
DC_5A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	14.085	14.75	PASS
DC_5A_n66A	15	5+15	CP-64QAM	M+M	Outer_Full	14.061	14.79	PASS
DC_5A_n66A	15	5+15	CP-256QAM	M+M	Outer_Full	14.058	14.78	PASS
DC_5A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	18.880	20.45	PASS
DC_5A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	18.896	19.87	PASS
DC_5A_n66A	15	5+20	CP-64QAM	M+M	Outer_Full	18.865	19.95	PASS
DC_5A_n66A	15	5+20	CP-256QAM	M+M	Outer_Full	18.867	19.88	PASS
DC_5A_n66A	15	5+40	CP-QPSK	M+M	Outer_Full	38.416	40.11	PASS
DC_5A_n66A	15	5+40	CP-16QAM	M+M	Outer_Full	38.390	40.04	PASS
DC_5A_n66A	15	5+40	CP-64QAM	M+M	Outer_Full	38.362	39.94	PASS
DC_5A_n66A	15	5+40	CP-256QAM	M+M	Outer_Full	38.400	40.08	PASS

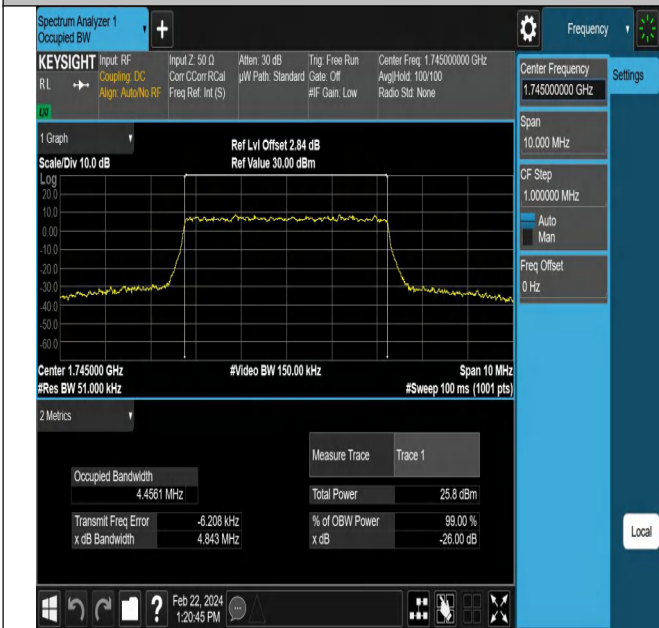
Test Graphs



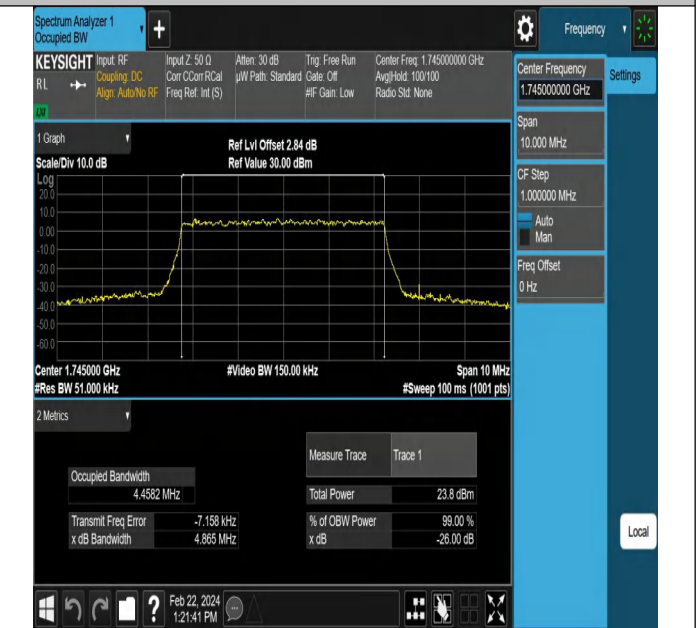
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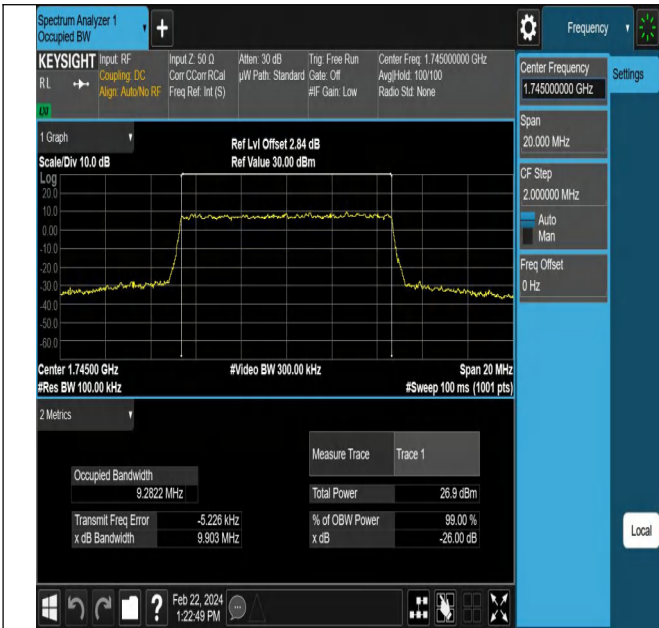
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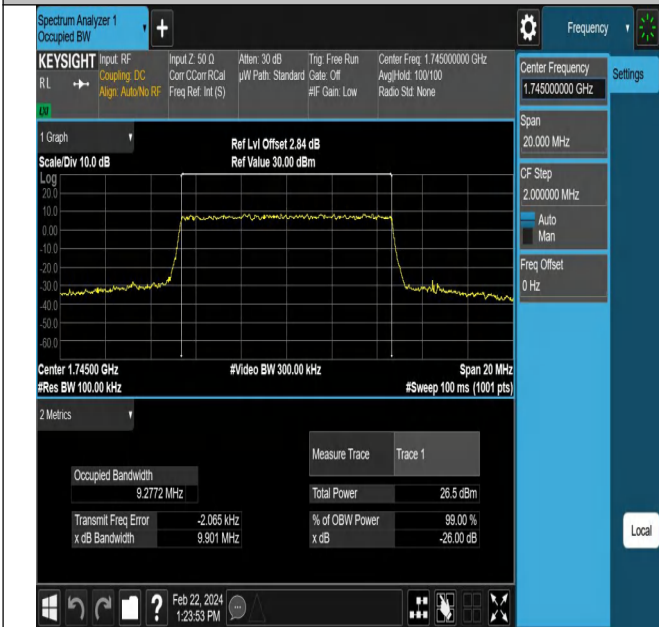
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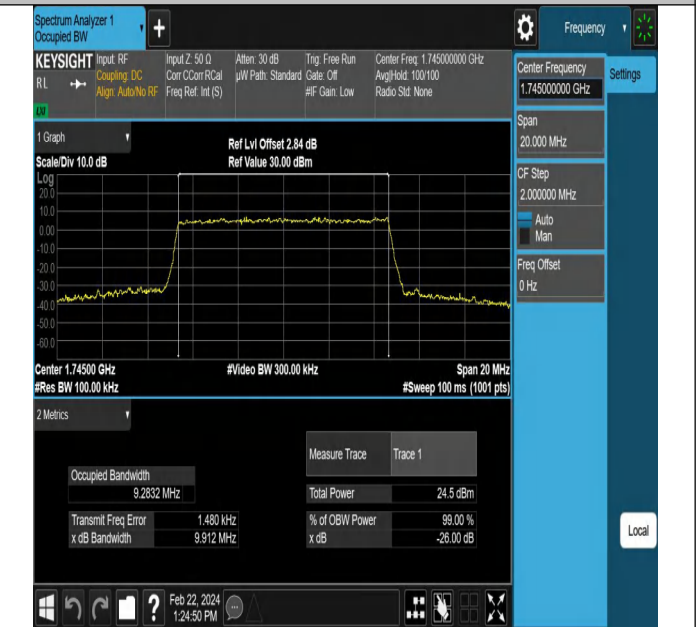
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Outer_Full-Ant1-PASS



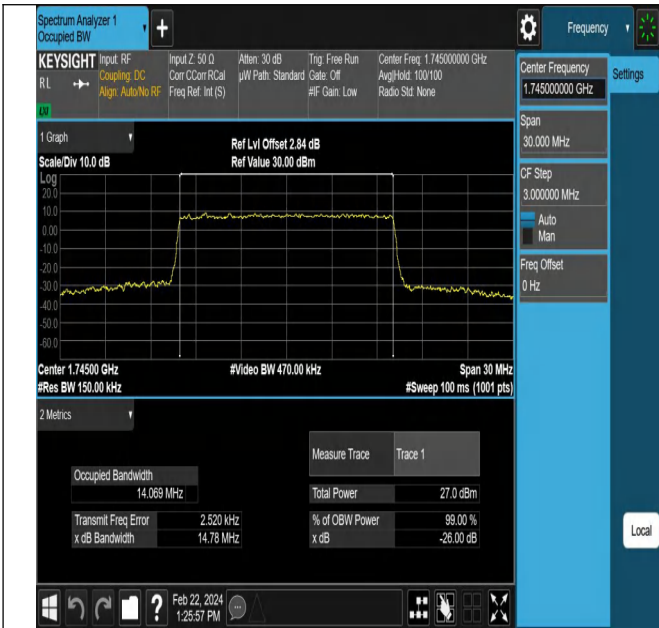
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Outer_Full-Ant1-PASS



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-Outer_Full-Ant1-PASS



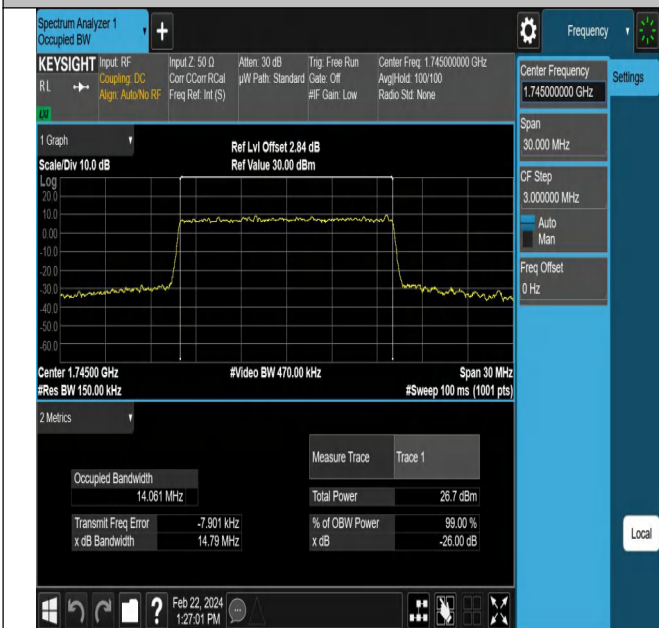
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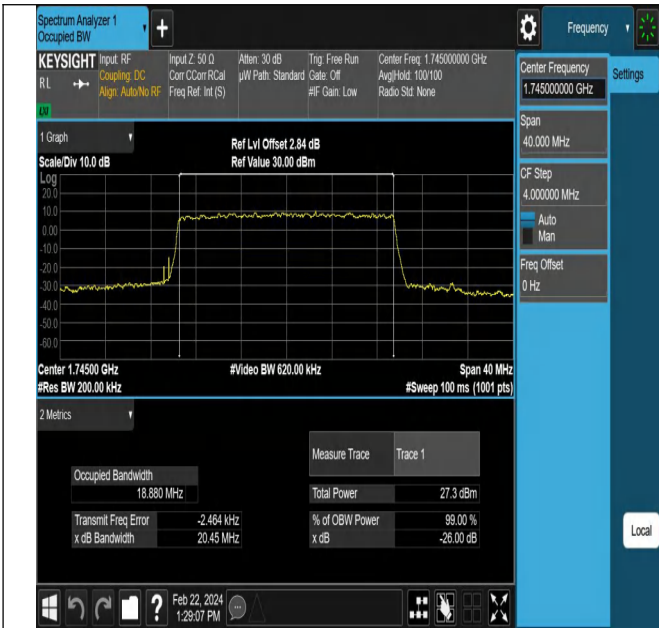
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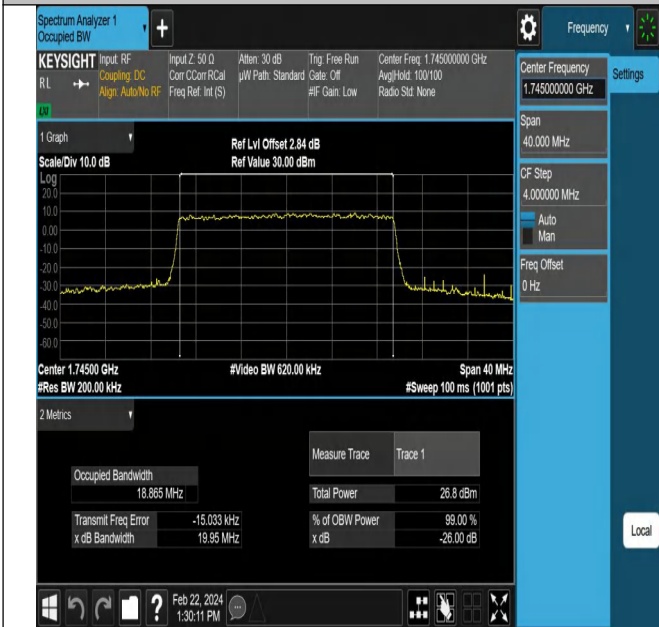
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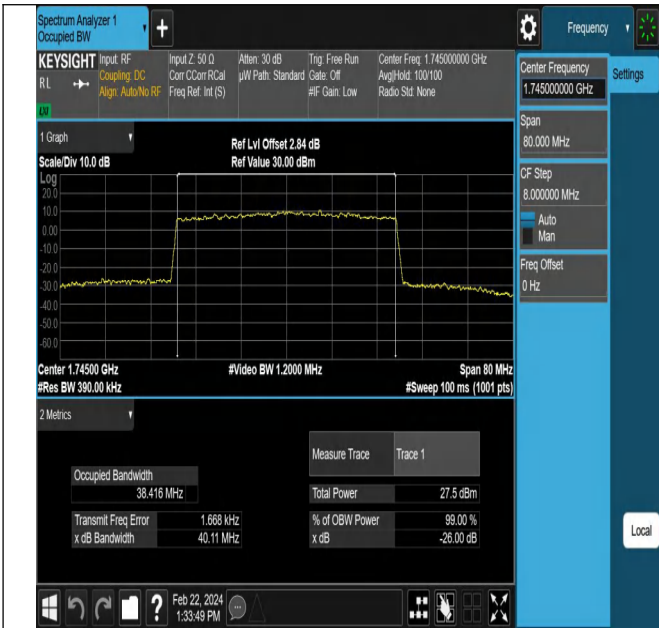
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-Outer_Full-Ant1-PASS



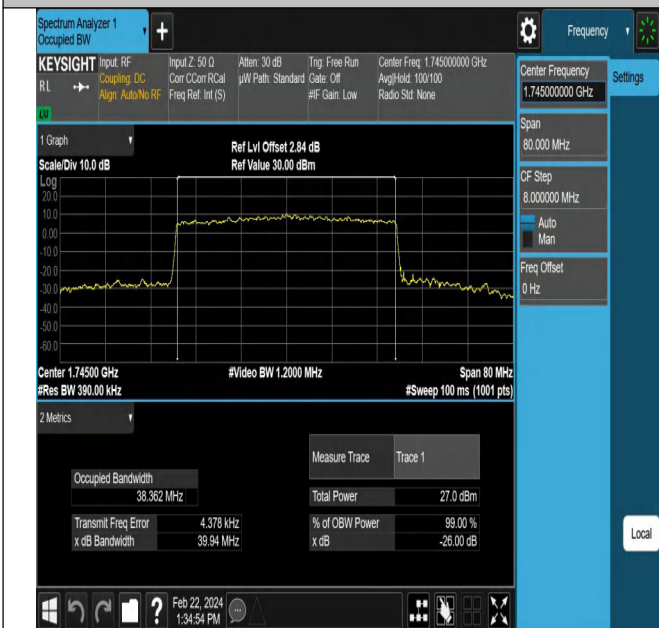
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M-Outer_Full-Ant1-PASS



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Outer_Full-Ant1-PASS



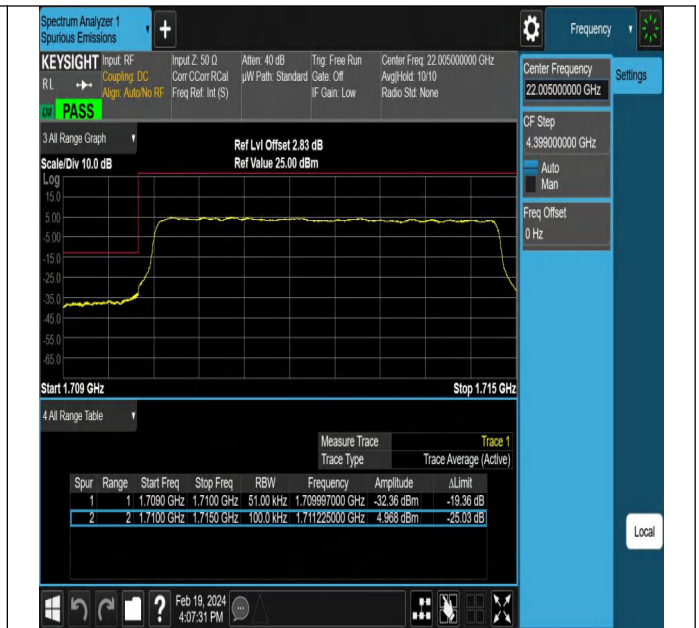
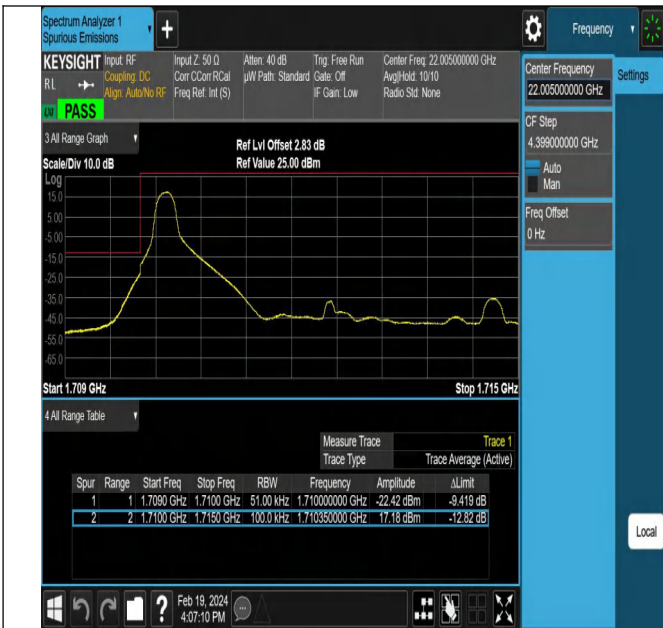
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-Outer_Full-Ant1-PASS



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M-Outer_Full-Ant1-PASS

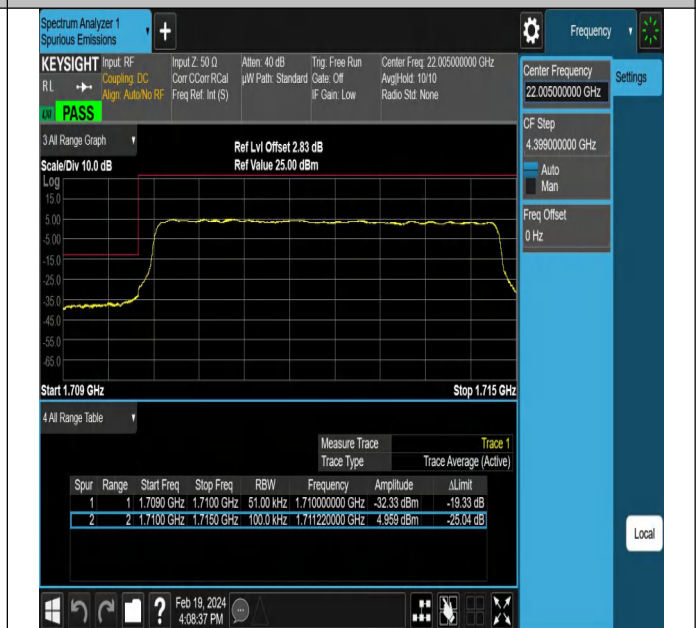
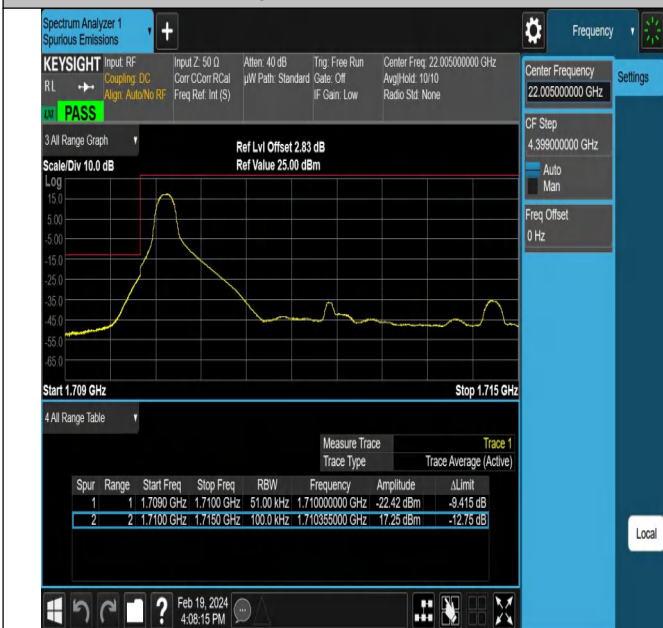
Band Edge for NSA

Test Graphs



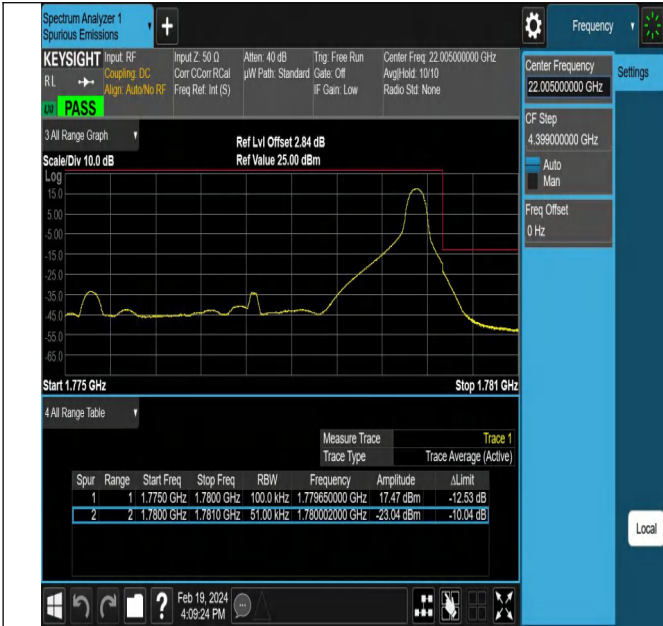
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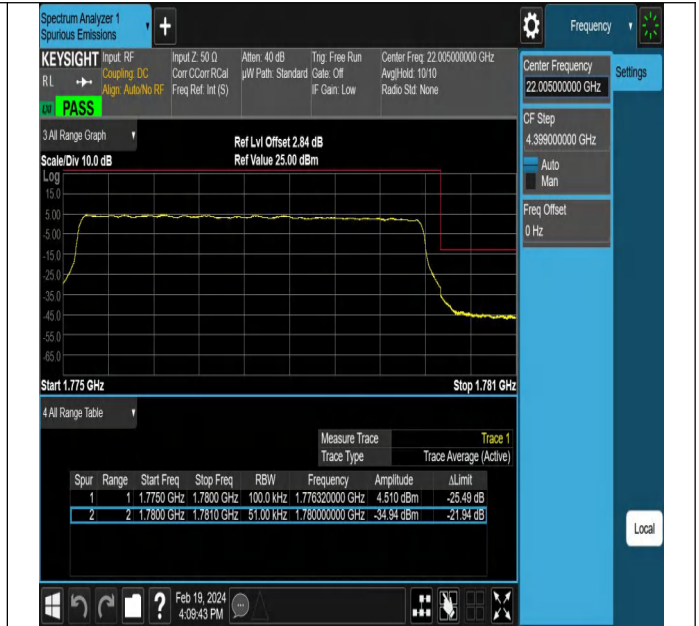
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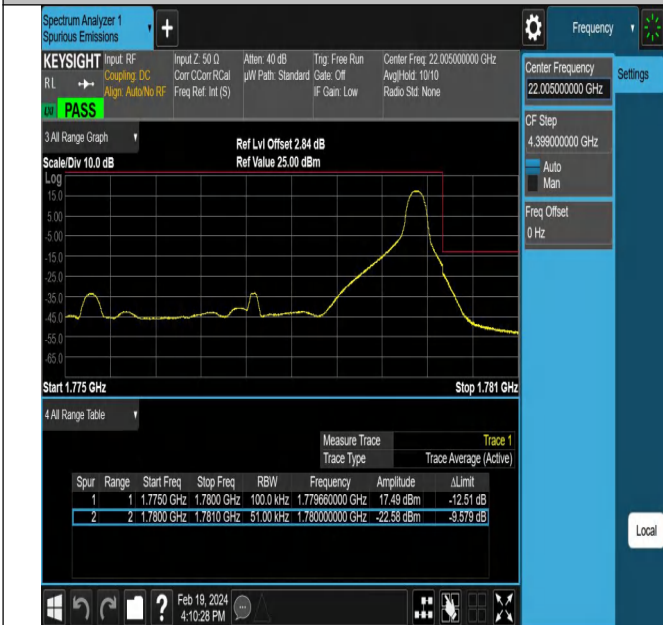
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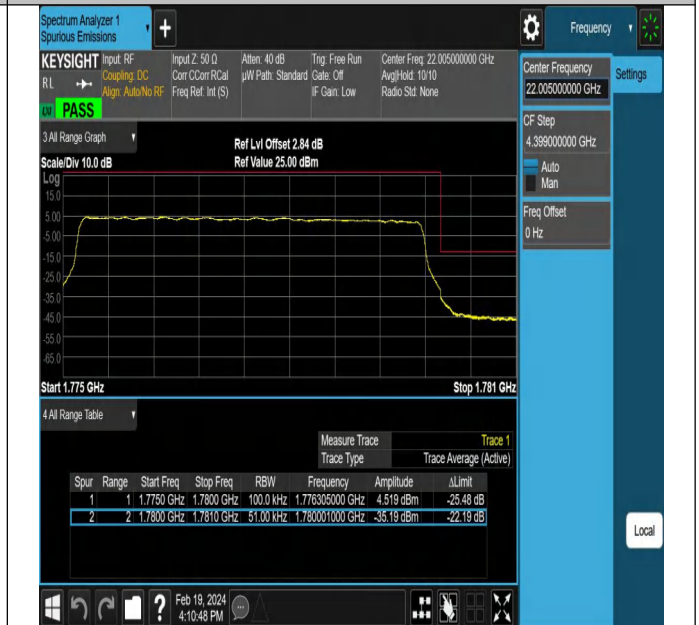
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-Outer_Full-PASS



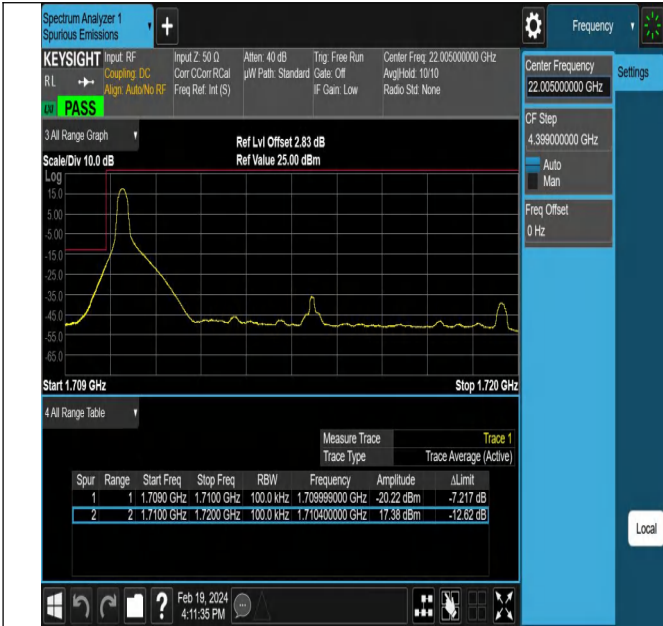
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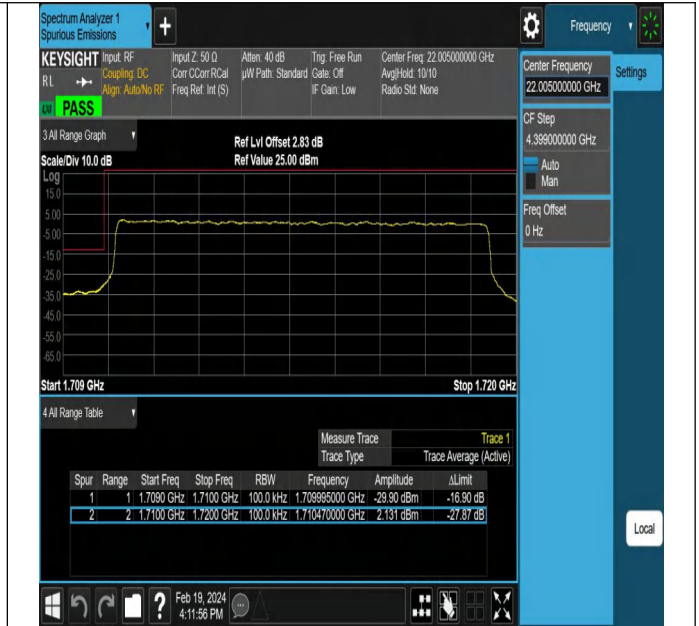


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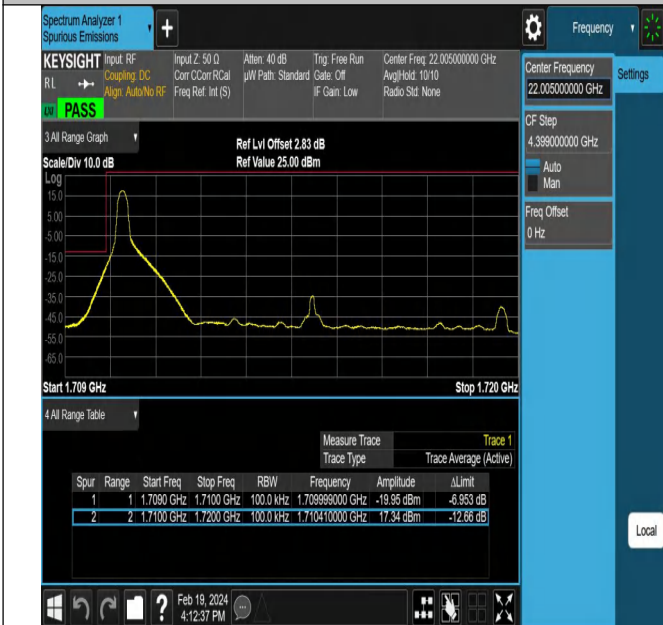
uter_Full-PASS



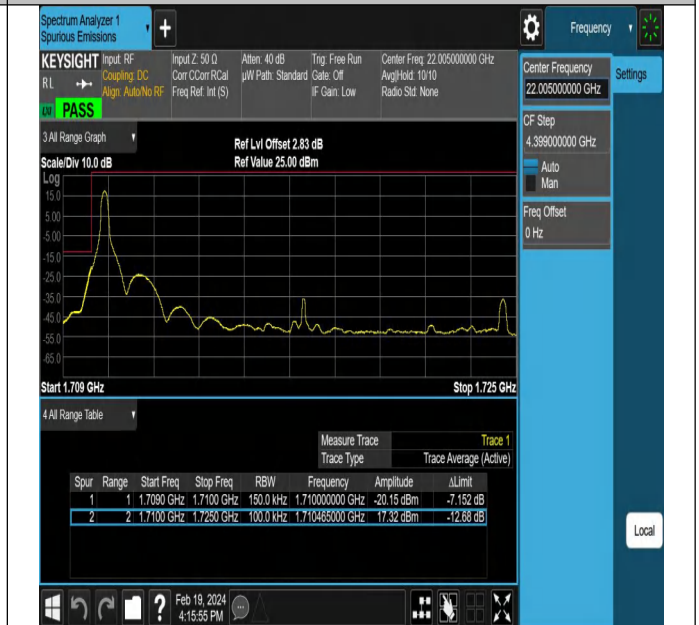
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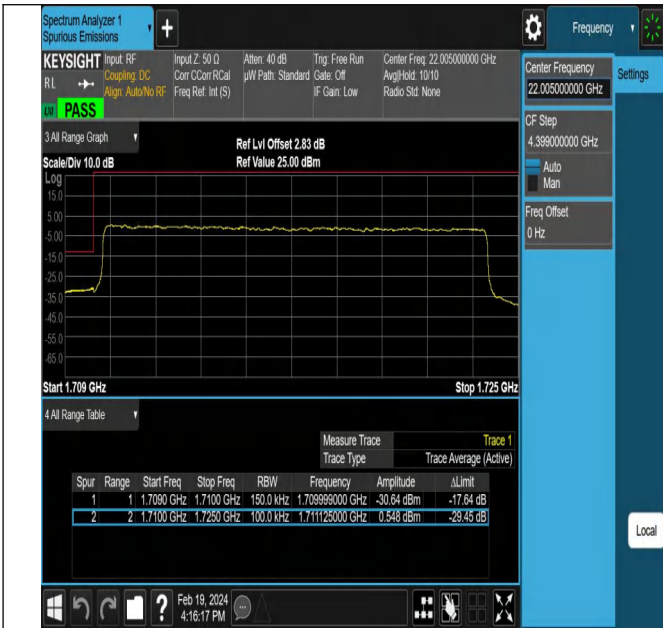
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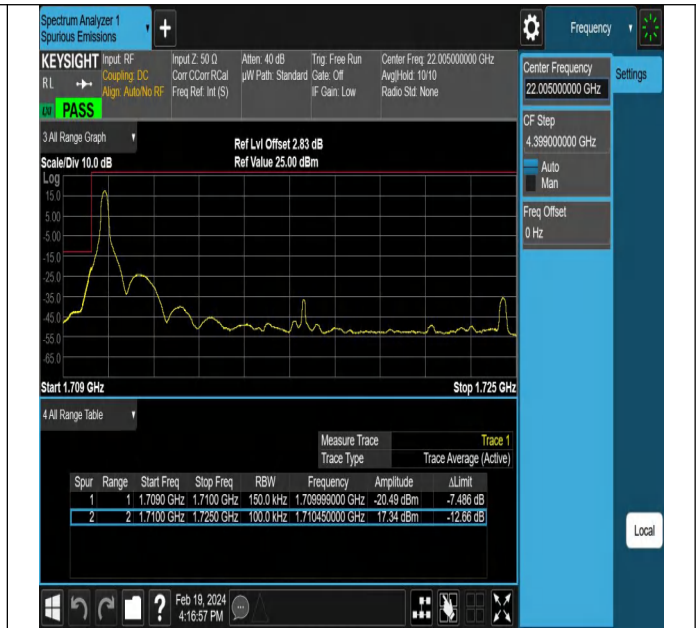
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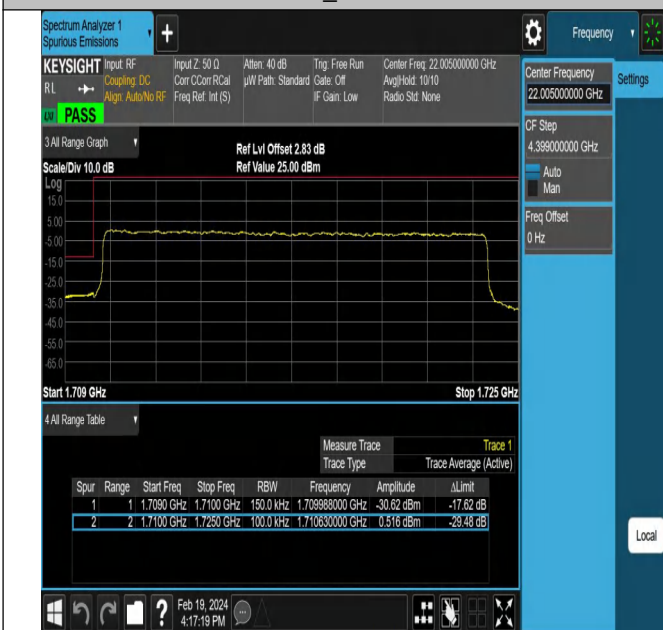
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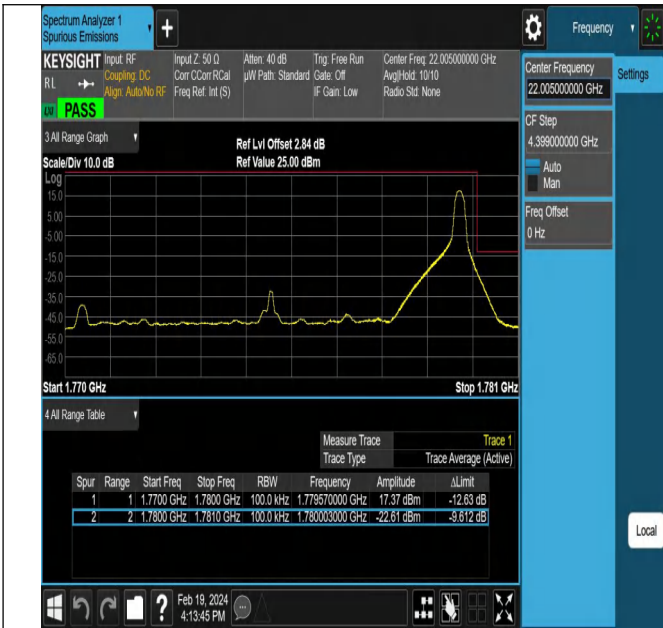
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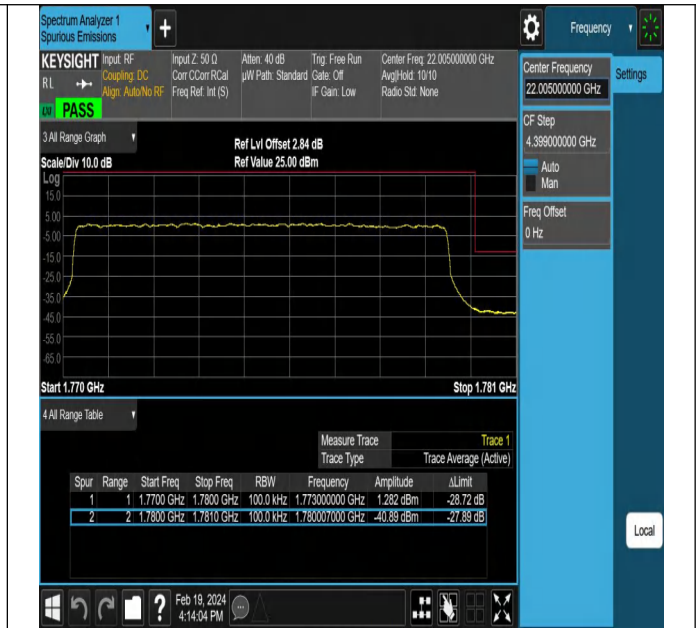
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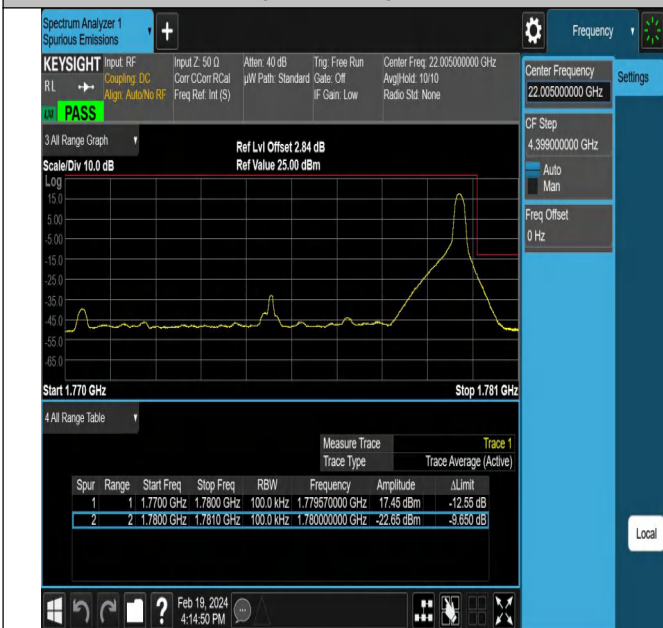
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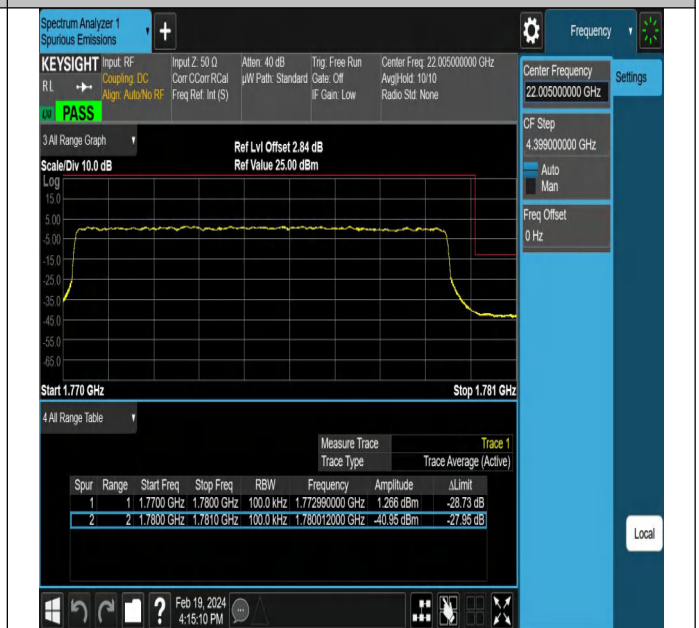
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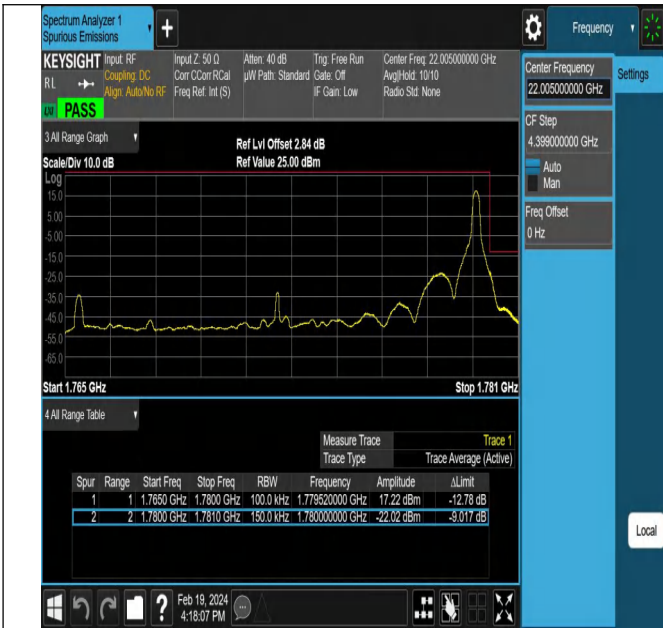
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K-Outer_Full-PASS



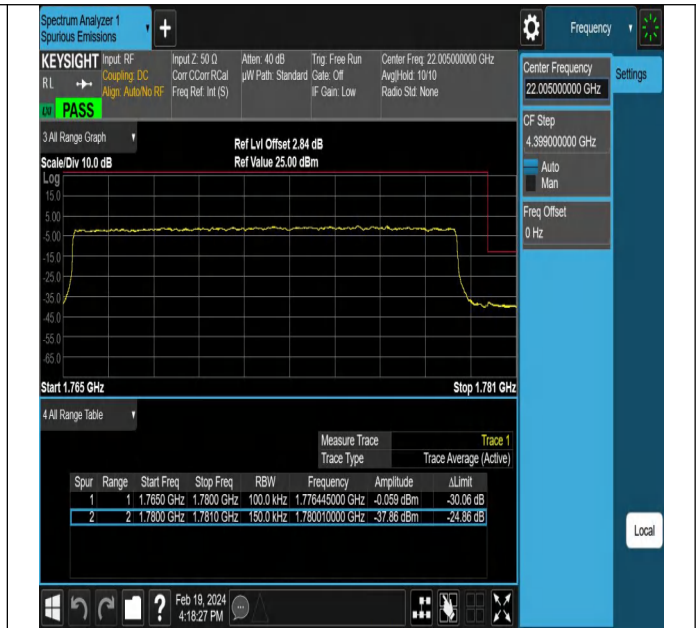
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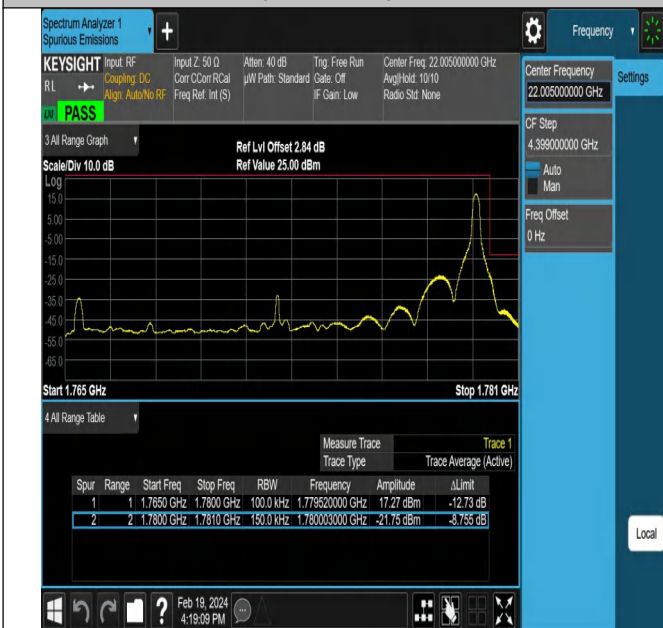
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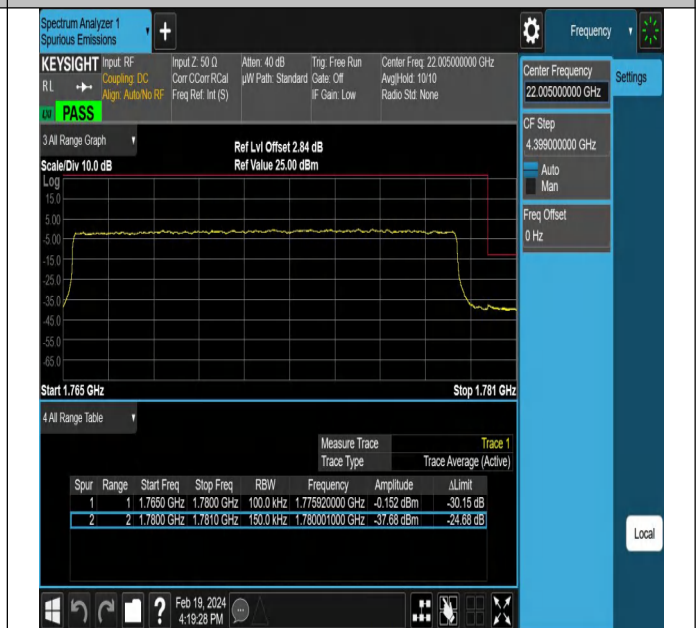
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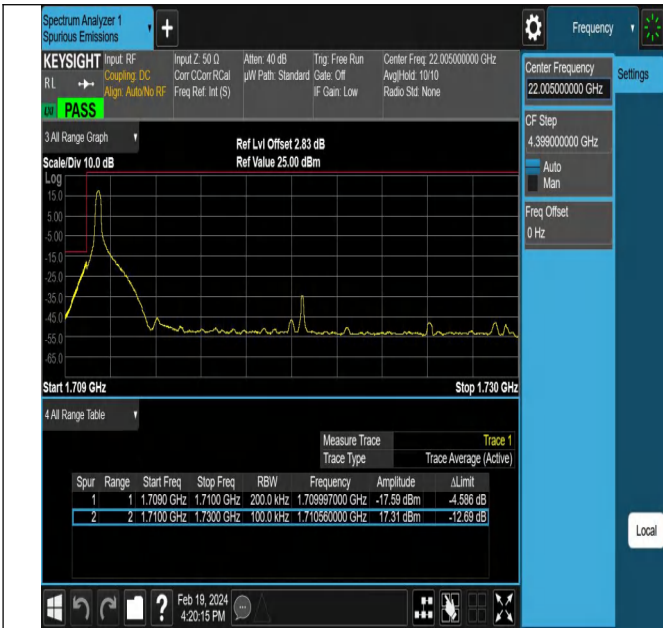
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K-Outer_Full-PASS



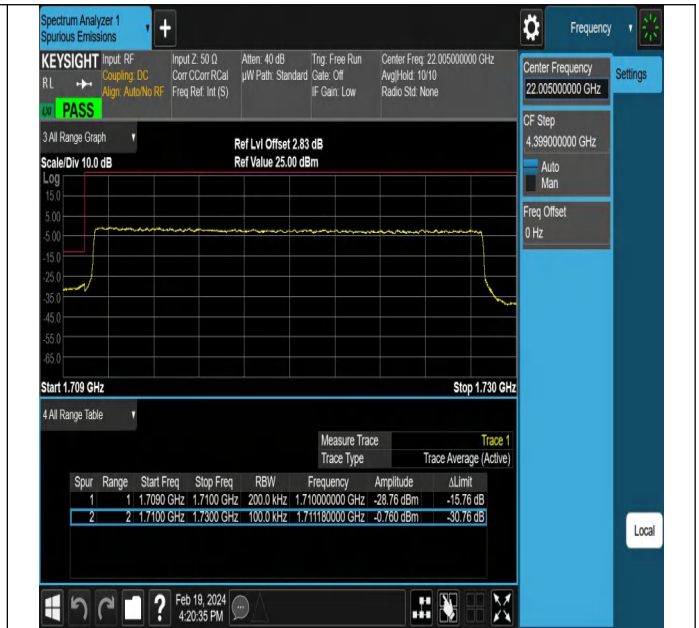
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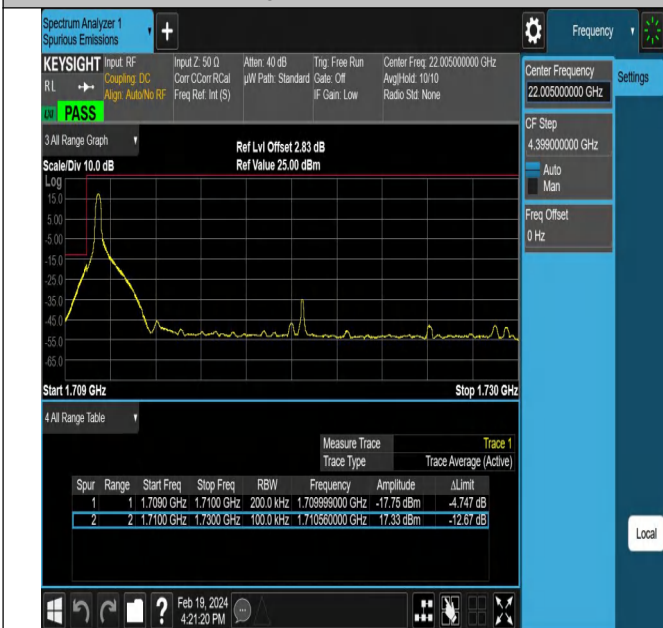
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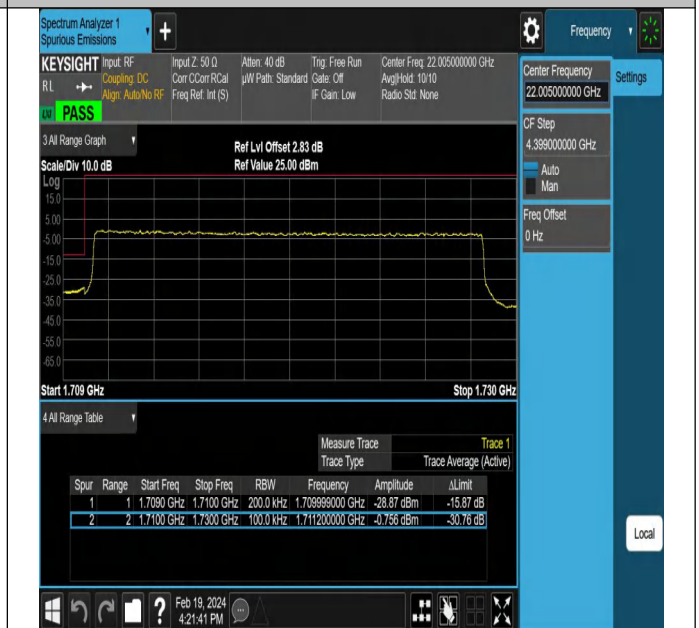
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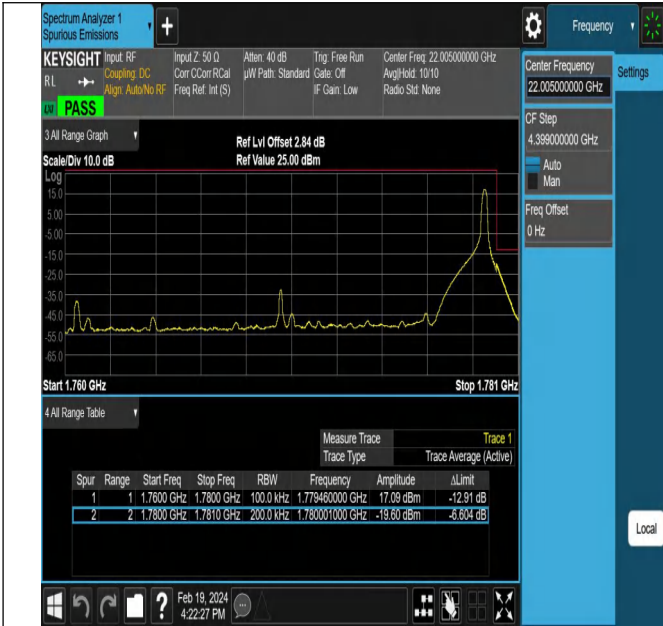
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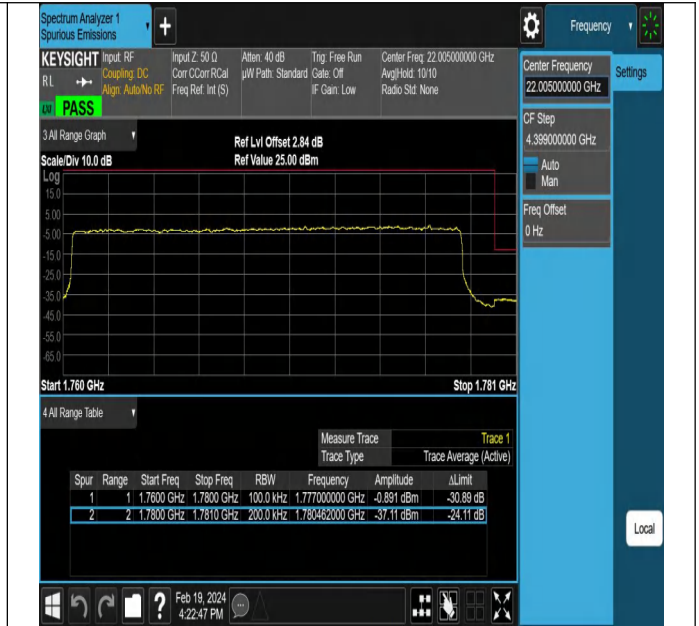
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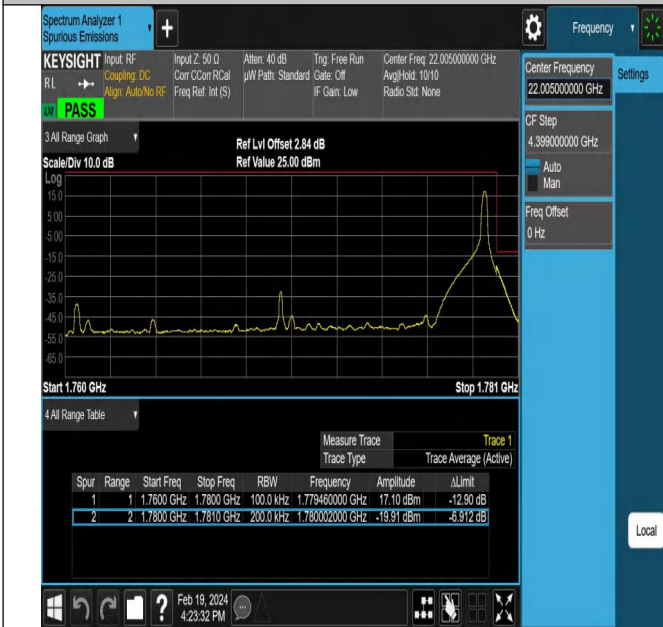
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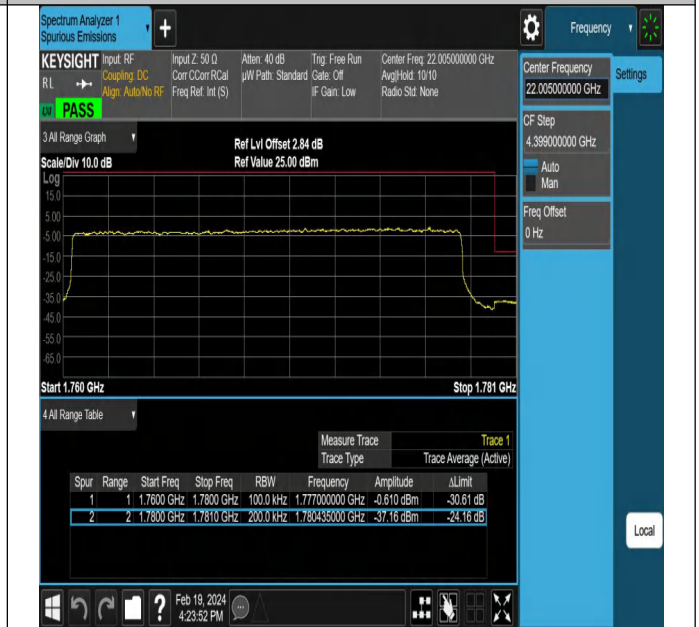
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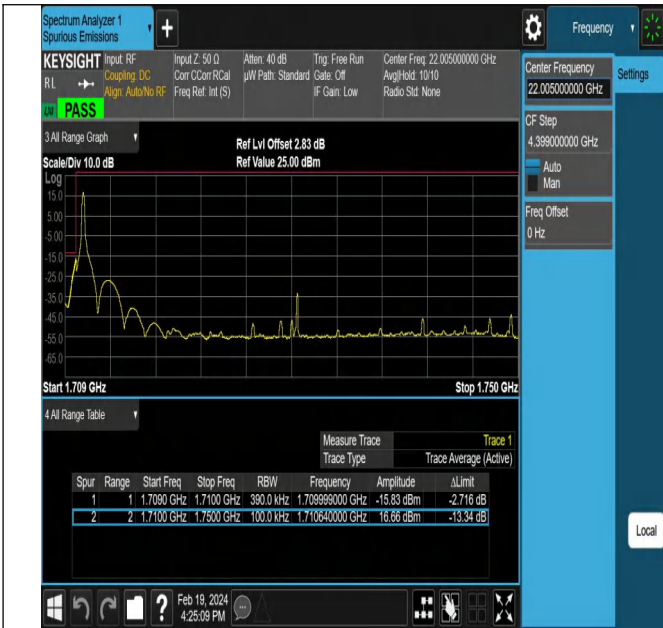
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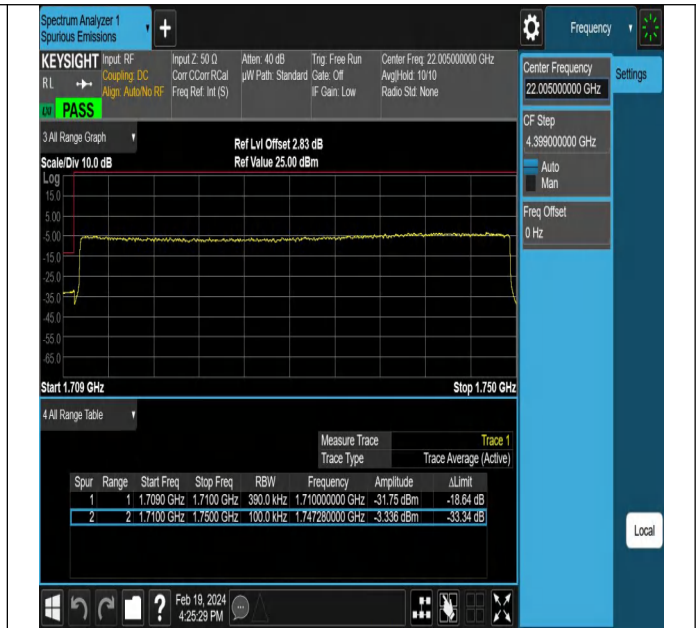
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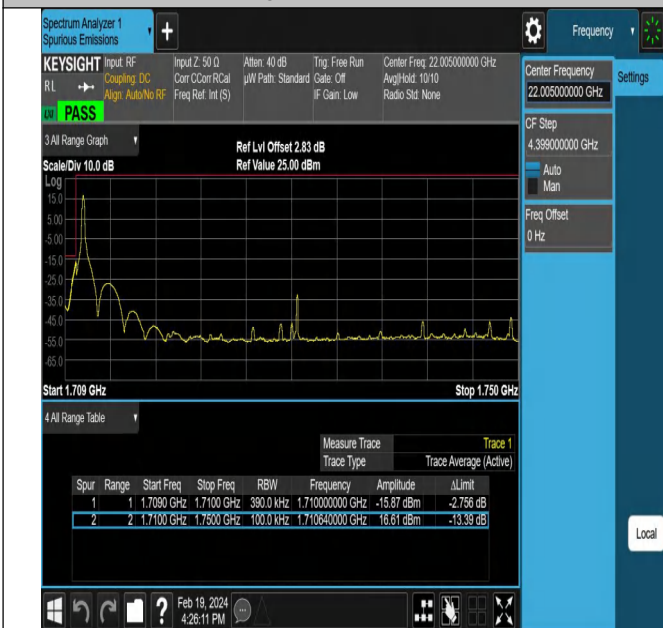
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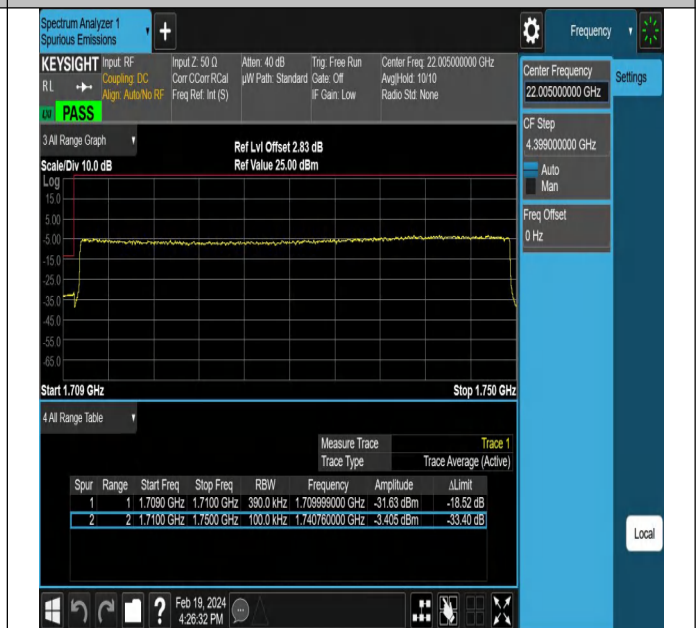
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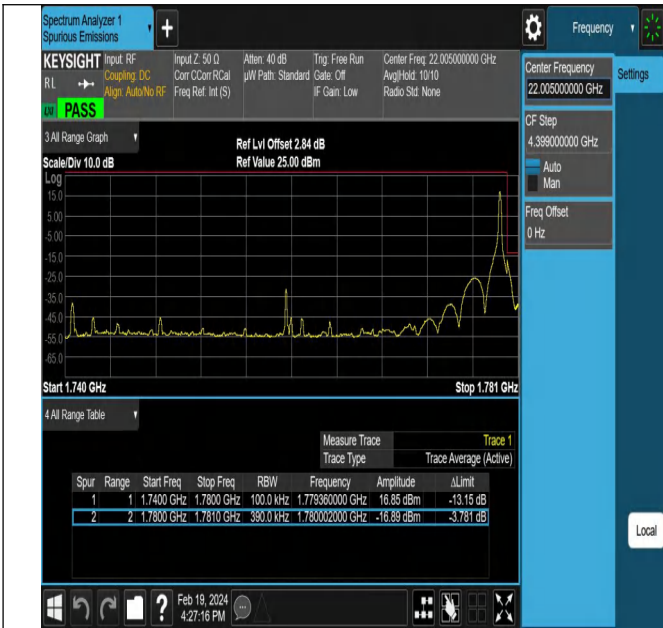
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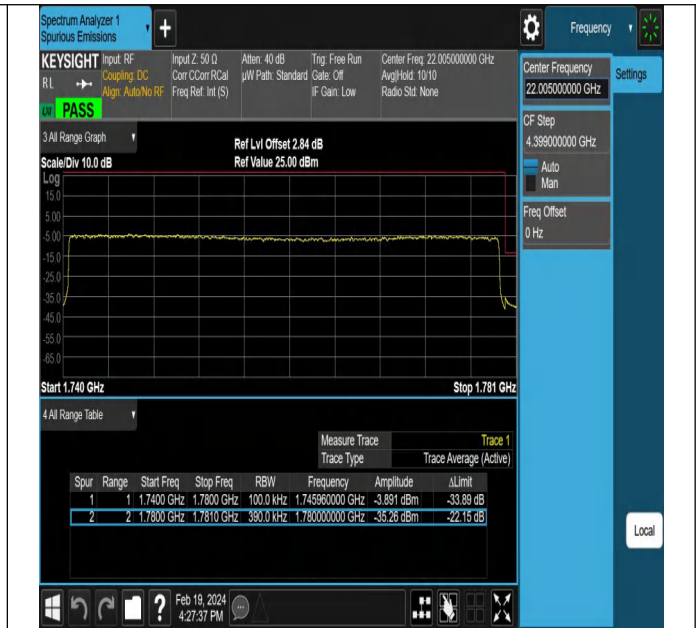
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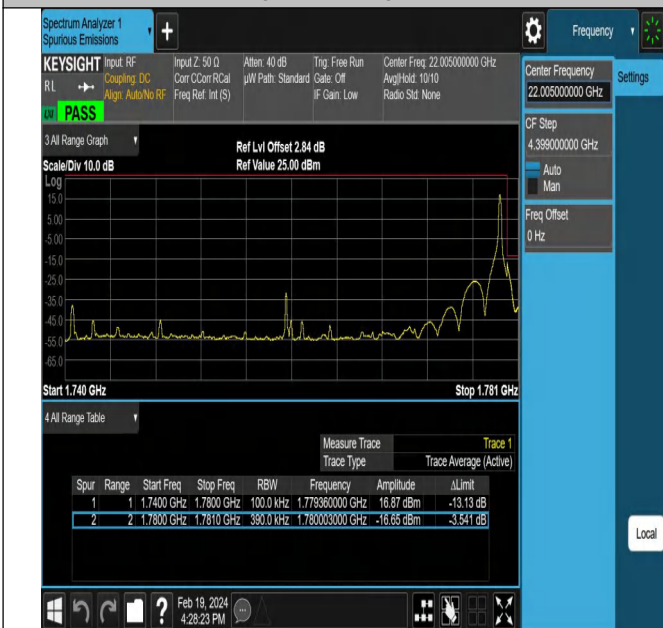
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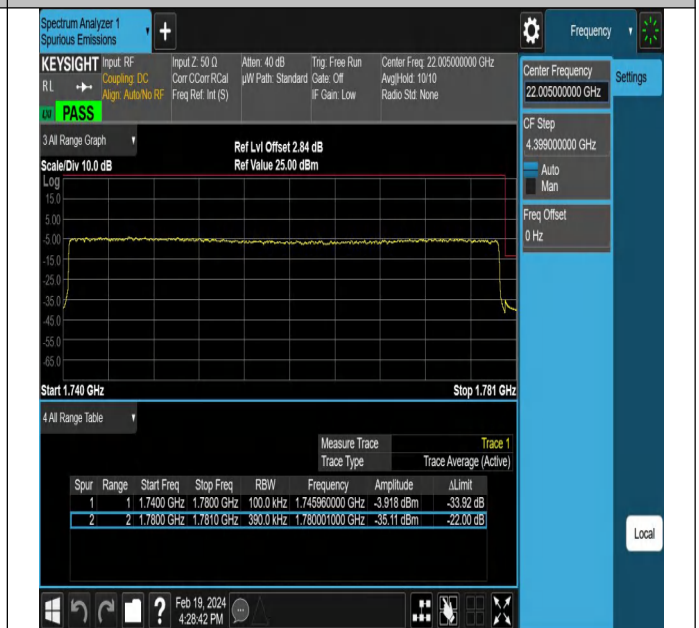
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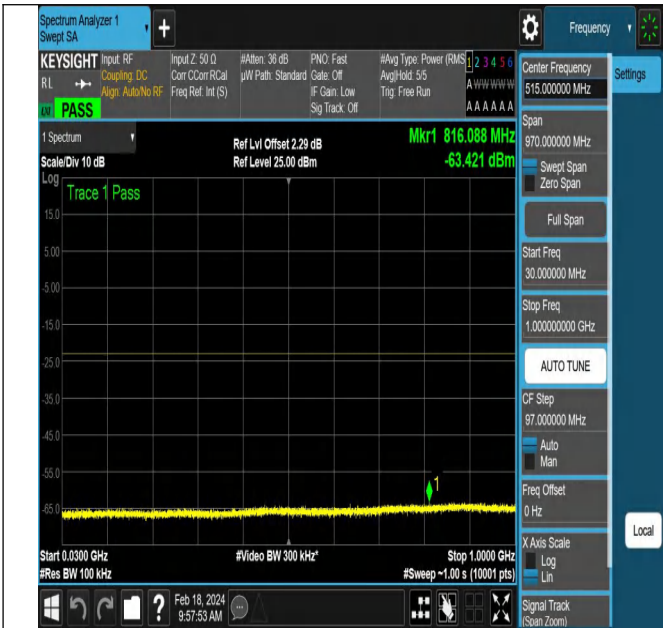
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K-Outer_Full-PASS



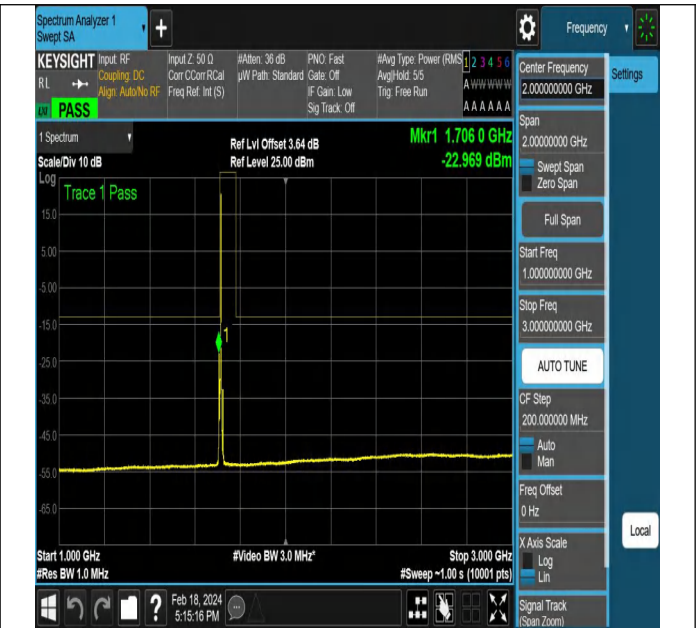
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ge_1RB_Right-PASS



NTNV-DC_5A_n66A-PC3-15-5+40-M+H-QPSK-DFT-QPSK-
Outer_Full-PASS



NTNV-DC_5A_n66A-PC3-15-5+5-M+L-QPSK-DFT-PI2BPSK-Edge_1RB_Right-PASS



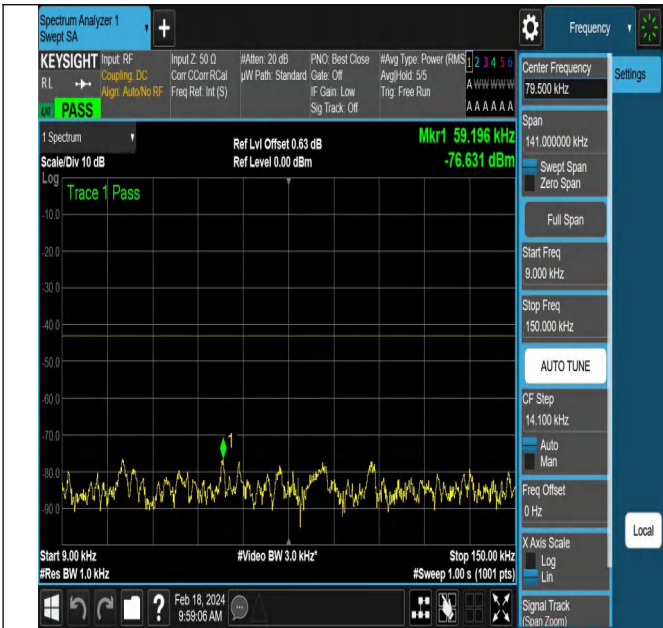
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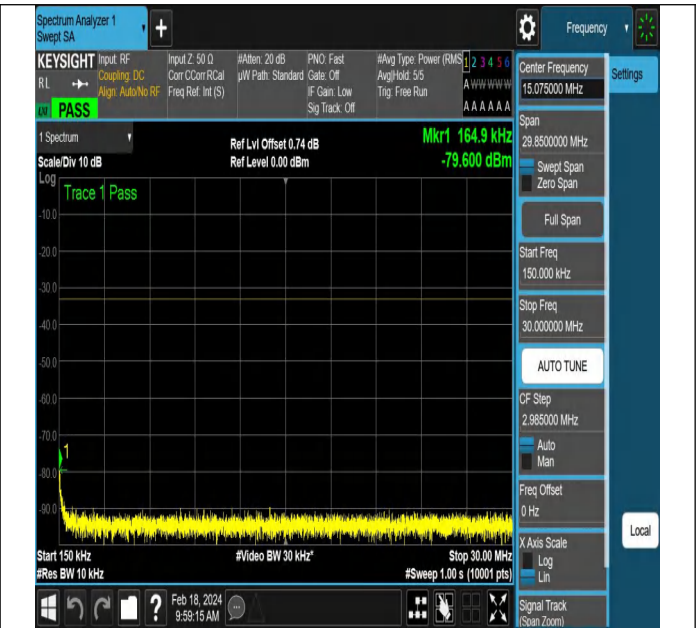
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NTNV-DC_5A_n66A-PC3-15-5+5-M+L-QPSK-DFT-PI2BPSK-Edge_1RB_Right-PASS



NTNV-DC_5A_n66A-PC3-15-5+5-M+L-QPSK-DFT-QPSK-Edge
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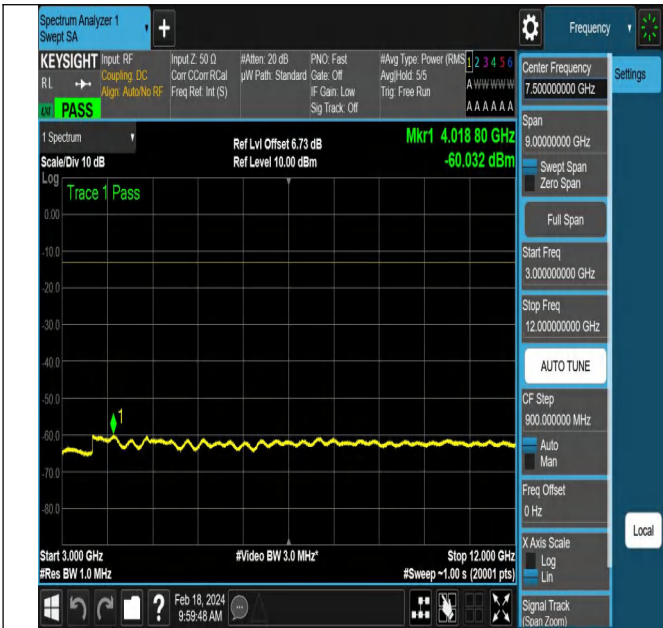
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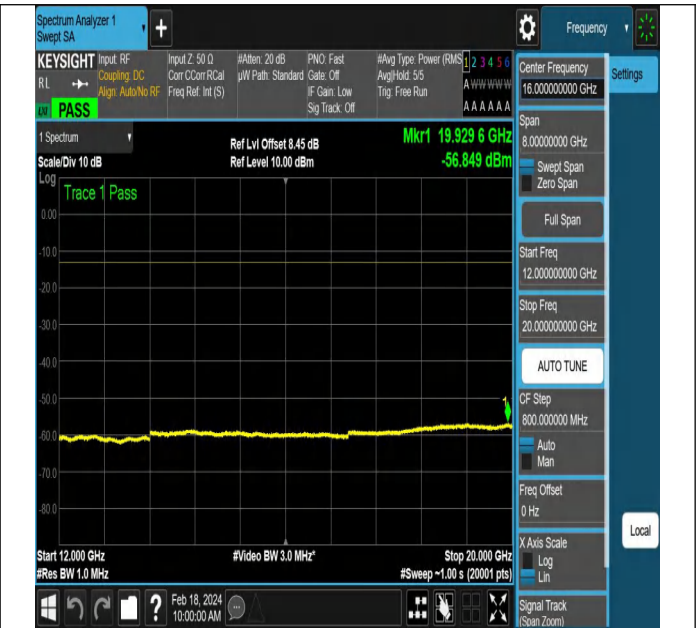
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e_1RB_Left-PASS



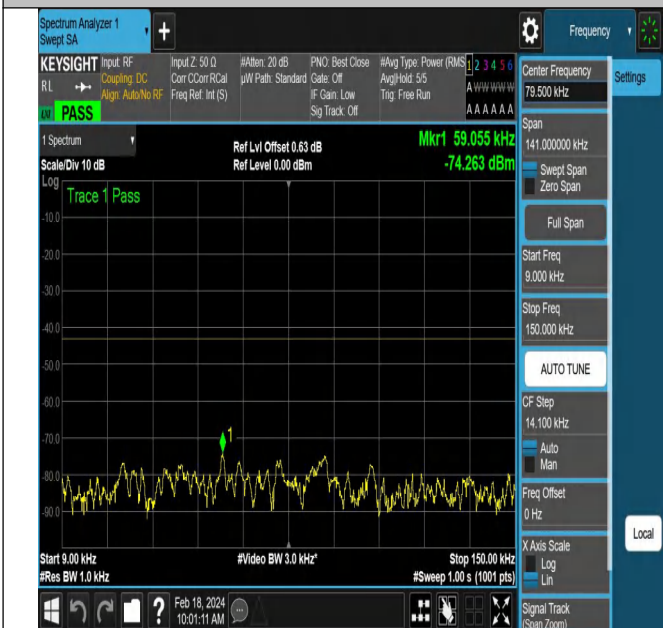
NTNV-DC_5A_n66A-PC3-15-5+5-M+L-QPSK-DFT-QPSK-Edge
e_1RB_Left-PASS



NTNV-DC_5A_n66A-PC3-15-5+5-M+L-QPSK-DFT-QPSK-Edge_e_1RB_Left-PASS



NTNV-DC_5A_n66A-PC3-15-5+5-M+L-QPSK-DFT-QPSK-Edge_e_1RB_Left-PASS



NTNV-DC_5A_n66A-PC3-15-5+5-M+L-QPSK-DFT-QPSK-Edge_e_1RB_Right-PASS



NTNV-DC_5A_n66A-PC3-15-5+5-M+L-QPSK-DFT-QPSK-Edge_e_1RB_Right-PASS