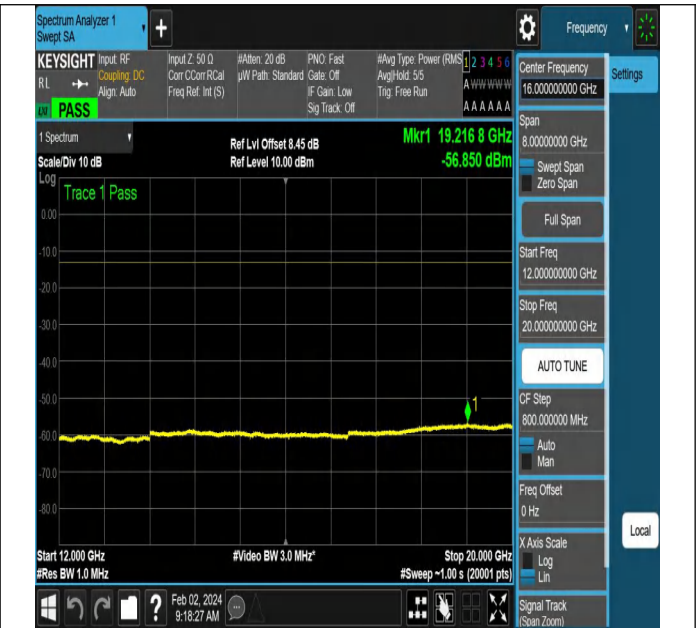


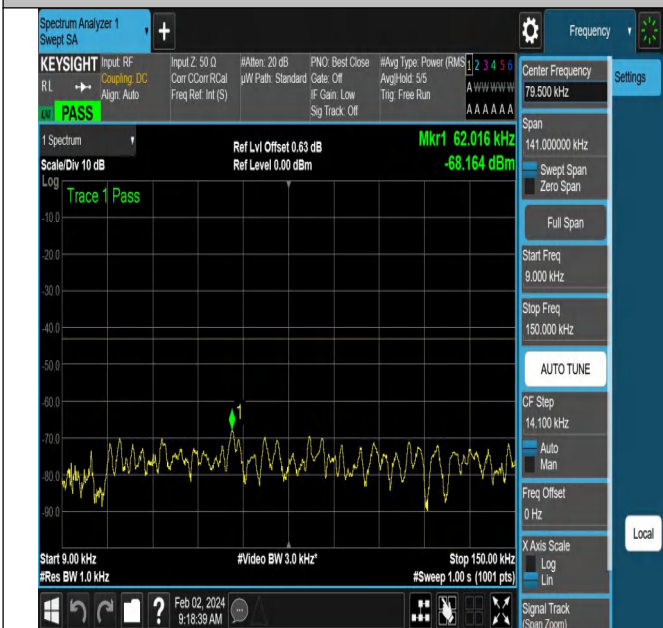
NTNV-N66-PC3-15-40-L-DFT-QPSK-Edge\_1RB\_Left-Ant1-PA

SS



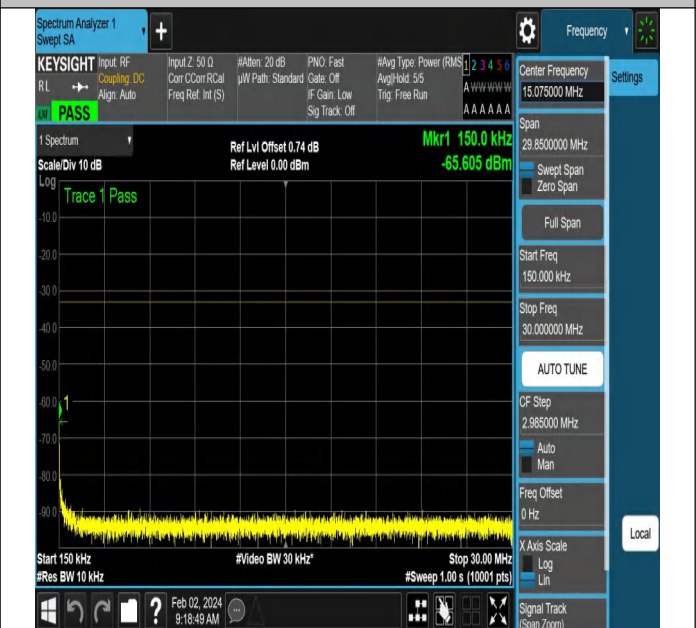
NTNV-N66-PC3-15-40-L-DFT-QPSK-Edge\_1RB\_Left-Ant1-PA

SS



NTNV-N66-PC3-15-40-L-DFT-QPSK-Edge\_1RB\_Right-Ant1-P

ASS

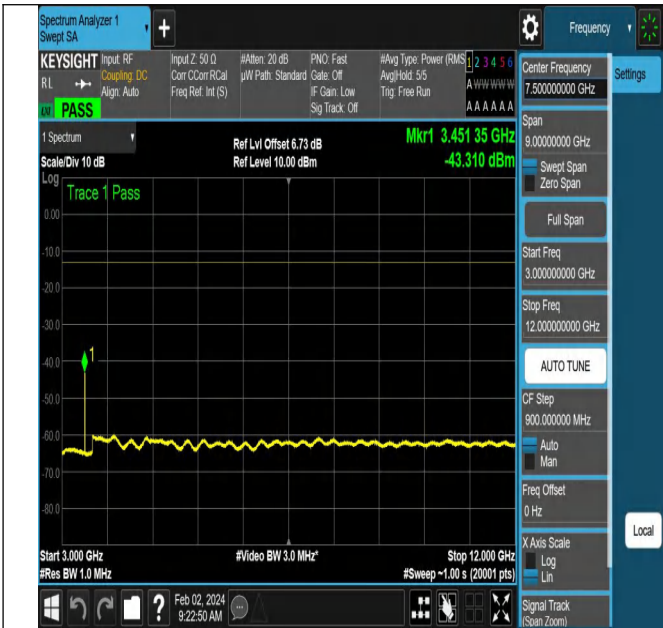


NTNV-N66-PC3-15-40-L-DFT-QPSK-Edge\_1RB\_Right-Ant1-P

ASS

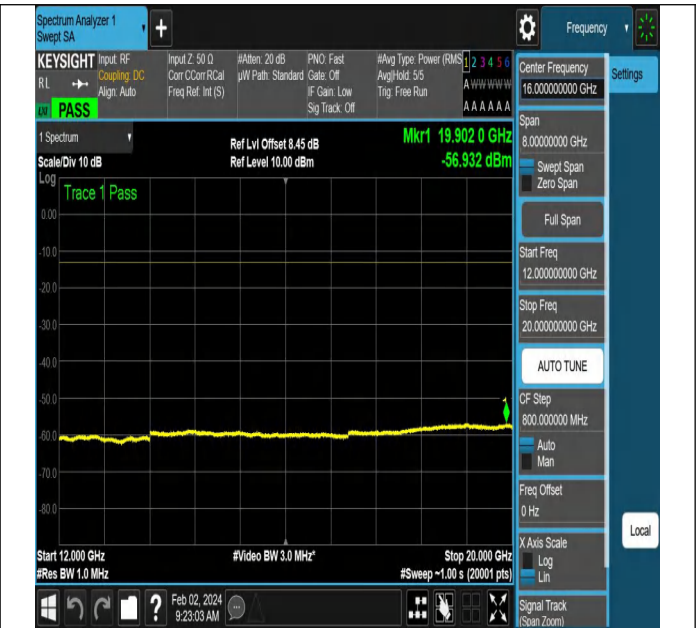






NTNV-N66-PC3-15-40-M-DFT-PI2BPSK-Edge\_1RB\_Left-Ant1

-PASS



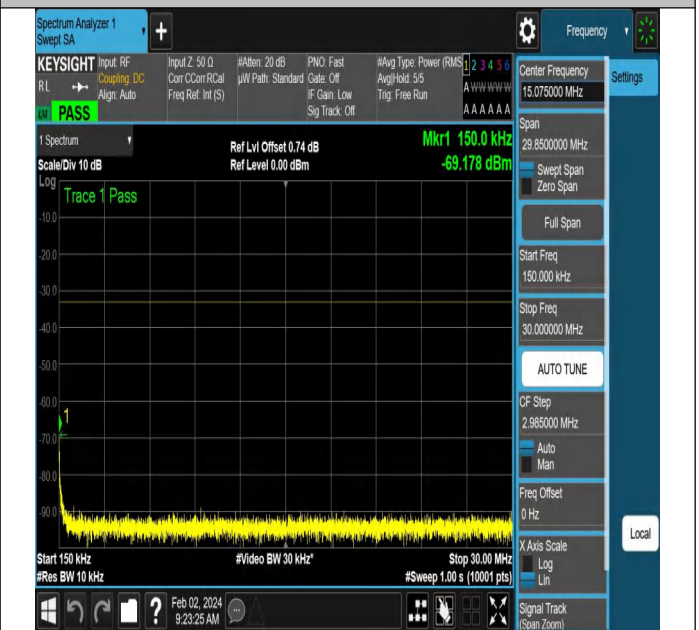
NTNV-N66-PC3-15-40-M-DFT-PI2BPSK-Edge\_1RB\_Left-Ant1

-PASS



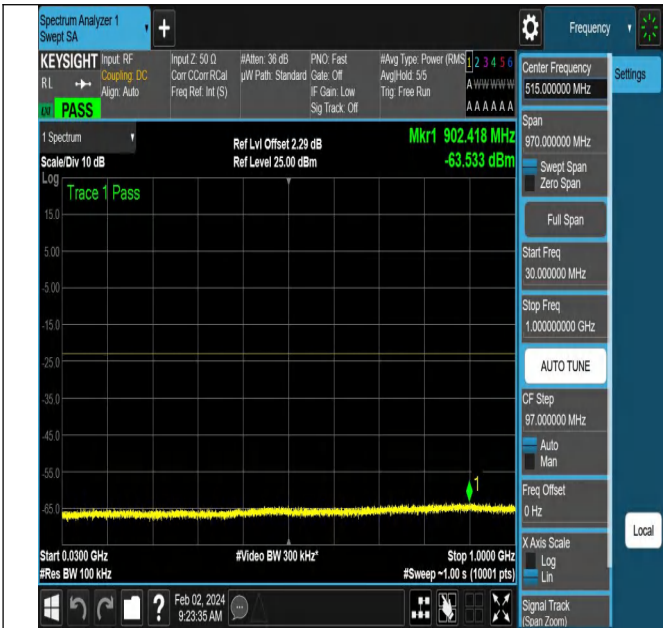
NTNV-N66-PC3-15-40-M-DFT-PI2BPSK-Edge\_1RB\_Right-An

t1-PASS

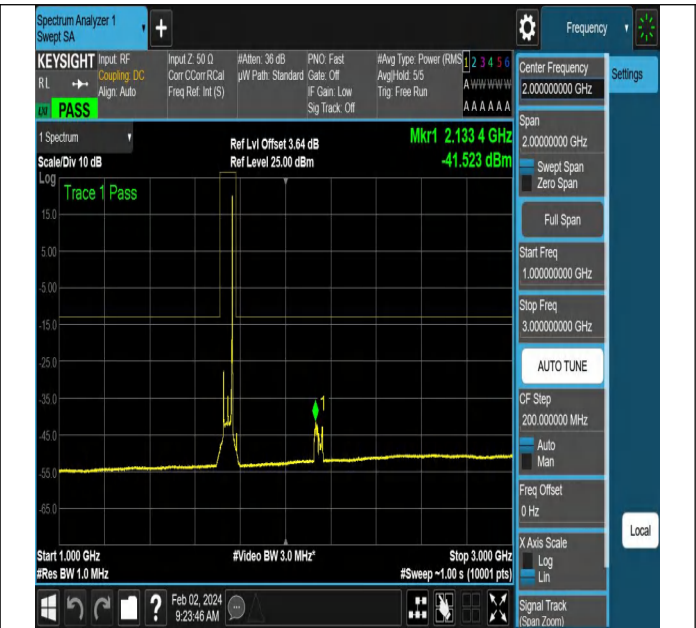


NTNV-N66-PC3-15-40-M-DFT-PI2BPSK-Edge\_1RB\_Right-An

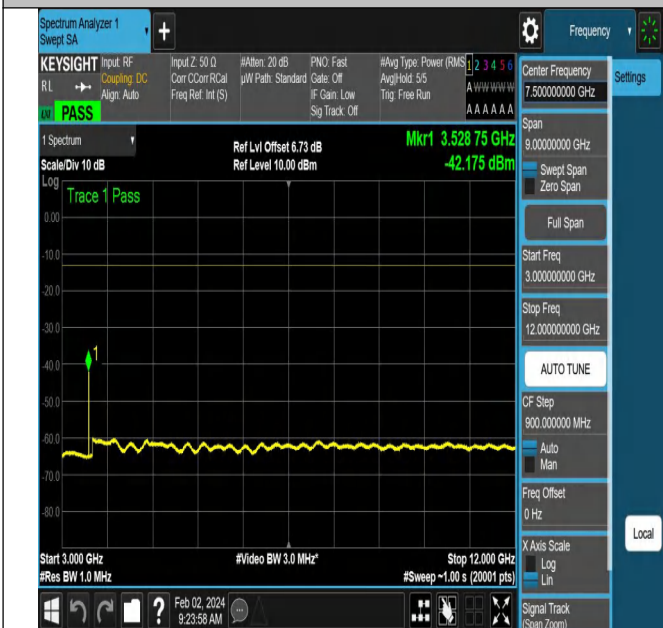
t1-PASS



NTNV-N66-PC3-15-40-M-DFT-PI2BPSK-Edge\_1RB\_Right-An  
t1-PASS



NTNV-N66-PC3-15-40-M-DFT-PI2BPSK-Edge\_1RB\_Right-An  
t1-PASS

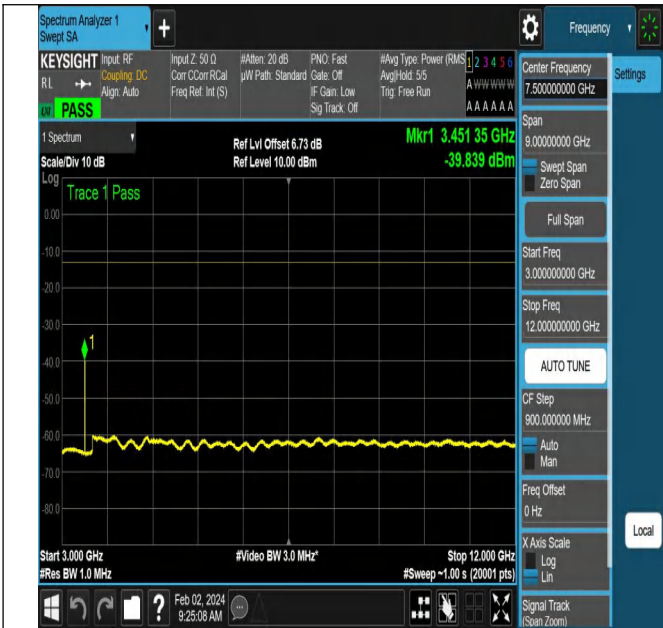


NTNV-N66-PC3-15-40-M-DFT-PI2BPSK-Edge\_1RB\_Right-An  
t1-PASS



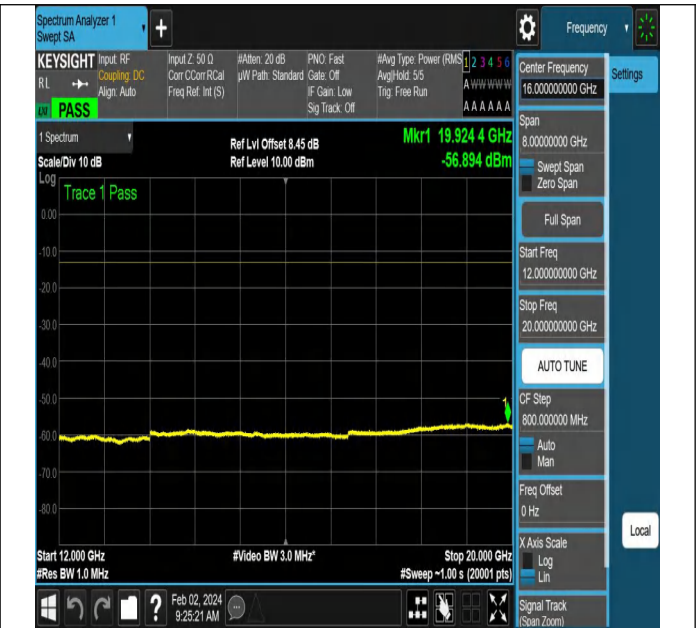
NTNV-N66-PC3-15-40-M-DFT-PI2BPSK-Edge\_1RB\_Right-An  
t1-PASS





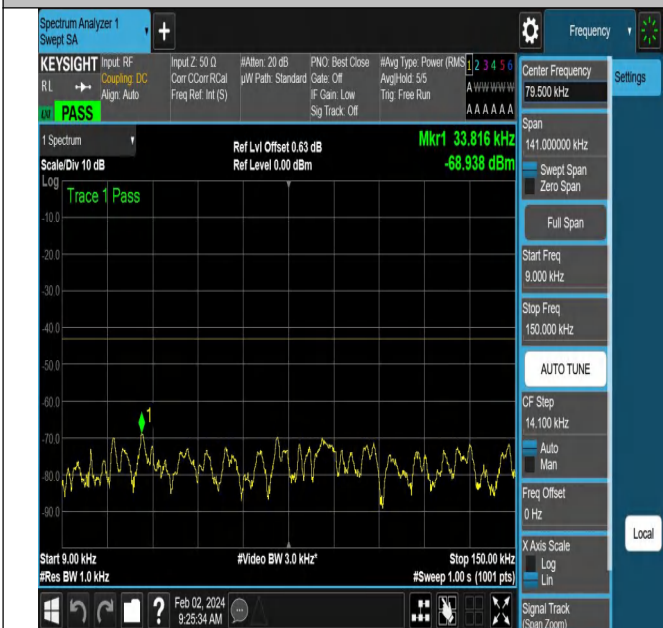
NTNV-N66-PC3-15-40-M-DFT-QPSK-Edge\_1RB\_Left-Ant1-P

ASS



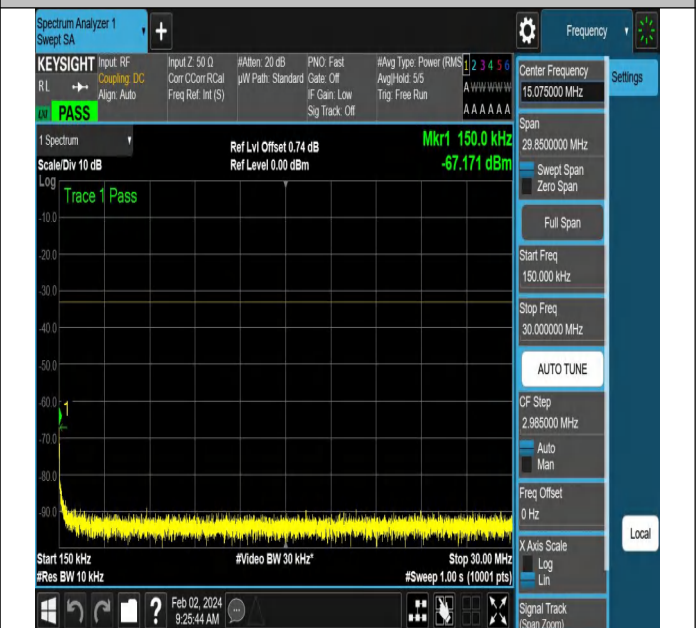
NTNV-N66-PC3-15-40-M-DFT-QPSK-Edge\_1RB\_Left-Ant1-P

ASS



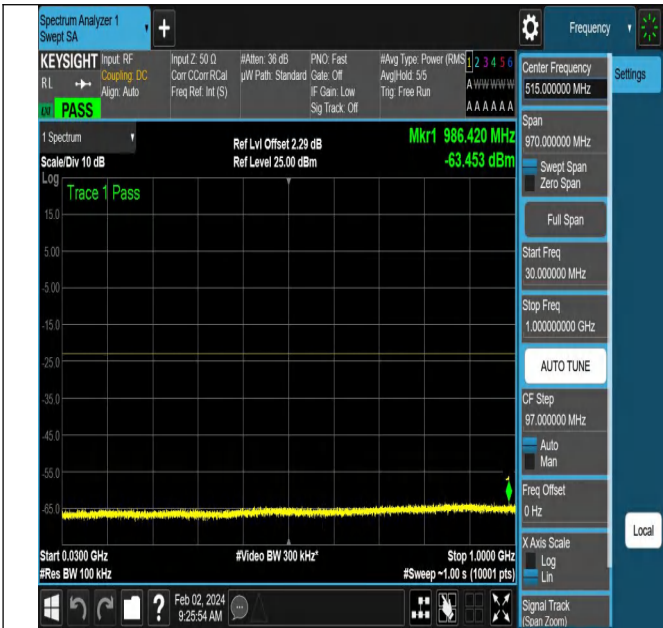
NTNV-N66-PC3-15-40-M-DFT-QPSK-Edge\_1RB\_Right-Ant1-

PASS



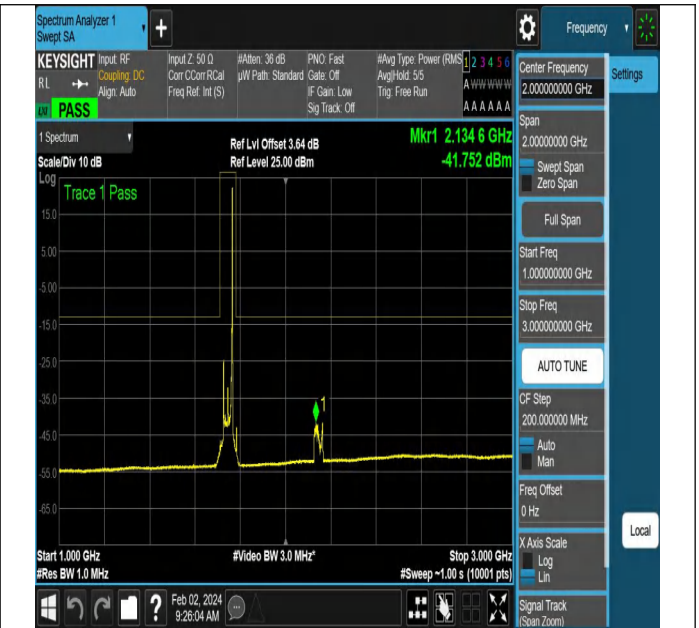
NTNV-N66-PC3-15-40-M-DFT-QPSK-Edge\_1RB\_Right-Ant1-

PASS



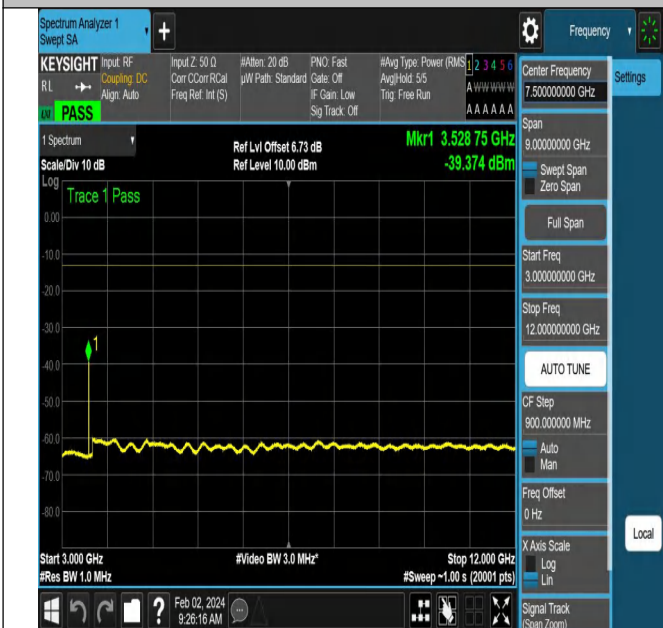
NTNV-N66-PC3-15-40-M-DFT-QPSK-Edge\_1RB\_Right-Ant1-

PASS



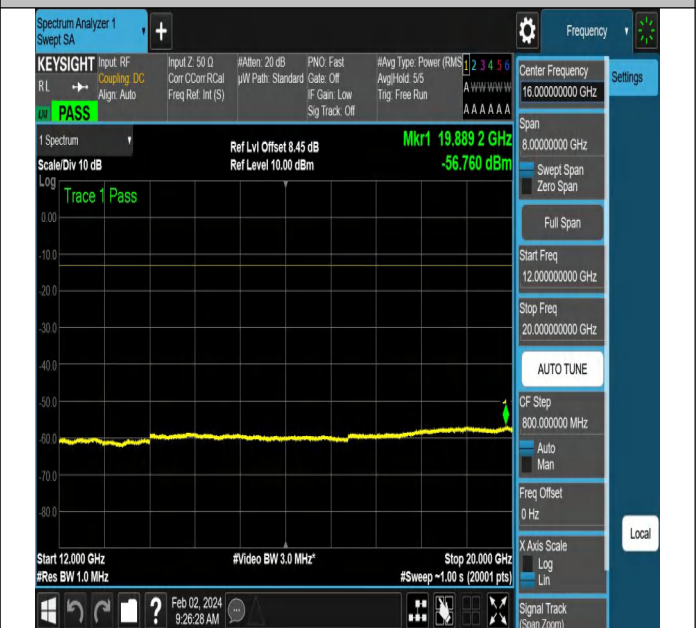
NTNV-N66-PC3-15-40-M-DFT-QPSK-Edge\_1RB\_Right-Ant1-

PASS



NTNV-N66-PC3-15-40-M-DFT-QPSK-Edge\_1RB\_Right-Ant1-

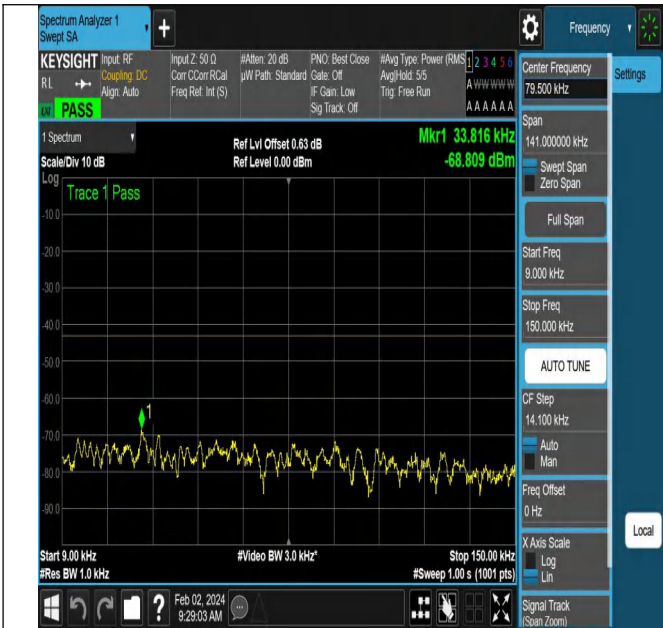
PASS



NTNV-N66-PC3-15-40-M-DFT-QPSK-Edge\_1RB\_Right-Ant1-

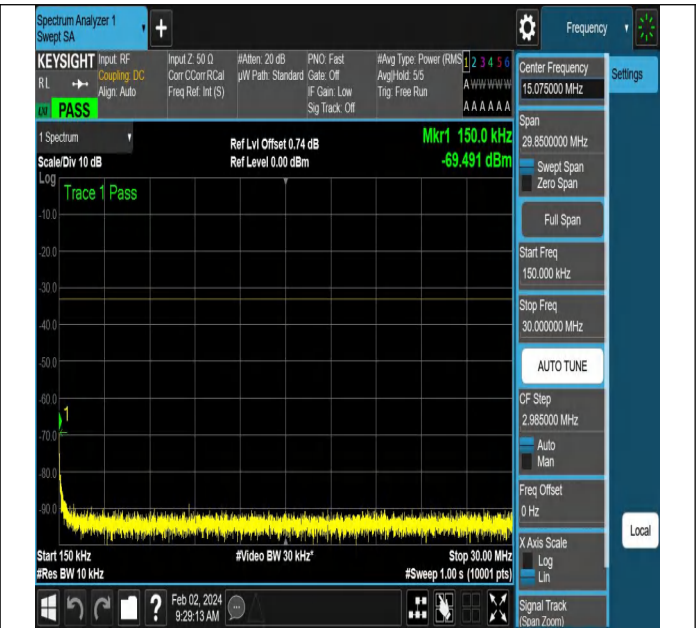
PASS





NTNV-N66-PC3-15-40-H-DFT-PI2BPSK-Edge\_1RB\_Left-Ant1

-PASS



NTNV-N66-PC3-15-40-H-DFT-PI2BPSK-Edge\_1RB\_Left-Ant1

-PASS



NTNV-N66-PC3-15-40-H-DFT-PI2BPSK-Edge\_1RB\_Left-Ant1

-PASS



NTNV-N66-PC3-15-40-H-DFT-PI2BPSK-Edge\_1RB\_Left-Ant1

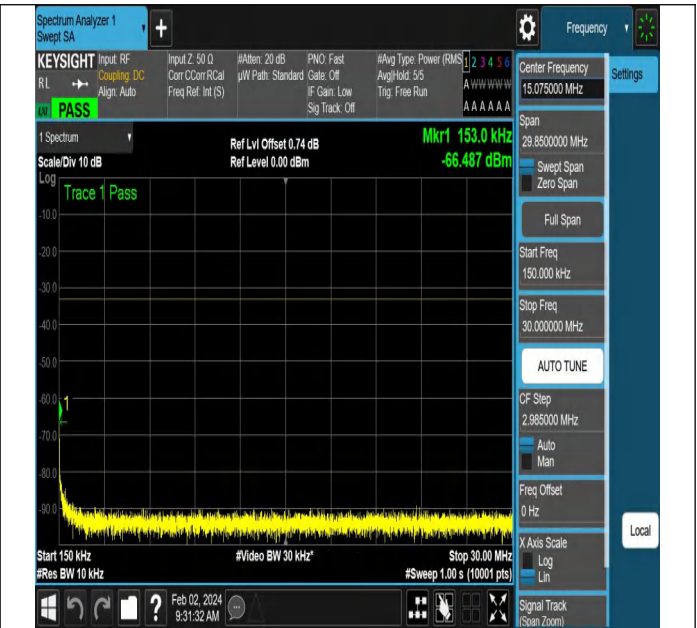
-PASS







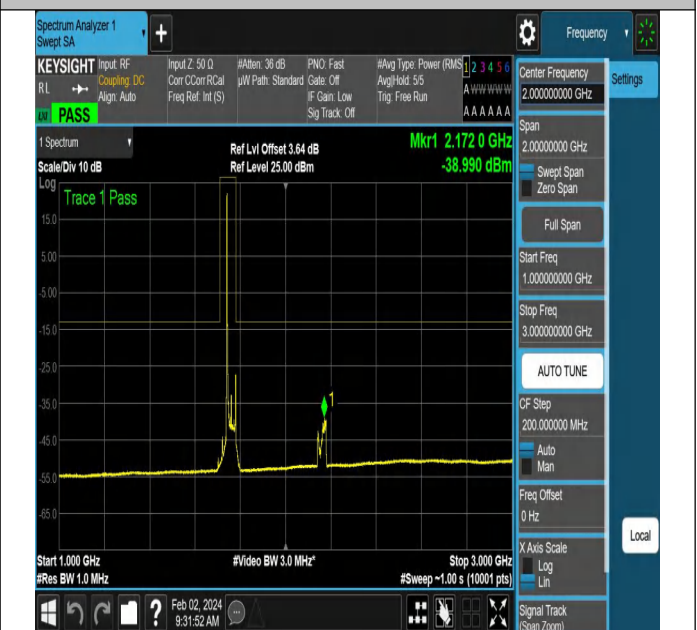
NTNV-N66-PC3-15-40-H-DFT-QPSK-Edge\_1RB\_Left-Ant1-P  
ASS



NTNV-N66-PC3-15-40-H-DFT-QPSK-Edge\_1RB\_Left-Ant1-P  
ASS



NTNV-N66-PC3-15-40-H-DFT-QPSK-Edge\_1RB\_Left-Ant1-P  
ASS



NTNV-N66-PC3-15-40-H-DFT-QPSK-Edge\_1RB\_Left-Ant1-P  
ASS





## Frequency Stability for SA

### Test Result

#### Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N66	15	40	DFT-QPSK	M	Outer_Full	VL	NT	-2.63	0.00	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	VN	NT	-2.43	0.00	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	VH	NT	-3.64	0.00	PASS

#### Frequency Error VS. Temperature

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N66	15	40	DFT-QPSK	M	Outer_Full	NV	-30	-2.900000	-0.001662	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	NV	-20	-2.200000	-0.001261	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	NV	-10	-1.220000	-0.000699	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	NV	0	-3.490000	-0.002000	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	NV	10	-4.060000	-0.002327	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	NV	20	-4.460000	-0.002556	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	NV	30	-3.280000	-0.001880	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	NV	40	-4.500000	-0.002579	PASS
N66	15	40	DFT-QPSK	M	Outer_Full	NV	50	-2.840000	-0.001628	PASS

## Other PA with Ant.2 for NSA mode

### Effective (Isotropic) Radiated Power Output Data for NSA

#### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Power Class	Verdict
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.13	22.52	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.17	22.56	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	21.95	22.34	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+L	Inner_Full	22.99	23.38	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	22.14	22.53	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	22.15	22.54	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+L	Outer_Full	21.93	22.32	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+L	Inner_Full	22.97	23.36	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	21.55	21.94	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	21.52	21.91	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+L	Outer_Full	21.09	21.48	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+L	Inner_Full	21.83	22.22	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	20.37	20.76	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	20.47	20.86	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+L	Outer_Full	20.61	21.00	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+L	Inner_Full	20.66	21.05	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	18.59	18.98	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	18.67	19.06	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+L	Outer_Full	18.50	18.89	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+L	Inner_Full	18.49	18.88	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	19.68	20.07	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	19.86	20.25	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	19.07	19.46	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+L	Inner_Full	21.50	21.89	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	19.69	20.08	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	19.88	20.27	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	19.14	19.53	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+L	Inner_Full	21.14	21.53	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	19.35	19.74	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	19.53	19.92	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+L	Outer_Full	18.63	19.02	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+L	Inner_Full	19.53	19.92	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	16.23	16.62	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	16.39	16.78	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+L	Outer_Full	16.52	16.91	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+L	Inner_Full	16.29	16.68	PC3	PASS



DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	16.13	16.52	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	16.02	16.41	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	16.27	16.66	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+M	Inner_Full	16.32	16.71	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	21.76	22.15	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	21.70	22.09	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+M	Outer_Full	21.65	22.04	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+M	Inner_Full	22.69	23.08	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	20.78	21.17	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	20.63	21.02	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+M	Outer_Full	20.72	21.11	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+M	Inner_Full	21.84	22.23	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	20.38	20.77	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	20.39	20.78	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+M	Outer_Full	20.23	20.62	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+M	Inner_Full	20.21	20.60	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	18.23	18.62	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	18.12	18.51	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+M	Outer_Full	18.20	18.59	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+M	Inner_Full	18.14	18.53	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	19.25	19.64	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	19.18	19.57	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	18.47	18.86	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+M	Inner_Full	21.38	21.77	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	19.49	19.88	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	19.30	19.69	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	18.62	19.01	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+M	Inner_Full	20.75	21.14	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	18.70	19.09	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	18.44	18.83	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+M	Outer_Full	18.27	18.66	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+M	Inner_Full	19.24	19.63	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	16.34	16.73	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	16.21	16.60	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+M	Outer_Full	16.26	16.65	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+M	Inner_Full	16.38	16.77	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	15.46	15.85	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	15.53	15.92	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	15.94	16.33	PC3	PASS
DC_5A_n66A	15	5+5	DFT-PI2BPSK	M+H	Inner_Full	15.80	16.19	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	21.21	21.60	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	21.21	21.60	PC3	PASS
DC_5A_n66A	15	5+5	DFT-QPSK	M+H	Outer_Full	21.36	21.75	PC3	PASS

DC_5A_n66A	15	5+5	DFT-QPSK	M+H	Inner_Full	22.31	22.70	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	20.65	21.04	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	20.60	20.99	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+H	Outer_Full	20.47	20.86	PC3	PASS
DC_5A_n66A	15	5+5	DFT-16QAM	M+H	Inner_Full	21.18	21.57	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	20.28	20.67	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	20.25	20.64	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+H	Outer_Full	20.03	20.42	PC3	PASS
DC_5A_n66A	15	5+5	DFT-64QAM	M+H	Inner_Full	19.94	20.33	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	17.77	18.16	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	17.83	18.22	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+H	Outer_Full	17.88	18.27	PC3	PASS
DC_5A_n66A	15	5+5	DFT-256QAM	M+H	Inner_Full	17.89	18.28	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	19.64	20.03	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	19.77	20.16	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	18.42	18.81	PC3	PASS
DC_5A_n66A	15	5+5	CP-QPSK	M+H	Inner_Full	21.00	21.39	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	19.26	19.65	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	19.32	19.71	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	18.45	18.84	PC3	PASS
DC_5A_n66A	15	5+5	CP-16QAM	M+H	Inner_Full	20.39	20.78	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	19.00	19.39	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	19.09	19.48	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+H	Outer_Full	17.94	18.33	PC3	PASS
DC_5A_n66A	15	5+5	CP-64QAM	M+H	Inner_Full	18.80	19.19	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	15.47	15.86	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	15.48	15.87	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+H	Outer_Full	15.92	16.31	PC3	PASS
DC_5A_n66A	15	5+5	CP-256QAM	M+H	Inner_Full	15.76	16.15	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Left	16.65	17.04	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Right	16.77	17.16	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+L	Outer_Full	16.53	16.92	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+L	Inner_Full	16.48	16.87	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.66	22.05	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	21.85	22.24	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+L	Outer_Full	21.92	22.31	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+L	Inner_Full	22.93	23.32	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.69	21.08	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.85	21.24	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+L	Outer_Full	21.18	21.57	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+L	Inner_Full	21.97	22.36	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Left	20.21	20.60	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Right	20.33	20.72	PC3	PASS

DC_5A_n66A	15	5+10	DFT-64QAM	M+L	Outer_Full	20.57	20.96	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+L	Inner_Full	20.73	21.12	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Left	18.17	18.56	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Right	18.37	18.76	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+L	Outer_Full	18.65	19.04	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+L	Inner_Full	18.50	18.89	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	19.89	20.28	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	19.98	20.37	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	19.09	19.48	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+L	Inner_Full	21.42	21.81	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	19.62	20.01	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	19.98	20.37	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	19.10	19.49	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+L	Inner_Full	21.23	21.62	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+L	Edge_1RB_Left	19.21	19.60	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+L	Edge_1RB_Right	19.25	19.64	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+L	Outer_Full	18.55	18.94	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+L	Inner_Full	19.59	19.98	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+L	Edge_1RB_Left	16.61	17.00	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+L	Edge_1RB_Right	16.78	17.17	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+L	Outer_Full	16.57	16.96	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+L	Inner_Full	16.51	16.90	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Left	16.12	16.51	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Right	16.11	16.50	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+M	Outer_Full	16.12	16.51	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+M	Inner_Full	16.23	16.62	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	21.57	21.96	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	21.60	21.99	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+M	Outer_Full	21.60	21.99	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+M	Inner_Full	22.67	23.06	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.81	21.20	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.70	21.09	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+M	Outer_Full	20.71	21.10	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+M	Inner_Full	21.61	22.00	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Left	20.24	20.63	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Right	20.26	20.65	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+M	Outer_Full	20.17	20.56	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+M	Inner_Full	20.36	20.75	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Left	18.37	18.76	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Right	18.32	18.71	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+M	Outer_Full	18.17	18.56	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+M	Inner_Full	18.26	18.65	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	20.19	20.58	PC3	PASS

DC_5A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	20.07	20.46	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	18.73	19.12	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+M	Inner_Full	21.24	21.63	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	19.34	19.73	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	19.39	19.78	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	18.76	19.15	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+M	Inner_Full	20.77	21.16	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+M	Edge_1RB_Left	19.53	19.92	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+M	Edge_1RB_Right	19.36	19.75	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+M	Outer_Full	18.14	18.53	PC3	PASS
DC_5A_n66A	15	5+10	CP-64QAM	M+M	Inner_Full	19.23	19.62	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+M	Edge_1RB_Left	16.11	16.50	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+M	Edge_1RB_Right	16.10	16.49	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+M	Outer_Full	16.13	16.52	PC3	PASS
DC_5A_n66A	15	5+10	CP-256QAM	M+M	Inner_Full	16.43	16.82	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Left	15.31	15.70	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Right	15.08	15.47	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+H	Outer_Full	15.85	16.24	PC3	PASS
DC_5A_n66A	15	5+10	DFT-PI2BPSK	M+H	Inner_Full	15.98	16.37	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	21.40	21.79	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	21.41	21.80	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+H	Outer_Full	21.22	21.61	PC3	PASS
DC_5A_n66A	15	5+10	DFT-QPSK	M+H	Inner_Full	22.36	22.75	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	20.33	20.72	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	20.35	20.74	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+H	Outer_Full	20.46	20.85	PC3	PASS
DC_5A_n66A	15	5+10	DFT-16QAM	M+H	Inner_Full	21.41	21.80	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Left	20.05	20.44	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Right	20.09	20.48	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+H	Outer_Full	19.96	20.35	PC3	PASS
DC_5A_n66A	15	5+10	DFT-64QAM	M+H	Inner_Full	20.10	20.49	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Left	18.11	18.50	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Right	18.14	18.53	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+H	Outer_Full	17.86	18.25	PC3	PASS
DC_5A_n66A	15	5+10	DFT-256QAM	M+H	Inner_Full	17.97	18.36	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	19.27	19.66	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	19.18	19.57	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	18.33	18.72	PC3	PASS
DC_5A_n66A	15	5+10	CP-QPSK	M+H	Inner_Full	21.18	21.57	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	18.86	19.25	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	18.78	19.17	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	18.43	18.82	PC3	PASS
DC_5A_n66A	15	5+10	CP-16QAM	M+H	Inner_Full	20.64	21.03	PC3	PASS