

DC_5A_n2A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	16.36	16.36	PC3	PASS
DC_5A_n2A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	16.19	16.19	PC3	PASS
DC_5A_n2A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	16.48	16.48	PC3	PASS
DC_5A_n2A	15	5+5	DFT-PI2BPSK	M+M	Inner_Full	16.67	16.67	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	21.66	21.66	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	21.61	21.61	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	22.06	22.06	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+M	Inner_Full	23.06	23.06	PC3	PASS
DC_5A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	20.98	20.98	PC3	PASS
DC_5A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	20.95	20.95	PC3	PASS
DC_5A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	21.12	21.12	PC3	PASS
DC_5A_n2A	15	5+5	DFT-16QAM	M+M	Inner_Full	21.99	21.99	PC3	PASS
DC_5A_n2A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	21.08	21.08	PC3	PASS
DC_5A_n2A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	21.04	21.04	PC3	PASS
DC_5A_n2A	15	5+5	DFT-64QAM	M+M	Outer_Full	20.65	20.65	PC3	PASS
DC_5A_n2A	15	5+5	DFT-64QAM	M+M	Inner_Full	20.72	20.72	PC3	PASS
DC_5A_n2A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	18.30	18.30	PC3	PASS
DC_5A_n2A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	18.35	18.35	PC3	PASS
DC_5A_n2A	15	5+5	DFT-256QAM	M+M	Outer_Full	18.73	18.73	PC3	PASS
DC_5A_n2A	15	5+5	DFT-256QAM	M+M	Inner_Full	18.73	18.73	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	20.38	20.38	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	20.27	20.27	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	18.97	18.97	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Inner_Full	21.58	21.58	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	19.83	19.83	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	19.68	19.68	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	19.03	19.03	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Inner_Full	20.91	20.91	PC3	PASS
DC_5A_n2A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	19.18	19.18	PC3	PASS
DC_5A_n2A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	19.04	19.04	PC3	PASS
DC_5A_n2A	15	5+5	CP-64QAM	M+M	Outer_Full	18.63	18.63	PC3	PASS
DC_5A_n2A	15	5+5	CP-64QAM	M+M	Inner_Full	19.48	19.48	PC3	PASS
DC_5A_n2A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	16.36	16.36	PC3	PASS
DC_5A_n2A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	16.26	16.26	PC3	PASS
DC_5A_n2A	15	5+5	CP-256QAM	M+M	Outer_Full	16.54	16.54	PC3	PASS
DC_5A_n2A	15	5+5	CP-256QAM	M+M	Inner_Full	16.64	16.64	PC3	PASS
DC_5A_n2A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	16.81	16.81	PC3	PASS
DC_5A_n2A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	16.89	16.89	PC3	PASS
DC_5A_n2A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	16.58	16.58	PC3	PASS
DC_5A_n2A	15	5+5	DFT-PI2BPSK	M+H	Inner_Full	16.53	16.53	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	21.59	21.59	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	21.68	21.68	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	22.06	22.06	PC3	PASS

DC_5A_n2A	15	5+5	DFT-QPSK	M+H	Inner_Full	23.01	23.01	PC3	PASS
DC_5A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	20.85	20.85	PC3	PASS
DC_5A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	20.91	20.91	PC3	PASS
DC_5A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	21.25	21.25	PC3	PASS
DC_5A_n2A	15	5+5	DFT-16QAM	M+H	Inner_Full	22.19	22.19	PC3	PASS
DC_5A_n2A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	20.21	20.21	PC3	PASS
DC_5A_n2A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	20.18	20.18	PC3	PASS
DC_5A_n2A	15	5+5	DFT-64QAM	M+H	Outer_Full	20.53	20.53	PC3	PASS
DC_5A_n2A	15	5+5	DFT-64QAM	M+H	Inner_Full	20.78	20.78	PC3	PASS
DC_5A_n2A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	18.29	18.29	PC3	PASS
DC_5A_n2A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	18.21	18.21	PC3	PASS
DC_5A_n2A	15	5+5	DFT-256QAM	M+H	Outer_Full	18.71	18.71	PC3	PASS
DC_5A_n2A	15	5+5	DFT-256QAM	M+H	Inner_Full	18.69	18.69	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	20.41	20.41	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	20.39	20.39	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	19.30	19.30	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Inner_Full	21.73	21.73	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	19.38	19.38	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	19.44	19.44	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	19.16	19.16	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Inner_Full	20.98	20.98	PC3	PASS
DC_5A_n2A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	19.13	19.13	PC3	PASS
DC_5A_n2A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	19.14	19.14	PC3	PASS
DC_5A_n2A	15	5+5	CP-64QAM	M+H	Outer_Full	18.53	18.53	PC3	PASS
DC_5A_n2A	15	5+5	CP-64QAM	M+H	Inner_Full	19.91	19.91	PC3	PASS
DC_5A_n2A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	16.86	16.86	PC3	PASS
DC_5A_n2A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	16.92	16.92	PC3	PASS
DC_5A_n2A	15	5+5	CP-256QAM	M+H	Outer_Full	16.60	16.60	PC3	PASS
DC_5A_n2A	15	5+5	CP-256QAM	M+H	Inner_Full	16.72	16.72	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Left	16.30	16.30	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Right	16.34	16.34	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+L	Outer_Full	16.61	16.61	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+L	Inner_Full	16.74	16.74	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.88	21.88	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	21.90	21.90	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	22.01	22.01	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+L	Inner_Full	23.09	23.09	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.73	20.73	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.70	20.70	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	21.21	21.21	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+L	Inner_Full	22.07	22.07	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Left	20.92	20.92	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Right	20.89	20.89	PC3	PASS

DC_5A_n2A	15	5+10	DFT-64QAM	M+L	Outer_Full	20.74	20.74	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+L	Inner_Full	20.80	20.80	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Left	18.49	18.49	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Right	18.53	18.53	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+L	Outer_Full	18.65	18.65	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+L	Inner_Full	18.80	18.80	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	19.97	19.97	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	19.95	19.95	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	19.21	19.21	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Inner_Full	21.68	21.68	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	19.21	19.21	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	19.14	19.14	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	19.09	19.09	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Inner_Full	21.06	21.06	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+L	Edge_1RB_Left	19.63	19.63	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+L	Edge_1RB_Right	19.63	19.63	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+L	Outer_Full	18.71	18.71	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+L	Inner_Full	19.85	19.85	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+L	Edge_1RB_Left	16.37	16.37	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+L	Edge_1RB_Right	16.27	16.27	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+L	Outer_Full	16.61	16.61	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+L	Inner_Full	16.73	16.73	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Left	16.53	16.53	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Right	16.68	16.68	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+M	Outer_Full	16.65	16.65	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+M	Inner_Full	16.67	16.67	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	21.34	21.34	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	21.44	21.44	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	22.01	22.01	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+M	Inner_Full	23.04	23.04	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.79	20.79	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.88	20.88	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	21.23	21.23	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+M	Inner_Full	22.05	22.05	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Left	20.10	20.10	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Right	20.31	20.31	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+M	Outer_Full	20.64	20.64	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+M	Inner_Full	20.65	20.65	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Left	18.10	18.10	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Right	18.26	18.26	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+M	Outer_Full	18.67	18.67	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+M	Inner_Full	18.75	18.75	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	20.26	20.26	PC3	PASS

DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	20.51	20.51	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	18.98	18.98	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Inner_Full	21.76	21.76	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	19.66	19.66	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	19.84	19.84	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	19.08	19.08	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Inner_Full	21.22	21.22	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+M	Edge_1RB_Left	18.85	18.85	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+M	Edge_1RB_Right	18.94	18.94	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+M	Outer_Full	18.78	18.78	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+M	Inner_Full	19.62	19.62	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+M	Edge_1RB_Left	16.53	16.53	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+M	Edge_1RB_Right	16.63	16.63	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+M	Outer_Full	16.48	16.48	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+M	Inner_Full	16.54	16.54	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Left	16.38	16.38	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Right	16.41	16.41	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+H	Outer_Full	16.73	16.73	PC3	PASS
DC_5A_n2A	15	5+10	DFT-PI2BPSK	M+H	Inner_Full	16.63	16.63	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	21.56	21.56	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	21.84	21.84	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	21.99	21.99	PC3	PASS
DC_5A_n2A	15	5+10	DFT-QPSK	M+H	Inner_Full	23.08	23.08	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	20.08	20.08	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	20.25	20.25	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	21.22	21.22	PC3	PASS
DC_5A_n2A	15	5+10	DFT-16QAM	M+H	Inner_Full	22.16	22.16	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Left	20.55	20.55	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Right	20.75	20.75	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+H	Outer_Full	20.60	20.60	PC3	PASS
DC_5A_n2A	15	5+10	DFT-64QAM	M+H	Inner_Full	20.57	20.57	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Left	18.32	18.32	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Right	18.44	18.44	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+H	Outer_Full	18.71	18.71	PC3	PASS
DC_5A_n2A	15	5+10	DFT-256QAM	M+H	Inner_Full	18.66	18.66	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	20.21	20.21	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	20.13	20.13	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	19.14	19.14	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Inner_Full	21.80	21.80	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	19.66	19.66	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	19.56	19.56	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	19.01	19.01	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Inner_Full	21.20	21.20	PC3	PASS

DC_5A_n2A	15	5+10	CP-64QAM	M+H	Edge_1RB_Left	19.38	19.38	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+H	Edge_1RB_Right	19.34	19.34	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+H	Outer_Full	18.66	18.66	PC3	PASS
DC_5A_n2A	15	5+10	CP-64QAM	M+H	Inner_Full	19.88	19.88	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+H	Edge_1RB_Left	16.41	16.41	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+H	Edge_1RB_Right	16.40	16.40	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+H	Outer_Full	16.64	16.64	PC3	PASS
DC_5A_n2A	15	5+10	CP-256QAM	M+H	Inner_Full	16.69	16.69	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Left	16.36	16.36	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Right	16.40	16.40	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+L	Outer_Full	16.54	16.54	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+L	Inner_Full	16.48	16.48	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	21.48	21.48	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	21.34	21.34	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	21.99	21.99	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+L	Inner_Full	22.97	22.97	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.91	20.91	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	20.91	20.91	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	21.11	21.11	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+L	Inner_Full	21.97	21.97	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Left	20.32	20.32	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Right	20.24	20.24	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+L	Outer_Full	20.64	20.64	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+L	Inner_Full	20.52	20.52	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Left	18.32	18.32	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Right	18.22	18.22	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+L	Outer_Full	18.63	18.63	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+L	Inner_Full	18.40	18.40	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	19.85	19.85	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	20.00	20.00	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	19.03	19.03	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Inner_Full	21.65	21.65	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	19.19	19.19	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	19.19	19.19	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	19.09	19.09	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Inner_Full	21.15	21.15	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+L	Edge_1RB_Left	18.69	18.69	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+L	Edge_1RB_Right	18.82	18.82	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+L	Outer_Full	18.50	18.50	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+L	Inner_Full	19.68	19.68	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+L	Edge_1RB_Left	16.36	16.36	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+L	Edge_1RB_Right	16.40	16.40	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+L	Outer_Full	16.48	16.48	PC3	PASS

DC_5A_n2A	15	5+15	CP-256QAM	M+L	Inner_Full	16.45	16.45	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Left	16.08	16.08	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Right	16.17	16.17	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+M	Outer_Full	16.46	16.46	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+M	Inner_Full	16.58	16.58	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	21.54	21.54	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	21.61	21.61	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	21.89	21.89	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+M	Inner_Full	22.95	22.95	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.98	20.98	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	21.10	21.10	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	21.09	21.09	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+M	Inner_Full	21.89	21.89	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Left	20.49	20.49	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Right	20.62	20.62	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+M	Outer_Full	20.54	20.54	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+M	Inner_Full	20.53	20.53	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Left	18.09	18.09	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Right	18.25	18.25	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+M	Outer_Full	18.54	18.54	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+M	Inner_Full	18.48	18.48	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	19.79	19.79	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	19.76	19.76	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	19.08	19.08	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Inner_Full	21.55	21.55	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	19.65	19.65	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	19.66	19.66	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	19.14	19.14	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Inner_Full	21.12	21.12	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+M	Edge_1RB_Left	19.45	19.45	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+M	Edge_1RB_Right	19.56	19.56	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+M	Outer_Full	18.59	18.59	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+M	Inner_Full	19.47	19.47	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+M	Edge_1RB_Left	16.08	16.08	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+M	Edge_1RB_Right	16.07	16.07	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+M	Outer_Full	16.52	16.52	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+M	Inner_Full	16.53	16.53	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Left	16.45	16.45	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Right	16.73	16.73	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+H	Outer_Full	16.45	16.45	PC3	PASS
DC_5A_n2A	15	5+15	DFT-PI2BPSK	M+H	Inner_Full	16.63	16.63	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	21.28	21.28	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	21.38	21.38	PC3	PASS

DC_5A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	21.96	21.96	PC3	PASS
DC_5A_n2A	15	5+15	DFT-QPSK	M+H	Inner_Full	23.00	23.00	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	23.00	23.00	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	21.14	21.14	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	21.20	21.20	PC3	PASS
DC_5A_n2A	15	5+15	DFT-16QAM	M+H	Inner_Full	21.97	21.97	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Left	20.38	20.38	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Right	20.55	20.55	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+H	Outer_Full	20.58	20.58	PC3	PASS
DC_5A_n2A	15	5+15	DFT-64QAM	M+H	Inner_Full	20.54	20.54	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Left	18.46	18.46	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Right	18.60	18.60	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+H	Outer_Full	18.56	18.56	PC3	PASS
DC_5A_n2A	15	5+15	DFT-256QAM	M+H	Inner_Full	18.63	18.63	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	19.88	19.88	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	20.15	20.15	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	19.12	19.12	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Inner_Full	21.45	21.45	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	19.43	19.43	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	19.69	19.69	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	19.01	19.01	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Inner_Full	21.15	21.15	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+H	Edge_1RB_Left	19.56	19.56	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+H	Edge_1RB_Right	19.79	19.79	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+H	Outer_Full	18.48	18.48	PC3	PASS
DC_5A_n2A	15	5+15	CP-64QAM	M+H	Inner_Full	19.49	19.49	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+H	Edge_1RB_Left	16.47	16.47	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+H	Edge_1RB_Right	16.67	16.67	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+H	Outer_Full	16.37	16.37	PC3	PASS
DC_5A_n2A	15	5+15	CP-256QAM	M+H	Inner_Full	16.34	16.34	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	16.55	16.55	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	16.53	16.53	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	16.40	16.40	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+L	Inner_Full	16.57	16.57	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.61	21.61	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.49	21.49	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	22.00	22.00	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+L	Inner_Full	22.00	22.00	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	20.83	20.83	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	20.63	20.63	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	21.10	21.10	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+L	Inner_Full	21.93	21.93	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.74	20.74	PC3	PASS

DC_5A_n2A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	20.63	20.63	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+L	Outer_Full	20.62	20.62	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+L	Inner_Full	20.58	20.58	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	18.77	18.77	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	18.72	18.72	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+L	Outer_Full	18.59	18.59	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+L	Inner_Full	18.52	18.52	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	20.01	20.01	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	19.89	19.89	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	19.11	19.11	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Inner_Full	21.58	21.58	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	19.86	19.86	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	19.70	19.70	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	19.13	19.13	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Inner_Full	21.13	21.13	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	19.60	19.60	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	19.48	19.48	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+L	Outer_Full	18.56	18.56	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+L	Inner_Full	19.66	19.66	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	16.58	16.58	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	16.44	16.44	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+L	Outer_Full	16.47	16.47	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+L	Inner_Full	16.44	16.44	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	16.78	16.78	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	16.88	16.88	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	16.42	16.42	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+M	Inner_Full	16.45	16.45	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.39	21.39	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	21.55	21.55	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	21.85	21.85	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+M	Inner_Full	22.98	22.98	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.05	21.05	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.09	21.09	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	20.92	20.92	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+M	Inner_Full	21.98	21.98	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	20.61	20.61	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	20.74	20.74	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+M	Outer_Full	20.55	20.55	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+M	Inner_Full	20.63	20.63	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	18.26	18.26	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	18.36	18.36	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+M	Outer_Full	18.55	18.55	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+M	Inner_Full	18.54	18.54	PC3	PASS

DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	19.96	19.96	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	20.09	20.09	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	18.96	18.96	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Inner_Full	21.62	21.62	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	19.60	19.60	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	19.70	19.70	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	18.98	18.98	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Inner_Full	21.23	21.23	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	19.76	19.76	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	19.84	19.84	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+M	Outer_Full	18.57	18.57	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+M	Inner_Full	19.59	19.59	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	16.53	16.53	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	16.56	16.56	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+M	Outer_Full	16.54	16.54	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+M	Inner_Full	16.57	16.57	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	16.46	16.46	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	16.55	16.55	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	16.40	16.40	PC3	PASS
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+H	Inner_Full	16.62	16.62	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	21.54	21.54	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.60	21.60	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	21.95	21.95	PC3	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+H	Inner_Full	23.02	23.02	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	20.71	20.71	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	20.72	20.72	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	21.10	21.10	PC3	PASS
DC_5A_n2A	15	5+20	DFT-16QAM	M+H	Inner_Full	21.91	21.91	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	20.56	20.56	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	20.63	20.63	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+H	Outer_Full	20.68	20.68	PC3	PASS
DC_5A_n2A	15	5+20	DFT-64QAM	M+H	Inner_Full	20.59	20.59	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.69	18.69	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	18.77	18.77	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+H	Outer_Full	18.65	18.65	PC3	PASS
DC_5A_n2A	15	5+20	DFT-256QAM	M+H	Inner_Full	18.60	18.60	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	19.93	19.93	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	19.93	19.93	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	19.03	19.03	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Inner_Full	21.54	21.54	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	19.67	19.67	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	19.86	19.86	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	19.17	19.17	PC3	PASS

DC_5A_n2A	15	5+20	CP-16QAM	M+H	Inner_Full	21.16	21.16	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	19.49	19.49	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	19.54	19.54	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+H	Outer_Full	18.53	18.53	PC3	PASS
DC_5A_n2A	15	5+20	CP-64QAM	M+H	Inner_Full	19.71	19.71	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	16.47	16.47	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	16.59	16.59	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+H	Outer_Full	16.46	16.46	PC3	PASS
DC_5A_n2A	15	5+20	CP-256QAM	M+H	Inner_Full	16.47	16.47	PC3	PASS

Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n2A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	8.38	≤13	PASS
DC_5A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	8.41	≤13	PASS