

## Effective (Isotropic) Radiated Power Output Data for NSA

### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Limit (dBm)	Verdict
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+L	Outer_Full	21	21.6	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Outer_Full	21.04	21.64	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+H	Outer_Full	21.04	21.64	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+L	Inner_Full	21.59	22.19	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Inner_Full	21.55	22.15	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+H	Inner_Full	21.68	22.28	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.05	21.65	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.12	21.72	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.02	21.62	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.12	21.72	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Right	20.89	21.49	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Right	20.96	21.56	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+L	Outer_Full	20.53	21.13	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Outer_Full	20.69	21.29	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+H	Outer_Full	20.52	21.12	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+L	Inner_Full	21.5	22.1	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Inner_Full	21.59	22.19	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+H	Inner_Full	21.58	22.18	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	20.61	21.21	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.49	21.09	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	20.56	21.16	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	20.72	21.32	23	PASS

DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	20.36	20.96	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	20.56	21.16	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+L	Outer_Full	19.61	20.21	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Outer_Full	19.67	20.27	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+H	Outer_Full	19.5	20.1	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+L	Inner_Full	20.54	21.14	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Inner_Full	20.54	21.14	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+H	Inner_Full	20.63	21.23	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	19.17	19.77	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.98	20.58	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	19.58	20.18	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.32	19.92	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	19.65	20.25	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.46	20.06	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+L	Outer_Full	18.98	19.58	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Outer_Full	19.04	19.64	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+H	Outer_Full	19.02	19.62	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+L	Inner_Full	19.11	19.71	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Inner_Full	19.21	19.81	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+H	Inner_Full	18.97	19.57	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+L	Edge_1RB_Left	18.59	19.19	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Edge_1RB_Left	19.19	19.79	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+H	Edge_1RB_Left	18.92	19.52	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+L	Edge_1RB_Right	18.74	19.34	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Edge_1RB_Right	18.98	19.58	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+H	Edge_1RB_Right	18.82	19.42	23	PASS

DC_13A_n48A	30	5+10	DFT-256QAM	M+L	Outer_Full	17.04	17.64	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Outer_Full	17.12	17.72	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+H	Outer_Full	17.13	17.73	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+L	Inner_Full	17.09	17.69	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Inner_Full	17.17	17.77	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+H	Inner_Full	17.06	17.66	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+L	Edge_1RB_Left	16.96	17.56	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Edge_1RB_Left	17.1	17.7	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+H	Edge_1RB_Left	17.03	17.63	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+L	Edge_1RB_Right	17.03	17.63	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Edge_1RB_Right	16.9	17.5	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+H	Edge_1RB_Right	17.12	17.72	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+L	Outer_Full	21.15	21.75	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Outer_Full	21.34	21.94	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+H	Outer_Full	21.1	21.7	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+L	Inner_Full	21.64	22.24	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Inner_Full	21.86	22.46	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+H	Inner_Full	21.77	22.37	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.29	21.89	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.16	21.76	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.18	21.78	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.49	22.09	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Right	20.87	21.47	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.17	21.77	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+L	Outer_Full	20.7	21.3	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Outer_Full	20.6	21.2	23	PASS

DC_13A_n48A	30	5+15	DFT-QPSK	M+H	Outer_Full	20.68	21.28	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+L	Inner_Full	21.57	22.17	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Inner_Full	21.78	22.38	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+H	Inner_Full	21.53	22.13	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	20.57	21.17	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	20.91	21.51	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	20.76	21.36	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	20.82	21.42	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	20.82	21.42	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	20.84	21.44	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+L	Outer_Full	19.57	20.17	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Outer_Full	19.68	20.28	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+H	Outer_Full	19.63	20.23	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+L	Inner_Full	20.65	21.25	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Inner_Full	20.66	21.26	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+H	Inner_Full	20.65	21.25	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.43	20.03	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	19.66	20.26	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.67	20.27	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.68	20.28	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	19.57	20.17	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	19.69	20.29	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+L	Outer_Full	19.04	19.64	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Outer_Full	19.27	19.87	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+H	Outer_Full	19.1	19.7	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+L	Inner_Full	19.09	19.69	23	PASS

DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Inner_Full	19.16	19.76	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+H	Inner_Full	19.19	19.79	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+L	Edge_1RB_Left	19.14	19.74	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Edge_1RB_Left	19.33	19.93	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+H	Edge_1RB_Left	19.53	20.13	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+L	Edge_1RB_Right	19.45	20.05	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Edge_1RB_Right	19.05	19.65	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+H	Edge_1RB_Right	19.51	20.11	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+L	Outer_Full	17.22	17.82	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Outer_Full	17.06	17.66	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+H	Outer_Full	17.1	17.7	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+L	Inner_Full	17.17	17.77	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Inner_Full	17.13	17.73	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+H	Inner_Full	17.15	17.75	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+L	Edge_1RB_Left	17.31	17.91	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Edge_1RB_Left	17.37	17.97	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+H	Edge_1RB_Left	17.09	17.69	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+L	Edge_1RB_Right	17.53	18.13	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Edge_1RB_Right	17.19	17.79	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+H	Edge_1RB_Right	17.16	17.76	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	21.28	21.88	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	21.29	21.89	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	21.15	21.75	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+L	Inner_Full	21.69	22.29	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Inner_Full	21.79	22.39	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+H	Inner_Full	21.7	22.3	23	PASS

DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.04	21.64	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.47	22.07	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.02	21.62	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.33	21.93	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.02	21.62	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	20.95	21.55	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+L	Outer_Full	21.73	22.33	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Outer_Full	21.7	22.3	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+H	Outer_Full	21.53	22.13	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+L	Inner_Full	21.72	22.32	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Inner_Full	21.81	22.41	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+H	Inner_Full	21.56	22.16	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	20.45	21.05	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.76	21.36	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	21.67	22.27	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	20.84	21.44	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	20.53	21.13	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.69	22.29	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+L	Outer_Full	19.75	20.35	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Outer_Full	19.78	20.38	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+H	Outer_Full	20.56	21.16	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+L	Inner_Full	20.76	21.36	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Inner_Full	20.74	21.34	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+H	Inner_Full	21.63	22.23	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.45	20.05	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	20.05	20.65	23	PASS

DC_13A_n48A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	20.84	21.44	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.75	20.35	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	19.79	20.39	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	20.78	21.38	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+L	Outer_Full	19.23	19.83	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Outer_Full	19.28	19.88	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+H	Outer_Full	20.16	20.76	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+L	Inner_Full	19.12	19.72	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Inner_Full	19.1	19.7	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+H	Inner_Full	20	20.6	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	19.36	19.96	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	19.08	19.68	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	20.51	21.11	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	19.52	20.12	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	18.63	19.23	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	20.5	21.1	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+L	Outer_Full	17.18	17.78	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Outer_Full	17.2	17.8	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+H	Outer_Full	17.95	18.55	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+L	Inner_Full	17.12	17.72	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Inner_Full	17.08	17.68	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+H	Inner_Full	18.19	18.79	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	17.41	18.01	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	17.56	18.16	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.03	18.63	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	17.8	18.4	23	PASS

DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	17.33	17.93	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	18.07	18.67	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+L	Outer_Full	21.34	21.94	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Outer_Full	21.19	21.79	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+H	Outer_Full	21.2	21.8	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+L	Inner_Full	21.88	22.48	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Inner_Full	21.66	22.26	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+H	Inner_Full	21.68	22.28	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.02	21.62	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.33	21.93	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.28	21.88	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.04	21.64	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Right	20.9	21.5	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.19	21.79	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	21.33	21.93	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	21.33	21.93	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	21.12	21.72	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+L	Inner_Full	21.77	22.37	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Inner_Full	21.74	22.34	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+H	Inner_Full	21.76	22.36	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	20.83	21.43	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.38	21.98	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.28	21.88	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.24	21.84	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.28	21.88	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.32	21.92	23	PASS



DC_13A_n48A	30	5+40	DFT-QPSK	M+L	Outer_Full	21.81	22.41	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Outer_Full	21.8	22.4	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+H	Outer_Full	21.56	22.16	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+L	Inner_Full	21.86	22.46	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Inner_Full	21.71	22.31	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+H	Inner_Full	21.72	22.32	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.55	22.15	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.97	22.57	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.78	22.38	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.87	22.47	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.9	22.5	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.14	22.74	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+L	Outer_Full	20.86	21.46	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Outer_Full	20.89	21.49	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+H	Outer_Full	20.68	21.28	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+L	Inner_Full	21.8	22.4	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Inner_Full	21.77	22.37	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+H	Inner_Full	21.57	22.17	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	20.7	21.3	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.96	21.56	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.53	21.13	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	21.03	21.63	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.66	21.26	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.75	21.35	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+L	Outer_Full	20.27	20.87	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Outer_Full	20.27	20.87	23	PASS

DC_13A_n48A	30	5+40	DFT-64QAM	M+H	Outer_Full	20.15	20.75	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+L	Inner_Full	20.25	20.85	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Inner_Full	20.13	20.73	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+H	Inner_Full	20.21	20.81	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	20.13	20.73	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Left	20.43	21.03	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	20.39	20.99	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Right	20.65	21.25	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Right	20.17	20.77	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Right	20.49	21.09	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+L	Outer_Full	18.46	19.06	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Outer_Full	18.35	18.95	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+H	Outer_Full	18.04	18.64	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+L	Inner_Full	18.48	19.08	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Inner_Full	18.35	18.95	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+H	Inner_Full	18.27	18.87	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	18.09	18.69	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Left	18.53	19.13	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	18.16	18.76	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Right	18.46	19.06	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Right	18.45	19.05	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	18.52	19.12	23	PASS

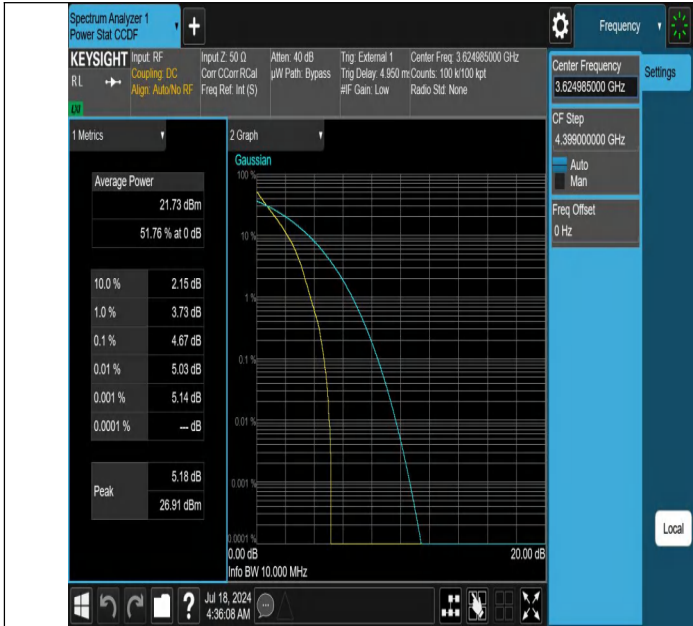
## Peak-to-Average Ratio for NSA

### Peak-to-Average Ratio(CCDF)

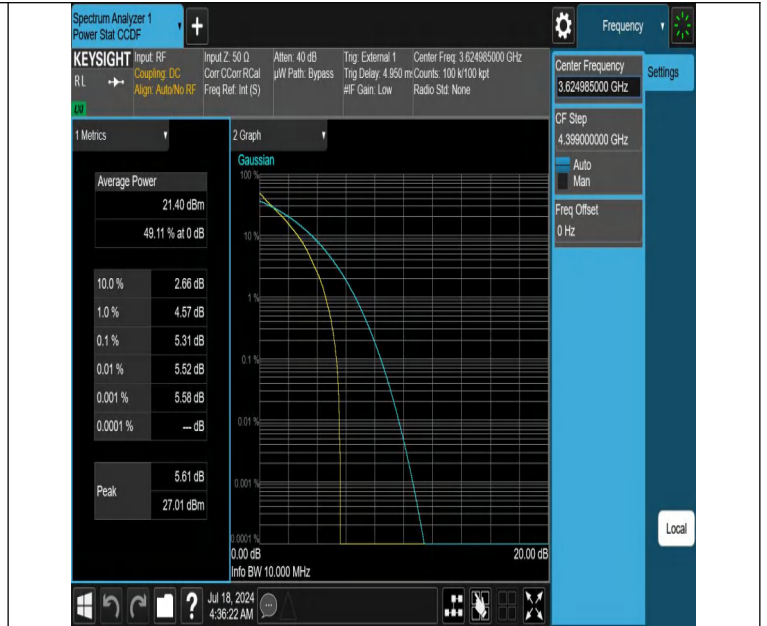
#### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (dB)	Limit (dB)	Verdict
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Outer_Full	4.67	≤13	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Outer_Full	5.31	≤13	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Outer_Full	6.20	≤13	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Outer_Full	6.73	≤13	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Outer_Full	6.80	≤13	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Outer_Full	4.79	≤13	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Outer_Full	5.53	≤13	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Outer_Full	6.42	≤13	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Outer_Full	6.86	≤13	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Outer_Full	7.02	≤13	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	4.69	≤13	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Outer_Full	5.24	≤13	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Outer_Full	6.24	≤13	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Outer_Full	6.67	≤13	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Outer_Full	6.93	≤13	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Outer_Full	4.64	≤13	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+M	Outer_Full	5.36	≤13	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+M	Outer_Full	6.39	≤13	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+M	Outer_Full	6.75	≤13	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+M	Outer_Full	7.07	≤13	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	4.54	≤13	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Outer_Full	5.33	≤13	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Outer_Full	6.31	≤13	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Outer_Full	6.79	≤13	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Outer_Full	7.04	≤13	PASS

Test Graphs



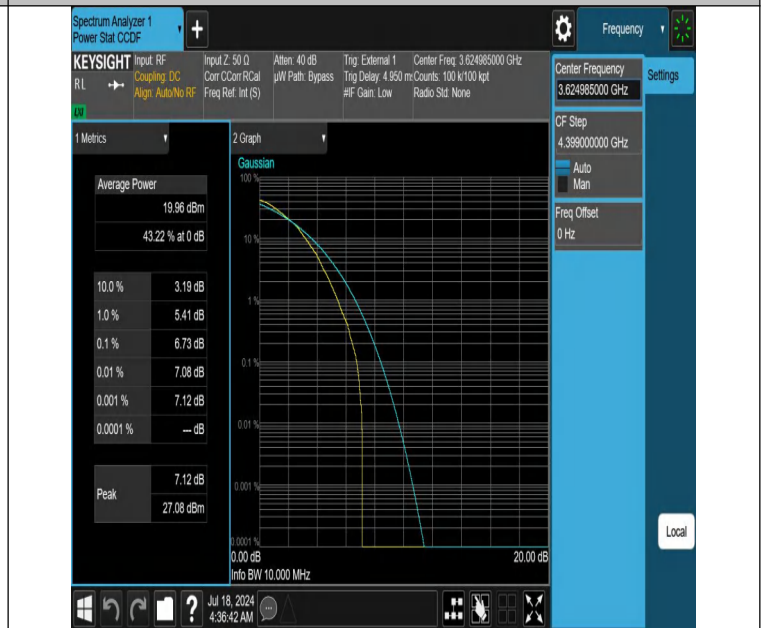
NTNV-DC\_13A\_n48A-PC3-30-5+10-M+M-N/A-N/A-DFT-PI2BPSK-Outer\_Full-PASS



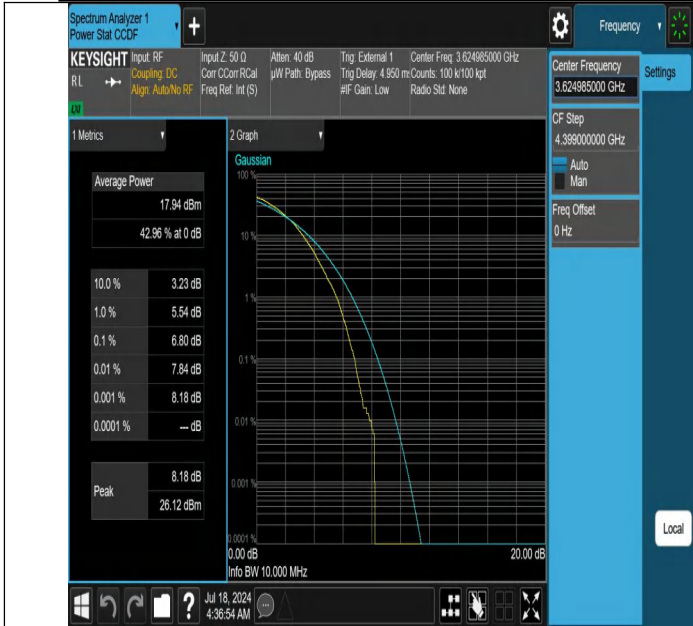
NTNV-DC\_13A\_n48A-PC3-30-5+10-M+M-N/A-N/A-DFT-QPSK-Out\_er\_Full-PASS



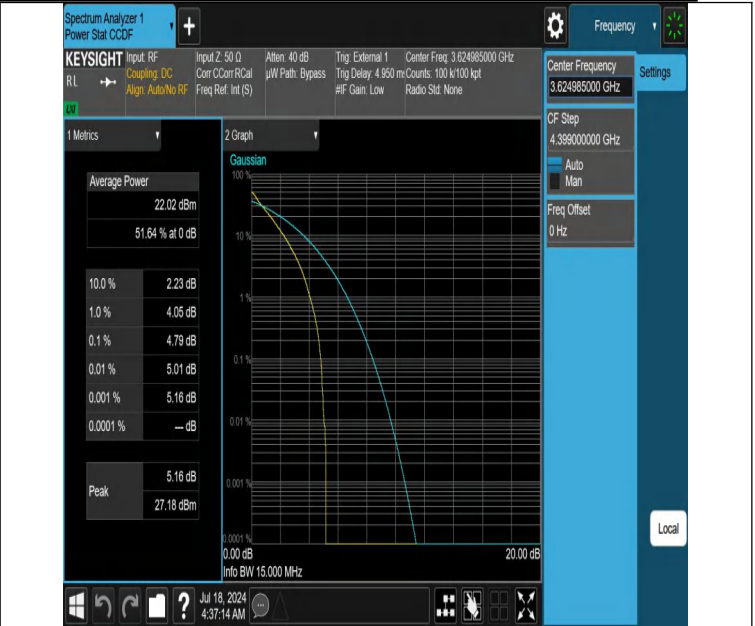
NTNV-DC\_13A\_n48A-PC3-30-5+10-M+M-N/A-N/A-DFT-16QAM-Outer\_Full-PASS



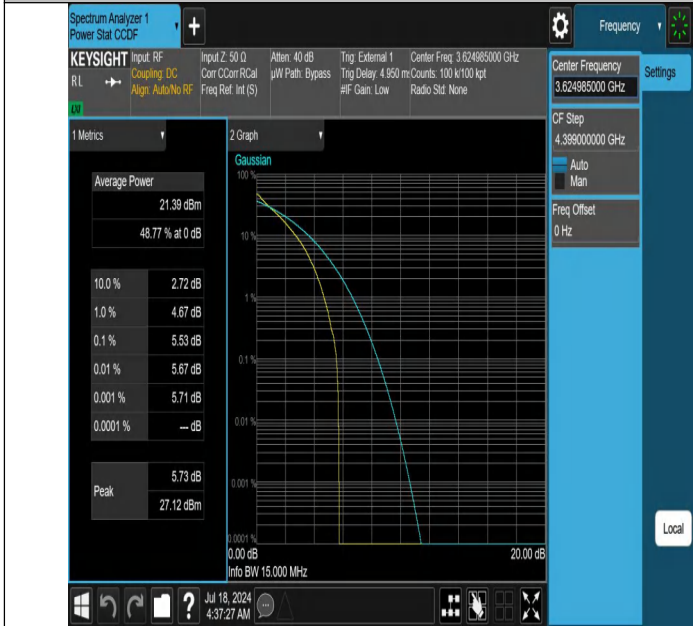
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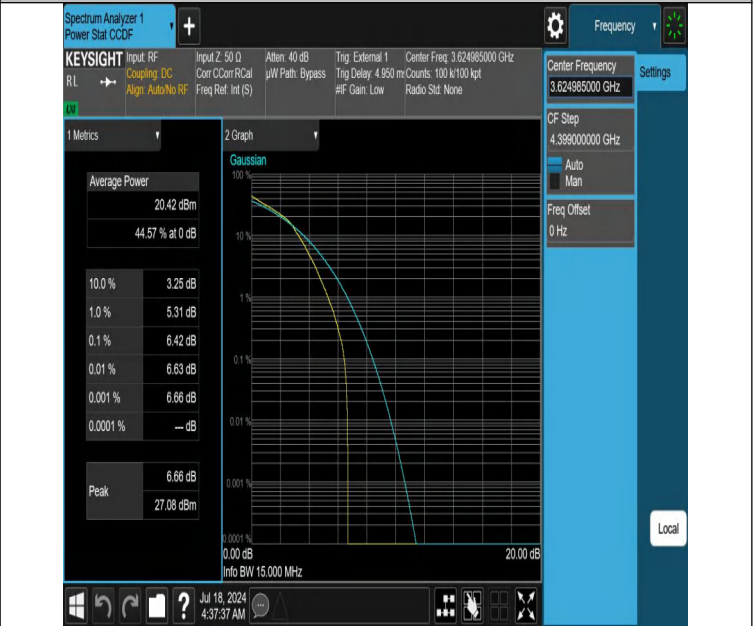
NTNV-DC\_13A\_n48A-PC3-30-5+10-M+M-N/A-N/A-DFT-256QAM-Outer\_Full-PASS



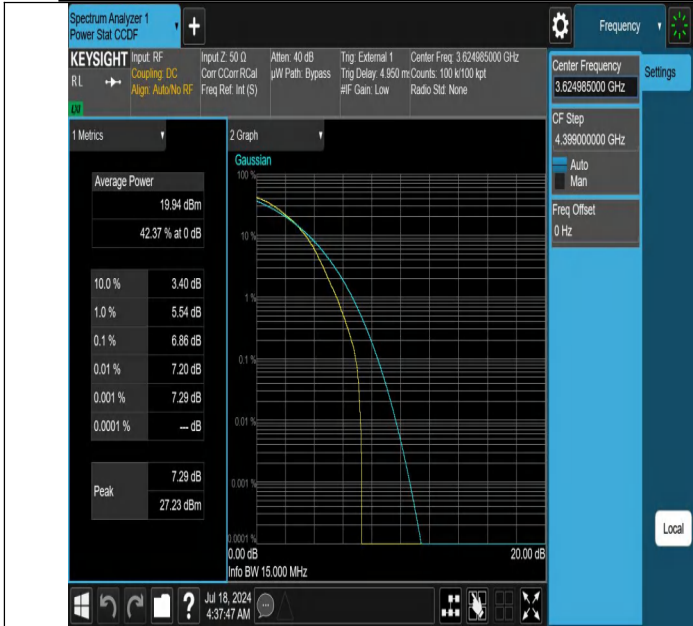
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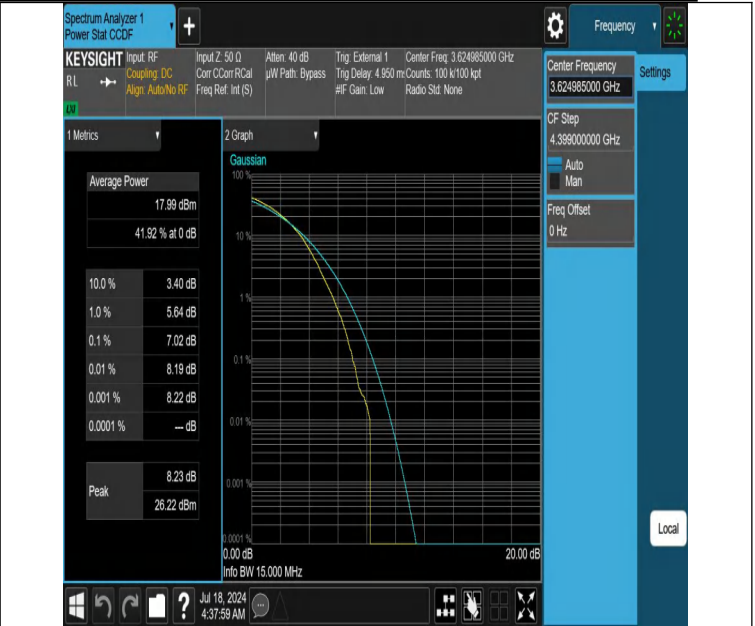
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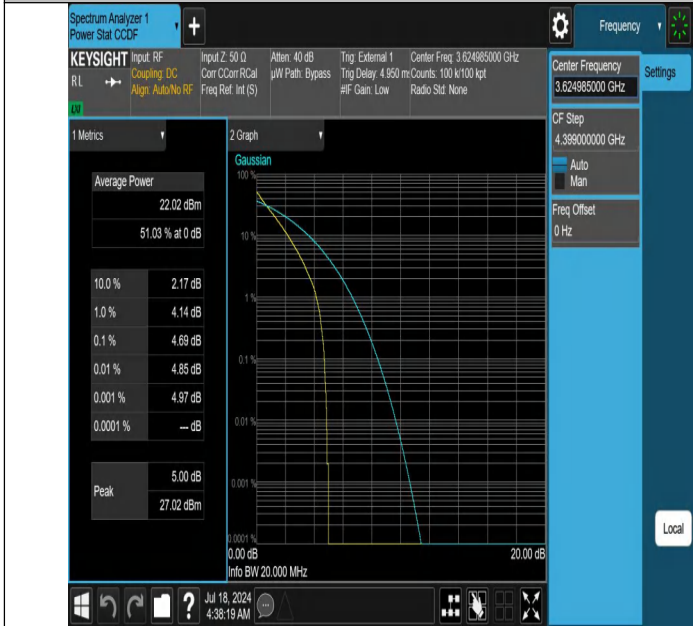
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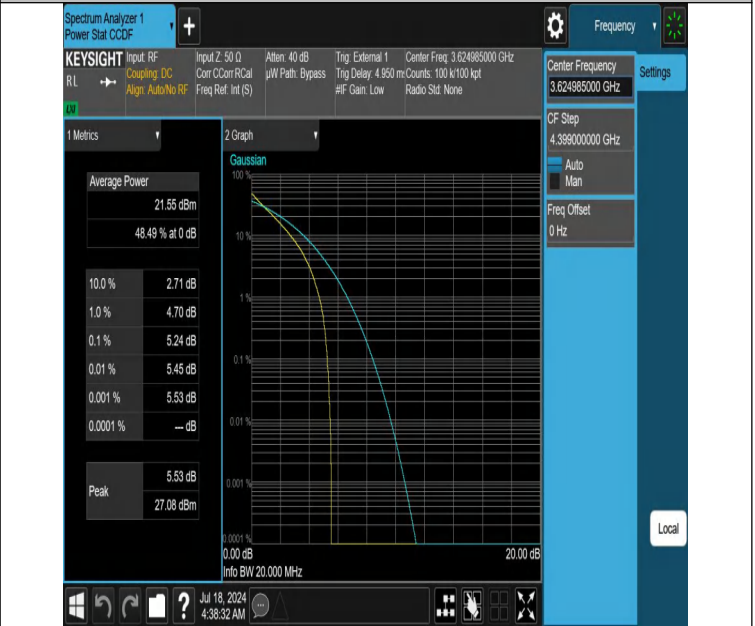
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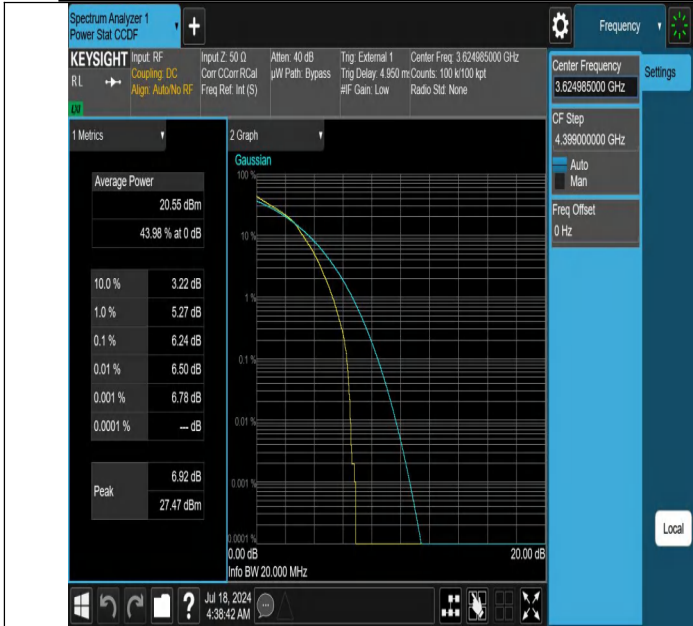
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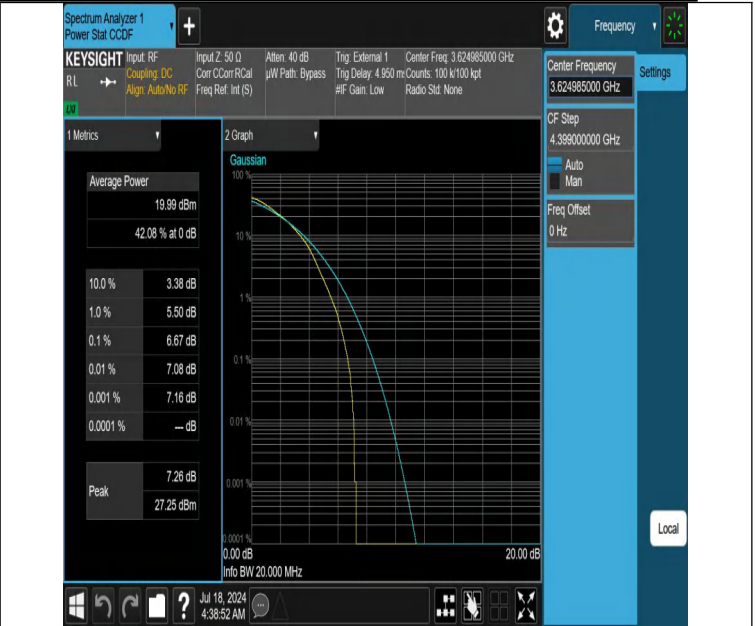
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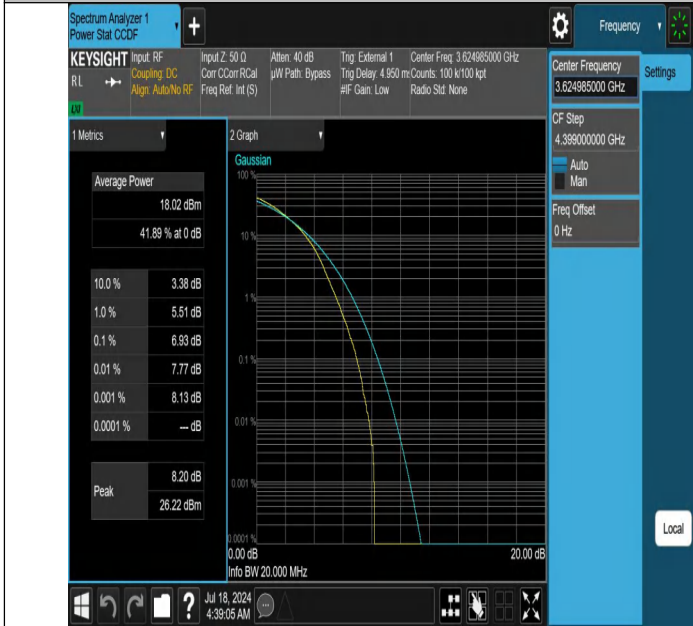
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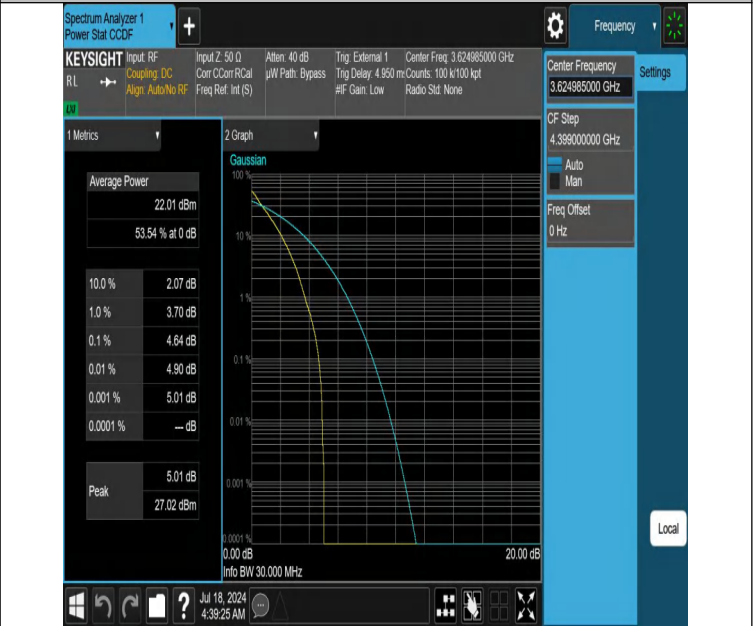
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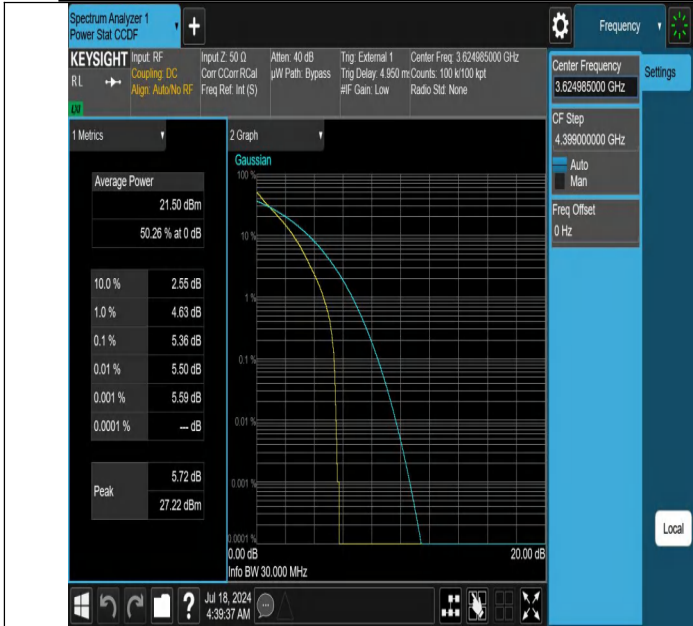
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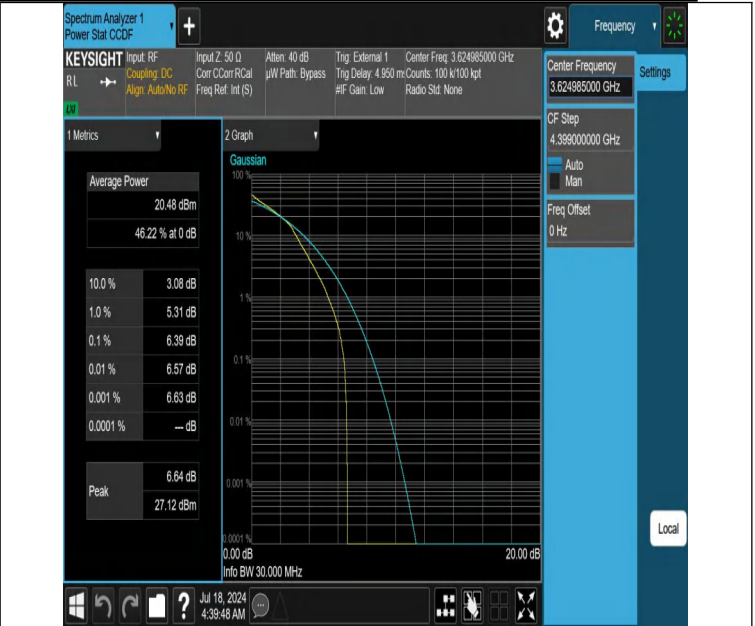
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Outer\_Full-PASS



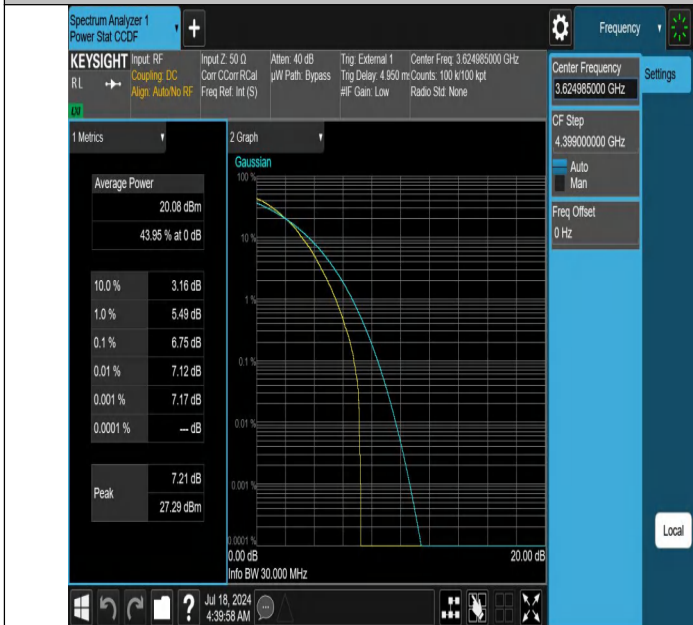
NTNV-DC\_13A\_n48A-PC3-30-5+30-M+M-N/A-N/A-DFT-PI2BPSK-O  
Outer\_Full-PASS



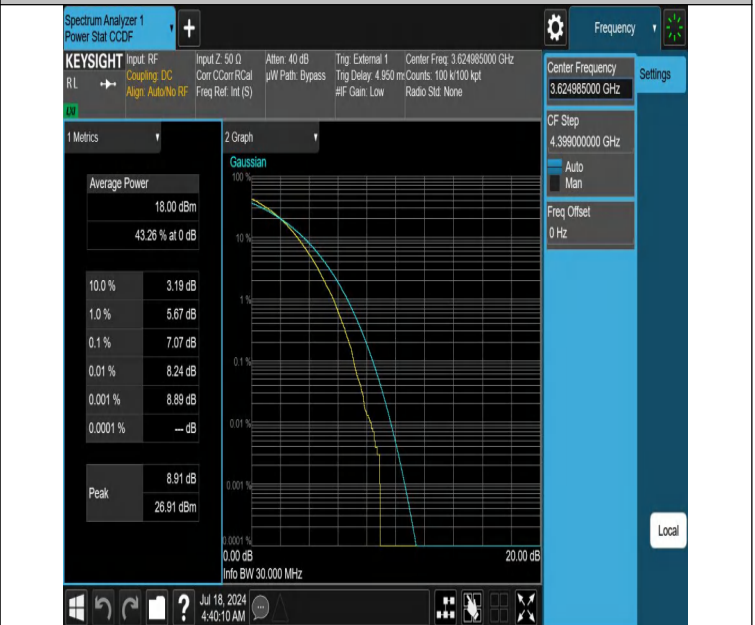
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NTNV-DC\_13A\_n48A-PC3-30-5+30-M+M-N/A-N/A-DFT-16QAM-O uter\_Full-PASS

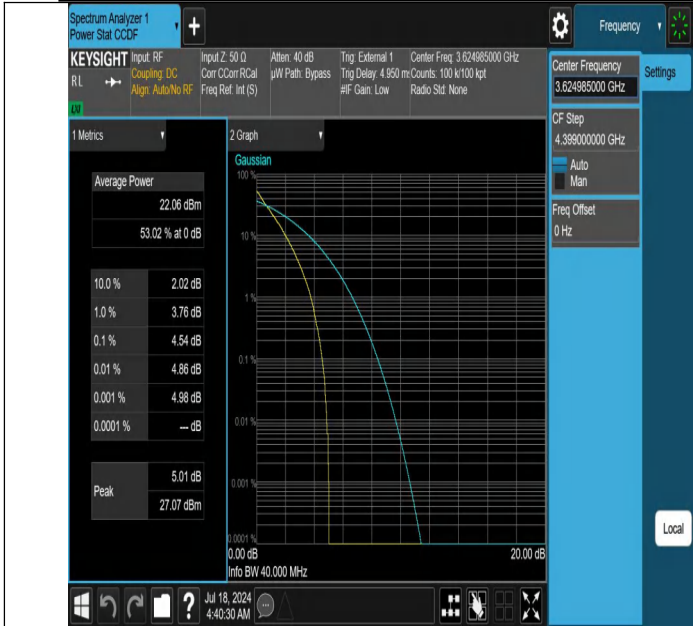


NTNV-DC\_13A\_n48A-PC3-30-5+30-M+M-N/A-N/A-DFT-64QAM-O uter\_Full-PASS

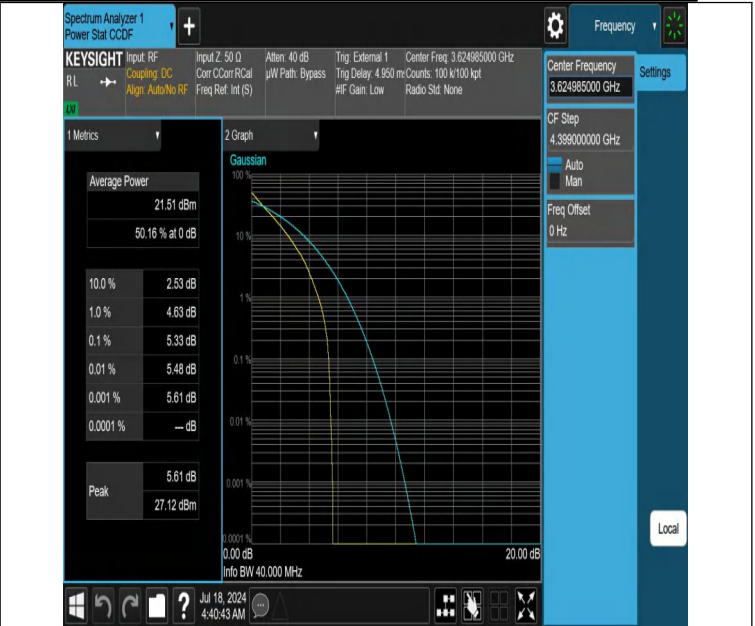


NTNV-DC\_13A\_n48A-PC3-30-5+30-M+M-N/A-N/A-DFT-256QAM-O uter\_Full-PASS

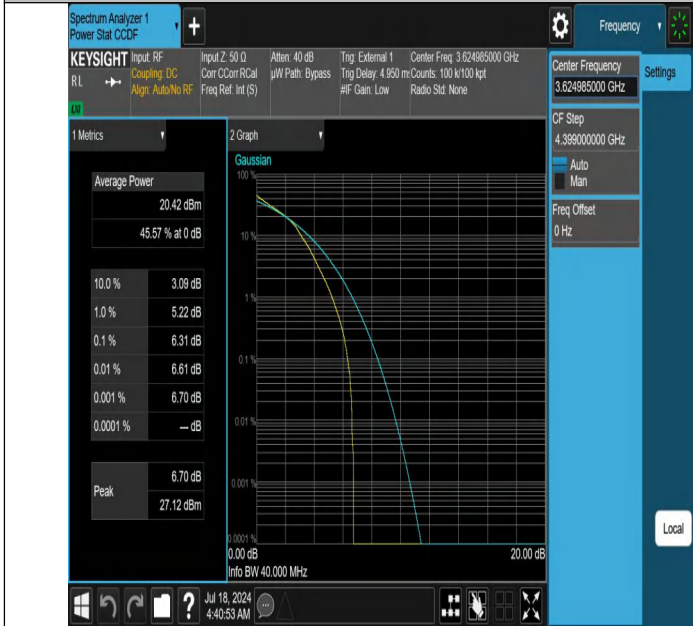




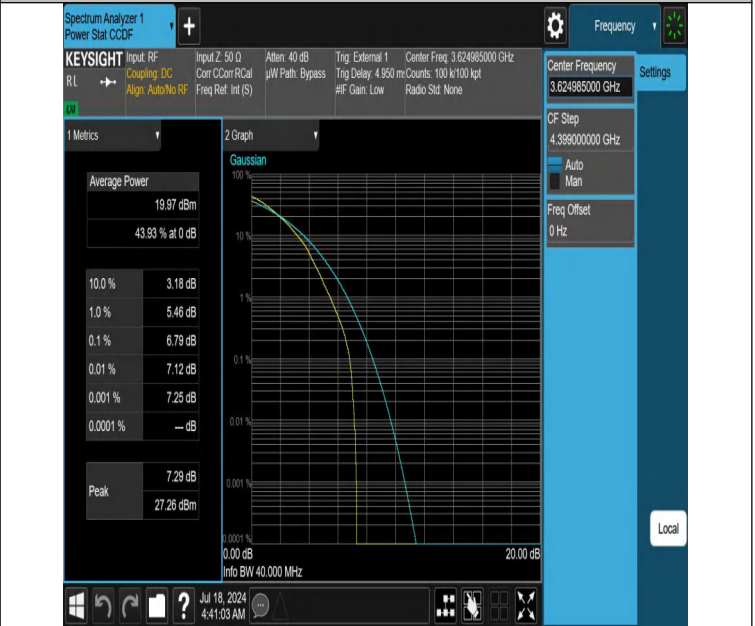
NTNV-DC\_13A\_n48A-PC3-30-5+40-M+M-N/A-N/A-DFT-PI2BPSK-Outer\_Full-PASS



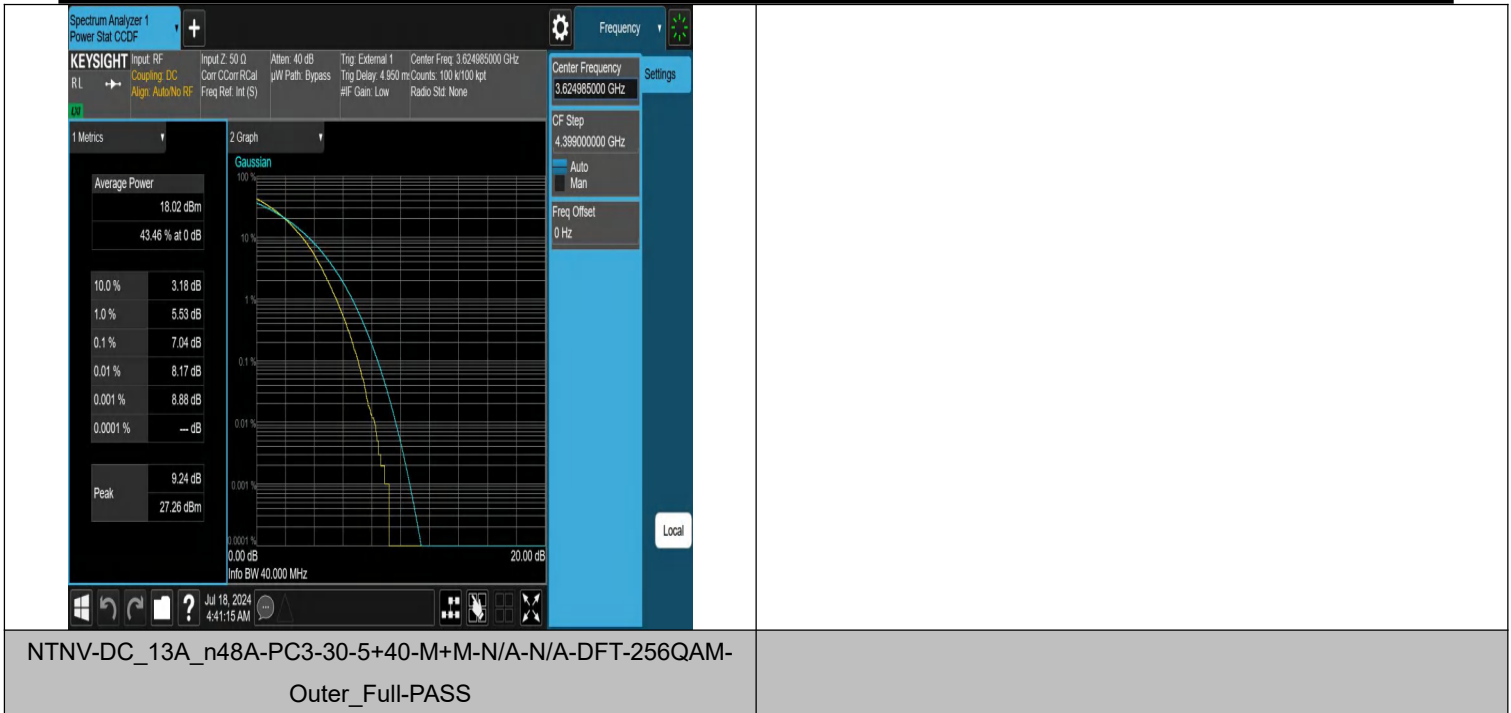
NTNV-DC\_13A\_n48A-PC3-30-5+40-M+M-N/A-N/A-DFT-QPSK-Out er\_Full-PASS



NTNV-DC\_13A\_n48A-PC3-30-5+40-M+M-N/A-N/A-DFT-16QAM-O uter\_Full-PASS



NTNV-DC\_13A\_n48A-PC3-30-5+40-M+M-N/A-N/A-DFT-64QAM-O uter\_Full-PASS

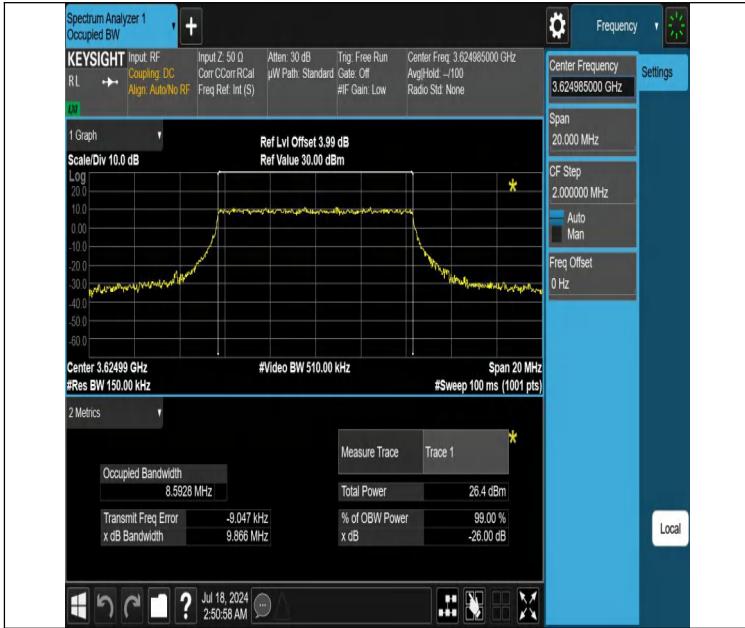


## 26dB Bandwidth and Occupied Bandwidth for NSA

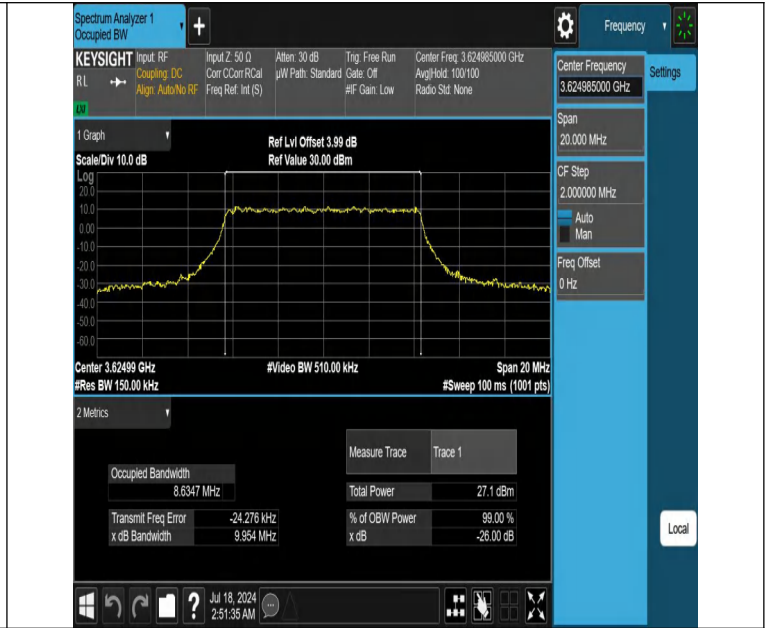
### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_13A_n48A	30	5+10	CP-QPSK	M+M	Outer_Full	8.5928	9.866	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+M	Outer_Full	8.6347	9.954	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+M	Outer_Full	8.6457	10.05	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+M	Outer_Full	8.6164	9.925	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+M	Outer_Full	13.603	15.03	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+M	Outer_Full	13.586	15.15	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+M	Outer_Full	13.604	15.11	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+M	Outer_Full	13.598	14.91	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+M	Outer_Full	18.218	20.23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+M	Outer_Full	18.259	20.00	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+M	Outer_Full	18.244	20.05	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+M	Outer_Full	18.221	19.97	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+M	Outer_Full	27.840	30.01	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+M	Outer_Full	27.907	30.17	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+M	Outer_Full	27.969	29.95	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+M	Outer_Full	27.835	30.09	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+M	Outer_Full	37.870	40.53	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+M	Outer_Full	37.836	40.32	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+M	Outer_Full	37.865	40.21	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+M	Outer_Full	37.849	40.16	PASS

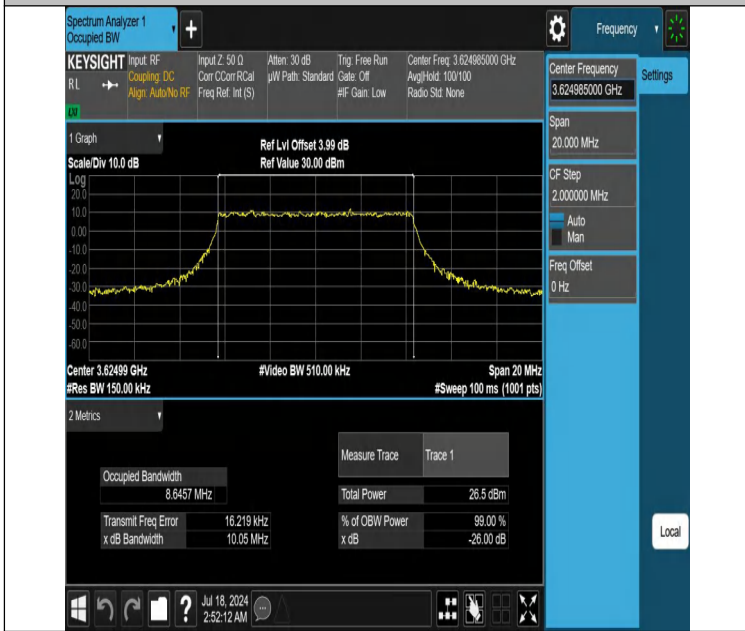
Test Graphs



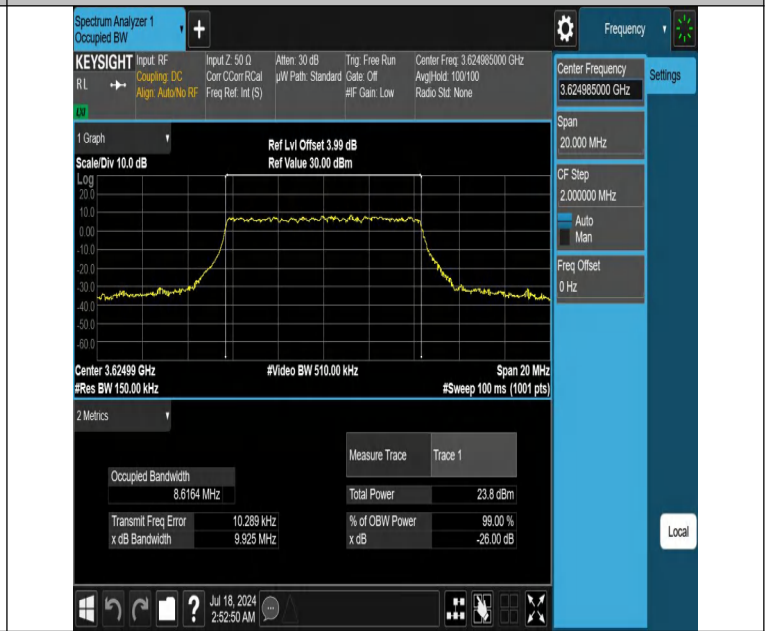
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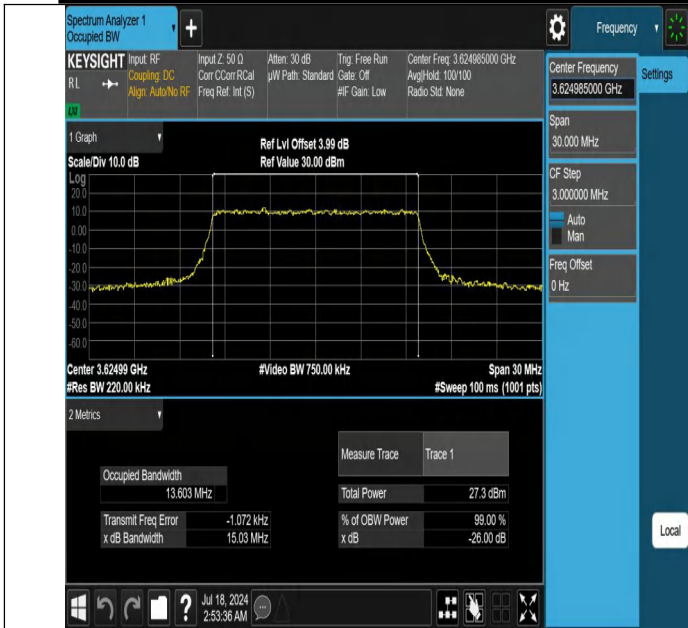
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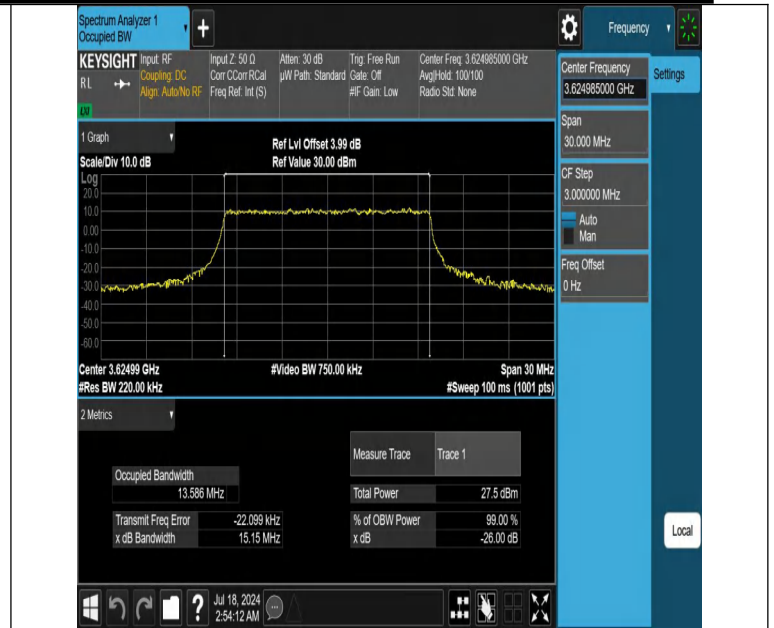
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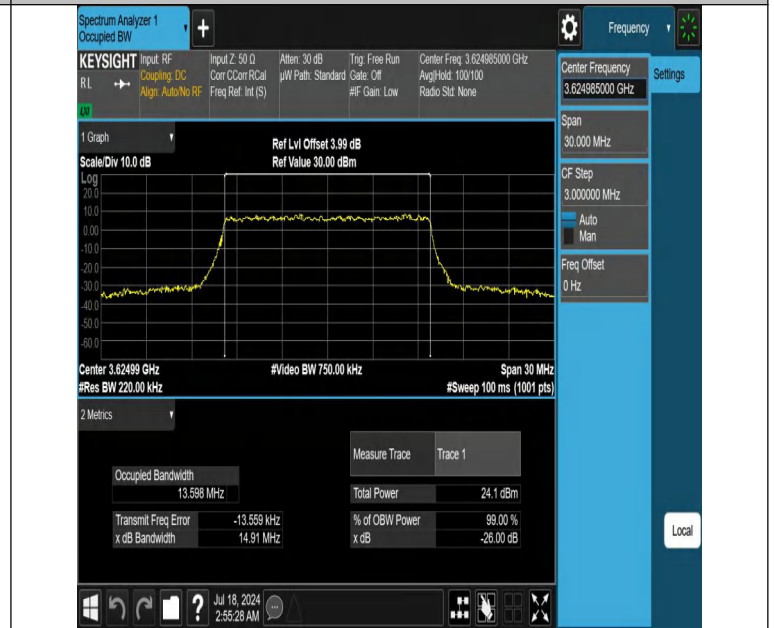
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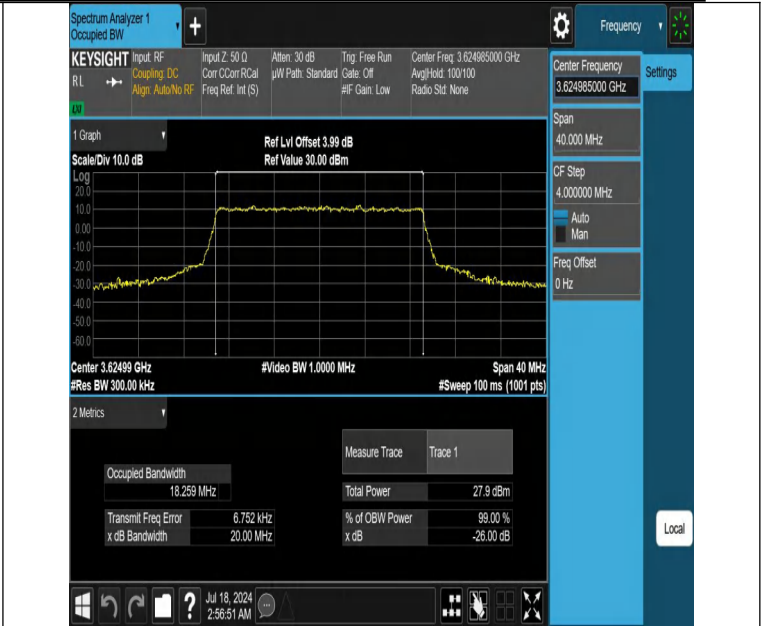
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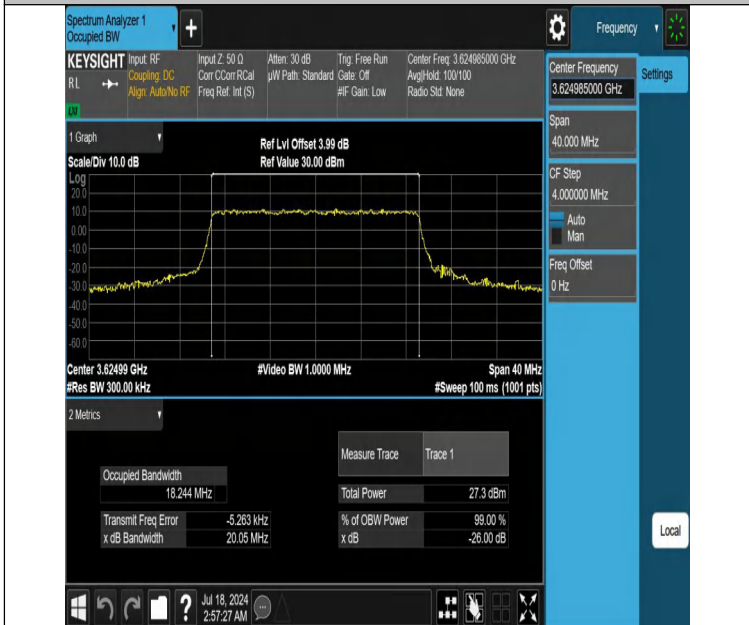
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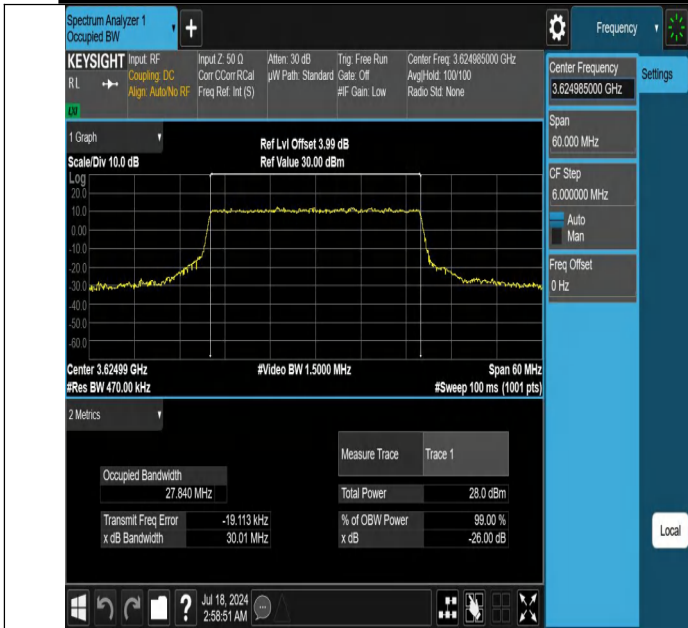
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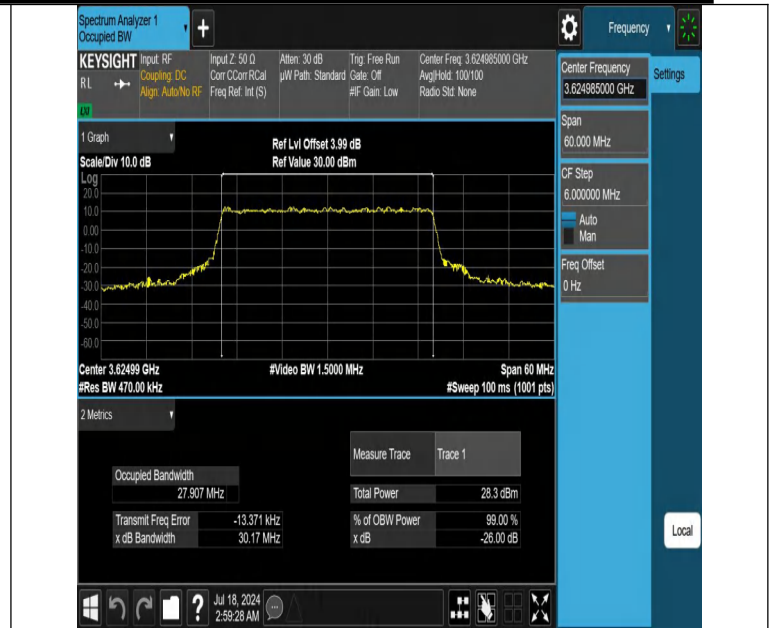
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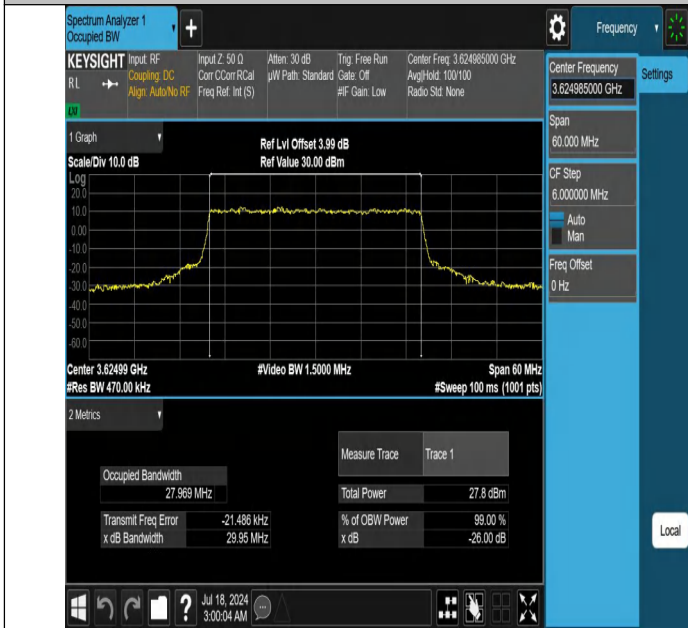
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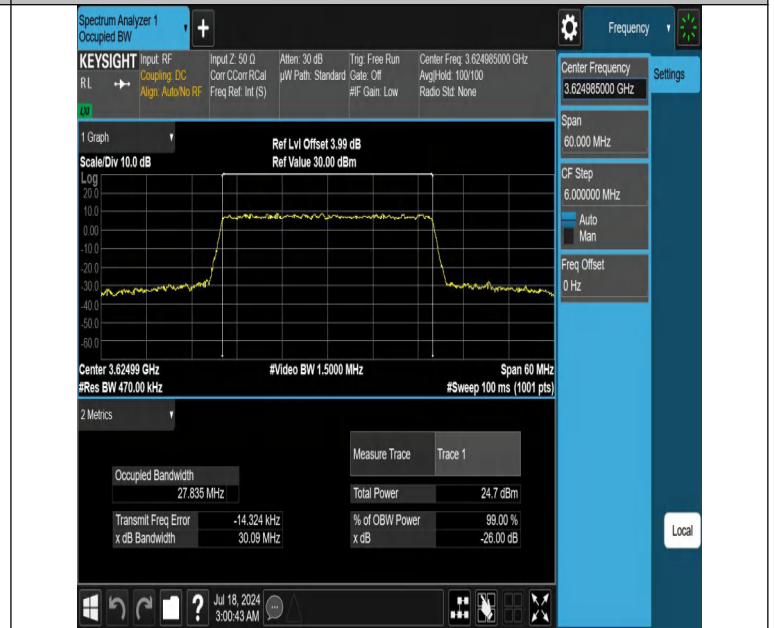
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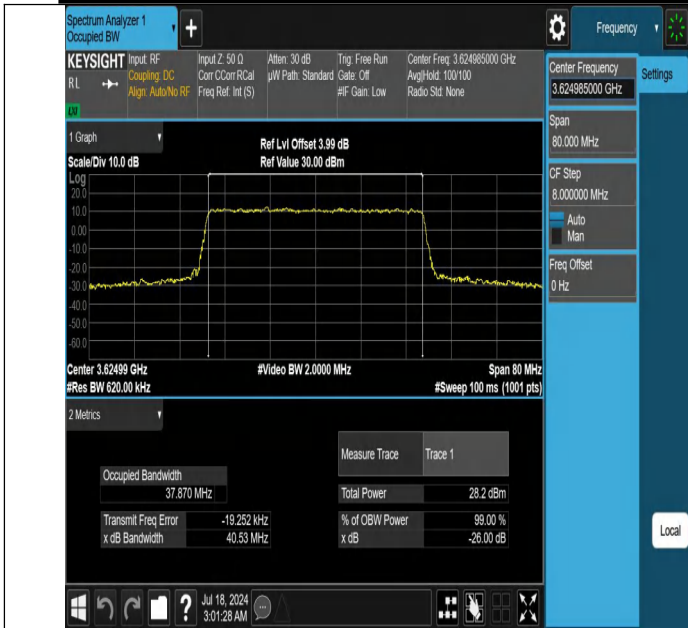
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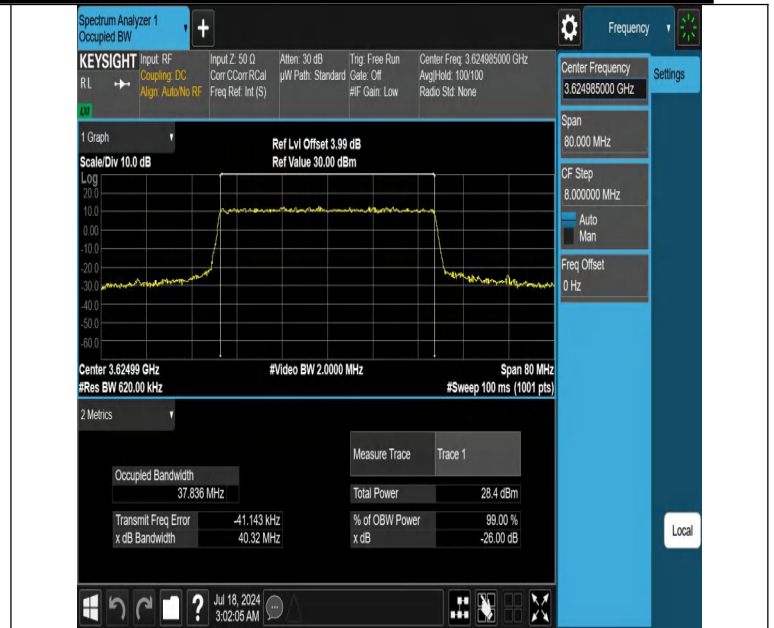
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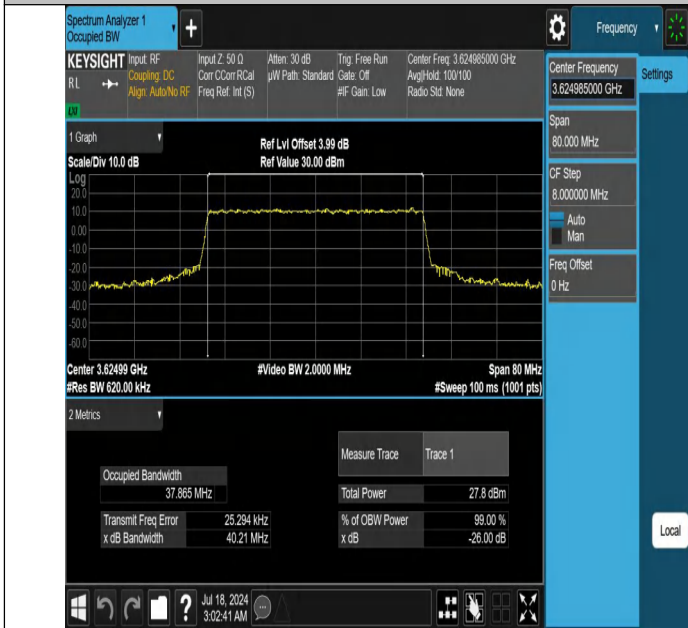
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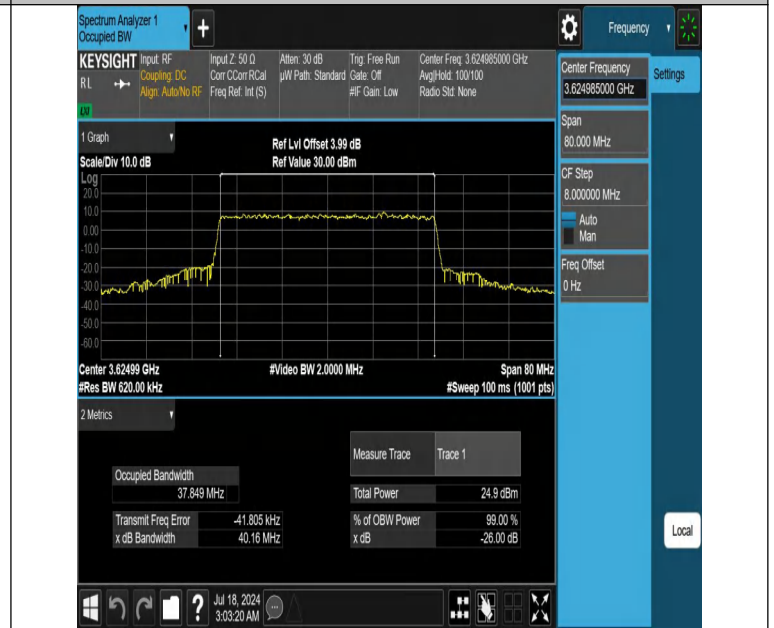
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er\_Full-Ant1-PASS



NTNV-DC\_13A\_n48A-PC3-30-5+40-M+M-N/A-N/A-CP-16QAM-Out  
er\_Full-Ant1-PASS



NTNV-DC\_13A\_n48A-PC3-30-5+40-M+M-N/A-N/A-CP-64QAM-Ou  
ter\_Full-Ant1-PASS



NTNV-DC\_13A\_n48A-PC3-30-5+40-M+M-N/A-N/A-CP-256QAM-Ou  
ter\_Full-Ant1-PASS



## Maximum eirp Density

### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (dBm)	Limit (dBm)	Verdict
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.73	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+L	Outer_Full	21.16	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+L	Inner_Full	22.03	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.78	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.21	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+L	Outer_Full	20.57	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+L	Inner_Full	22.08	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	21.07	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.29	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+L	Outer_Full	19.56	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+L	Inner_Full	21.04	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.37	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+L	Edge_1RB_Left	19.69	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+L	Outer_Full	18.90	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+L	Inner_Full	19.38	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+L	Edge_1RB_Right	19.58	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+L	Edge_1RB_Left	18.04	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+L	Outer_Full	17.14	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+L	Inner_Full	17.59	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+L	Edge_1RB_Right	17.34	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+L	Edge_1RB_Left	19.11	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+L	Outer_Full	18.60	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+L	Inner_Full	20.62	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+L	Edge_1RB_Right	19.33	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+L	Edge_1RB_Left	19.20	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+L	Outer_Full	18.69	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+L	Inner_Full	20.02	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+L	Edge_1RB_Right	19.66	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+L	Edge_1RB_Left	18.71	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+L	Outer_Full	17.95	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+L	Inner_Full	18.49	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+L	Edge_1RB_Right	18.44	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+L	Edge_1RB_Left	16.25	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+L	Outer_Full	15.14	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+L	Inner_Full	15.61	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+L	Edge_1RB_Right	16.29	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.23	23	PASS

DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Outer_Full	20.58	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Inner_Full	21.49	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.18	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.70	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Outer_Full	20.06	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Inner_Full	21.53	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	20.44	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.49	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Outer_Full	18.86	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Inner_Full	20.48	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	19.58	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Edge_1RB_Left	18.96	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Outer_Full	18.44	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Inner_Full	18.82	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+M	Edge_1RB_Right	18.91	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Edge_1RB_Left	17.37	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Outer_Full	16.35	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Inner_Full	16.87	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+M	Edge_1RB_Right	17.18	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+M	Edge_1RB_Left	18.89	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+M	Outer_Full	17.84	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+M	Inner_Full	19.92	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+M	Edge_1RB_Right	18.75	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+M	Edge_1RB_Left	18.76	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+M	Outer_Full	17.97	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+M	Inner_Full	19.38	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+M	Edge_1RB_Right	18.70	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+M	Edge_1RB_Left	18.07	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+M	Outer_Full	17.37	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+M	Inner_Full	17.98	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+M	Edge_1RB_Right	17.85	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+M	Edge_1RB_Left	15.20	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+M	Outer_Full	14.42	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+M	Inner_Full	14.69	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+M	Edge_1RB_Right	15.32	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.18	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+H	Outer_Full	20.57	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+H	Inner_Full	21.49	23	PASS
DC_13A_n48A	30	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.06	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	20.89	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+H	Outer_Full	19.99	23	PASS
DC_13A_n48A	30	5+10	DFT-QPSK	M+H	Inner_Full	21.42	23	PASS

DC_13A_n48A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	20.43	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	19.74	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+H	Outer_Full	19.01	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+H	Inner_Full	20.40	23	PASS
DC_13A_n48A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.52	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+H	Edge_1RB_Left	19.16	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+H	Outer_Full	18.47	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+H	Inner_Full	19.02	23	PASS
DC_13A_n48A	30	5+10	DFT-64QAM	M+H	Edge_1RB_Right	19.47	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+H	Edge_1RB_Left	17.23	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+H	Outer_Full	16.50	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+H	Inner_Full	16.95	23	PASS
DC_13A_n48A	30	5+10	DFT-256QAM	M+H	Edge_1RB_Right	17.02	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+H	Edge_1RB_Left	18.75	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+H	Outer_Full	17.97	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+H	Inner_Full	19.94	23	PASS
DC_13A_n48A	30	5+10	CP-QPSK	M+H	Edge_1RB_Right	18.52	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+H	Edge_1RB_Left	18.30	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+H	Outer_Full	18.05	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+H	Inner_Full	19.40	23	PASS
DC_13A_n48A	30	5+10	CP-16QAM	M+H	Edge_1RB_Right	18.64	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+H	Edge_1RB_Left	18.78	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+H	Outer_Full	17.57	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+H	Inner_Full	17.98	23	PASS
DC_13A_n48A	30	5+10	CP-64QAM	M+H	Edge_1RB_Right	18.05	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+H	Edge_1RB_Left	15.27	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+H	Outer_Full	14.48	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+H	Inner_Full	15.00	23	PASS
DC_13A_n48A	30	5+10	CP-256QAM	M+H	Edge_1RB_Right	14.91	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.96	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+L	Outer_Full	20.47	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+L	Inner_Full	22.09	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.97	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	21.44	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+L	Outer_Full	19.99	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+L	Inner_Full	22.12	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	21.38	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.70	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+L	Outer_Full	18.94	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+L	Inner_Full	21.00	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.69	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+L	Edge_1RB_Left	20.32	23	PASS

DC_13A_n48A	30	5+15	DFT-64QAM	M+L	Outer_Full	18.46	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+L	Inner_Full	19.60	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+L	Edge_1RB_Right	20.25	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+L	Edge_1RB_Left	18.07	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+L	Outer_Full	16.47	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+L	Inner_Full	17.61	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+L	Edge_1RB_Right	18.03	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+L	Edge_1RB_Left	19.53	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+L	Outer_Full	17.89	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+L	Inner_Full	20.54	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+L	Edge_1RB_Right	19.52	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+L	Edge_1RB_Left	19.77	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+L	Outer_Full	17.88	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+L	Inner_Full	19.90	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+L	Edge_1RB_Right	19.68	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+L	Edge_1RB_Left	19.36	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+L	Outer_Full	17.39	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+L	Inner_Full	18.40	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+L	Edge_1RB_Right	18.61	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+L	Edge_1RB_Left	15.80	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+L	Outer_Full	14.34	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+L	Inner_Full	15.52	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+L	Edge_1RB_Right	16.24	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.42	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Outer_Full	20.00	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Inner_Full	21.66	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.55	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	20.73	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Outer_Full	19.72	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Inner_Full	21.60	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	20.92	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	19.95	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Outer_Full	18.67	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Inner_Full	20.52	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	19.89	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Edge_1RB_Left	19.99	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Outer_Full	18.11	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Inner_Full	18.94	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+M	Edge_1RB_Right	19.57	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Edge_1RB_Left	17.22	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Outer_Full	16.04	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Inner_Full	16.88	23	PASS

DC_13A_n48A	30	5+15	DFT-256QAM	M+M	Edge_1RB_Right	17.42	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+M	Edge_1RB_Left	19.03	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+M	Outer_Full	17.62	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+M	Inner_Full	20.06	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+M	Edge_1RB_Right	18.86	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+M	Edge_1RB_Left	19.12	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+M	Outer_Full	17.71	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+M	Inner_Full	19.65	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+M	Edge_1RB_Right	18.74	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+M	Edge_1RB_Left	18.78	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+M	Outer_Full	16.77	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+M	Inner_Full	18.03	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+M	Edge_1RB_Right	18.52	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+M	Edge_1RB_Left	15.19	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+M	Outer_Full	13.75	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+M	Inner_Full	14.83	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+M	Edge_1RB_Right	15.06	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.23	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+H	Outer_Full	19.88	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+H	Inner_Full	21.46	23	PASS
DC_13A_n48A	30	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.08	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	20.73	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+H	Outer_Full	19.42	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+H	Inner_Full	21.47	23	PASS
DC_13A_n48A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	20.67	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.62	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+H	Outer_Full	18.42	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+H	Inner_Full	20.42	23	PASS
DC_13A_n48A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	19.38	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+H	Edge_1RB_Left	19.15	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+H	Outer_Full	17.93	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+H	Inner_Full	18.90	23	PASS
DC_13A_n48A	30	5+15	DFT-64QAM	M+H	Edge_1RB_Right	19.02	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+H	Edge_1RB_Left	17.10	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+H	Outer_Full	15.83	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+H	Inner_Full	16.90	23	PASS
DC_13A_n48A	30	5+15	DFT-256QAM	M+H	Edge_1RB_Right	17.10	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+H	Edge_1RB_Left	18.78	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+H	Outer_Full	17.25	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+H	Inner_Full	19.83	23	PASS
DC_13A_n48A	30	5+15	CP-QPSK	M+H	Edge_1RB_Right	18.67	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+H	Edge_1RB_Left	19.19	23	PASS

DC_13A_n48A	30	5+15	CP-16QAM	M+H	Outer_Full	17.23	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+H	Inner_Full	19.34	23	PASS
DC_13A_n48A	30	5+15	CP-16QAM	M+H	Edge_1RB_Right	18.95	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+H	Edge_1RB_Left	18.40	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+H	Outer_Full	16.82	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+H	Inner_Full	17.87	23	PASS
DC_13A_n48A	30	5+15	CP-64QAM	M+H	Edge_1RB_Right	18.37	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+H	Edge_1RB_Left	15.17	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+H	Outer_Full	13.66	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+H	Inner_Full	14.75	23	PASS
DC_13A_n48A	30	5+15	CP-256QAM	M+H	Edge_1RB_Right	15.29	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.82	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	19.44	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+L	Inner_Full	21.83	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.99	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.46	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+L	Outer_Full	18.90	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+L	Inner_Full	21.76	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.53	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	20.54	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+L	Outer_Full	18.03	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+L	Inner_Full	20.76	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	20.71	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.87	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+L	Outer_Full	17.39	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+L	Inner_Full	19.35	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	20.33	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	18.03	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+L	Outer_Full	15.37	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+L	Inner_Full	17.30	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	18.06	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	19.52	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+L	Outer_Full	16.77	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+L	Inner_Full	20.27	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	19.54	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	19.06	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+L	Outer_Full	16.67	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+L	Inner_Full	19.74	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	19.54	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	19.87	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+L	Outer_Full	16.41	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+L	Inner_Full	18.27	23	PASS

DC_13A_n48A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	20.01	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+L	Edge_1RB_Left	15.84	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+L	Outer_Full	13.39	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+L	Inner_Full	15.33	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	16.07	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.39	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	18.91	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Inner_Full	21.22	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.28	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.82	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Outer_Full	18.39	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Inner_Full	21.30	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	20.67	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	19.86	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Outer_Full	17.42	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Inner_Full	20.21	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.21	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	19.37	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Outer_Full	16.83	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Inner_Full	18.76	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	18.82	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	17.28	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Outer_Full	14.95	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Inner_Full	16.63	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	17.26	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	19.11	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+M	Outer_Full	16.38	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+M	Inner_Full	19.74	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	19.04	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	18.90	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+M	Outer_Full	16.37	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+M	Inner_Full	19.29	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	19.42	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	18.49	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+M	Outer_Full	15.90	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+M	Inner_Full	17.75	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	17.67	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	15.47	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+M	Outer_Full	12.73	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+M	Inner_Full	14.71	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	15.04	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.31	23	PASS

DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	18.84	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+H	Inner_Full	21.14	23	PASS
DC_13A_n48A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.19	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	20.76	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+H	Outer_Full	18.36	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+H	Inner_Full	21.13	23	PASS
DC_13A_n48A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	20.70	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.91	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+H	Outer_Full	17.23	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+H	Inner_Full	20.15	23	PASS
DC_13A_n48A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	19.86	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	19.17	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+H	Outer_Full	16.80	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+H	Inner_Full	18.59	23	PASS
DC_13A_n48A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	19.09	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	16.87	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+H	Outer_Full	14.69	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+H	Inner_Full	16.55	23	PASS
DC_13A_n48A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	16.82	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	19.03	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+H	Outer_Full	16.19	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+H	Inner_Full	19.59	23	PASS
DC_13A_n48A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	18.58	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	18.80	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+H	Outer_Full	16.18	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+H	Inner_Full	18.93	23	PASS
DC_13A_n48A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	18.73	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+H	Edge_1RB_Left	18.32	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+H	Outer_Full	15.74	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+H	Inner_Full	17.59	23	PASS
DC_13A_n48A	30	5+20	CP-64QAM	M+H	Edge_1RB_Right	18.26	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+H	Edge_1RB_Left	15.20	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+H	Outer_Full	12.64	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+H	Inner_Full	14.55	23	PASS
DC_13A_n48A	30	5+20	CP-256QAM	M+H	Edge_1RB_Right	15.67	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.23	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+L	Outer_Full	17.87	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+L	Inner_Full	21.17	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.09	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	21.64	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+L	Outer_Full	17.21	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+L	Inner_Full	21.15	23	PASS



DC_13A_n48A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	21.53	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	20.67	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+L	Outer_Full	16.10	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+L	Inner_Full	20.16	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	20.56	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+L	Edge_1RB_Left	19.95	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+L	Outer_Full	15.87	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+L	Inner_Full	18.54	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+L	Edge_1RB_Right	19.84	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+L	Edge_1RB_Left	18.40	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+L	Outer_Full	13.69	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+L	Inner_Full	16.62	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+L	Edge_1RB_Right	18.30	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+L	Edge_1RB_Left	19.64	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+L	Outer_Full	15.11	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+L	Inner_Full	19.40	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+L	Edge_1RB_Right	19.54	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+L	Edge_1RB_Left	19.64	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+L	Outer_Full	15.07	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+L	Inner_Full	18.91	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+L	Edge_1RB_Right	19.52	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+L	Edge_1RB_Left	18.89	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+L	Outer_Full	14.61	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+L	Inner_Full	17.40	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+L	Edge_1RB_Right	18.81	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+L	Edge_1RB_Left	16.25	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+L	Outer_Full	11.69	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+L	Inner_Full	14.50	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+L	Edge_1RB_Right	16.07	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.78	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Outer_Full	17.37	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Inner_Full	20.81	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.43	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	21.06	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+M	Outer_Full	16.93	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+M	Inner_Full	20.76	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	21.12	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	19.83	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+M	Outer_Full	15.88	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+M	Inner_Full	19.88	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	19.87	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+M	Edge_1RB_Left	20.06	23	PASS

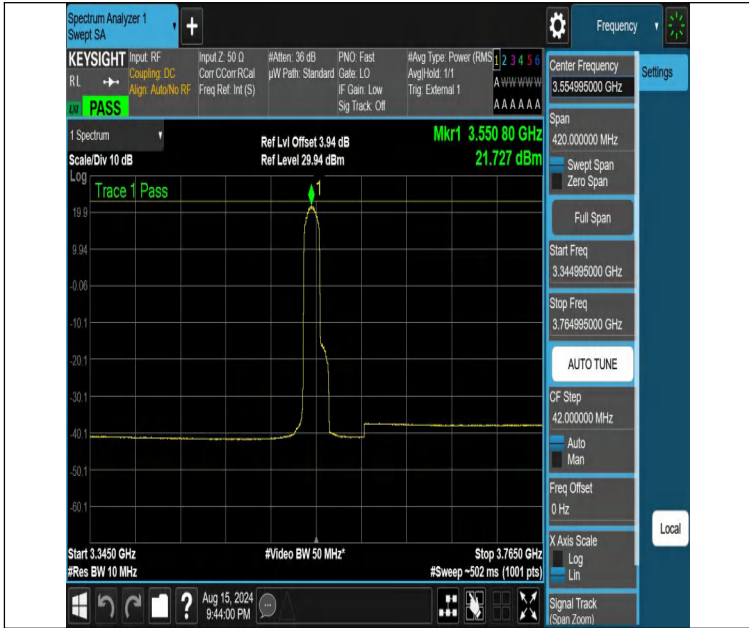
DC_13A_n48A	30	5+30	DFT-64QAM	M+M	Outer_Full	15.36	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+M	Inner_Full	18.13	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+M	Edge_1RB_Right	20.02	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+M	Edge_1RB_Left	17.71	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+M	Outer_Full	13.23	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+M	Inner_Full	16.29	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+M	Edge_1RB_Right	17.56	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+M	Edge_1RB_Left	19.28	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+M	Outer_Full	14.65	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+M	Inner_Full	19.03	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+M	Edge_1RB_Right	19.03	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+M	Edge_1RB_Left	18.81	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+M	Outer_Full	14.63	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+M	Inner_Full	18.41	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+M	Edge_1RB_Right	19.08	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+M	Edge_1RB_Left	18.71	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+M	Outer_Full	14.19	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+M	Inner_Full	17.09	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+M	Edge_1RB_Right	18.54	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+M	Edge_1RB_Left	15.63	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+M	Outer_Full	11.23	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+M	Inner_Full	13.80	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+M	Edge_1RB_Right	15.60	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.52	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+H	Outer_Full	17.31	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+H	Inner_Full	20.70	23	PASS
DC_13A_n48A	30	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.29	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	21.12	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+H	Outer_Full	16.67	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+H	Inner_Full	20.71	23	PASS
DC_13A_n48A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	20.76	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	20.22	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+H	Outer_Full	15.75	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+H	Inner_Full	19.63	23	PASS
DC_13A_n48A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	19.11	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+H	Edge_1RB_Left	18.92	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+H	Outer_Full	15.36	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+H	Inner_Full	18.16	23	PASS
DC_13A_n48A	30	5+30	DFT-64QAM	M+H	Edge_1RB_Right	18.74	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+H	Edge_1RB_Left	17.50	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+H	Outer_Full	13.32	23	PASS
DC_13A_n48A	30	5+30	DFT-256QAM	M+H	Inner_Full	16.11	23	PASS

DC_13A_n48A	30	5+30	DFT-256QAM	M+H	Edge_1RB_Right	17.49	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+H	Edge_1RB_Left	18.97	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+H	Outer_Full	14.53	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+H	Inner_Full	18.89	23	PASS
DC_13A_n48A	30	5+30	CP-QPSK	M+H	Edge_1RB_Right	18.83	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+H	Edge_1RB_Left	19.39	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+H	Outer_Full	14.64	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+H	Inner_Full	18.39	23	PASS
DC_13A_n48A	30	5+30	CP-16QAM	M+H	Edge_1RB_Right	19.00	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+H	Edge_1RB_Left	18.23	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+H	Outer_Full	14.07	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+H	Inner_Full	16.90	23	PASS
DC_13A_n48A	30	5+30	CP-64QAM	M+H	Edge_1RB_Right	18.00	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+H	Edge_1RB_Left	15.33	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+H	Outer_Full	11.01	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+H	Inner_Full	14.02	23	PASS
DC_13A_n48A	30	5+30	CP-256QAM	M+H	Edge_1RB_Right	15.61	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.05	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	16.63	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+L	Inner_Full	20.00	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.06	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.68	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+L	Outer_Full	16.20	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+L	Inner_Full	20.05	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.37	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	20.71	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+L	Outer_Full	15.17	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+L	Inner_Full	18.96	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.56	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	20.56	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+L	Outer_Full	14.68	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+L	Inner_Full	17.49	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Right	20.24	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	18.13	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+L	Outer_Full	12.66	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+L	Inner_Full	15.55	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Right	17.84	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	19.68	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+L	Outer_Full	14.02	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+L	Inner_Full	18.19	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	19.62	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	19.57	23	PASS

DC_13A_n48A	30	5+40	CP-16QAM	M+L	Outer_Full	13.96	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+L	Inner_Full	17.94	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	19.56	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+L	Edge_1RB_Left	19.47	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+L	Outer_Full	13.49	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+L	Inner_Full	16.27	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+L	Edge_1RB_Right	19.50	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+L	Edge_1RB_Left	15.95	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+L	Outer_Full	10.59	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+L	Inner_Full	13.21	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+L	Edge_1RB_Right	15.94	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.61	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	16.16	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Inner_Full	19.55	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.60	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.22	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Outer_Full	15.76	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Inner_Full	19.67	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.12	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.32	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Outer_Full	14.71	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Inner_Full	18.56	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.40	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Left	19.49	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Outer_Full	14.25	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Inner_Full	16.97	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Right	19.36	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Left	17.59	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Outer_Full	12.14	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Inner_Full	15.07	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Right	17.52	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	19.19	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+M	Outer_Full	13.39	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+M	Inner_Full	17.81	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	19.11	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	19.43	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+M	Outer_Full	13.39	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+M	Inner_Full	17.34	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	18.86	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+M	Edge_1RB_Left	18.67	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+M	Outer_Full	12.89	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+M	Inner_Full	15.77	23	PASS

DC_13A_n48A	30	5+40	CP-64QAM	M+M	Edge_1RB_Right	18.63	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+M	Edge_1RB_Left	15.54	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+M	Outer_Full	9.89	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+M	Inner_Full	12.79	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+M	Edge_1RB_Right	15.75	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.23	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	16.16	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+H	Inner_Full	19.54	23	PASS
DC_13A_n48A	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.45	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.21	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+H	Outer_Full	15.65	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+H	Inner_Full	19.52	23	PASS
DC_13A_n48A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	20.83	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.18	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+H	Outer_Full	14.68	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+H	Inner_Full	18.54	23	PASS
DC_13A_n48A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.01	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	19.82	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+H	Outer_Full	14.29	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+H	Inner_Full	16.99	23	PASS
DC_13A_n48A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Right	20.35	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	17.81	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+H	Outer_Full	12.13	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+H	Inner_Full	15.03	23	PASS
DC_13A_n48A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	17.44	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	19.10	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+H	Outer_Full	13.36	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+H	Inner_Full	17.77	23	PASS
DC_13A_n48A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	18.88	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	19.17	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+H	Outer_Full	13.43	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+H	Inner_Full	17.26	23	PASS
DC_13A_n48A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	18.46	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+H	Edge_1RB_Left	19.26	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+H	Outer_Full	12.91	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+H	Inner_Full	15.86	23	PASS
DC_13A_n48A	30	5+40	CP-64QAM	M+H	Edge_1RB_Right	18.54	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+H	Edge_1RB_Left	15.54	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+H	Outer_Full	9.93	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+H	Inner_Full	12.56	23	PASS
DC_13A_n48A	30	5+40	CP-256QAM	M+H	Edge_1RB_Right	15.47	23	PASS

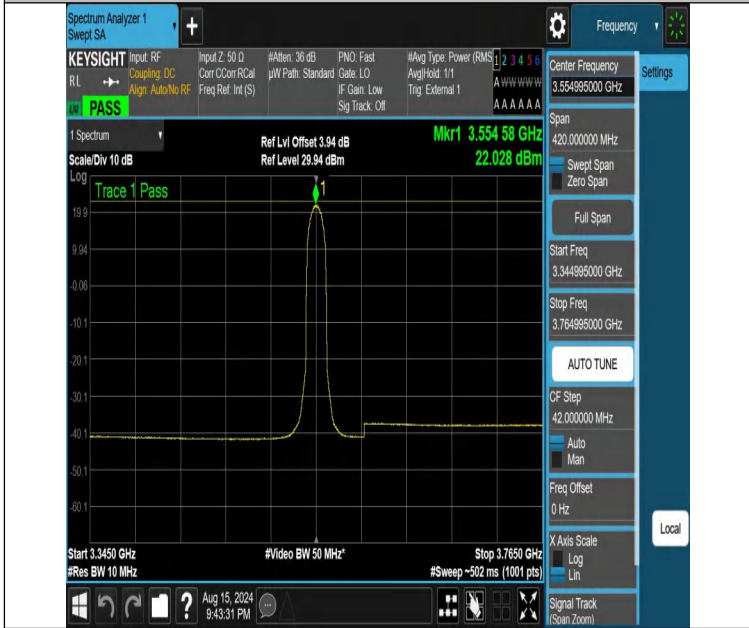
Test Graphs



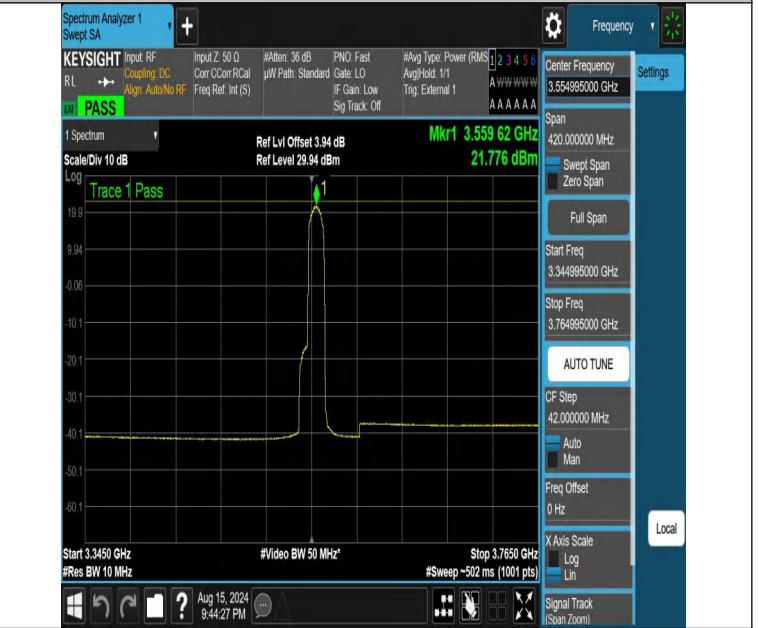
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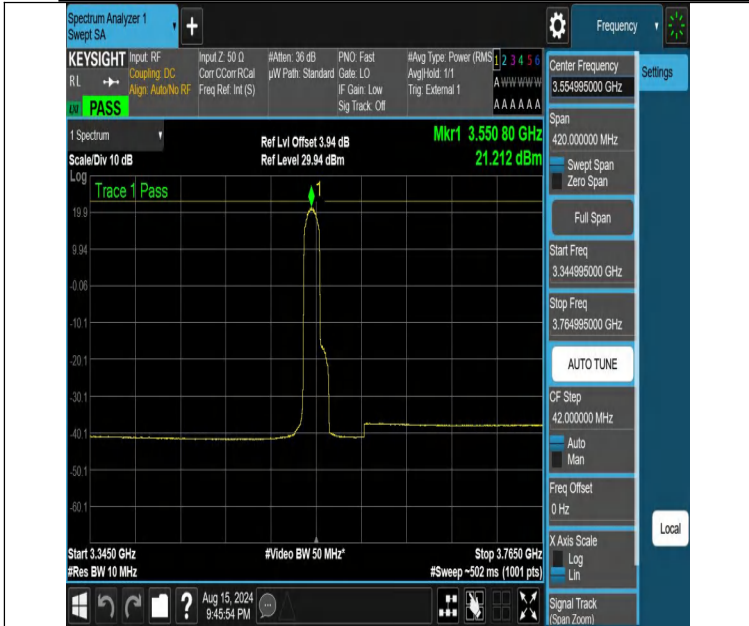
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NTNV-DC\_13A\_n48A-PC3-30-5+10-M+L-N/A-N/A-DFT-PI2BPSK-E dge\_1RB\_Right-3344.995-3764.995-PASS



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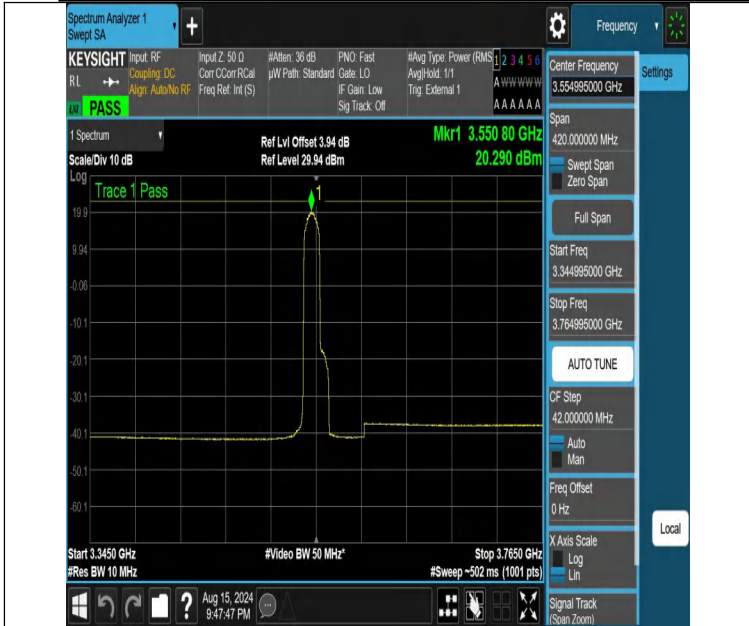
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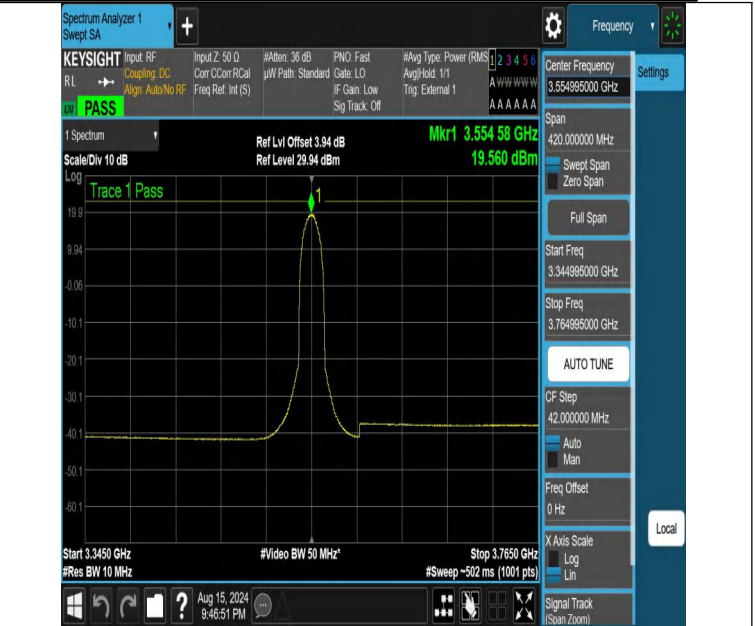
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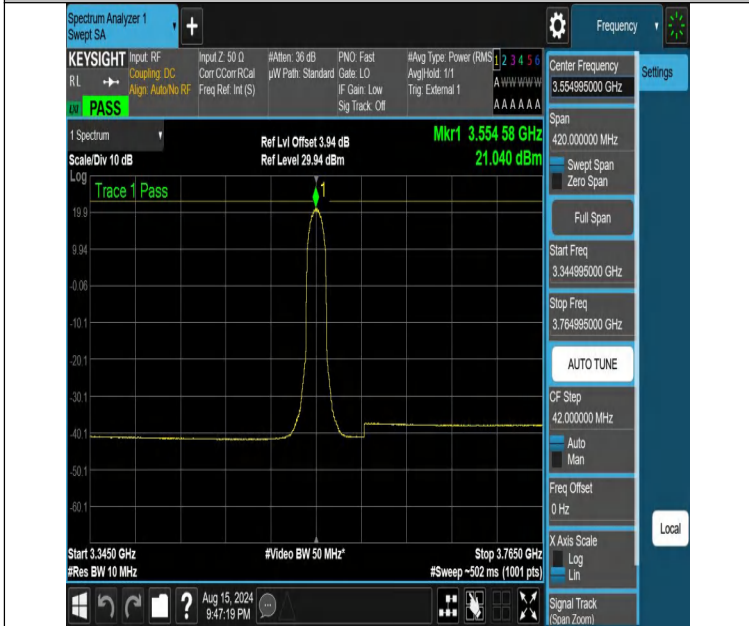
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NTNV-DC\_13A\_n48A-PC3-30-5+10-M+L-N/A-N/A-DFT-16QAM-Edge\_1RB\_Left-3344.995-3764.995-PASS



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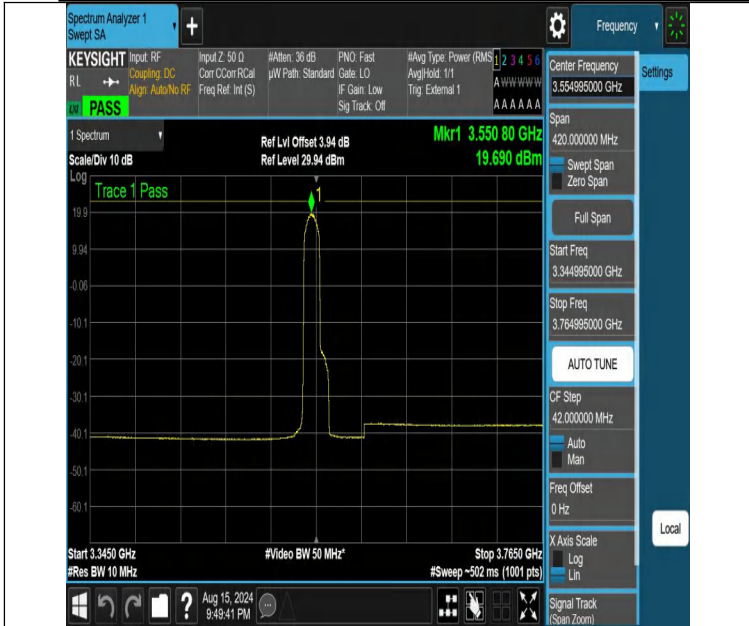


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NTNV-DC\_13A\_n48A-PC3-30-5+10-M+L-N/A-N/A-DFT-64QAM-Edge\_1RB\_Left-3344.995-3764.995-PASS



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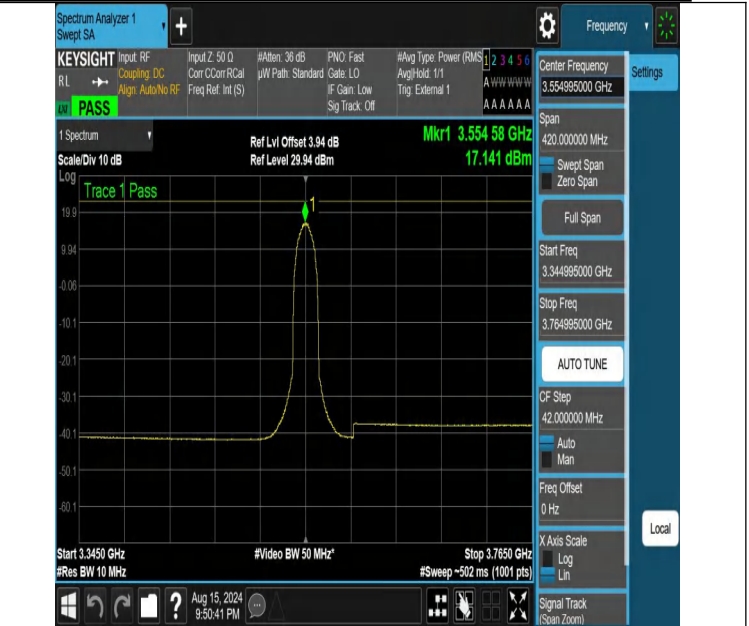
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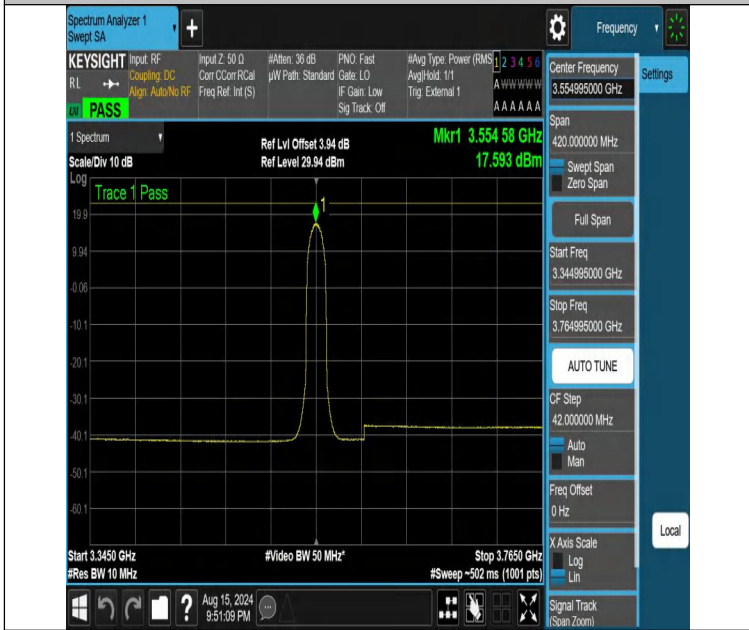
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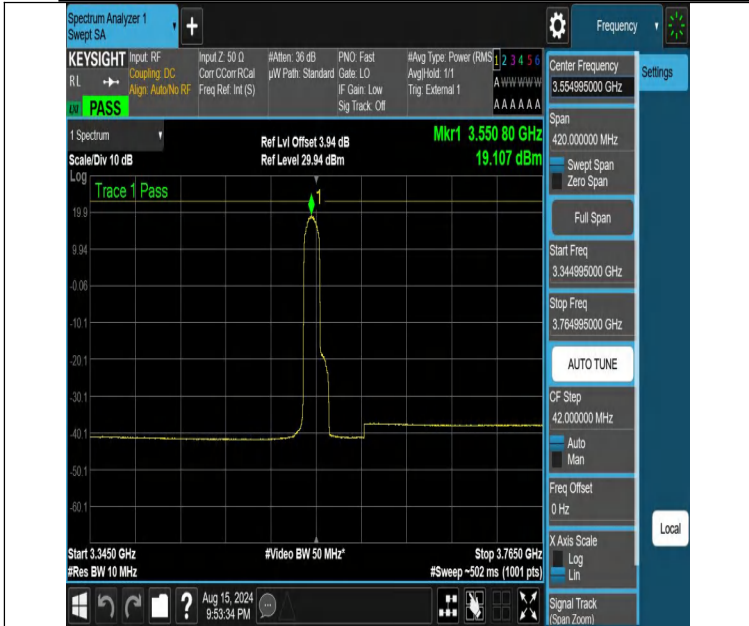
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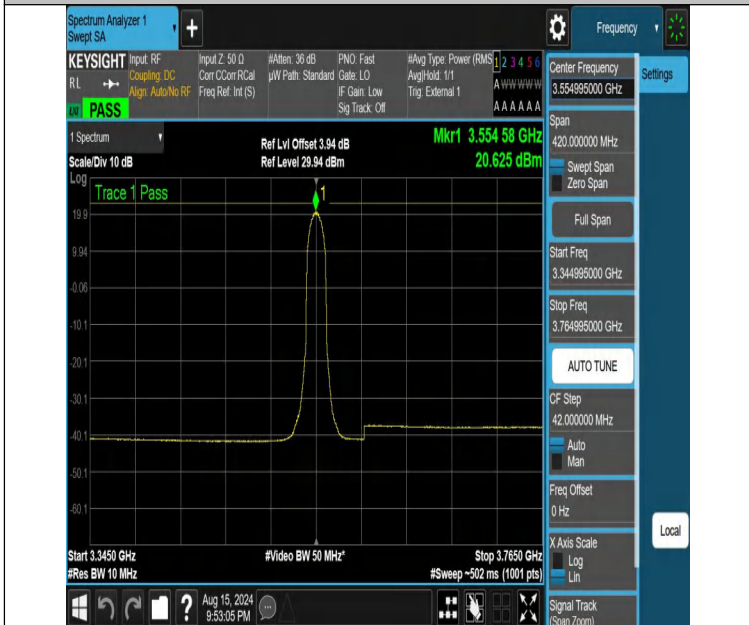
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NTNV-DC\_13A\_n48A-PC3-30-5+10-M+L-N/A-N/A-CP-QPSK-Edge\_1RB\_Left-3344.995-3764.995-PASS



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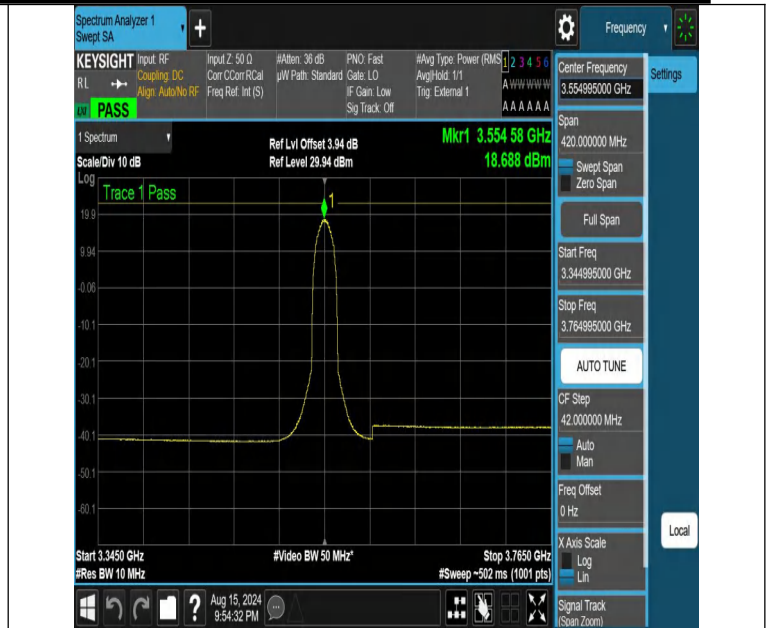
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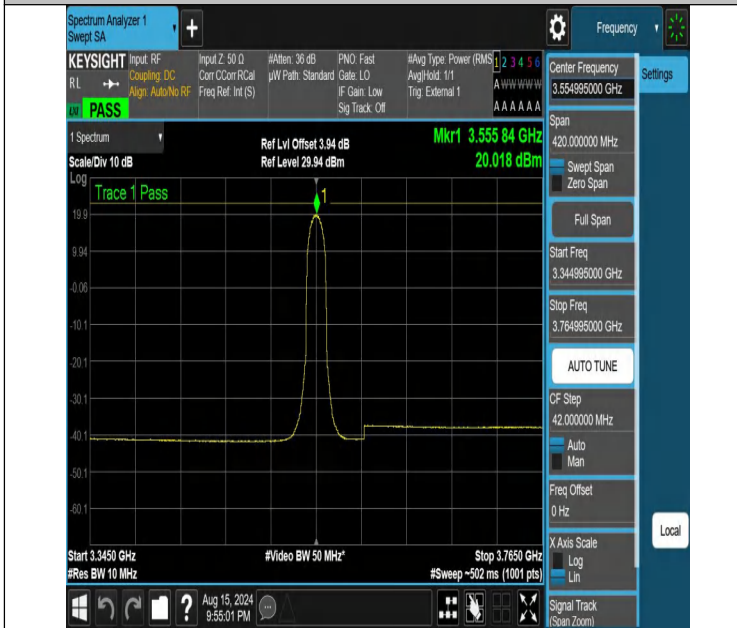
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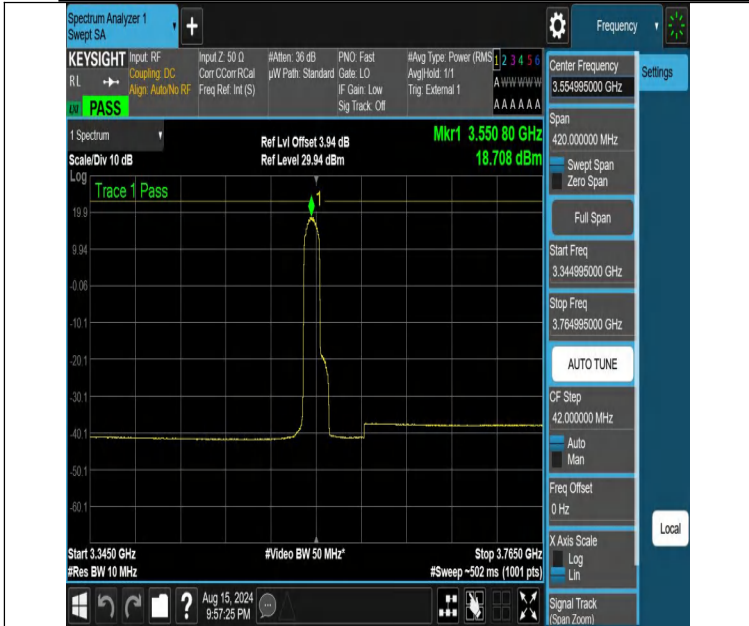
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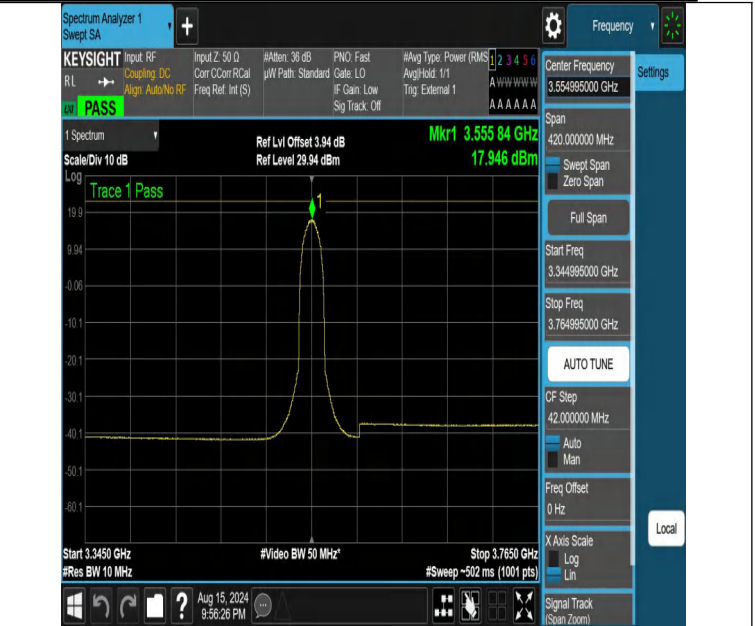
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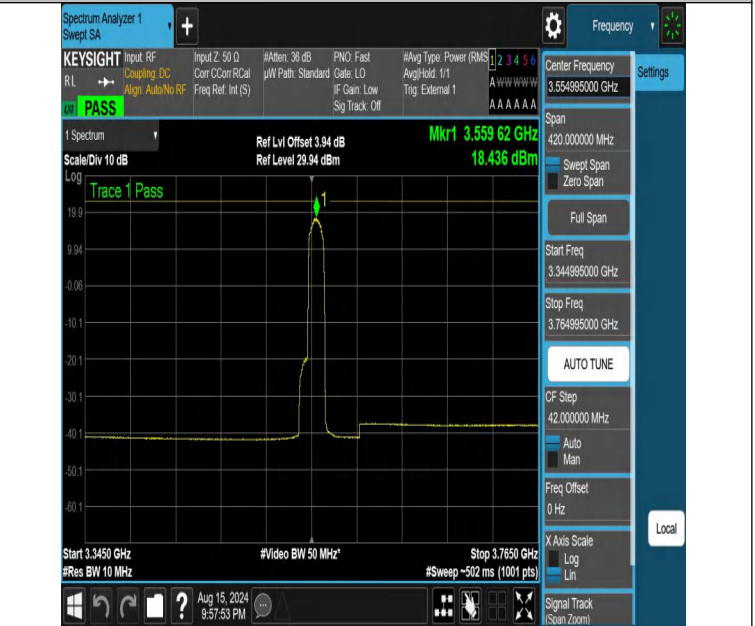
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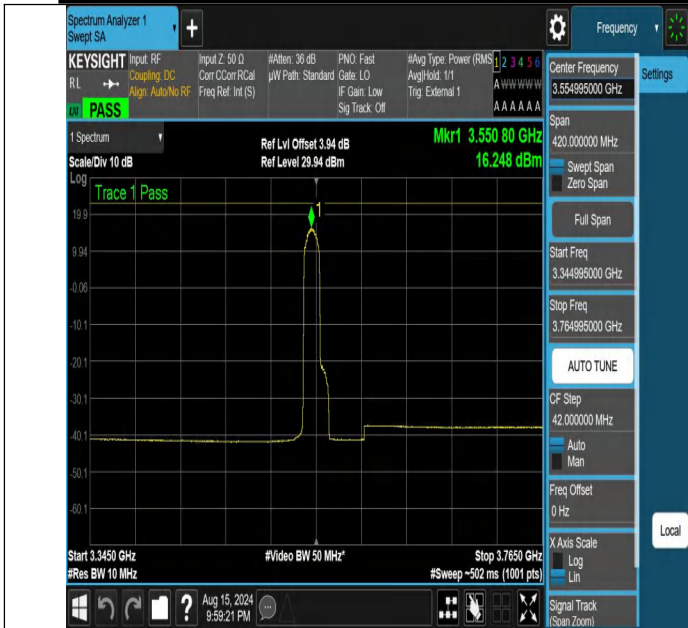
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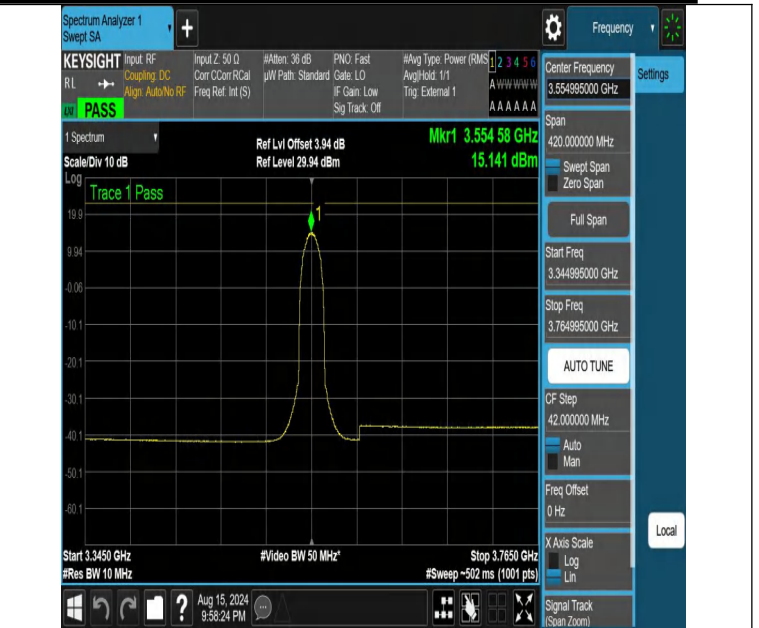
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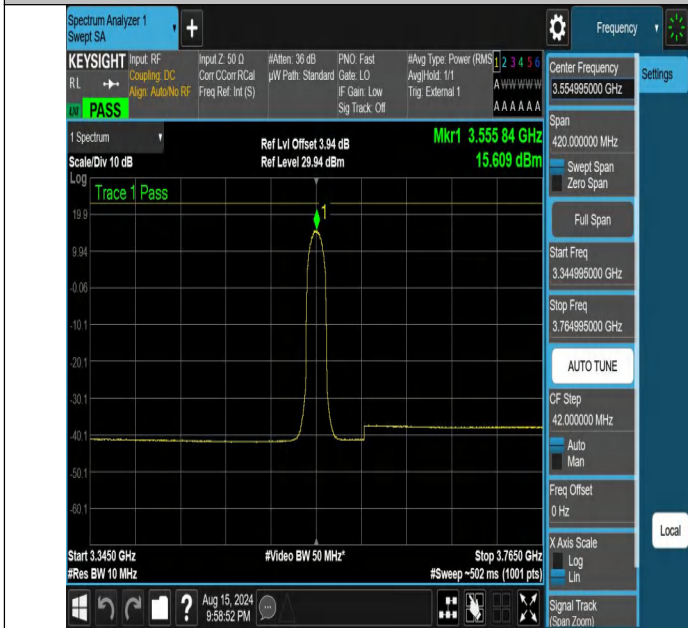
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NTNV-DC\_13A\_n48A-PC3-30-5+10-M+L-N/A-N/A-CP-256QAM-Edge\_1RB\_Left-3344.995-3764.995-PASS



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NTNV-DC\_13A\_n48A-PC3-30-5+10-M+L-N/A-N/A-CP-256QAM-Inner\_Full-3344.995-3764.995-PASS



NTNV-DC\_13A\_n48A-PC3-30-5+10-M+L-N/A-N/A-CP-256QAM-Edge\_1RB\_Right-3344.995-3764.995-PASS