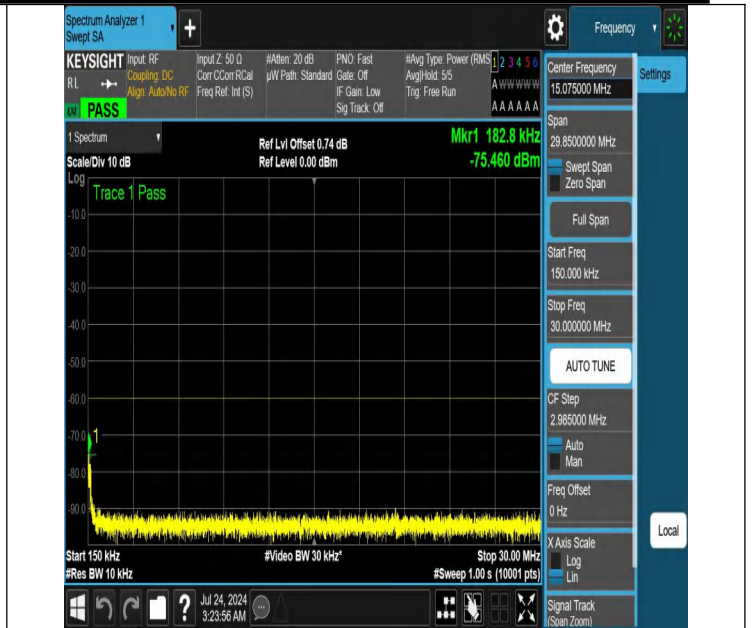
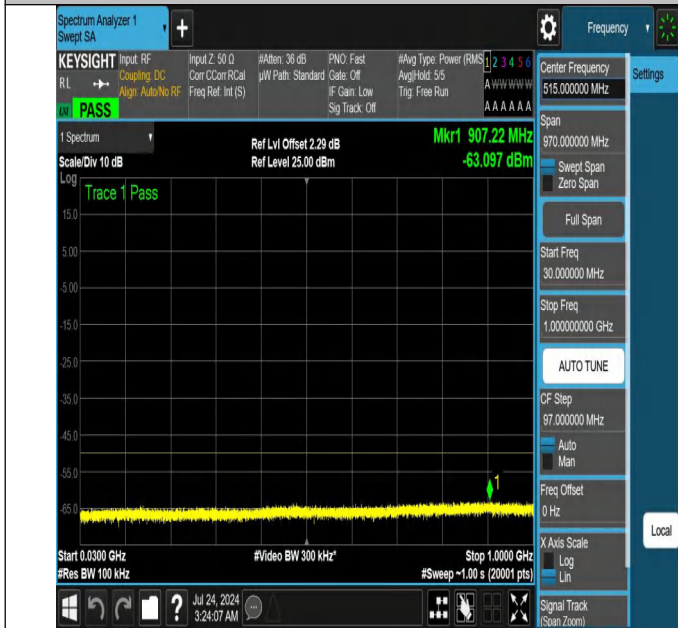


NTNV-N48-PC3-30-40-L-DFT-PI2BPSK-Edge_1RB_Left-0.009-0.15-Ant1-PASS



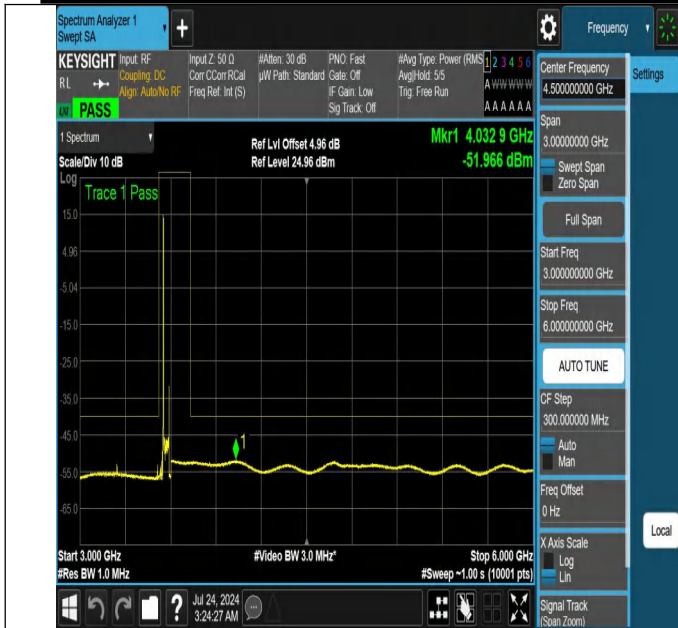
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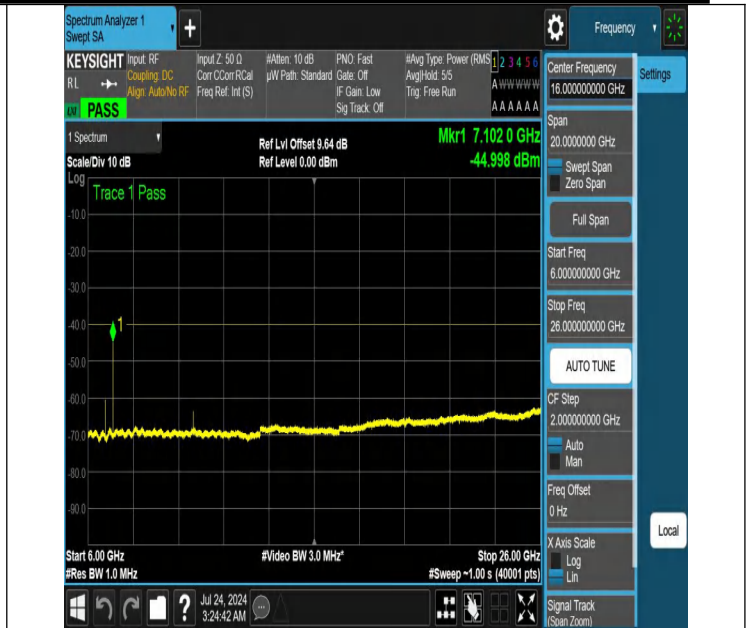
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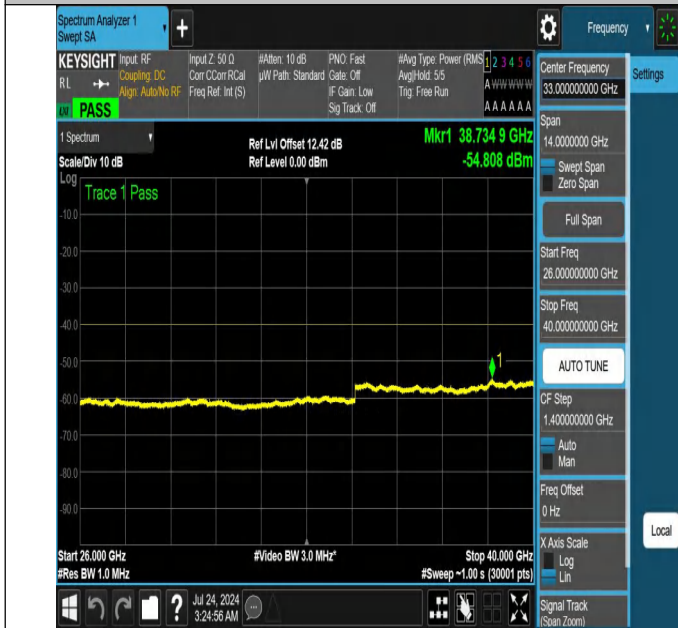
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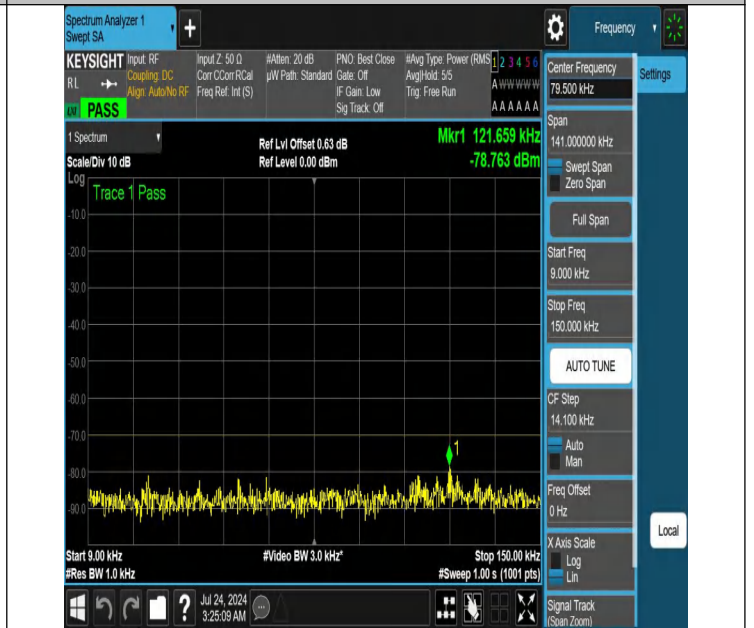
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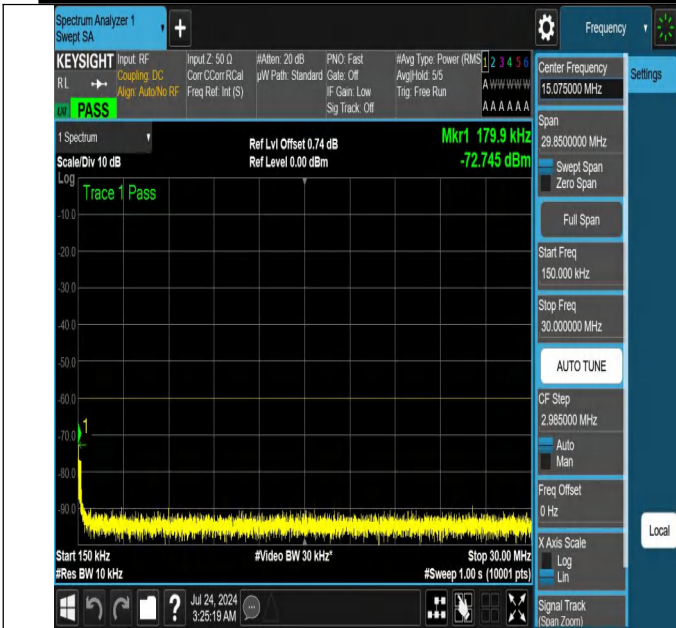
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 000-Ant1-PASS



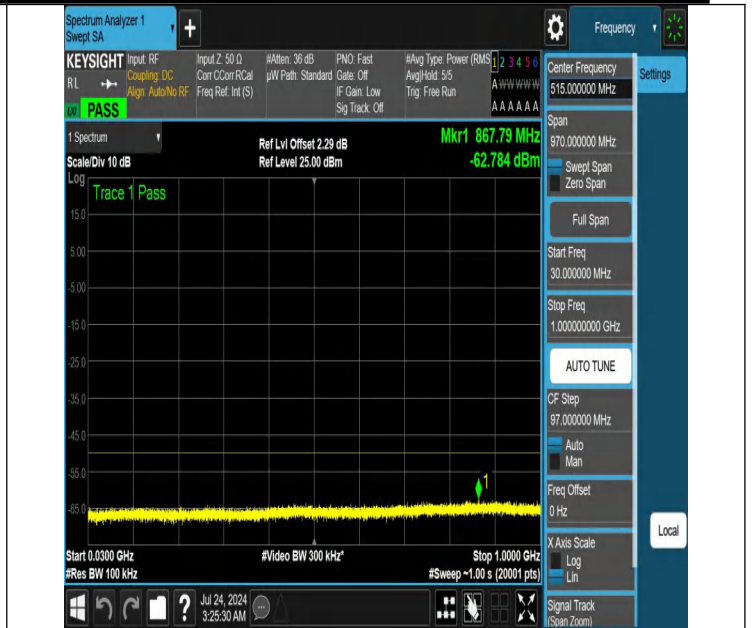
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NTNV-N48-PC3-30-40-L-DFT-PI2BPSK-Edge_1RB_Right-0.009-0
 .15-Ant1-PASS



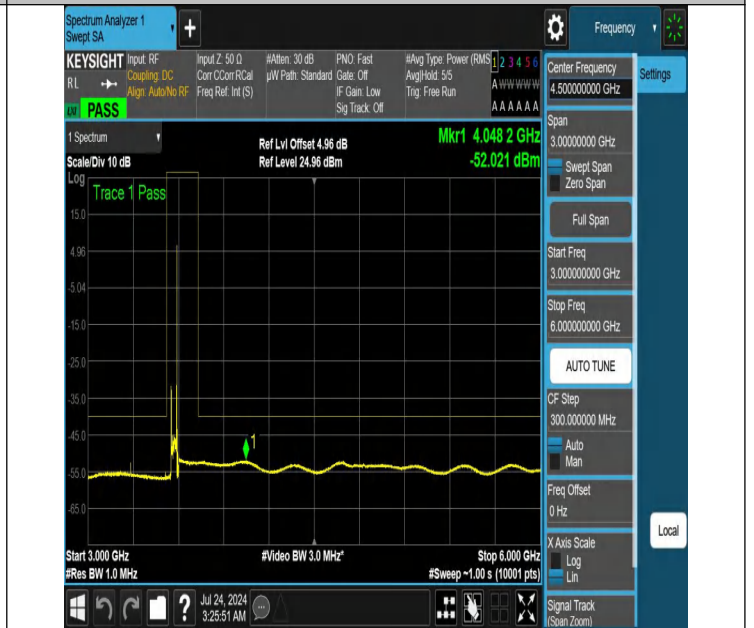
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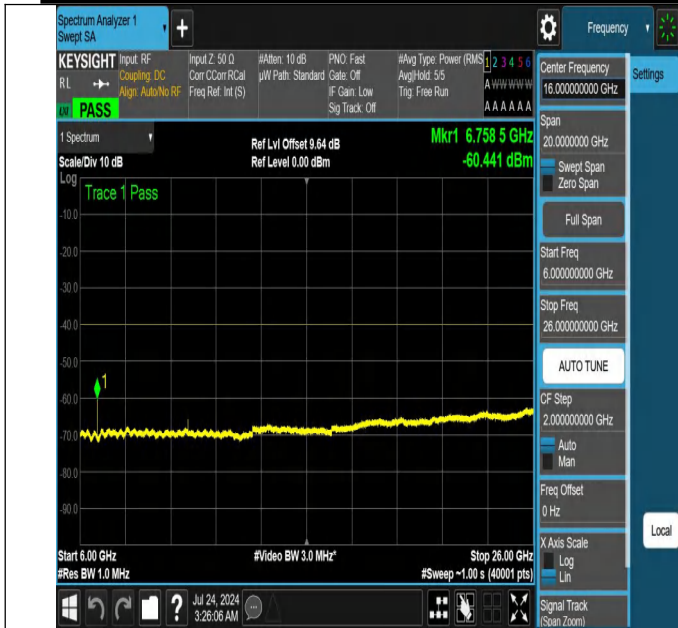
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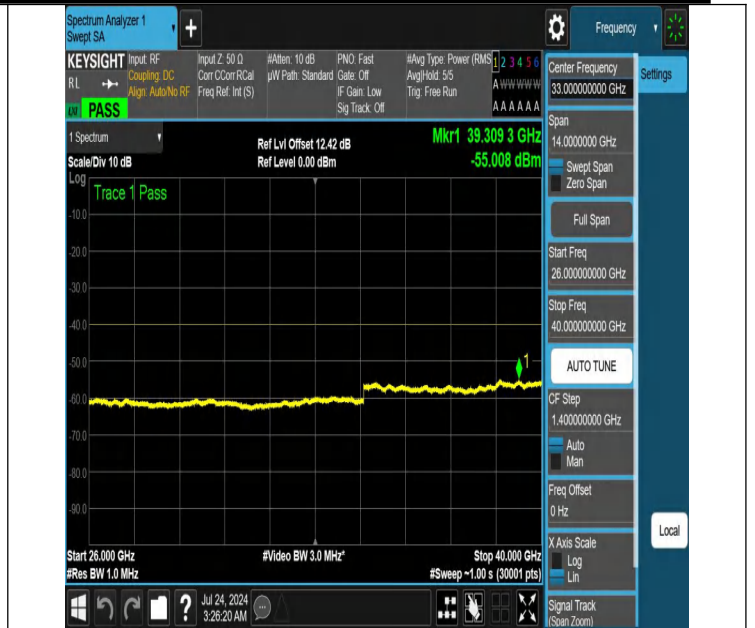
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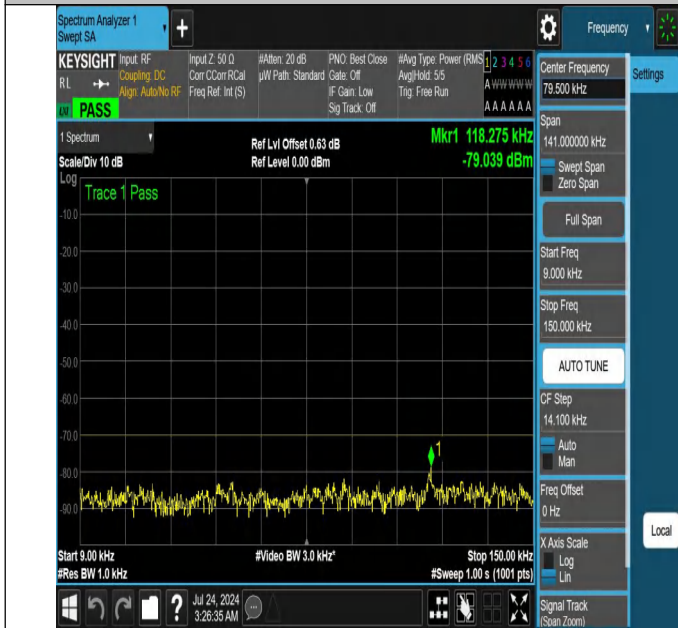
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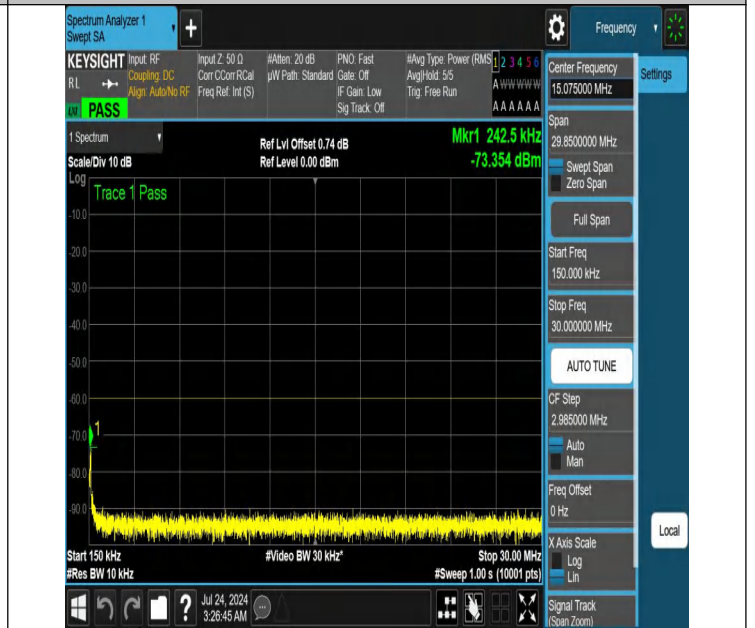
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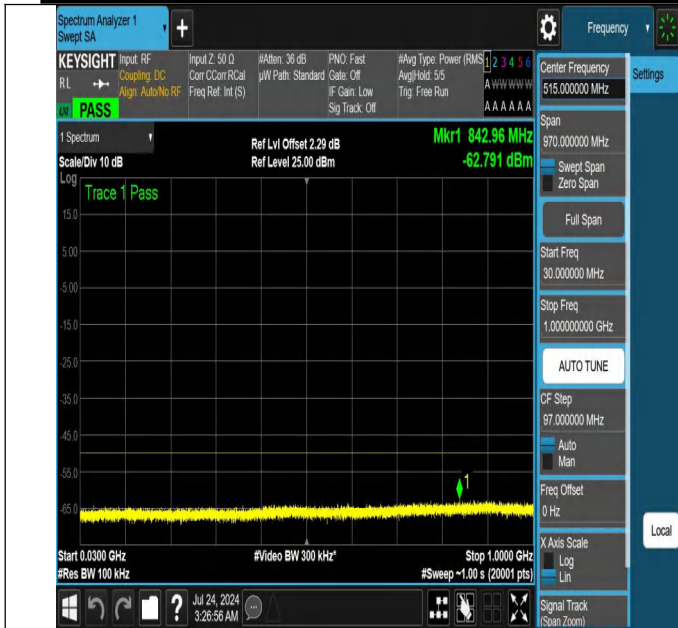
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NTNV-N48-PC3-30-40-L-DFT-QPSK-Edge_1RB_Left-0.009-0.15-Ant1-PASS



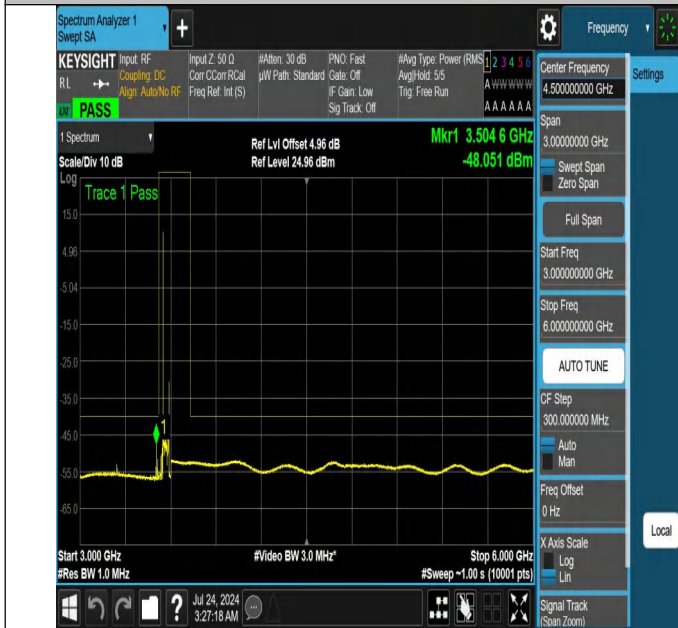
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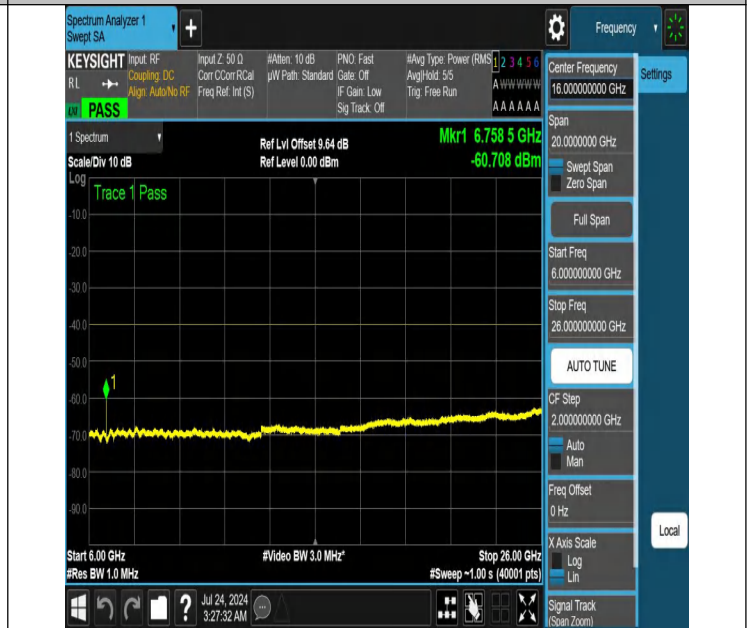
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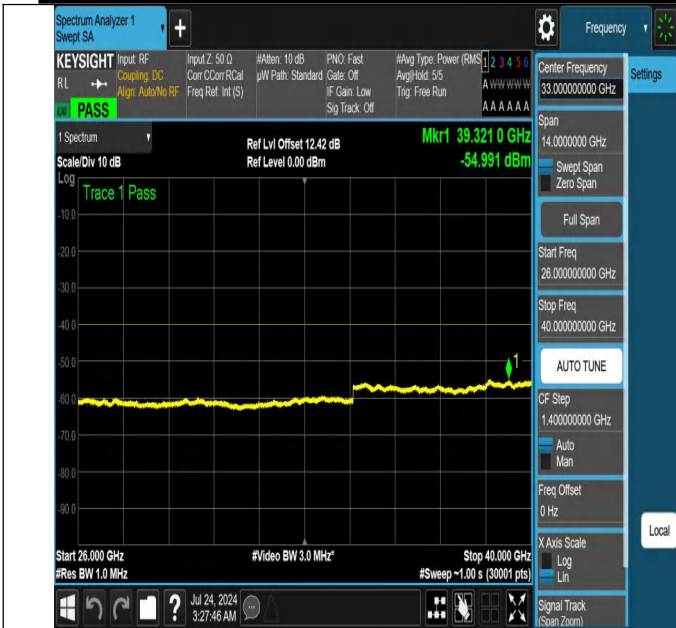
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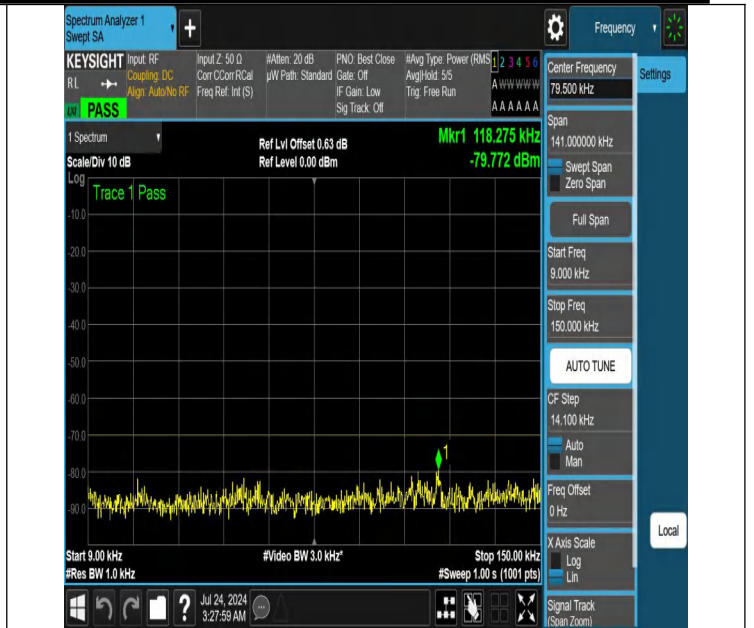
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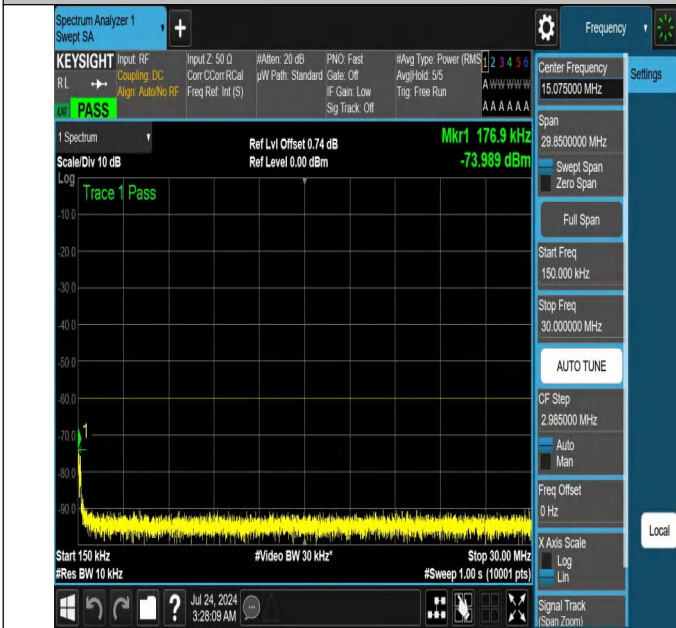
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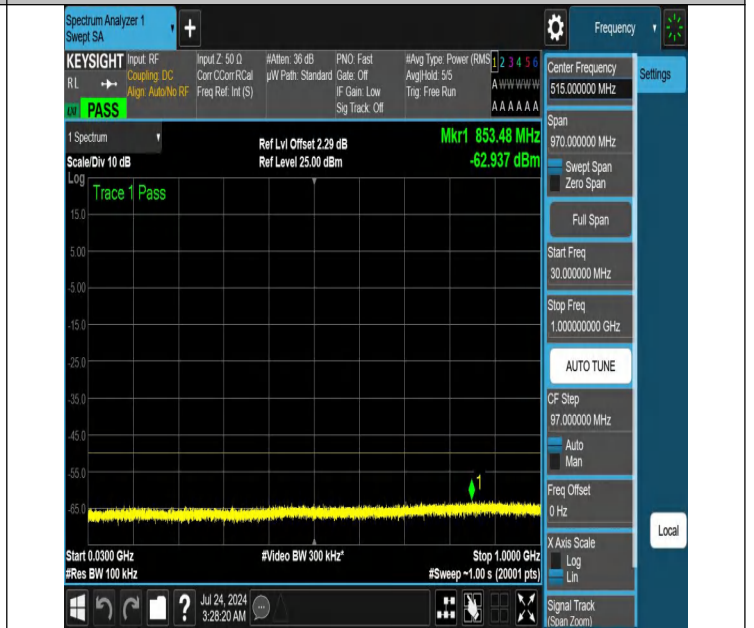
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NTNV-N48-PC3-30-40-L-DFT-QPSK-Edge_1RB_Right-0.009-0.150-Ant1-PASS



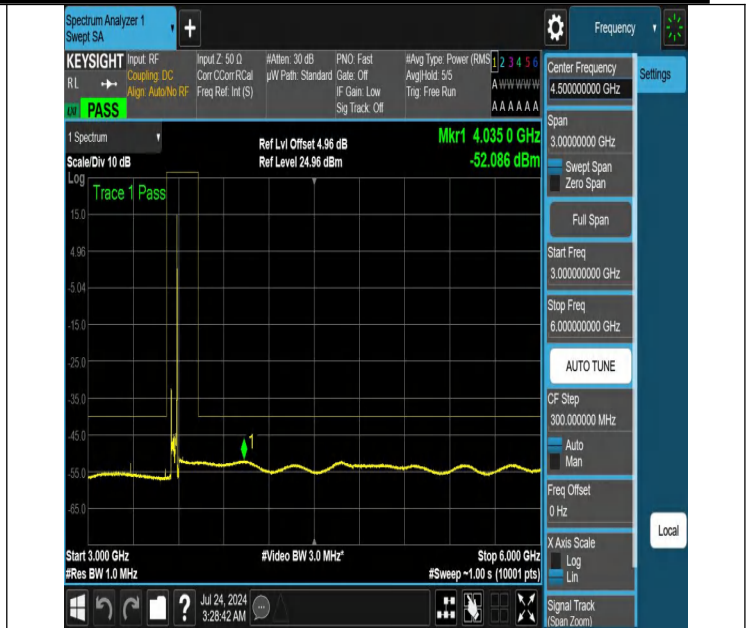
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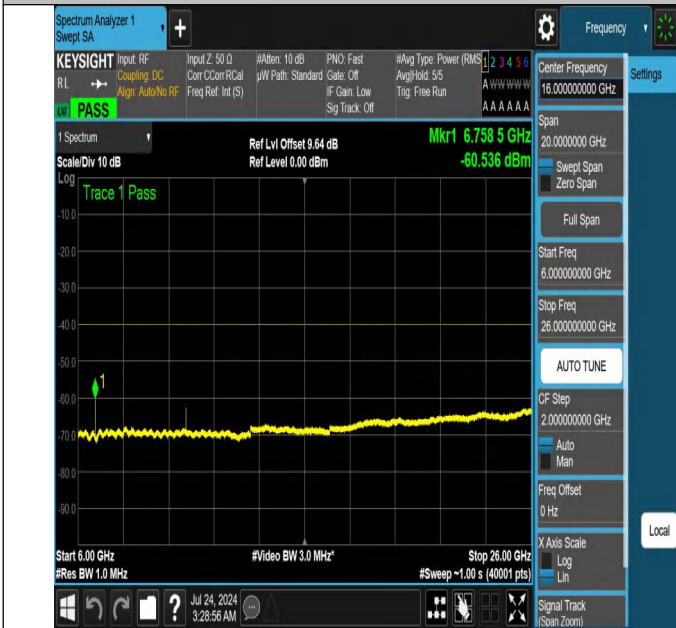
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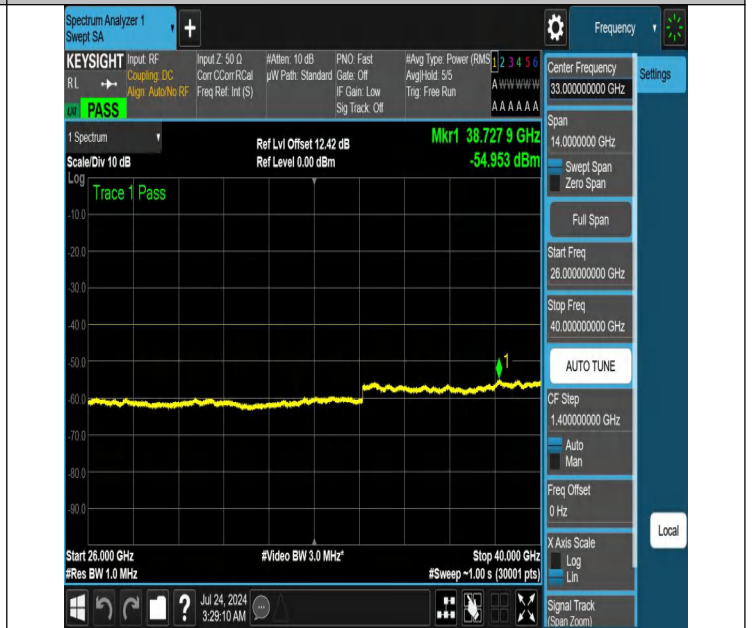
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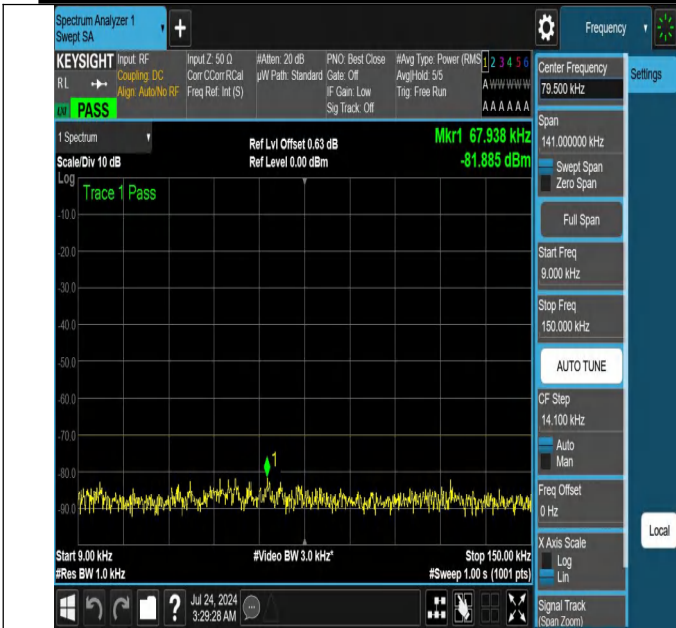
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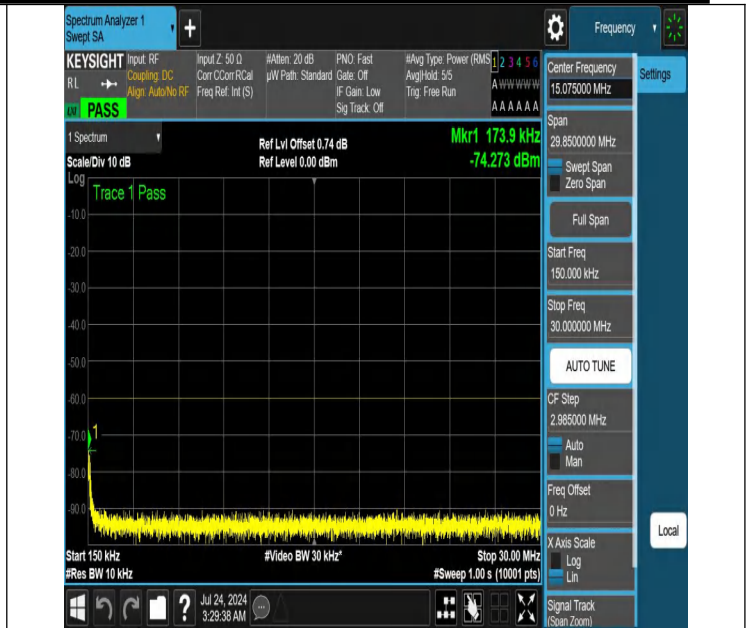
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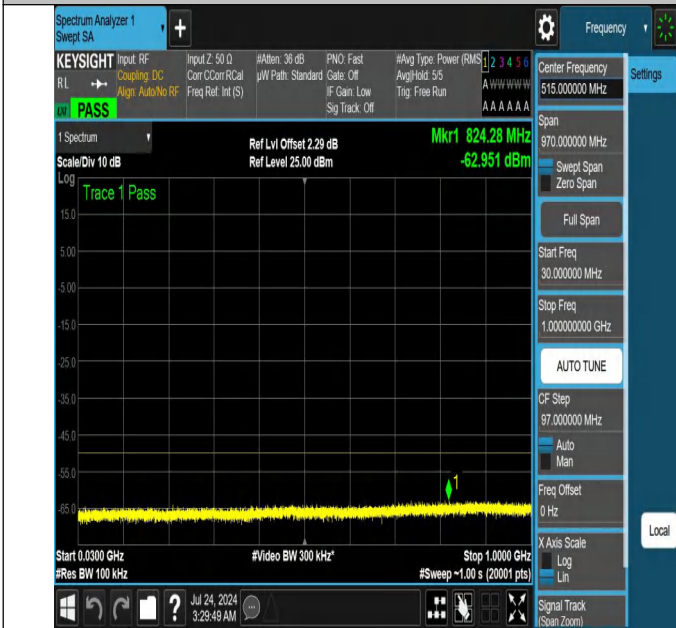
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NTNV-N48-PC3-30-40-M-DFT-PI2BPSK-Edge_1RB_Left-0.009-0.15-Ant1-PASS



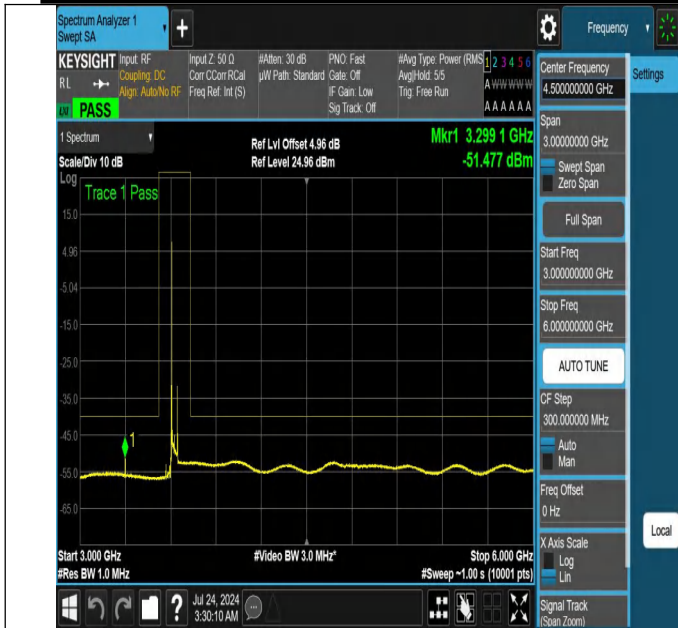
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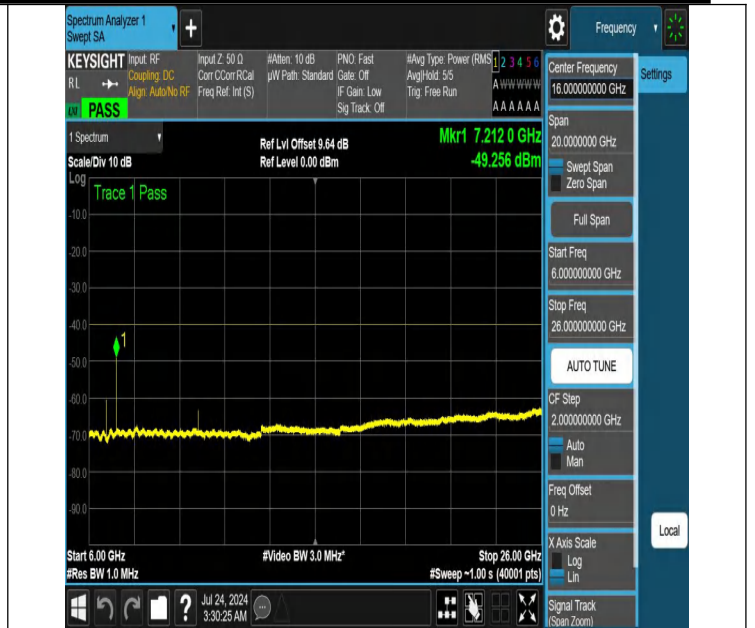
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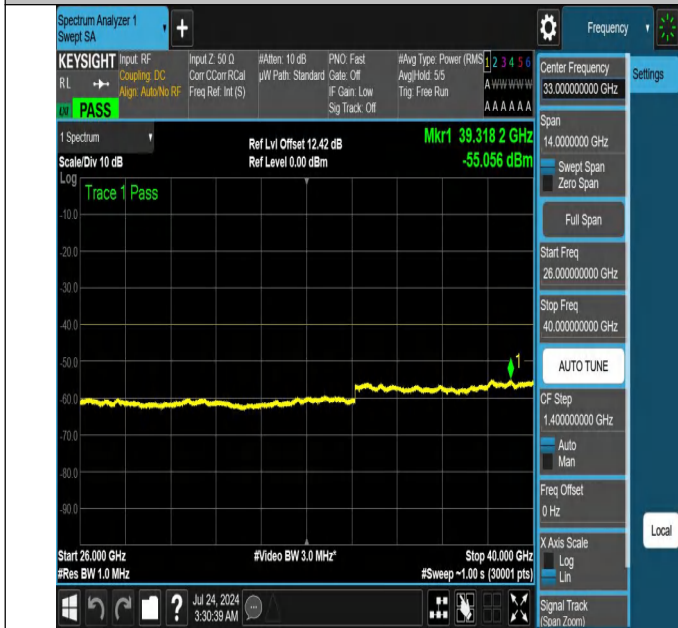
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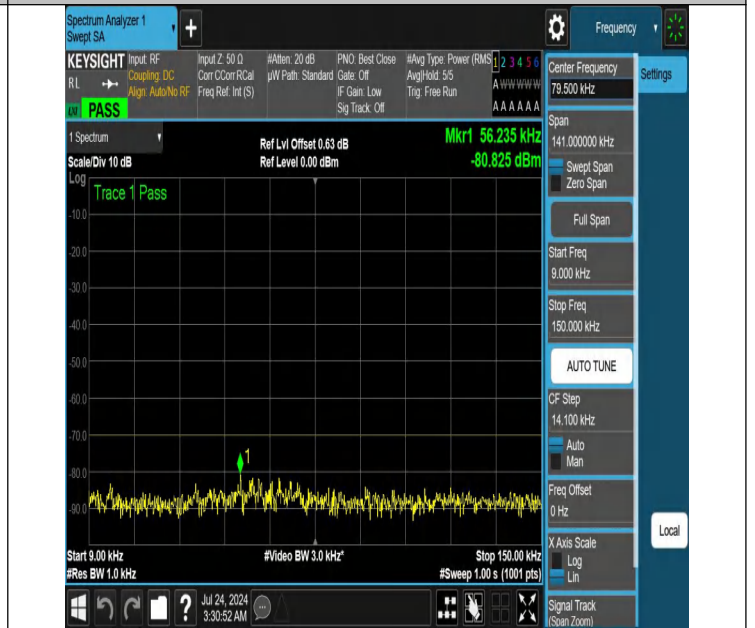
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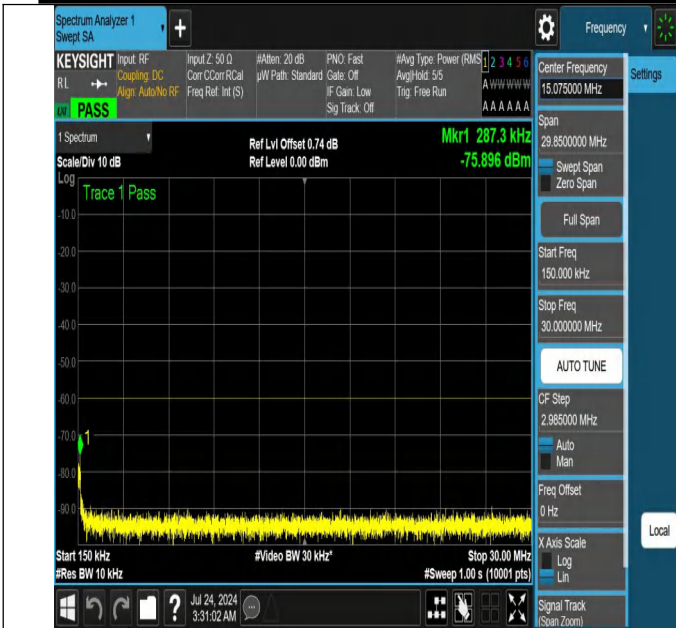
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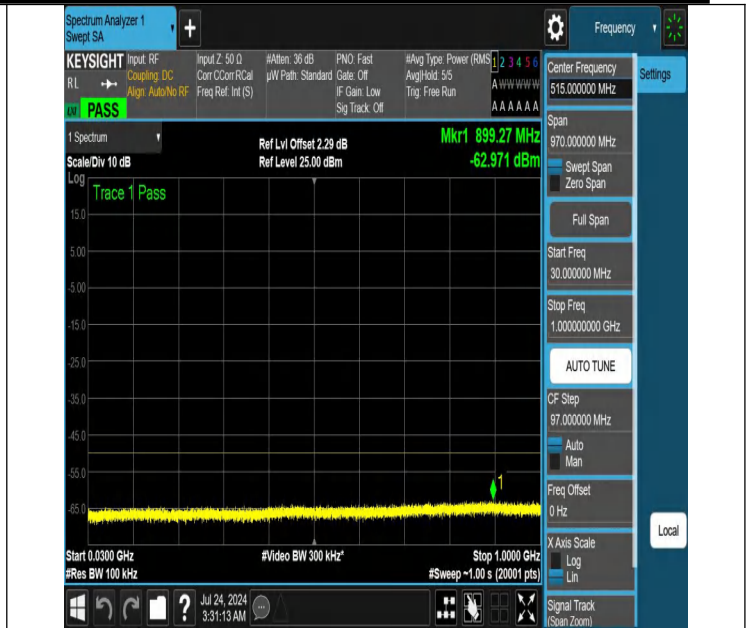
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NTNV-N48-PC3-30-40-M-DFT-PI2BPSK-Edge_1RB_Right-0.009-0.15-Ant1-PASS



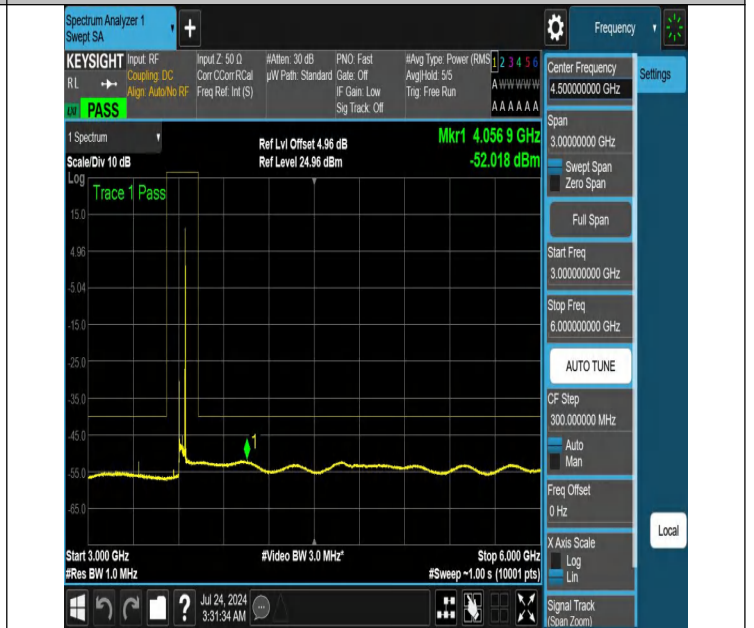
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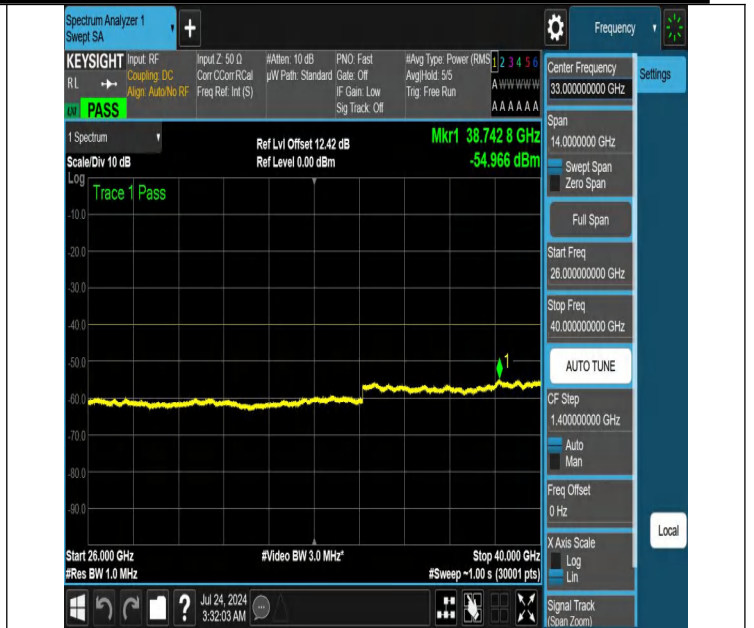
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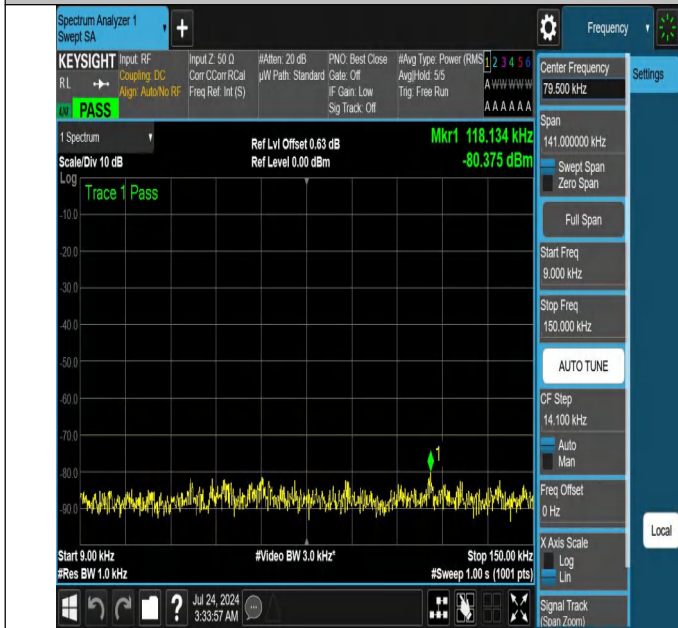
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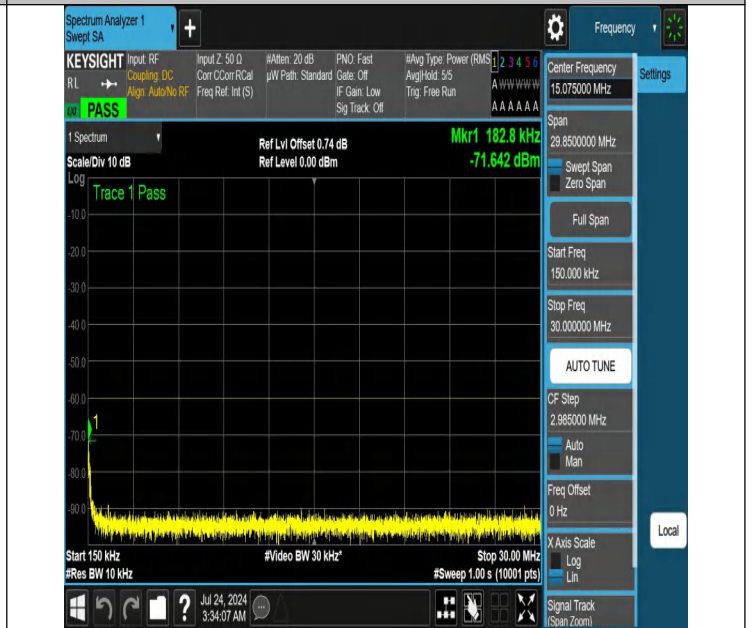
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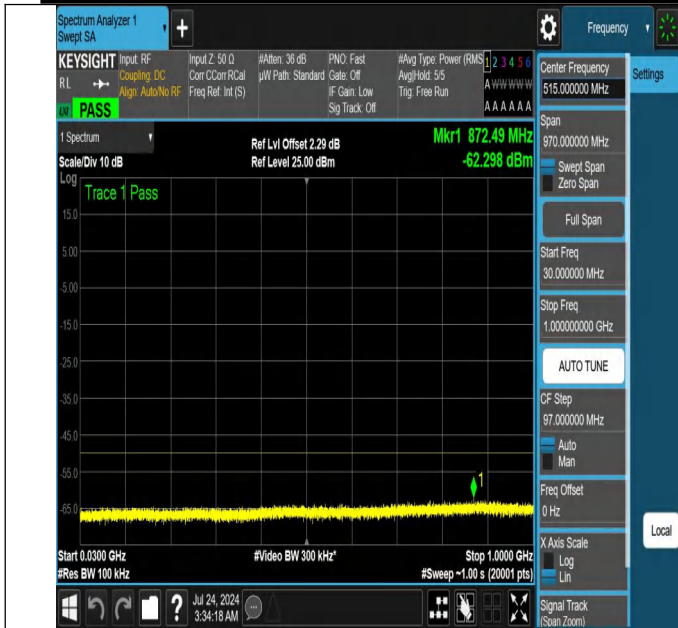
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NTNV-N48-PC3-30-40-M-DFT-QPSK-Edge_1RB_Left-0.009-0.15-Ant1-PASS



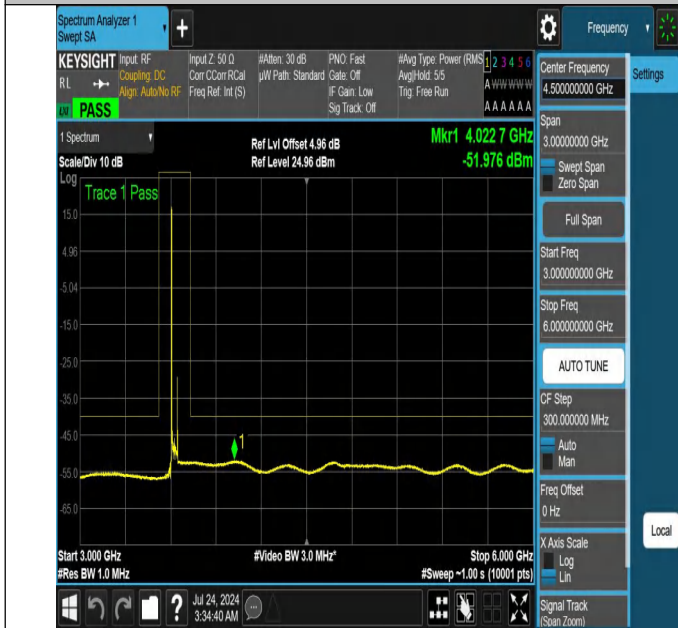
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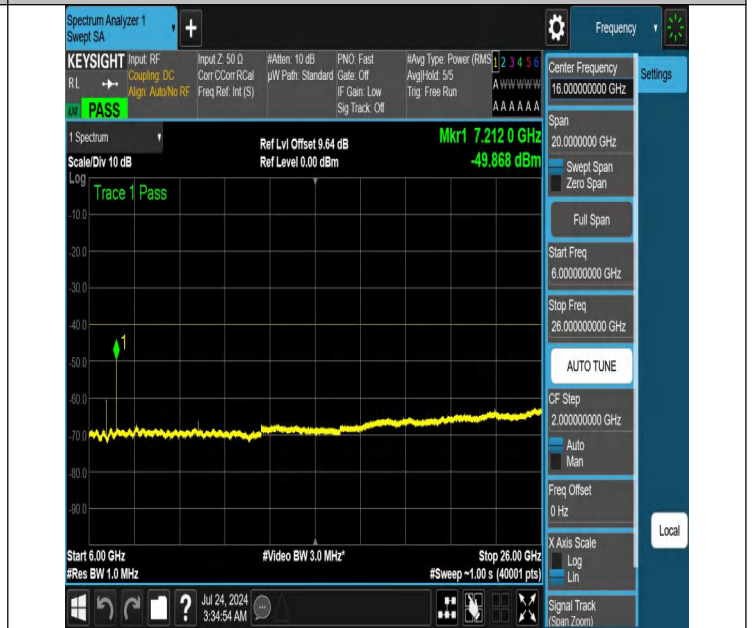
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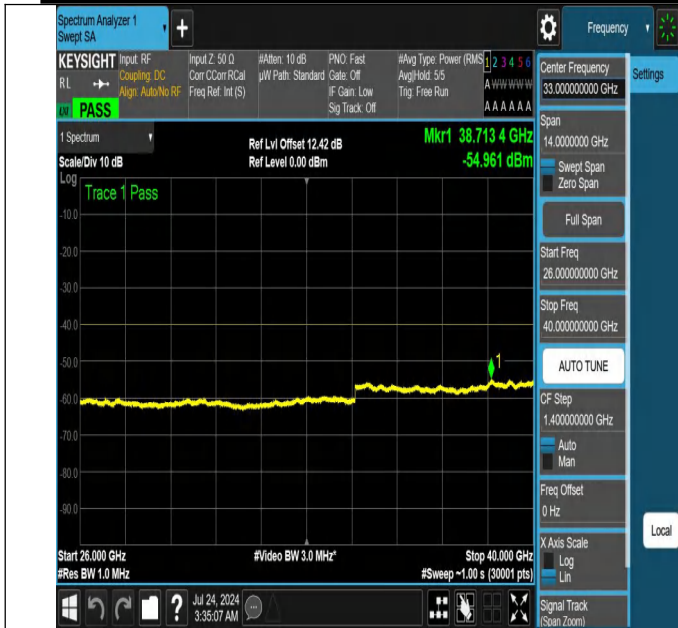
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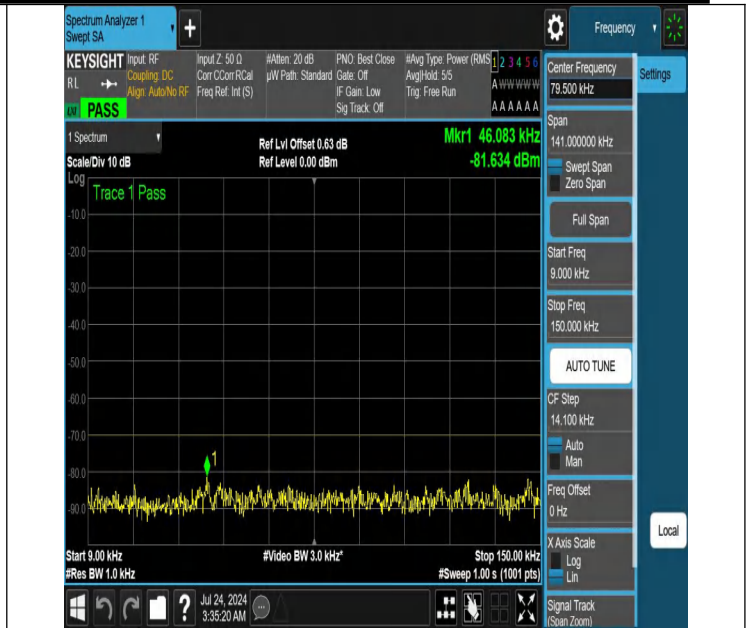
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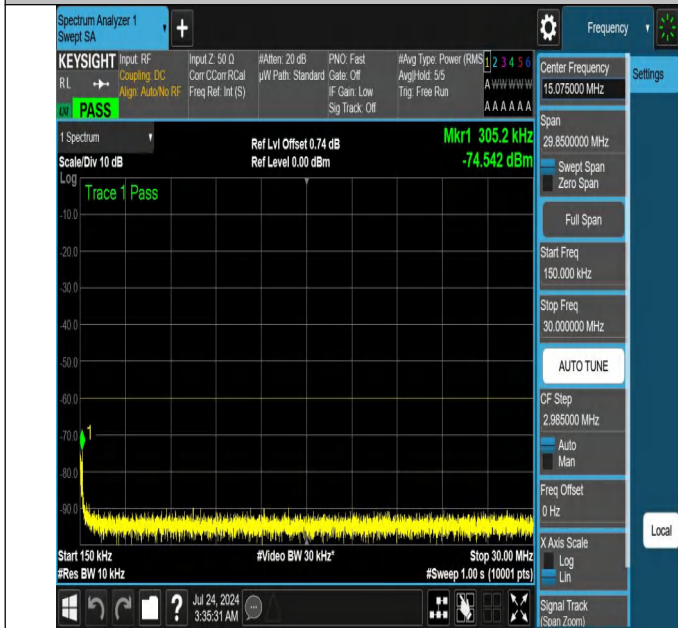
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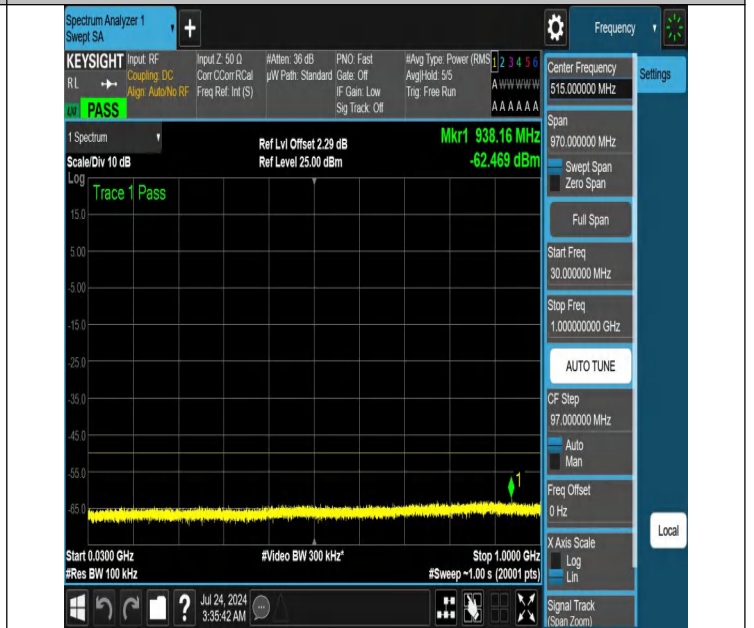
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NTNV-N48-PC3-30-40-M-DFT-QPSK-Edge_1RB_Right-0.009-0.150-Ant1-PASS



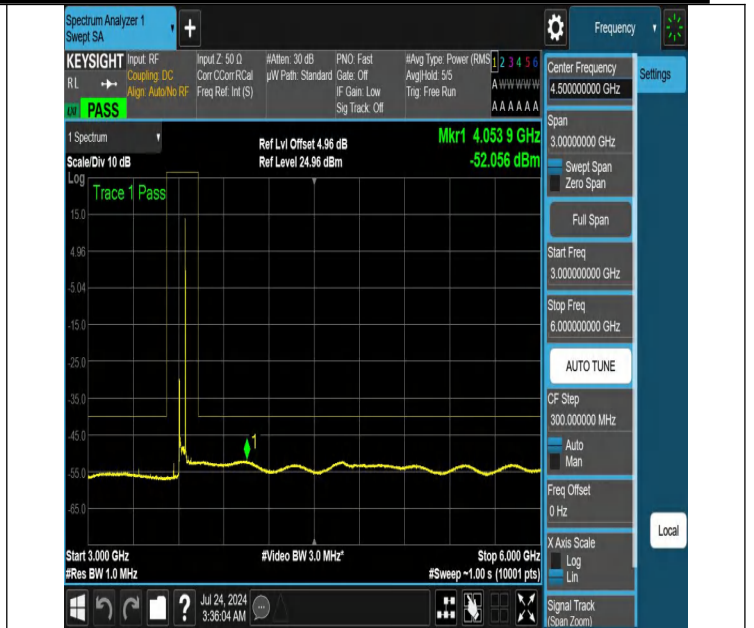
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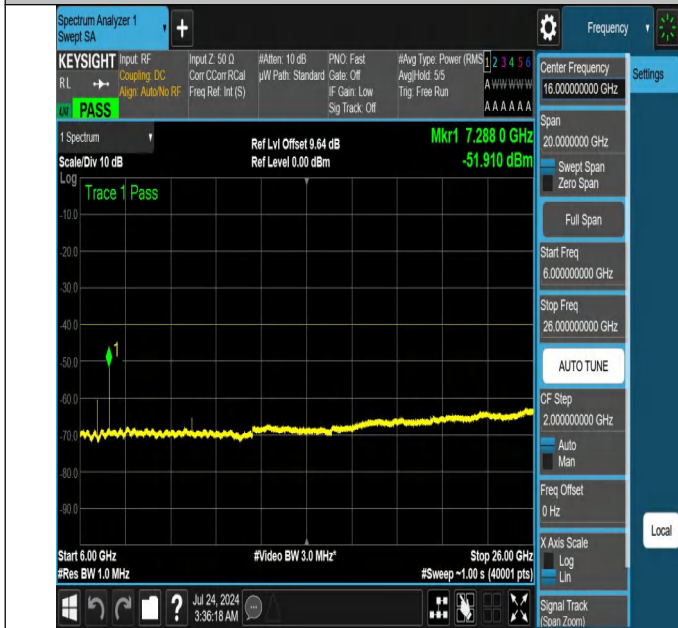
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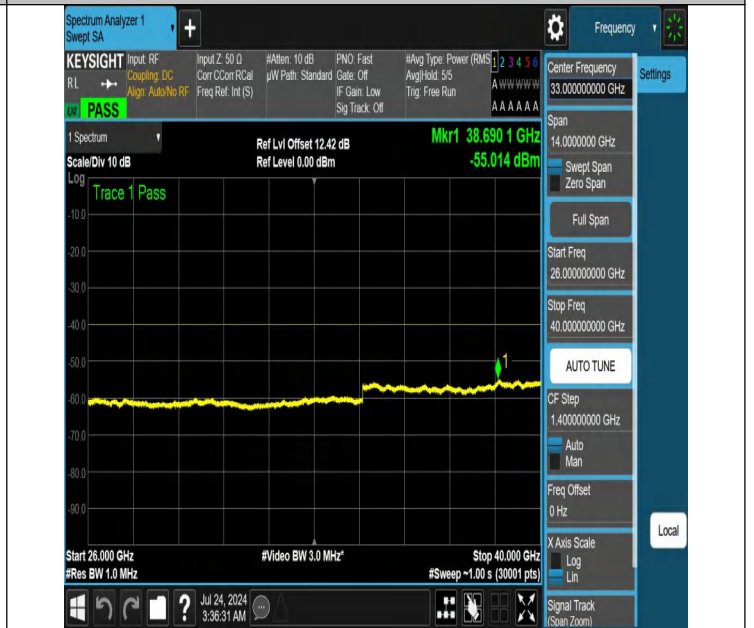
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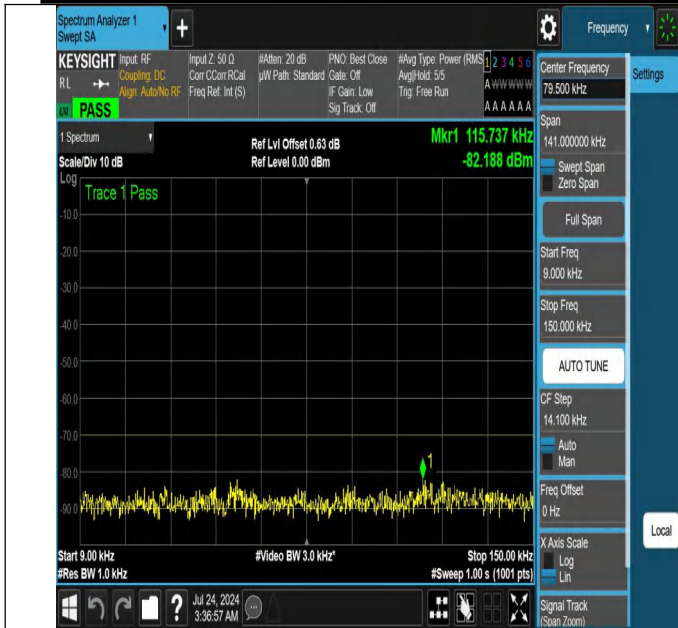
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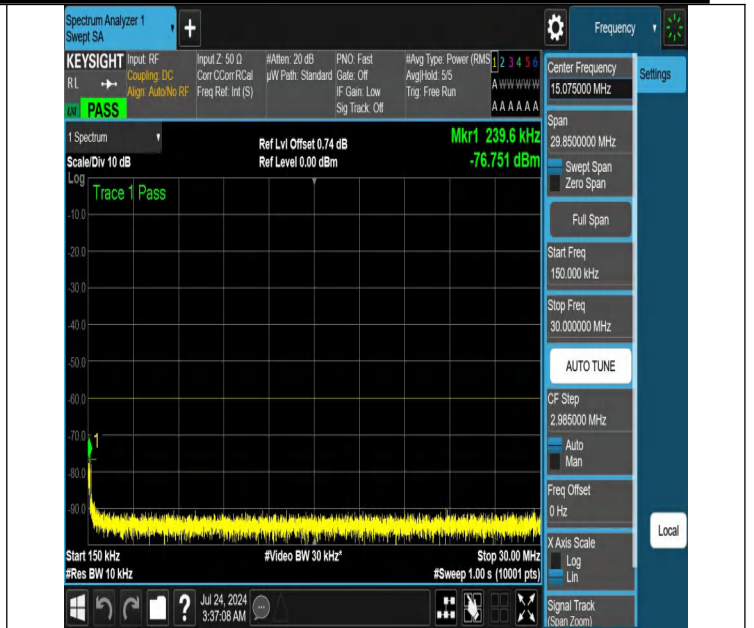
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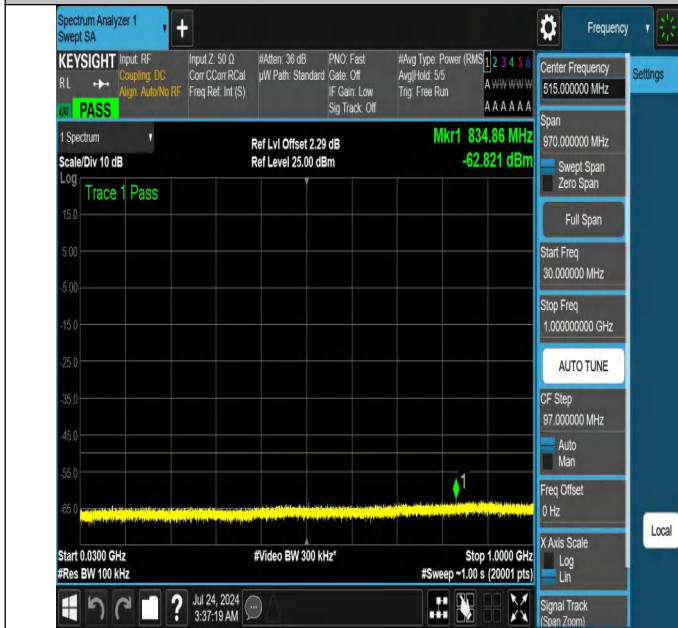
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NTNV-N48-PC3-30-40-H-DFT-PI2BPSK-Edge_1RB_Left-0.009-0.15-Ant1-PASS



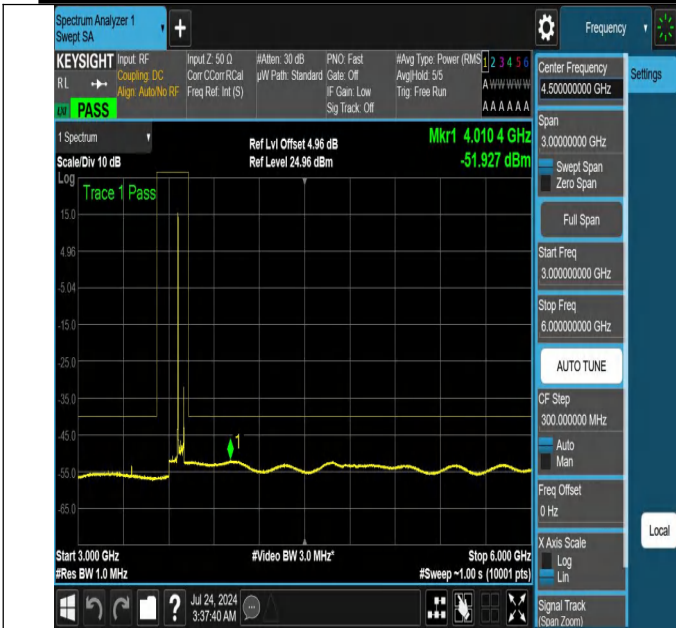
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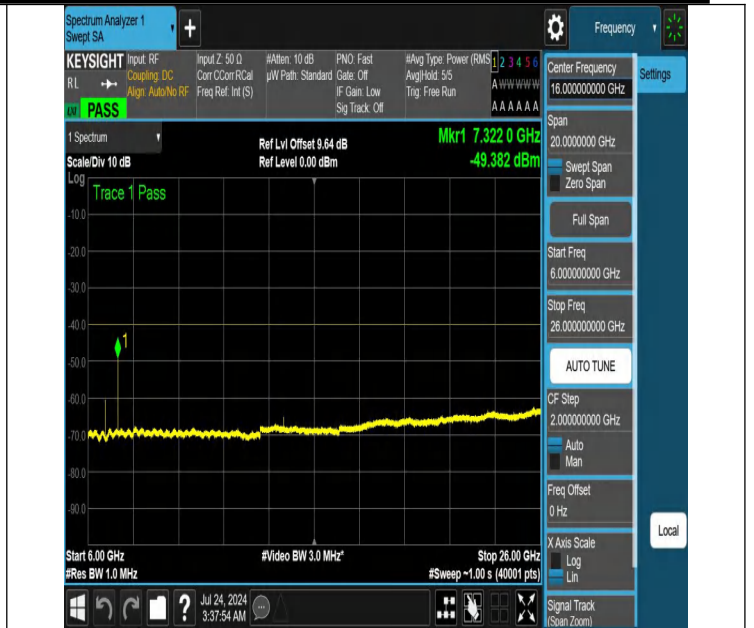
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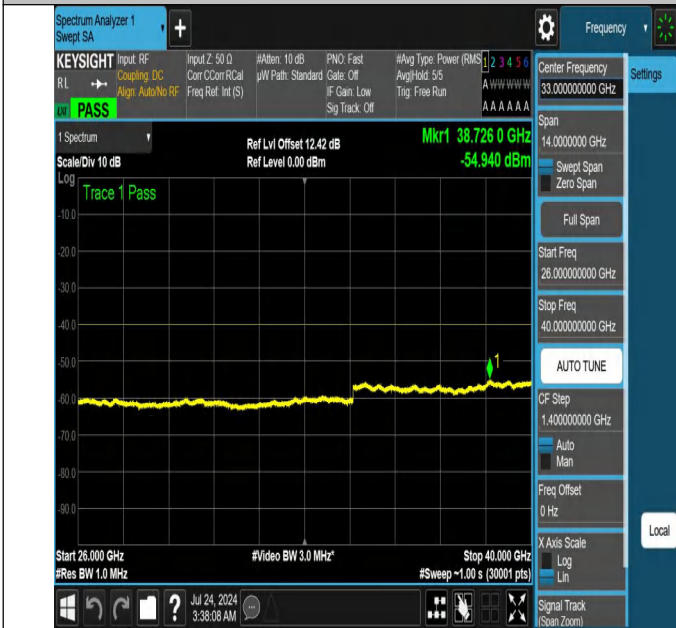
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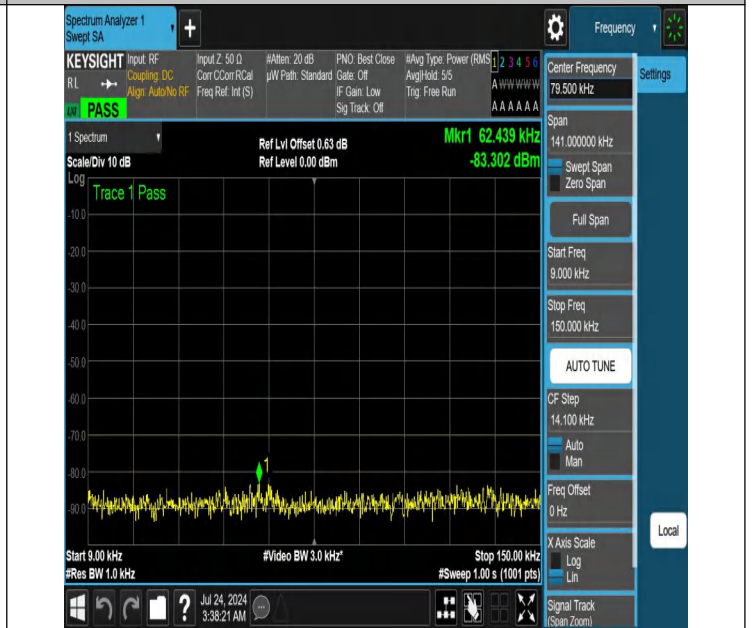
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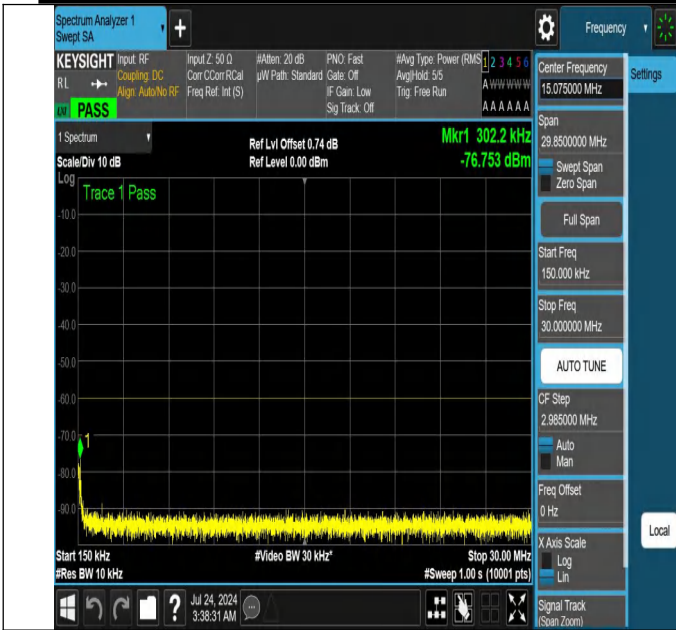
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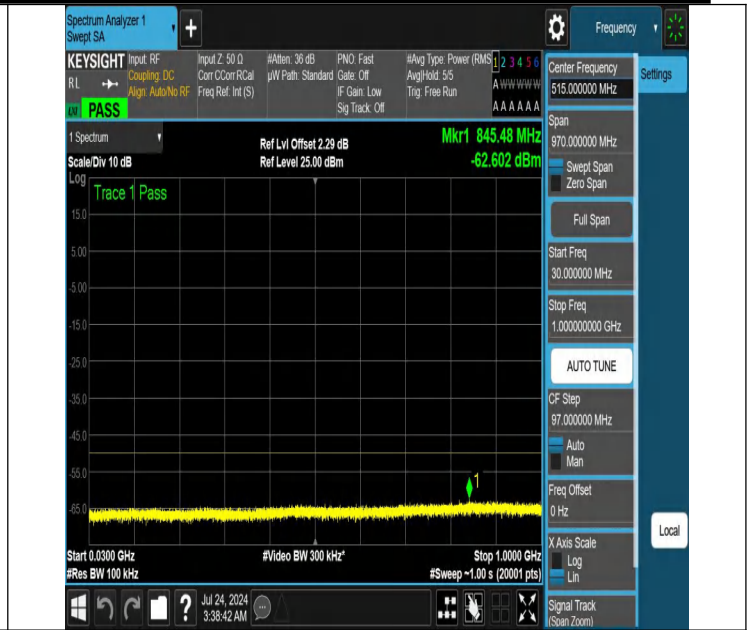
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NTNV-N48-PC3-30-40-H-DFT-PI2BPSK-Edge_1RB_Right-0.009-0.15-Ant1-PASS



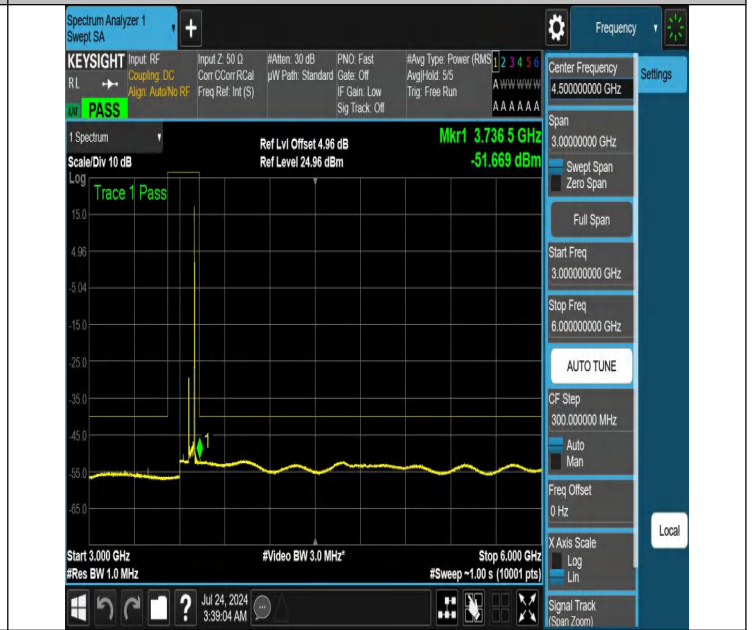
NTNV-N48-PC3-30-40-H-DFT-PI2BPSK-Edge_1RB_Right-0.15-3
0-Ant1-PASS



NTNV-N48-PC3-30-40-H-DFT-PI2BPSK-Edge_1RB_Right-30-100
0-Ant1-PASS



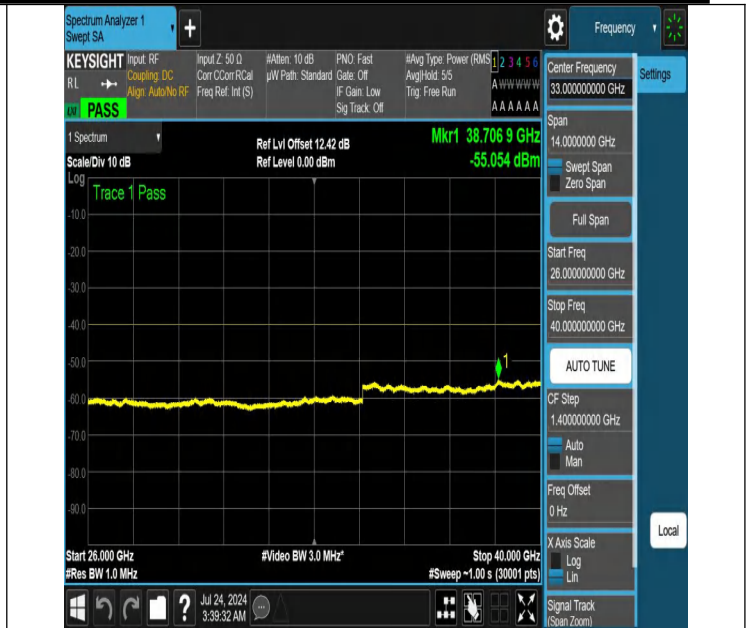
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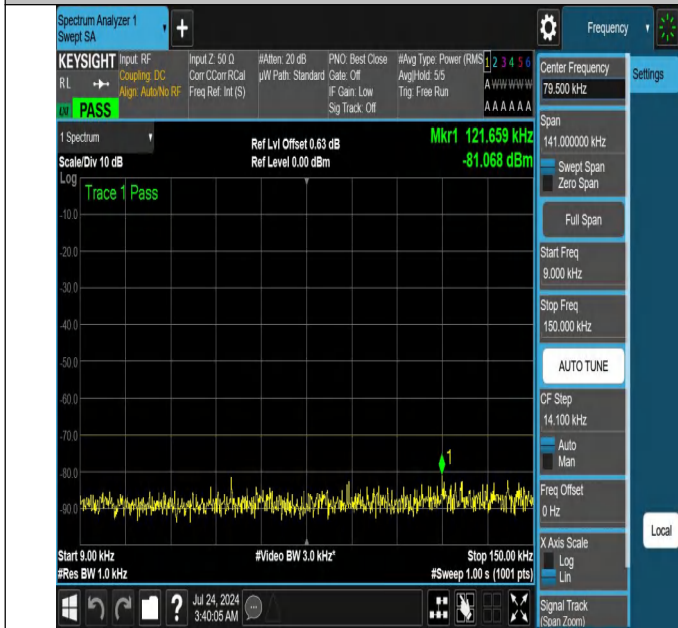
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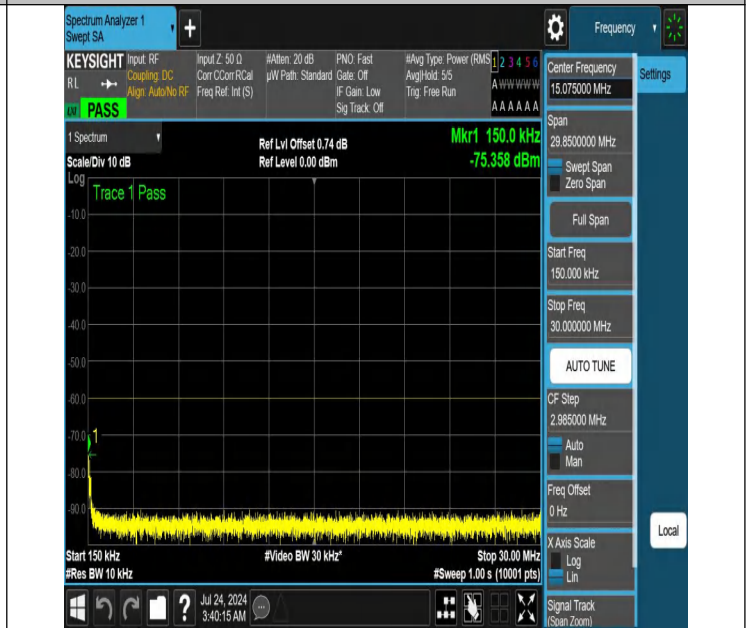
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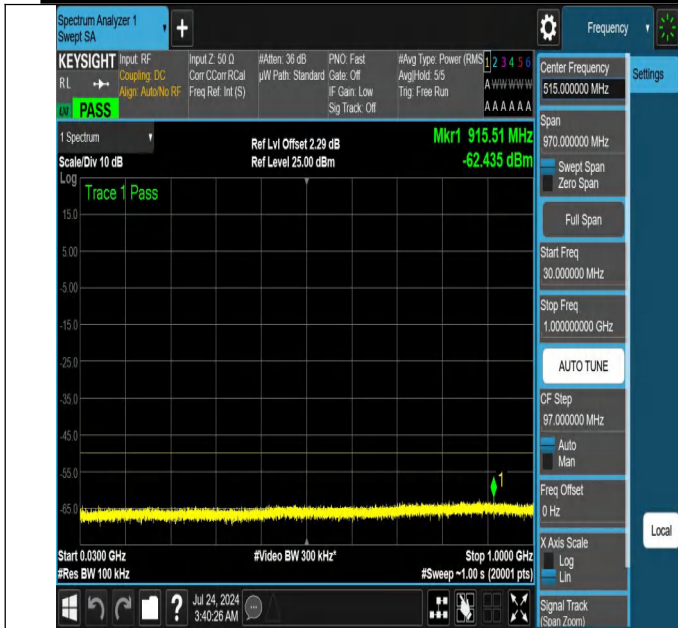
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NTNV-N48-PC3-30-40-H-DFT-QPSK-Edge_1RB_Left-0.009-0.15-Ant1-PASS



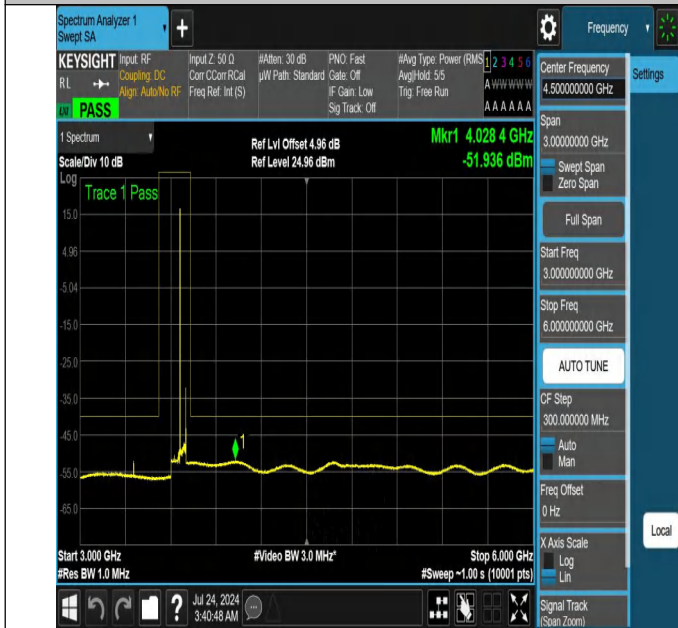
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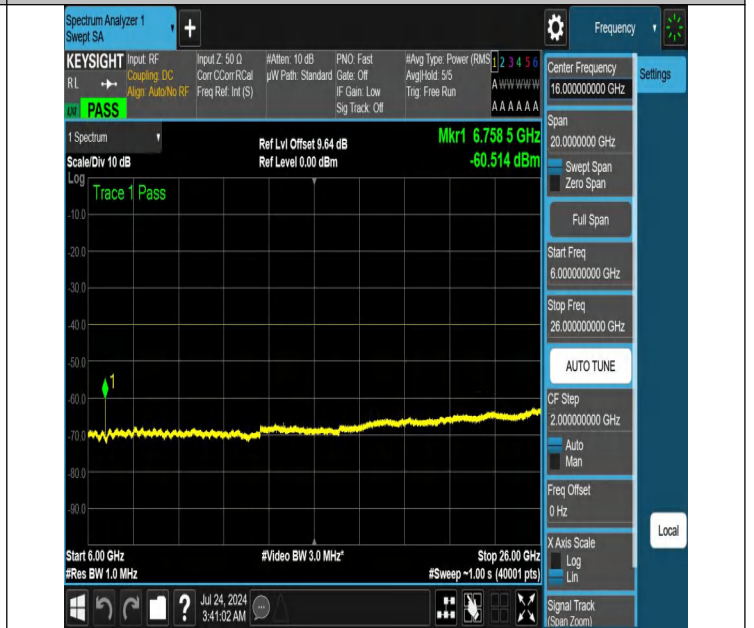
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nt1-PASS



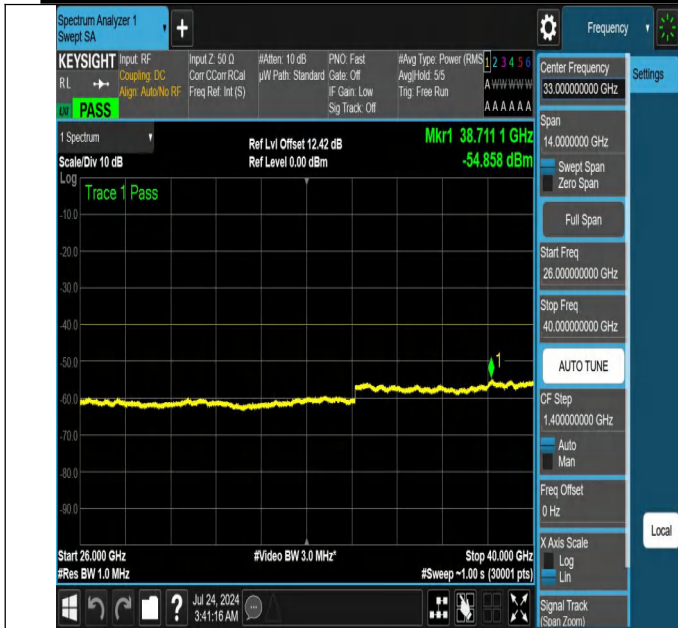
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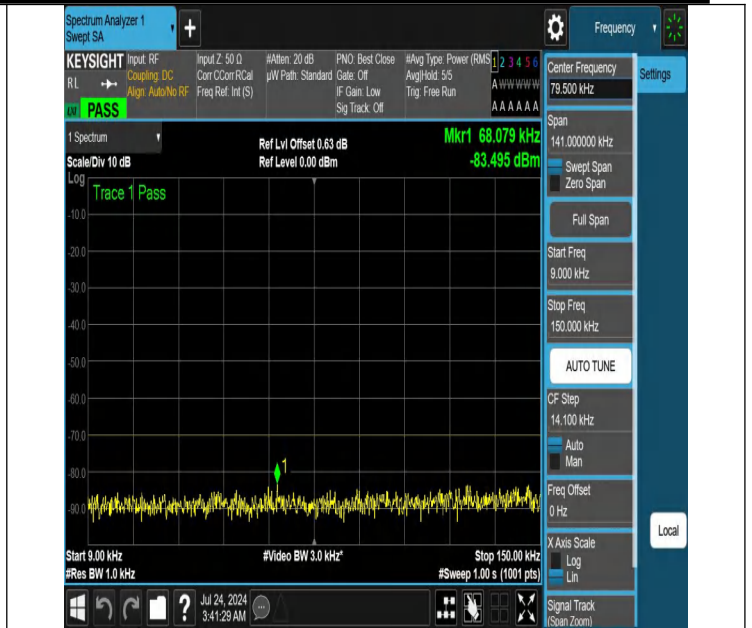
NTNV-N48-PC3-30-40-H-DFT-QPSK-Edge_1RB_Left-3000-6000
-Ant1-PASS



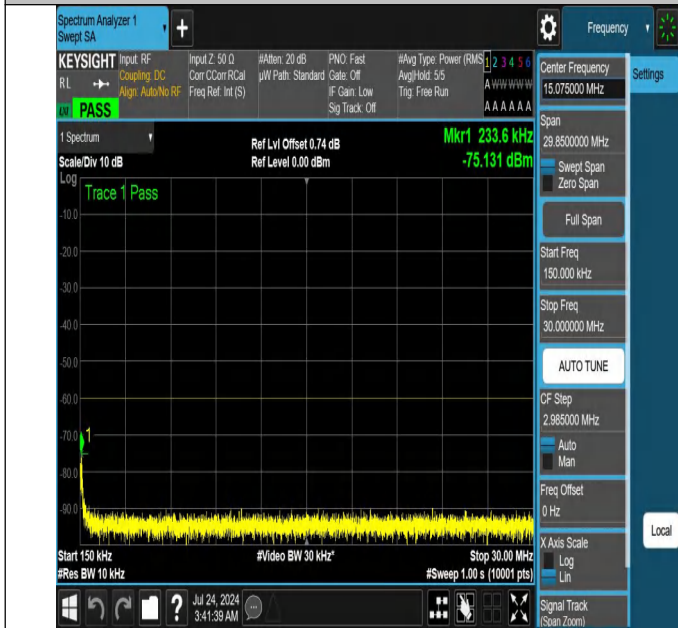
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-Ant1-PASS



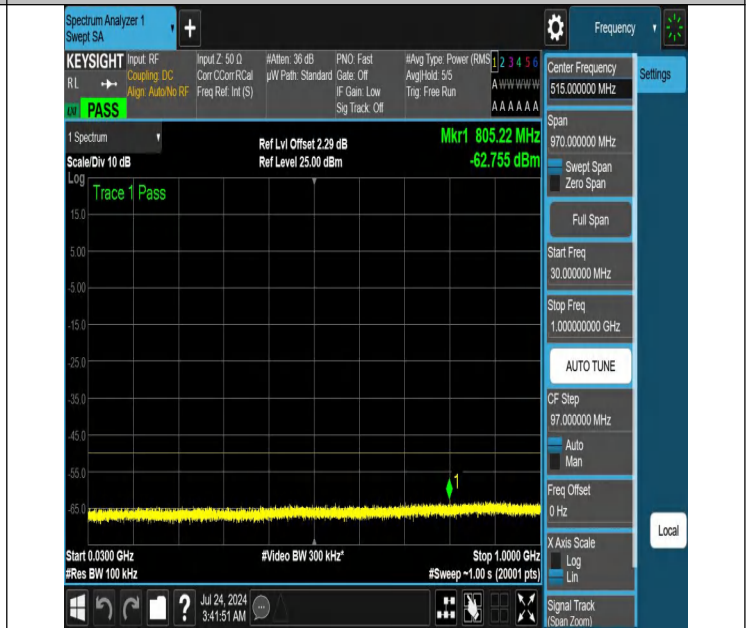
NTNV-N48-PC3-30-40-H-DFT-QPSK-Edge_1RB_Left-26000-40000-Ant1-PASS



NTNV-N48-PC3-30-40-H-DFT-QPSK-Edge_1RB_Right-0.009-0.150-Ant1-PASS



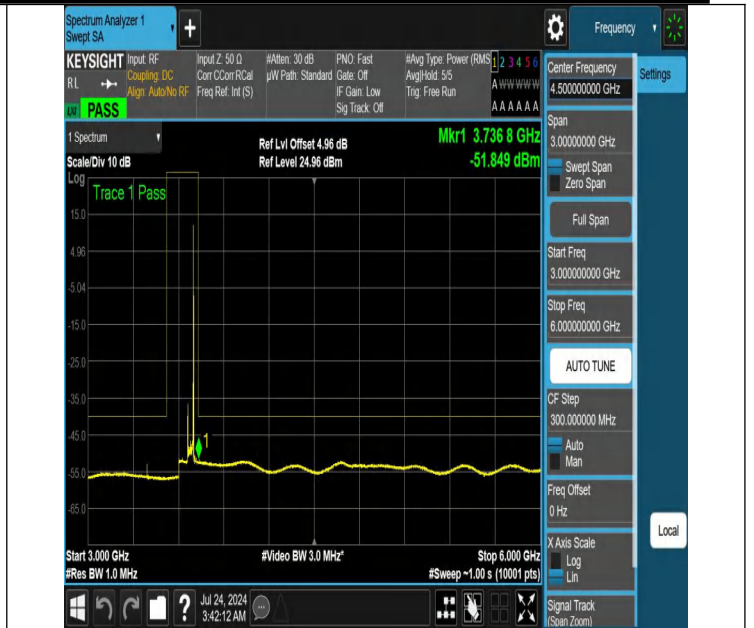
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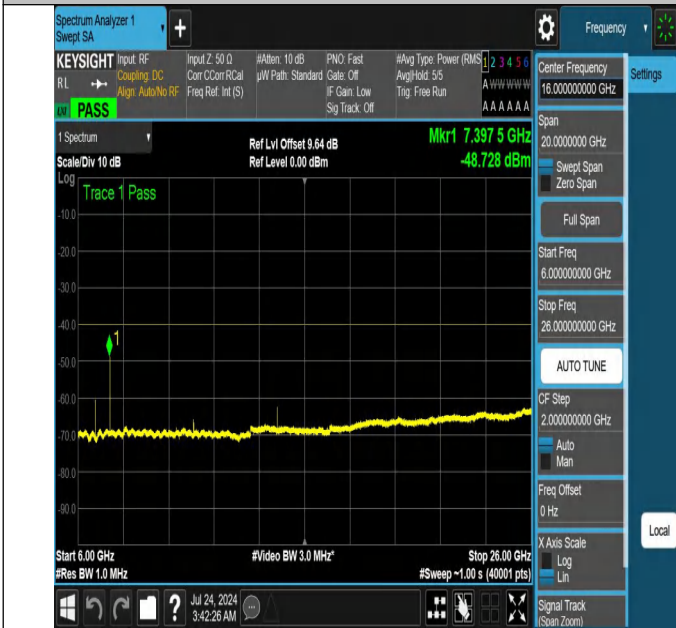
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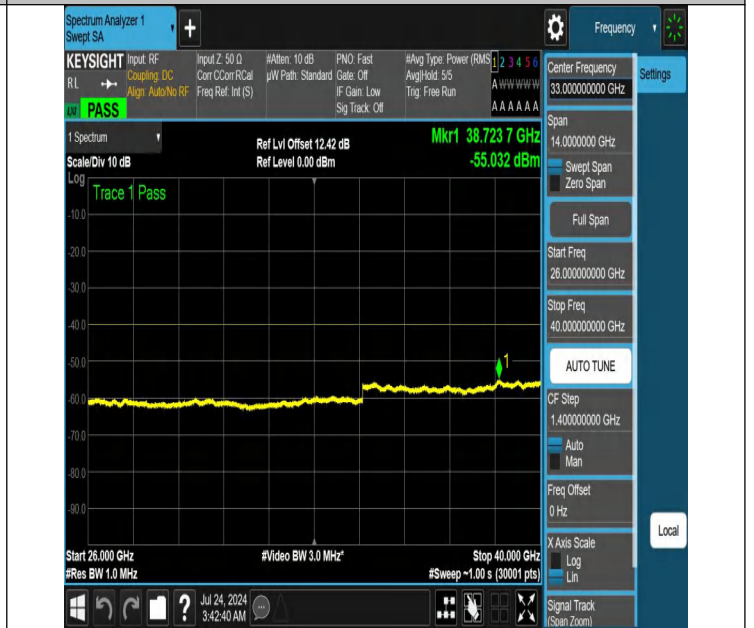
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 0-Ant1-PASS



NTNV-N48-PC3-30-40-H-DFT-QPSK-Edge_1RB_Right-3000-600
 0-Ant1-PASS



NTNV-N48-PC3-30-40-H-DFT-QPSK-Edge_1RB_Right-6000-260
 00-Ant1-PASS



NTNV-N48-PC3-30-40-H-DFT-QPSK-Edge_1RB_Right-26000-40
 000-Ant1-PASS

Frequency Stability for SA

Test Result

Frequency Error VS. Voltage											
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Limit (ppm)	Verdict
N48	30	40	DFT-QPSK	H	Outer_Full	VL	NT	-13.250000	-0.003601	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	VN	NT	-2.560000	-0.000696	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	VH	NT	-12.220000	-0.003321	±2.5	PASS

Frequency Error VS. Temperature											
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Limit (ppm)	Verdict
N48	30	40	DFT-QPSK	H	Outer_Full	NV	-30	-7.460000	-0.002027	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	NV	-20	-2.140000	-0.000582	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	NV	-10	-2.950000	-0.000802	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	NV	0	-5.460000	-0.001484	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	NV	10	-14.380000	-0.003908	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	NV	20	-0.200000	-0.000054	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	NV	30	-7.410000	-0.002014	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	NV	40	-9.440000	-0.002565	±2.5	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	NV	50	1.230000	0.000334	±2.5	PASS