

Effective (Isotropic) Radiated Power Output Data for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Limit (dBm)	Verdict
N48	30	10	DFT-PI2BPSK	L	Outer_Full	20.96	21.56	23	PASS
N48	30	10	DFT-PI2BPSK	L	Inner_Full	21.48	22.08	23	PASS
N48	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	20.95	21.55	23	PASS
N48	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	21.03	21.63	23	PASS
N48	30	10	DFT-QPSK	L	Outer_Full	20.52	21.12	23	PASS
N48	30	10	DFT-QPSK	L	Inner_Full	21.59	22.19	23	PASS
N48	30	10	DFT-QPSK	L	Edge_1RB_Left	20.56	21.16	23	PASS
N48	30	10	DFT-QPSK	L	Edge_1RB_Right	20.17	20.77	23	PASS
N48	30	10	DFT-16QAM	L	Outer_Full	19.42	20.02	23	PASS
N48	30	10	DFT-16QAM	L	Inner_Full	20.37	20.97	23	PASS
N48	30	10	DFT-16QAM	L	Edge_1RB_Left	19.60	20.20	23	PASS
N48	30	10	DFT-16QAM	L	Edge_1RB_Right	19.69	20.29	23	PASS
N48	30	10	DFT-64QAM	L	Outer_Full	18.95	19.55	23	PASS
N48	30	10	DFT-64QAM	L	Inner_Full	19.04	19.64	23	PASS
N48	30	10	DFT-64QAM	L	Edge_1RB_Left	19.21	19.81	23	PASS
N48	30	10	DFT-64QAM	L	Edge_1RB_Right	19.40	20.00	23	PASS
N48	30	10	DFT-256QAM	L	Outer_Full	16.86	17.46	23	PASS
N48	30	10	DFT-256QAM	L	Inner_Full	16.94	17.54	23	PASS
N48	30	10	DFT-256QAM	L	Edge_1RB_Left	17.03	17.63	23	PASS
N48	30	10	DFT-256QAM	L	Edge_1RB_Right	17.21	17.81	23	PASS
N48	30	10	DFT-PI2BPSK	M	Outer_Full	21.57	22.17	23	PASS
N48	30	10	DFT-PI2BPSK	M	Inner_Full	21.99	22.59	23	PASS
N48	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	21.53	22.13	23	PASS
N48	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	21.29	21.89	23	PASS
N48	30	10	DFT-QPSK	M	Outer_Full	20.91	21.51	23	PASS
N48	30	10	DFT-QPSK	M	Inner_Full	22.03	22.63	23	PASS
N48	30	10	DFT-QPSK	M	Edge_1RB_Left	20.92	21.52	23	PASS
N48	30	10	DFT-QPSK	M	Edge_1RB_Right	20.65	21.25	23	PASS
N48	30	10	DFT-16QAM	M	Outer_Full	20.07	20.67	23	PASS
N48	30	10	DFT-16QAM	M	Inner_Full	20.98	21.58	23	PASS
N48	30	10	DFT-16QAM	M	Edge_1RB_Left	19.88	20.48	23	PASS
N48	30	10	DFT-16QAM	M	Edge_1RB_Right	19.51	20.11	23	PASS
N48	30	10	DFT-64QAM	M	Outer_Full	19.41	20.01	23	PASS
N48	30	10	DFT-64QAM	M	Inner_Full	19.55	20.15	23	PASS
N48	30	10	DFT-64QAM	M	Edge_1RB_Left	19.47	20.07	23	PASS
N48	30	10	DFT-64QAM	M	Edge_1RB_Right	19.28	19.88	23	PASS

N48	30	10	DFT-256QAM	M	Outer_Full	17.50	18.10	23	PASS
N48	30	10	DFT-256QAM	M	Inner_Full	17.46	18.06	23	PASS
N48	30	10	DFT-256QAM	M	Edge_1RB_Left	17.68	18.28	23	PASS
N48	30	10	DFT-256QAM	M	Edge_1RB_Right	17.40	18.00	23	PASS
N48	30	10	DFT-PI2BPSK	H	Outer_Full	21.01	21.61	23	PASS
N48	30	10	DFT-PI2BPSK	H	Inner_Full	21.53	22.13	23	PASS
N48	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	21.18	21.78	23	PASS
N48	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	21.15	21.75	23	PASS
N48	30	10	DFT-QPSK	H	Outer_Full	20.46	21.06	23	PASS
N48	30	10	DFT-QPSK	H	Inner_Full	21.57	22.17	23	PASS
N48	30	10	DFT-QPSK	H	Edge_1RB_Left	20.52	21.12	23	PASS
N48	30	10	DFT-QPSK	H	Edge_1RB_Right	20.43	21.03	23	PASS
N48	30	10	DFT-16QAM	H	Outer_Full	19.66	20.26	23	PASS
N48	30	10	DFT-16QAM	H	Inner_Full	20.76	21.36	23	PASS
N48	30	10	DFT-16QAM	H	Edge_1RB_Left	19.38	19.98	23	PASS
N48	30	10	DFT-16QAM	H	Edge_1RB_Right	19.32	19.92	23	PASS
N48	30	10	DFT-64QAM	H	Outer_Full	19.07	19.67	23	PASS
N48	30	10	DFT-64QAM	H	Inner_Full	19.17	19.77	23	PASS
N48	30	10	DFT-64QAM	H	Edge_1RB_Left	19.29	19.89	23	PASS
N48	30	10	DFT-64QAM	H	Edge_1RB_Right	19.26	19.86	23	PASS
N48	30	10	DFT-256QAM	H	Outer_Full	17.09	17.69	23	PASS
N48	30	10	DFT-256QAM	H	Inner_Full	17.27	17.87	23	PASS
N48	30	10	DFT-256QAM	H	Edge_1RB_Left	16.93	17.53	23	PASS
N48	30	10	DFT-256QAM	H	Edge_1RB_Right	17.12	17.72	23	PASS
N48	30	15	DFT-PI2BPSK	L	Outer_Full	21.13	21.73	23	PASS
N48	30	15	DFT-PI2BPSK	L	Inner_Full	21.69	22.29	23	PASS
N48	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	20.88	21.48	23	PASS
N48	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	21.23	21.83	23	PASS
N48	30	15	DFT-QPSK	L	Outer_Full	20.65	21.25	23	PASS
N48	30	15	DFT-QPSK	L	Inner_Full	21.72	22.32	23	PASS
N48	30	15	DFT-QPSK	L	Edge_1RB_Left	20.92	21.52	23	PASS
N48	30	15	DFT-QPSK	L	Edge_1RB_Right	21.13	21.73	23	PASS
N48	30	15	DFT-16QAM	L	Outer_Full	19.72	20.32	23	PASS
N48	30	15	DFT-16QAM	L	Inner_Full	20.73	21.33	23	PASS
N48	30	15	DFT-16QAM	L	Edge_1RB_Left	19.69	20.29	23	PASS
N48	30	15	DFT-16QAM	L	Edge_1RB_Right	19.82	20.42	23	PASS
N48	30	15	DFT-64QAM	L	Outer_Full	19.06	19.66	23	PASS
N48	30	15	DFT-64QAM	L	Inner_Full	19.04	19.64	23	PASS
N48	30	15	DFT-64QAM	L	Edge_1RB_Left	19.10	19.70	23	PASS
N48	30	15	DFT-64QAM	L	Edge_1RB_Right	19.20	19.80	23	PASS
N48	30	15	DFT-256QAM	L	Outer_Full	17.29	17.89	23	PASS
N48	30	15	DFT-256QAM	L	Inner_Full	17.03	17.63	23	PASS

N48	30	15	DFT-256QAM	L	Edge_1RB_Left	17.39	17.99	23	PASS
N48	30	15	DFT-256QAM	L	Edge_1RB_Right	17.51	18.11	23	PASS
N48	30	15	DFT-PI2BPSK	M	Outer_Full	21.63	22.23	23	PASS
N48	30	15	DFT-PI2BPSK	M	Inner_Full	22.12	22.72	23	PASS
N48	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	21.44	22.04	23	PASS
N48	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	21.19	21.79	23	PASS
N48	30	15	DFT-QPSK	M	Outer_Full	20.96	21.56	23	PASS
N48	30	15	DFT-QPSK	M	Inner_Full	22.13	22.73	23	PASS
N48	30	15	DFT-QPSK	M	Edge_1RB_Left	21.01	21.61	23	PASS
N48	30	15	DFT-QPSK	M	Edge_1RB_Right	20.80	21.40	23	PASS
N48	30	15	DFT-16QAM	M	Outer_Full	20.09	20.69	23	PASS
N48	30	15	DFT-16QAM	M	Inner_Full	21.13	21.73	23	PASS
N48	30	15	DFT-16QAM	M	Edge_1RB_Left	19.82	20.42	23	PASS
N48	30	15	DFT-16QAM	M	Edge_1RB_Right	19.49	20.09	23	PASS
N48	30	15	DFT-64QAM	M	Outer_Full	19.53	20.13	23	PASS
N48	30	15	DFT-64QAM	M	Inner_Full	19.52	20.12	23	PASS
N48	30	15	DFT-64QAM	M	Edge_1RB_Left	18.99	19.59	23	PASS
N48	30	15	DFT-64QAM	M	Edge_1RB_Right	18.98	19.58	23	PASS
N48	30	15	DFT-256QAM	M	Outer_Full	17.48	18.08	23	PASS
N48	30	15	DFT-256QAM	M	Inner_Full	17.51	18.11	23	PASS
N48	30	15	DFT-256QAM	M	Edge_1RB_Left	17.98	18.58	23	PASS
N48	30	15	DFT-256QAM	M	Edge_1RB_Right	17.85	18.45	23	PASS
N48	30	15	DFT-PI2BPSK	H	Outer_Full	20.98	21.58	23	PASS
N48	30	15	DFT-PI2BPSK	H	Inner_Full	21.57	22.17	23	PASS
N48	30	15	DFT-PI2BPSK	H	Edge_1RB_Left	20.94	21.54	23	PASS
N48	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	20.87	21.47	23	PASS
N48	30	15	DFT-QPSK	H	Outer_Full	20.53	21.13	23	PASS
N48	30	15	DFT-QPSK	H	Inner_Full	21.45	22.05	23	PASS
N48	30	15	DFT-QPSK	H	Edge_1RB_Left	20.49	21.09	23	PASS
N48	30	15	DFT-QPSK	H	Edge_1RB_Right	20.34	20.94	23	PASS
N48	30	15	DFT-16QAM	H	Outer_Full	19.54	20.14	23	PASS
N48	30	15	DFT-16QAM	H	Inner_Full	20.61	21.21	23	PASS
N48	30	15	DFT-16QAM	H	Edge_1RB_Left	18.67	19.27	23	PASS
N48	30	15	DFT-16QAM	H	Edge_1RB_Right	18.65	19.25	23	PASS
N48	30	15	DFT-64QAM	H	Outer_Full	19.00	19.60	23	PASS
N48	30	15	DFT-64QAM	H	Inner_Full	19.07	19.67	23	PASS
N48	30	15	DFT-64QAM	H	Edge_1RB_Left	18.97	19.57	23	PASS
N48	30	15	DFT-64QAM	H	Edge_1RB_Right	19.02	19.62	23	PASS
N48	30	15	DFT-256QAM	H	Outer_Full	17.03	17.63	23	PASS
N48	30	15	DFT-256QAM	H	Inner_Full	16.94	17.54	23	PASS
N48	30	15	DFT-256QAM	H	Edge_1RB_Left	16.81	17.41	23	PASS
N48	30	15	DFT-256QAM	H	Edge_1RB_Right	16.79	17.39	23	PASS

N48	30	20	DFT-PI2BPSK	L	Outer_Full	20.96	21.56	23	PASS
N48	30	20	DFT-PI2BPSK	L	Inner_Full	21.55	22.15	23	PASS
N48	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	20.90	21.50	23	PASS
N48	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	21.09	21.69	23	PASS
N48	30	20	DFT-QPSK	L	Outer_Full	20.34	20.94	23	PASS
N48	30	20	DFT-QPSK	L	Inner_Full	21.47	22.07	23	PASS
N48	30	20	DFT-QPSK	L	Edge_1RB_Left	20.35	20.95	23	PASS
N48	30	20	DFT-QPSK	L	Edge_1RB_Right	20.63	21.23	23	PASS
N48	30	20	DFT-16QAM	L	Outer_Full	19.42	20.02	23	PASS
N48	30	20	DFT-16QAM	L	Inner_Full	20.45	21.05	23	PASS
N48	30	20	DFT-16QAM	L	Edge_1RB_Left	19.35	19.95	23	PASS
N48	30	20	DFT-16QAM	L	Edge_1RB_Right	19.66	20.26	23	PASS
N48	30	20	DFT-64QAM	L	Outer_Full	18.93	19.53	23	PASS
N48	30	20	DFT-64QAM	L	Inner_Full	18.98	19.58	23	PASS
N48	30	20	DFT-64QAM	L	Edge_1RB_Left	18.87	19.47	23	PASS
N48	30	20	DFT-64QAM	L	Edge_1RB_Right	18.94	19.54	23	PASS
N48	30	20	DFT-256QAM	L	Outer_Full	17.03	17.63	23	PASS
N48	30	20	DFT-256QAM	L	Inner_Full	16.92	17.52	23	PASS
N48	30	20	DFT-256QAM	L	Edge_1RB_Left	16.96	17.56	23	PASS
N48	30	20	DFT-256QAM	L	Edge_1RB_Right	17.17	17.77	23	PASS
N48	30	20	DFT-PI2BPSK	M	Outer_Full	21.33	21.93	23	PASS
N48	30	20	DFT-PI2BPSK	M	Inner_Full	22.00	22.60	23	PASS
N48	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	21.47	22.07	23	PASS
N48	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	21.20	21.80	23	PASS
N48	30	20	DFT-QPSK	M	Outer_Full	20.87	21.47	23	PASS
N48	30	20	DFT-QPSK	M	Inner_Full	21.95	22.55	23	PASS
N48	30	20	DFT-QPSK	M	Edge_1RB_Left	21.04	21.64	23	PASS
N48	30	20	DFT-QPSK	M	Edge_1RB_Right	20.67	21.27	23	PASS
N48	30	20	DFT-16QAM	M	Outer_Full	19.98	20.58	23	PASS
N48	30	20	DFT-16QAM	M	Inner_Full	20.89	21.49	23	PASS
N48	30	20	DFT-16QAM	M	Edge_1RB_Left	19.81	20.41	23	PASS
N48	30	20	DFT-16QAM	M	Edge_1RB_Right	19.55	20.15	23	PASS
N48	30	20	DFT-64QAM	M	Outer_Full	19.35	19.95	23	PASS
N48	30	20	DFT-64QAM	M	Inner_Full	19.53	20.13	23	PASS
N48	30	20	DFT-64QAM	M	Edge_1RB_Left	19.37	19.97	23	PASS
N48	30	20	DFT-64QAM	M	Edge_1RB_Right	19.09	19.69	23	PASS
N48	30	20	DFT-256QAM	M	Outer_Full	17.34	17.94	23	PASS
N48	30	20	DFT-256QAM	M	Inner_Full	17.53	18.13	23	PASS
N48	30	20	DFT-256QAM	M	Edge_1RB_Left	17.46	18.06	23	PASS
N48	30	20	DFT-256QAM	M	Edge_1RB_Right	17.29	17.89	23	PASS
N48	30	20	DFT-PI2BPSK	H	Outer_Full	20.91	21.51	23	PASS
N48	30	20	DFT-PI2BPSK	H	Inner_Full	21.38	21.98	23	PASS

N48	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	20.81	21.41	23	PASS
N48	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	20.71	21.31	23	PASS
N48	30	20	DFT-QPSK	H	Outer_Full	20.34	20.94	23	PASS
N48	30	20	DFT-QPSK	H	Inner_Full	21.38	21.98	23	PASS
N48	30	20	DFT-QPSK	H	Edge_1RB_Left	19.93	20.53	23	PASS
N48	30	20	DFT-QPSK	H	Edge_1RB_Right	20.06	20.66	23	PASS
N48	30	20	DFT-16QAM	H	Outer_Full	19.35	19.95	23	PASS
N48	30	20	DFT-16QAM	H	Inner_Full	20.37	20.97	23	PASS
N48	30	20	DFT-16QAM	H	Edge_1RB_Left	19.04	19.64	23	PASS
N48	30	20	DFT-16QAM	H	Edge_1RB_Right	19.22	19.82	23	PASS
N48	30	20	DFT-64QAM	H	Outer_Full	18.76	19.36	23	PASS
N48	30	20	DFT-64QAM	H	Inner_Full	18.77	19.37	23	PASS
N48	30	20	DFT-64QAM	H	Edge_1RB_Left	18.85	19.45	23	PASS
N48	30	20	DFT-64QAM	H	Edge_1RB_Right	18.88	19.48	23	PASS
N48	30	20	DFT-256QAM	H	Outer_Full	16.82	17.42	23	PASS
N48	30	20	DFT-256QAM	H	Inner_Full	16.78	17.38	23	PASS
N48	30	20	DFT-256QAM	H	Edge_1RB_Left	17.02	17.62	23	PASS
N48	30	20	DFT-256QAM	H	Edge_1RB_Right	17.08	17.68	23	PASS
N48	30	30	DFT-PI2BPSK	L	Outer_Full	20.97	21.57	23	PASS
N48	30	30	DFT-PI2BPSK	L	Inner_Full	21.49	22.09	23	PASS
N48	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	21.01	21.61	23	PASS
N48	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	21.17	21.77	23	PASS
N48	30	30	DFT-QPSK	L	Outer_Full	20.48	21.08	23	PASS
N48	30	30	DFT-QPSK	L	Inner_Full	21.42	22.02	23	PASS
N48	30	30	DFT-QPSK	L	Edge_1RB_Left	20.19	20.79	23	PASS
N48	30	30	DFT-QPSK	L	Edge_1RB_Right	20.36	20.96	23	PASS
N48	30	30	DFT-16QAM	L	Outer_Full	19.38	19.98	23	PASS
N48	30	30	DFT-16QAM	L	Inner_Full	20.42	21.02	23	PASS
N48	30	30	DFT-16QAM	L	Edge_1RB_Left	19.60	20.20	23	PASS
N48	30	30	DFT-16QAM	L	Edge_1RB_Right	19.80	20.40	23	PASS
N48	30	30	DFT-64QAM	L	Outer_Full	19.01	19.61	23	PASS
N48	30	30	DFT-64QAM	L	Inner_Full	18.86	19.46	23	PASS
N48	30	30	DFT-64QAM	L	Edge_1RB_Left	19.29	19.89	23	PASS
N48	30	30	DFT-64QAM	L	Edge_1RB_Right	19.37	19.97	23	PASS
N48	30	30	DFT-256QAM	L	Outer_Full	17.08	17.68	23	PASS
N48	30	30	DFT-256QAM	L	Inner_Full	16.94	17.54	23	PASS
N48	30	30	DFT-256QAM	L	Edge_1RB_Left	16.98	17.58	23	PASS
N48	30	30	DFT-256QAM	L	Edge_1RB_Right	16.98	17.58	23	PASS
N48	30	30	DFT-PI2BPSK	M	Outer_Full	21.33	21.93	23	PASS
N48	30	30	DFT-PI2BPSK	M	Inner_Full	21.97	22.57	23	PASS
N48	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	21.41	22.01	23	PASS
N48	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	21.15	21.75	23	PASS

N48	30	30	DFT-QPSK	M	Outer_Full	20.81	21.41	23	PASS
N48	30	30	DFT-QPSK	M	Inner_Full	21.89	22.49	23	PASS
N48	30	30	DFT-QPSK	M	Edge_1RB_Left	21.08	21.68	23	PASS
N48	30	30	DFT-QPSK	M	Edge_1RB_Right	20.78	21.38	23	PASS
N48	30	30	DFT-16QAM	M	Outer_Full	19.76	20.36	23	PASS
N48	30	30	DFT-16QAM	M	Inner_Full	20.88	21.48	23	PASS
N48	30	30	DFT-16QAM	M	Edge_1RB_Left	19.92	20.52	23	PASS
N48	30	30	DFT-16QAM	M	Edge_1RB_Right	19.86	20.46	23	PASS
N48	30	30	DFT-64QAM	M	Outer_Full	19.34	19.94	23	PASS
N48	30	30	DFT-64QAM	M	Inner_Full	19.29	19.89	23	PASS
N48	30	30	DFT-64QAM	M	Edge_1RB_Left	19.03	19.63	23	PASS
N48	30	30	DFT-64QAM	M	Edge_1RB_Right	18.87	19.47	23	PASS
N48	30	30	DFT-256QAM	M	Outer_Full	17.43	18.03	23	PASS
N48	30	30	DFT-256QAM	M	Inner_Full	17.47	18.07	23	PASS
N48	30	30	DFT-256QAM	M	Edge_1RB_Left	17.53	18.13	23	PASS
N48	30	30	DFT-256QAM	M	Edge_1RB_Right	17.25	17.85	23	PASS
N48	30	30	DFT-PI2BPSK	H	Outer_Full	20.80	21.40	23	PASS
N48	30	30	DFT-PI2BPSK	H	Inner_Full	21.50	22.10	23	PASS
N48	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	20.90	21.50	23	PASS
N48	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	20.89	21.49	23	PASS
N48	30	30	DFT-QPSK	H	Outer_Full	20.33	20.93	23	PASS
N48	30	30	DFT-QPSK	H	Inner_Full	21.34	21.94	23	PASS
N48	30	30	DFT-QPSK	H	Edge_1RB_Left	20.46	21.06	23	PASS
N48	30	30	DFT-QPSK	H	Edge_1RB_Right	20.40	21.00	23	PASS
N48	30	30	DFT-16QAM	H	Outer_Full	19.37	19.97	23	PASS
N48	30	30	DFT-16QAM	H	Inner_Full	20.34	20.94	23	PASS
N48	30	30	DFT-16QAM	H	Edge_1RB_Left	19.24	19.84	23	PASS
N48	30	30	DFT-16QAM	H	Edge_1RB_Right	19.21	19.81	23	PASS
N48	30	30	DFT-64QAM	H	Outer_Full	18.86	19.46	23	PASS
N48	30	30	DFT-64QAM	H	Inner_Full	18.97	19.57	23	PASS
N48	30	30	DFT-64QAM	H	Edge_1RB_Left	19.00	19.60	23	PASS
N48	30	30	DFT-64QAM	H	Edge_1RB_Right	18.86	19.46	23	PASS
N48	30	30	DFT-256QAM	H	Outer_Full	17.02	17.62	23	PASS
N48	30	30	DFT-256QAM	H	Inner_Full	16.82	17.42	23	PASS
N48	30	30	DFT-256QAM	H	Edge_1RB_Left	17.00	17.60	23	PASS
N48	30	30	DFT-256QAM	H	Edge_1RB_Right	17.05	17.65	23	PASS
N48	30	40	DFT-PI2BPSK	L	Outer_Full	20.97	21.57	23	PASS
N48	30	40	DFT-PI2BPSK	L	Inner_Full	21.46	22.06	23	PASS
N48	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	20.83	21.43	23	PASS
N48	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	21.39	21.99	23	PASS
N48	30	40	DFT-QPSK	L	Outer_Full	20.39	20.99	23	PASS
N48	30	40	DFT-QPSK	L	Inner_Full	21.49	22.09	23	PASS

N48	30	40	DFT-QPSK	L	Edge_1RB_Left	20.34	20.94	23	PASS
N48	30	40	DFT-QPSK	L	Edge_1RB_Right	20.75	21.35	23	PASS
N48	30	40	DFT-16QAM	L	Outer_Full	19.48	20.08	23	PASS
N48	30	40	DFT-16QAM	L	Inner_Full	20.44	21.04	23	PASS
N48	30	40	DFT-16QAM	L	Edge_1RB_Left	19.36	19.96	23	PASS
N48	30	40	DFT-16QAM	L	Edge_1RB_Right	19.69	20.29	23	PASS
N48	30	40	DFT-64QAM	L	Outer_Full	19.08	19.68	23	PASS
N48	30	40	DFT-64QAM	L	Inner_Full	18.90	19.50	23	PASS
N48	30	40	DFT-64QAM	L	Edge_1RB_Left	18.95	19.55	23	PASS
N48	30	40	DFT-64QAM	L	Edge_1RB_Right	19.25	19.85	23	PASS
N48	30	40	DFT-256QAM	L	Outer_Full	17.05	17.65	23	PASS
N48	30	40	DFT-256QAM	L	Inner_Full	17.04	17.64	23	PASS
N48	30	40	DFT-256QAM	L	Edge_1RB_Left	16.69	17.29	23	PASS
N48	30	40	DFT-256QAM	L	Edge_1RB_Right	17.11	17.71	23	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	21.41	22.01	23	PASS
N48	30	40	DFT-PI2BPSK	M	Inner_Full	21.90	22.50	23	PASS
N48	30	40	DFT-PI2BPSK	M	Edge_1RB_Left	21.27	21.87	23	PASS
N48	30	40	DFT-PI2BPSK	M	Edge_1RB_Right	21.04	21.64	23	PASS
N48	30	40	DFT-QPSK	M	Outer_Full	20.83	21.43	23	PASS
N48	30	40	DFT-QPSK	M	Inner_Full	21.87	22.47	23	PASS
N48	30	40	DFT-QPSK	M	Edge_1RB_Left	20.97	21.57	23	PASS
N48	30	40	DFT-QPSK	M	Edge_1RB_Right	20.73	21.33	23	PASS
N48	30	40	DFT-16QAM	M	Outer_Full	19.88	20.48	23	PASS
N48	30	40	DFT-16QAM	M	Inner_Full	20.81	21.41	23	PASS
N48	30	40	DFT-16QAM	M	Edge_1RB_Left	20.04	20.64	23	PASS
N48	30	40	DFT-16QAM	M	Edge_1RB_Right	19.88	20.48	23	PASS
N48	30	40	DFT-64QAM	M	Outer_Full	19.25	19.85	23	PASS
N48	30	40	DFT-64QAM	M	Inner_Full	19.38	19.98	23	PASS
N48	30	40	DFT-64QAM	M	Edge_1RB_Left	19.45	20.05	23	PASS
N48	30	40	DFT-64QAM	M	Edge_1RB_Right	19.21	19.81	23	PASS
N48	30	40	DFT-256QAM	M	Outer_Full	17.29	17.89	23	PASS
N48	30	40	DFT-256QAM	M	Inner_Full	17.24	17.84	23	PASS
N48	30	40	DFT-256QAM	M	Edge_1RB_Left	17.32	17.92	23	PASS
N48	30	40	DFT-256QAM	M	Edge_1RB_Right	17.33	17.93	23	PASS
N48	30	40	DFT-PI2BPSK	H	Outer_Full	20.88	21.48	23	PASS
N48	30	40	DFT-PI2BPSK	H	Inner_Full	21.41	22.01	23	PASS
N48	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	21.12	21.72	23	PASS
N48	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	20.89	21.49	23	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	20.50	21.10	23	PASS
N48	30	40	DFT-QPSK	H	Inner_Full	21.34	21.94	23	PASS
N48	30	40	DFT-QPSK	H	Edge_1RB_Left	20.47	21.07	23	PASS
N48	30	40	DFT-QPSK	H	Edge_1RB_Right	20.37	20.97	23	PASS

N48	30	40	DFT-16QAM	H	Outer_Full	19.55	20.15	23	PASS
N48	30	40	DFT-16QAM	H	Inner_Full	20.36	20.96	23	PASS
N48	30	40	DFT-16QAM	H	Edge_1RB_Left	19.67	20.27	23	PASS
N48	30	40	DFT-16QAM	H	Edge_1RB_Right	19.65	20.25	23	PASS
N48	30	40	DFT-64QAM	H	Outer_Full	18.86	19.46	23	PASS
N48	30	40	DFT-64QAM	H	Inner_Full	18.82	19.42	23	PASS
N48	30	40	DFT-64QAM	H	Edge_1RB_Left	19.03	19.63	23	PASS
N48	30	40	DFT-64QAM	H	Edge_1RB_Right	18.93	19.53	23	PASS
N48	30	40	DFT-256QAM	H	Outer_Full	16.91	17.51	23	PASS
N48	30	40	DFT-256QAM	H	Inner_Full	16.88	17.48	23	PASS
N48	30	40	DFT-256QAM	H	Edge_1RB_Left	17.01	17.61	23	PASS
N48	30	40	DFT-256QAM	H	Edge_1RB_Right	17.00	17.60	23	PASS

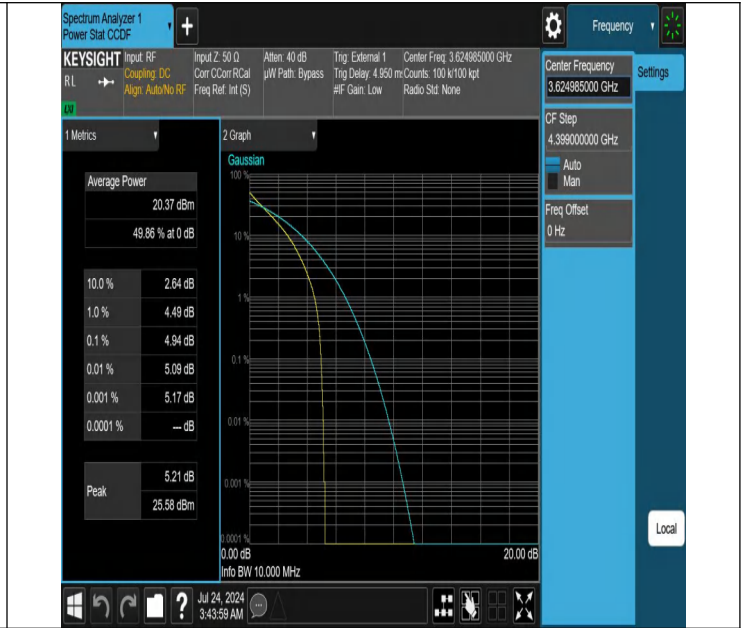
Peak-to-Average Ratio for SA

Peak-to-Average Ratio(CCDF)

Test Result

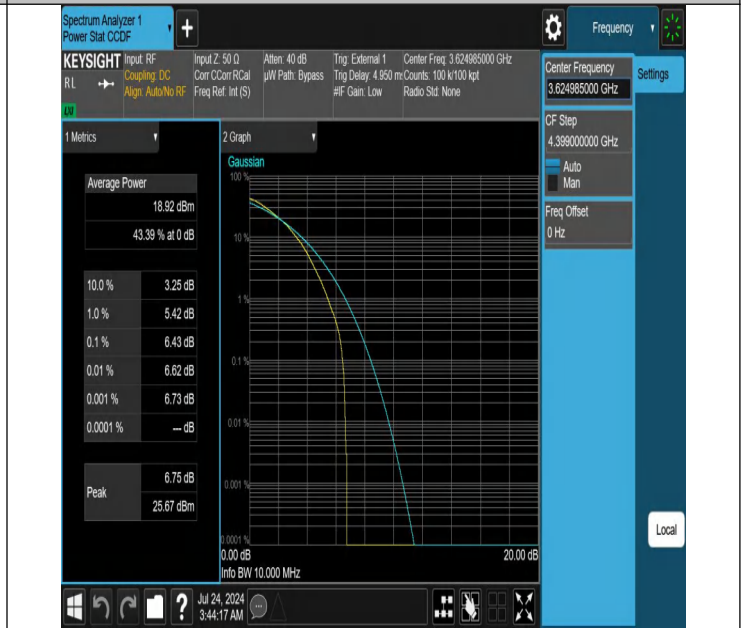
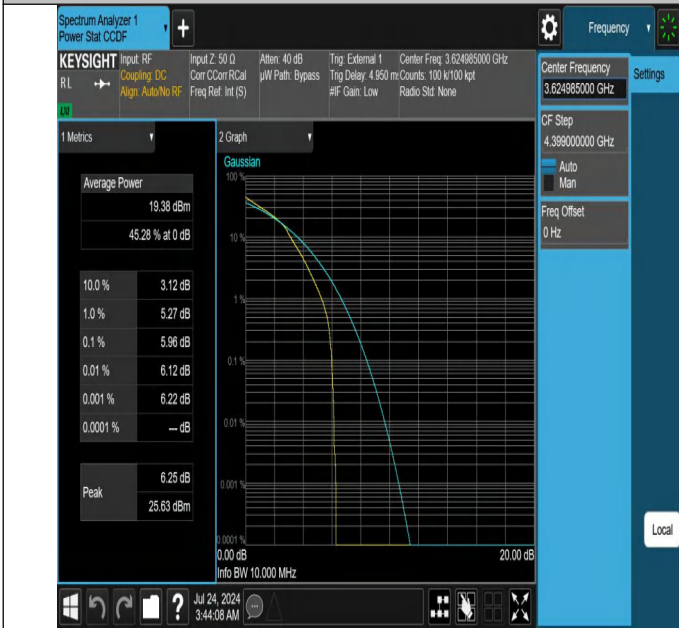
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (dB)	Limit (dB)	Verdict
N48	30	10	DFT-PI2BPSK	M	Outer_Full	4.35	≤13	PASS
N48	30	10	DFT-QPSK	M	Outer_Full	4.94	≤13	PASS
N48	30	10	DFT-16QAM	M	Outer_Full	5.96	≤13	PASS
N48	30	10	DFT-64QAM	M	Outer_Full	6.43	≤13	PASS
N48	30	10	DFT-256QAM	M	Outer_Full	7.16	≤13	PASS
N48	30	15	DFT-PI2BPSK	M	Outer_Full	4.40	≤13	PASS
N48	30	15	DFT-QPSK	M	Outer_Full	4.97	≤13	PASS
N48	30	15	DFT-16QAM	M	Outer_Full	5.88	≤13	PASS
N48	30	15	DFT-64QAM	M	Outer_Full	6.46	≤13	PASS
N48	30	15	DFT-256QAM	M	Outer_Full	7.19	≤13	PASS
N48	30	20	DFT-PI2BPSK	M	Outer_Full	5.09	≤13	PASS
N48	30	20	DFT-QPSK	M	Outer_Full	5.65	≤13	PASS
N48	30	20	DFT-16QAM	M	Outer_Full	6.52	≤13	PASS
N48	30	20	DFT-64QAM	M	Outer_Full	6.86	≤13	PASS
N48	30	20	DFT-256QAM	M	Outer_Full	6.87	≤13	PASS
N48	30	30	DFT-PI2BPSK	M	Outer_Full	4.60	≤13	PASS
N48	30	30	DFT-QPSK	M	Outer_Full	5.13	≤13	PASS
N48	30	30	DFT-16QAM	M	Outer_Full	6.14	≤13	PASS
N48	30	30	DFT-64QAM	M	Outer_Full	6.71	≤13	PASS
N48	30	30	DFT-256QAM	M	Outer_Full	7.02	≤13	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	4.57	≤13	PASS
N48	30	40	DFT-QPSK	M	Outer_Full	5.12	≤13	PASS
N48	30	40	DFT-16QAM	M	Outer_Full	6.12	≤13	PASS
N48	30	40	DFT-64QAM	M	Outer_Full	6.51	≤13	PASS
N48	30	40	DFT-256QAM	M	Outer_Full	7.05	≤13	PASS

Test Graphs



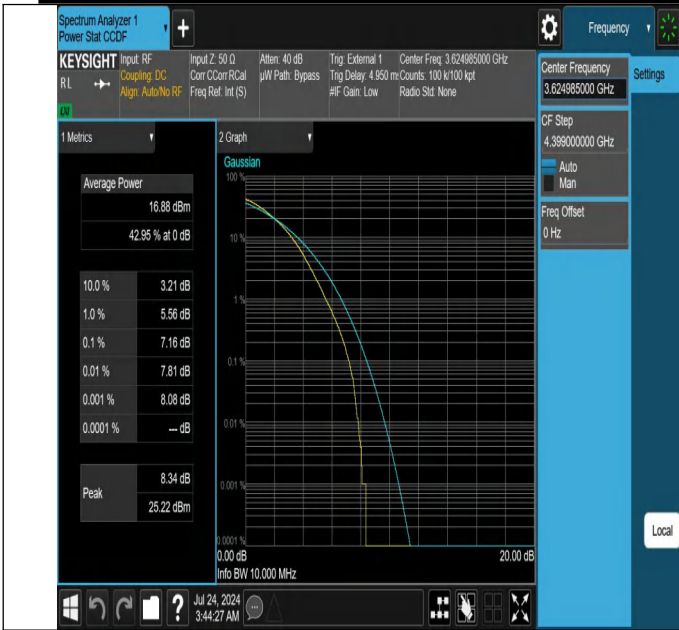
NTNV-N48-PC3-30-10-M-DFT-PI2BPSK-Outer_Full-PASS

NTNV-N48-PC3-30-10-M-DFT-QPSK-Outer_Full-PASS

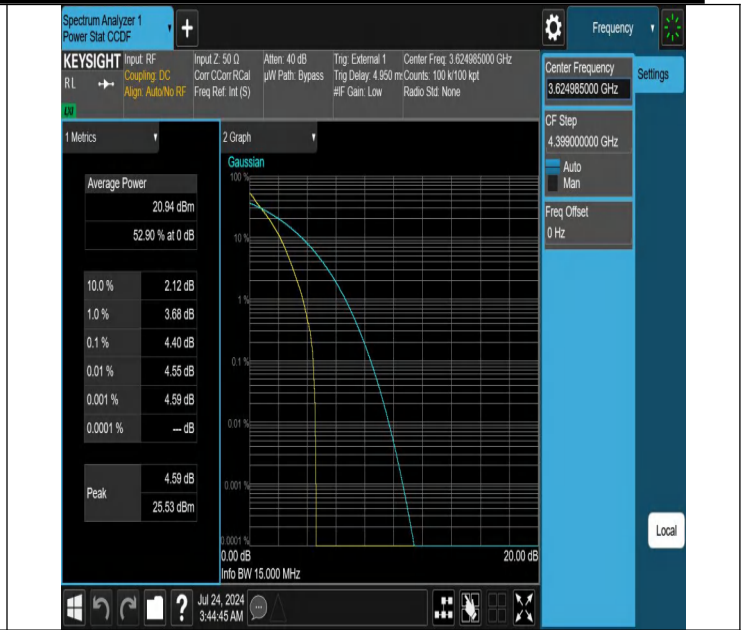


NTNV-N48-PC3-30-10-M-DFT-16QAM-Outer_Full-PASS

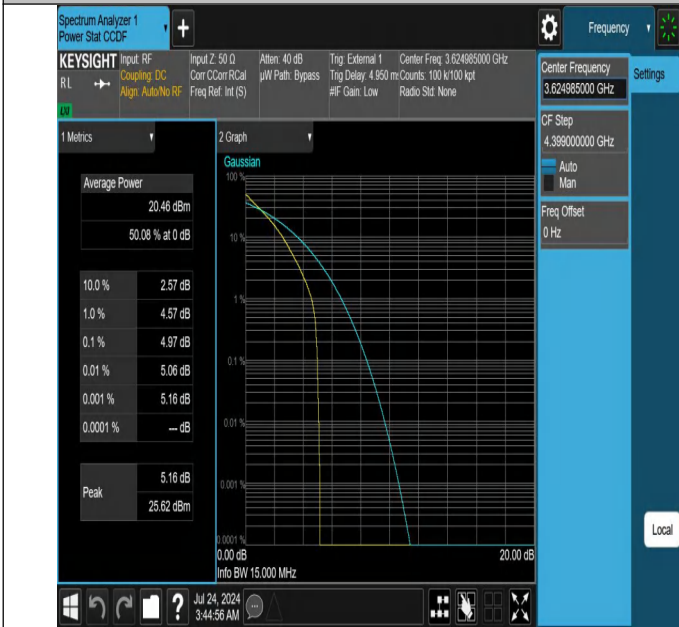
NTNV-N48-PC3-30-10-M-DFT-64QAM-Outer_Full-PASS



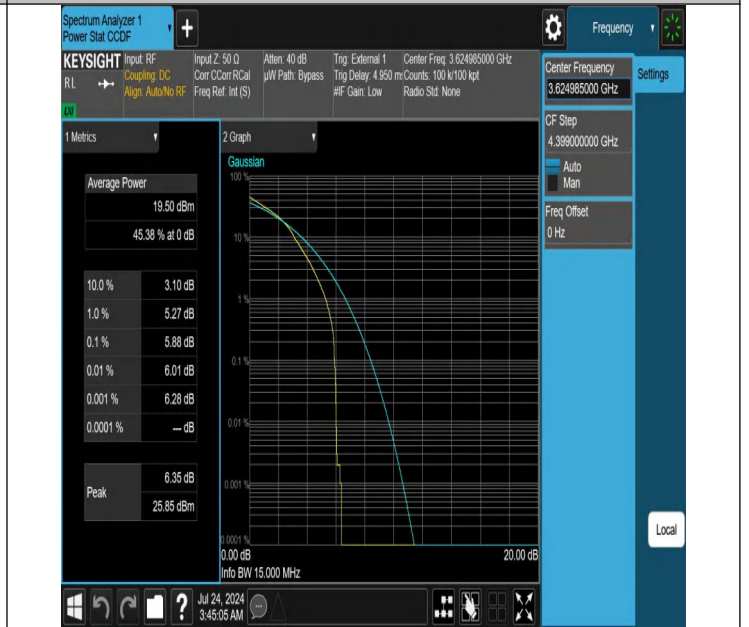
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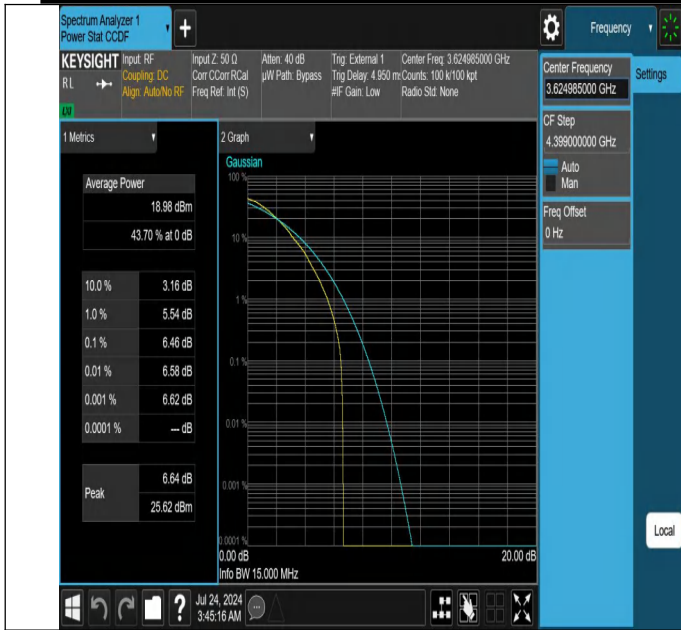
NTVN-N48-PC3-30-15-M-DFT-PI2BPSK-Outer_Full-PASS



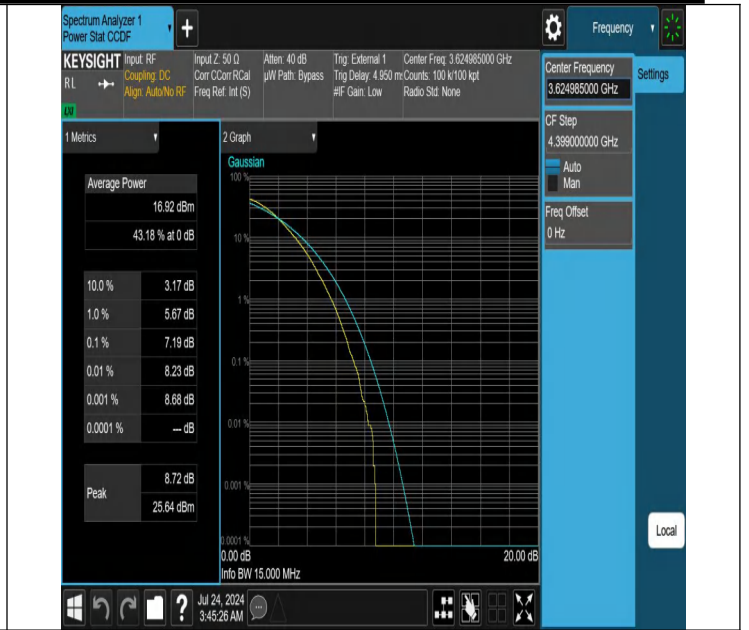
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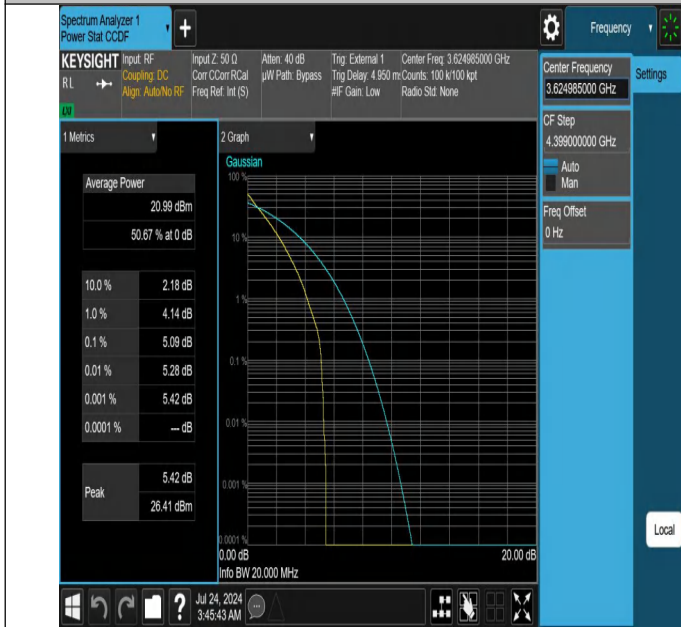
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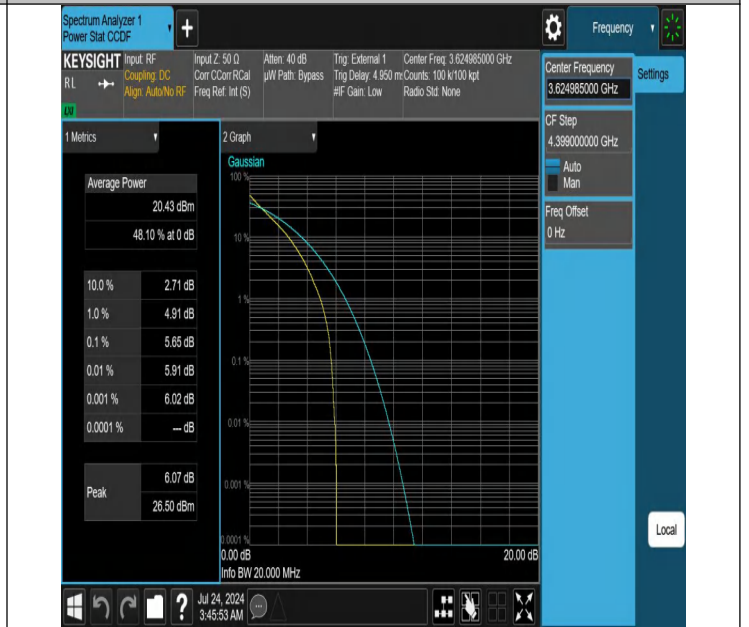
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NTVN-N48-PC3-30-15-M-DFT-256QAM-Outer_Full-PASS



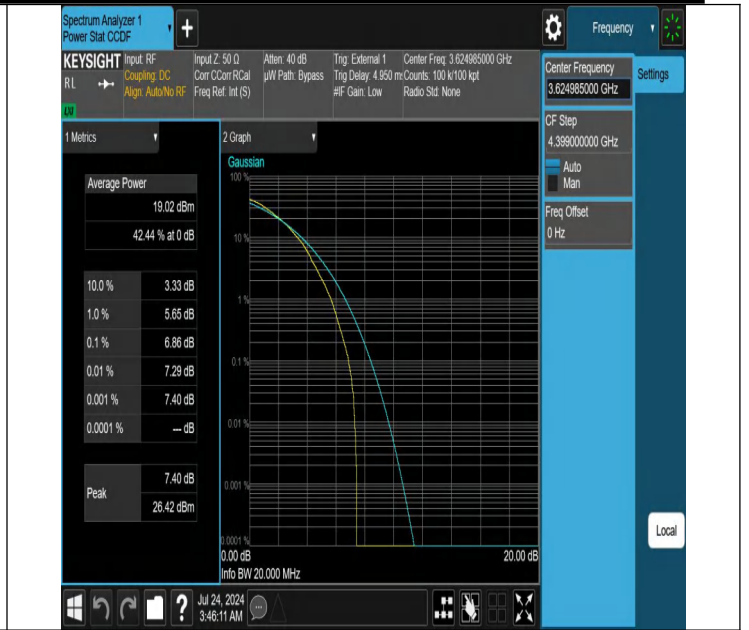
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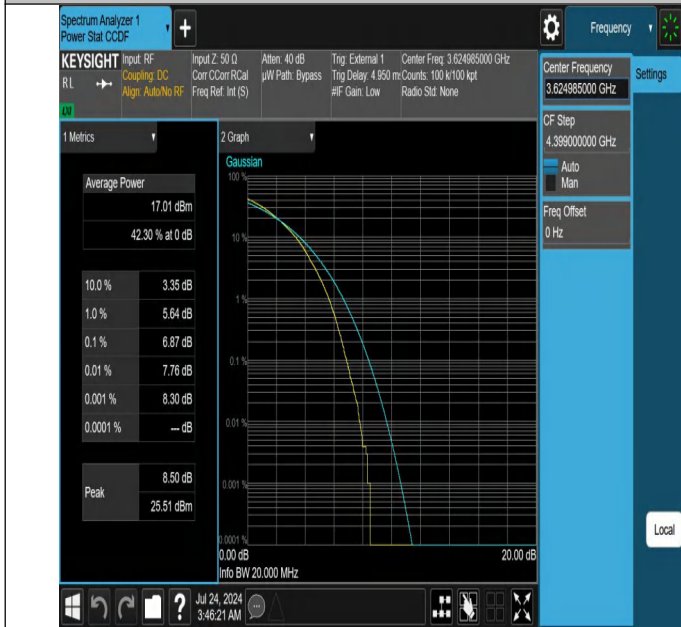
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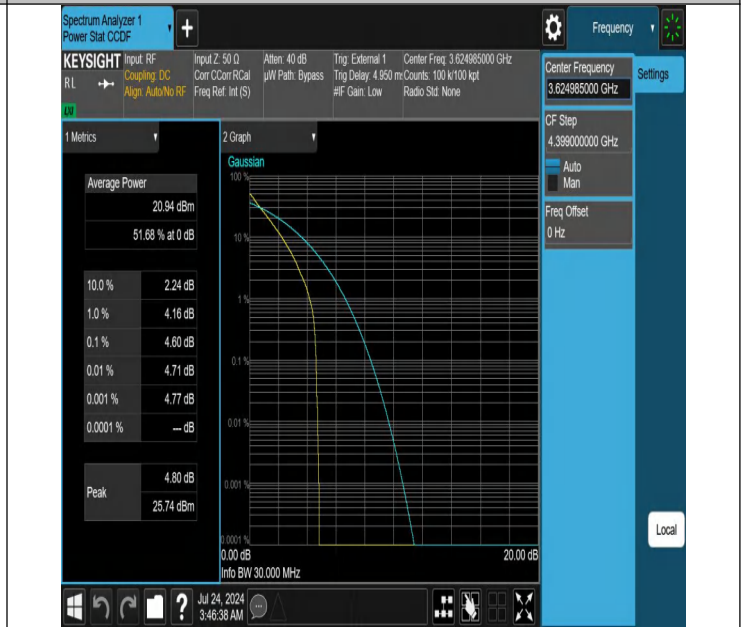
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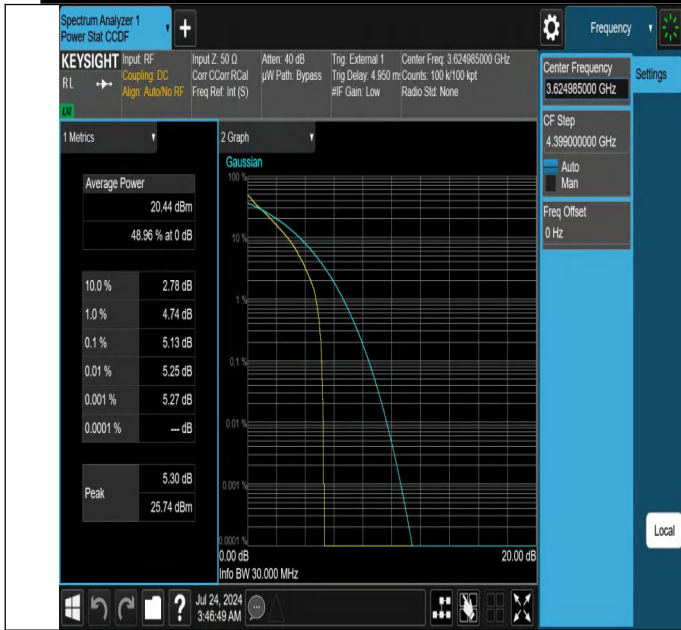
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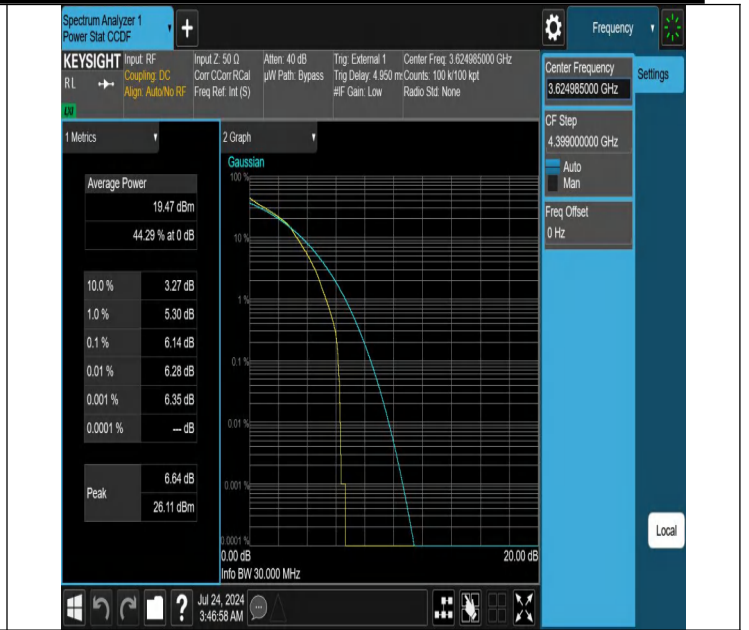
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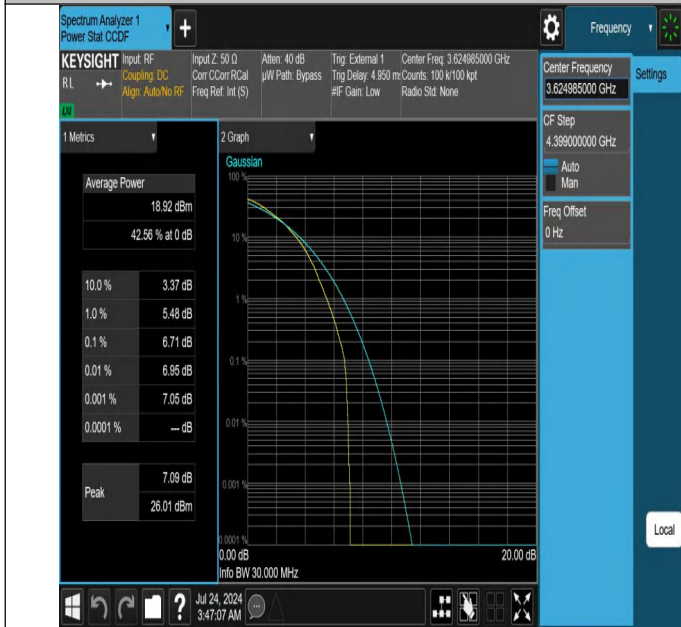
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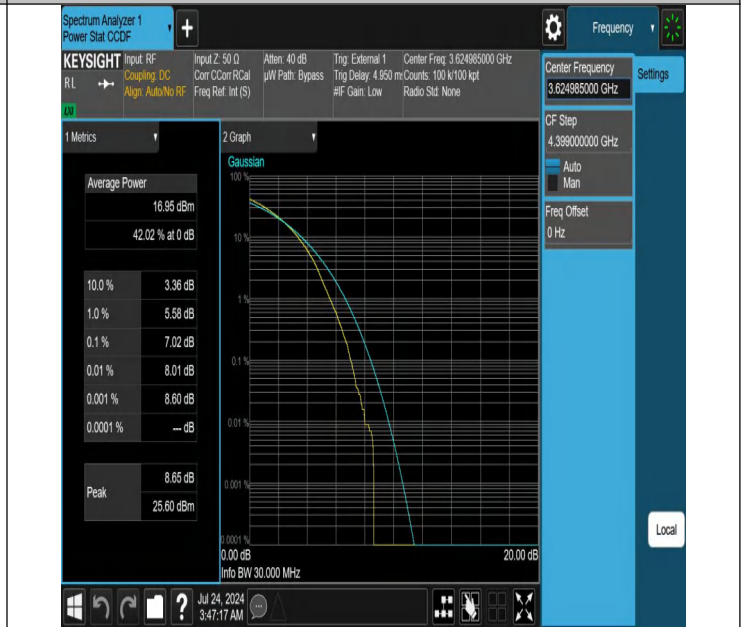
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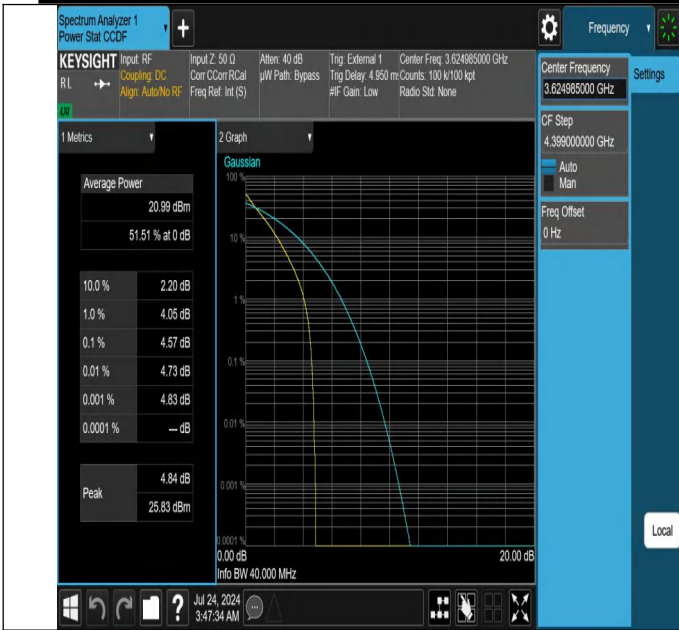
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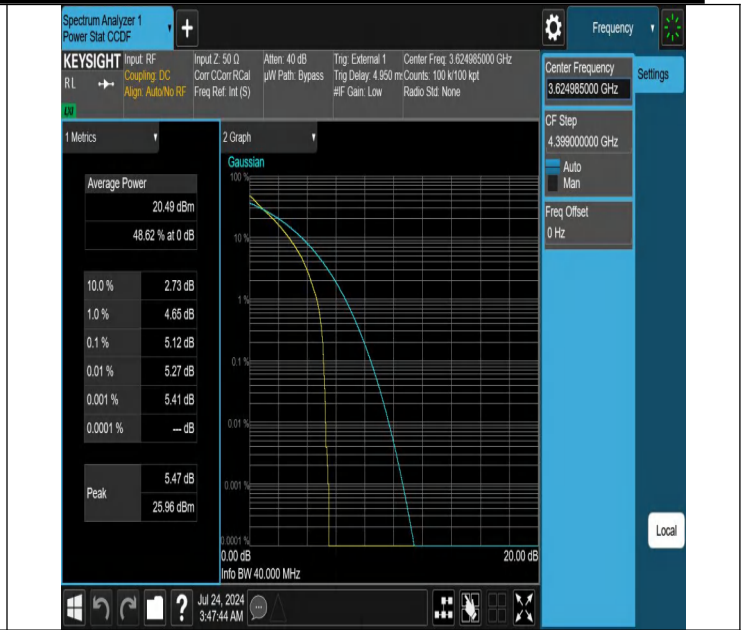
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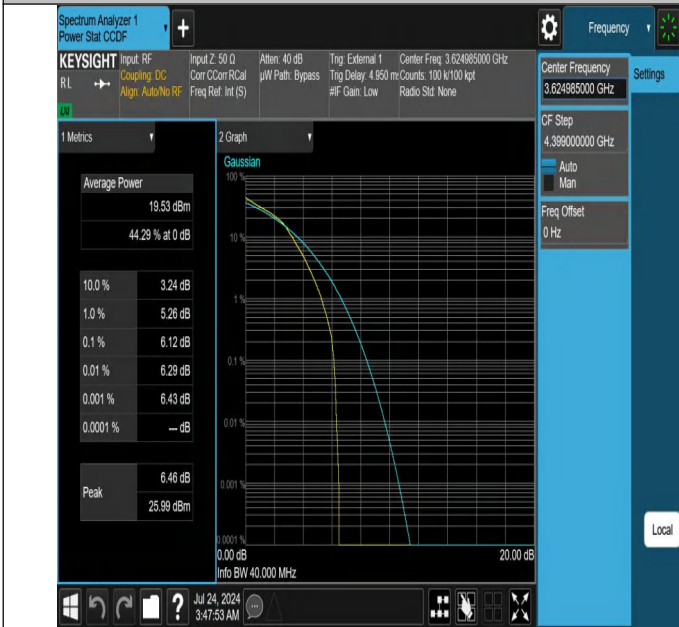
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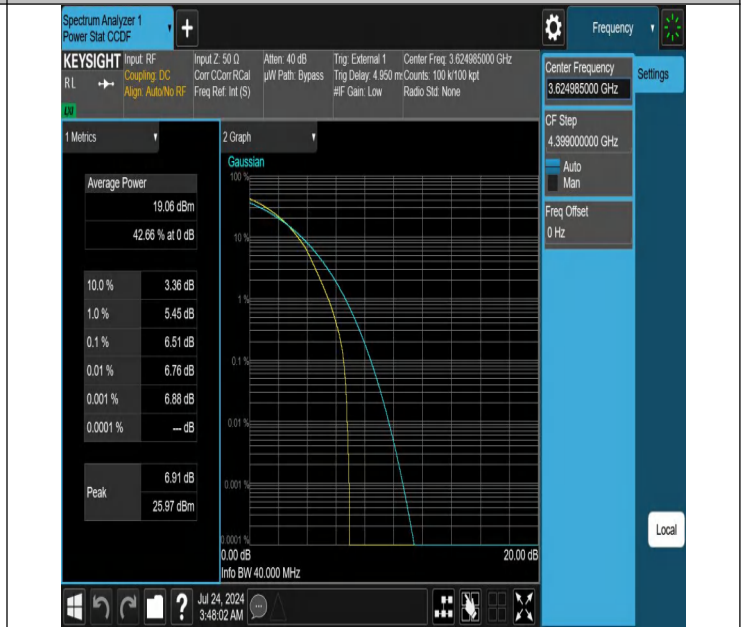
NTNV-N48-PC3-30-40-M-DFT-PI2BPSK-Outer_Full-PASS



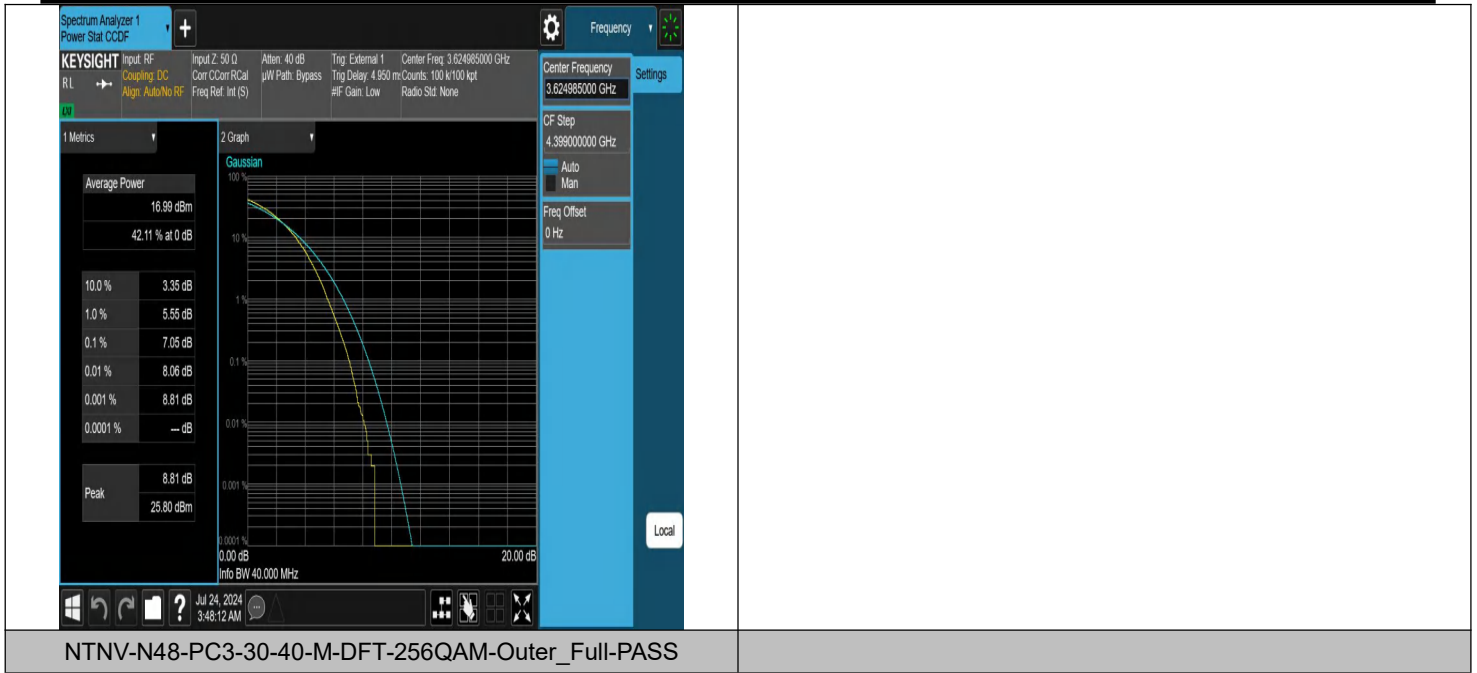
NTNV-N48-PC3-30-40-M-DFT-QPSK-Outer_Full-PASS



NTNV-N48-PC3-30-40-M-DFT-16QAM-Outer_Full-PASS



NTNV-N48-PC3-30-40-M-DFT-64QAM-Outer_Full-PASS



NTNV-N48-PC3-30-40-M-DFT-256QAM-Outer_Full-PASS

26dB Bandwidth and Occupied Bandwidth for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
N48	30	10	CP-QPSK	M	Outer_Full	8.5972	9.786	PASS
N48	30	10	CP-16QAM	M	Outer_Full	8.6467	10.26	PASS
N48	30	10	CP-64QAM	M	Outer_Full	8.6085	9.824	PASS
N48	30	10	CP-256QAM	M	Outer_Full	8.6081	9.838	PASS
N48	30	15	CP-QPSK	M	Outer_Full	13.594	15.17	PASS
N48	30	15	CP-16QAM	M	Outer_Full	13.619	15.21	PASS
N48	30	15	CP-64QAM	M	Outer_Full	13.617	15.19	PASS
N48	30	15	CP-256QAM	M	Outer_Full	13.595	14.97	PASS
N48	30	20	CP-QPSK	M	Outer_Full	18.215	20.12	PASS
N48	30	20	CP-16QAM	M	Outer_Full	18.261	19.82	PASS
N48	30	20	CP-64QAM	M	Outer_Full	18.239	20.04	PASS
N48	30	20	CP-256QAM	M	Outer_Full	18.270	20.02	PASS
N48	30	30	CP-QPSK	M	Outer_Full	27.875	29.74	PASS
N48	30	30	CP-16QAM	M	Outer_Full	27.831	29.85	PASS
N48	30	30	CP-64QAM	M	Outer_Full	27.938	30.06	PASS
N48	30	30	CP-256QAM	M	Outer_Full	27.865	29.75	PASS
N48	30	40	CP-QPSK	M	Outer_Full	37.872	40.14	PASS
N48	30	40	CP-16QAM	M	Outer_Full	37.809	40.27	PASS
N48	30	40	CP-64QAM	M	Outer_Full	37.935	40.16	PASS
N48	30	40	CP-256QAM	M	Outer_Full	37.802	40.28	PASS

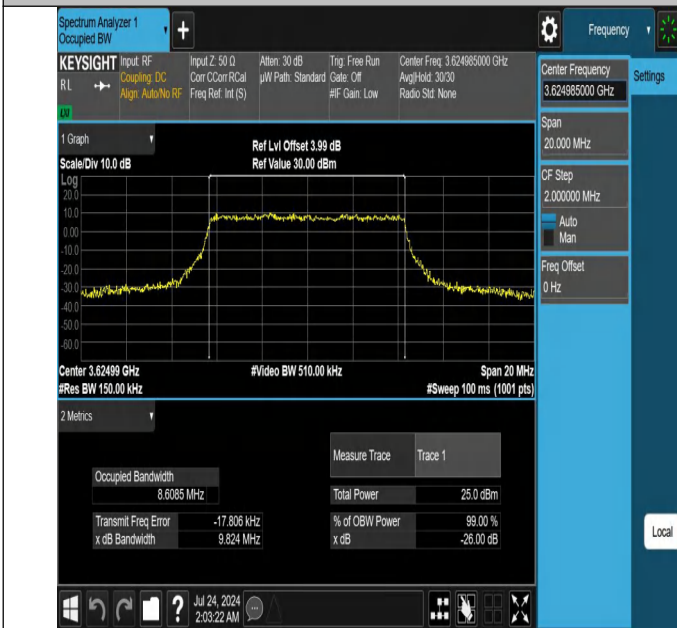
Test Graphs



NTNV-N48-PC3-30-10-M-CP-QPSK-Outer_Full-Ant1-PASS



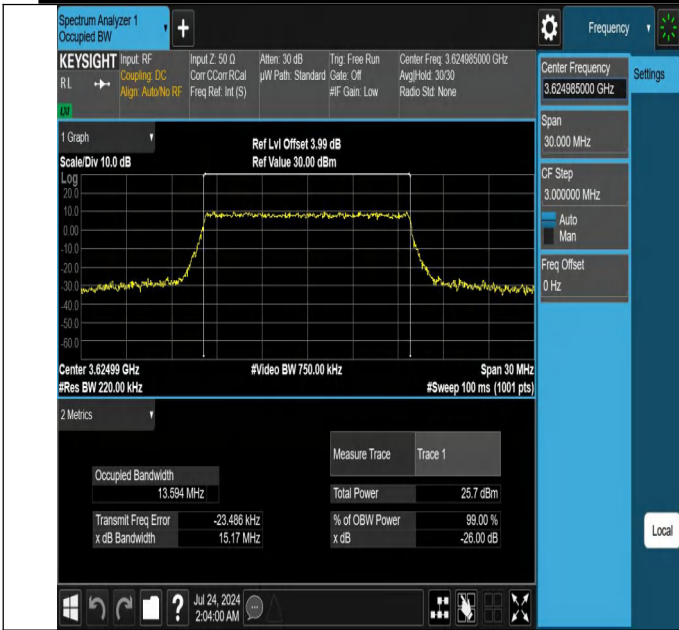
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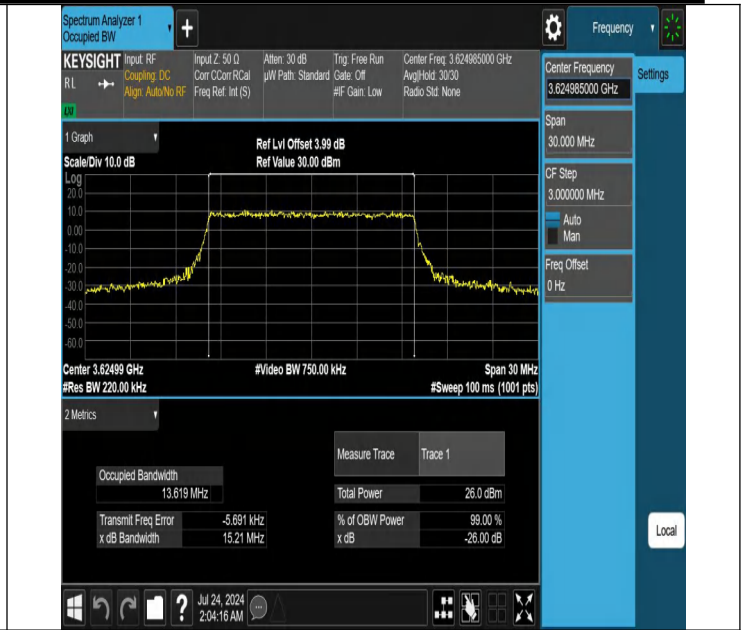
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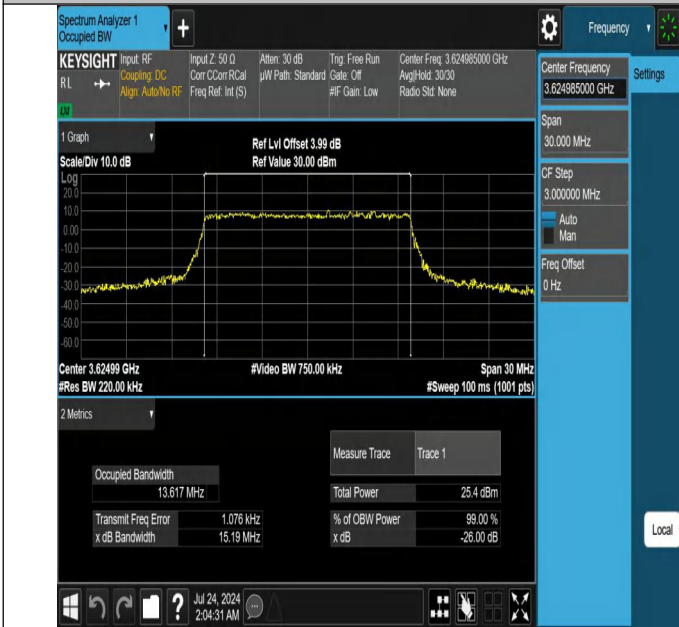
NTNV-N48-PC3-30-10-M-CP-256QAM-Outer_Full-Ant1-PASS



NTNV-N48-PC3-30-15-M-CP-QPSK-Outer_Full-Ant1-PASS



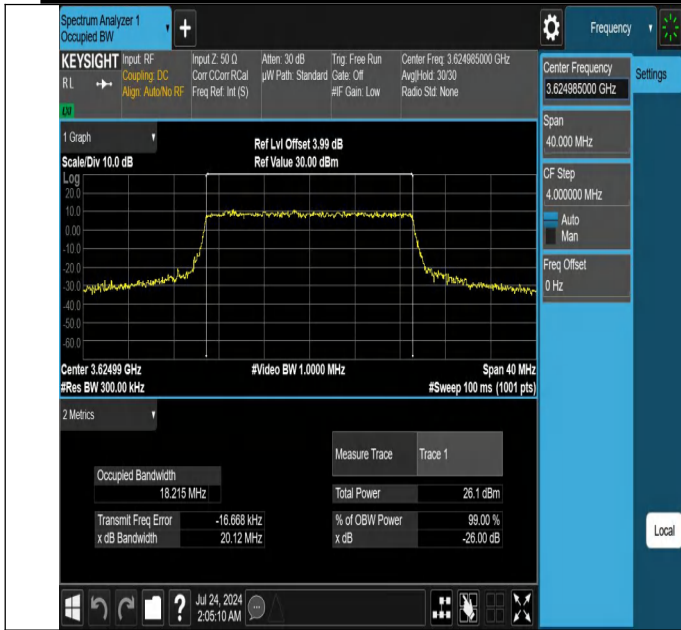
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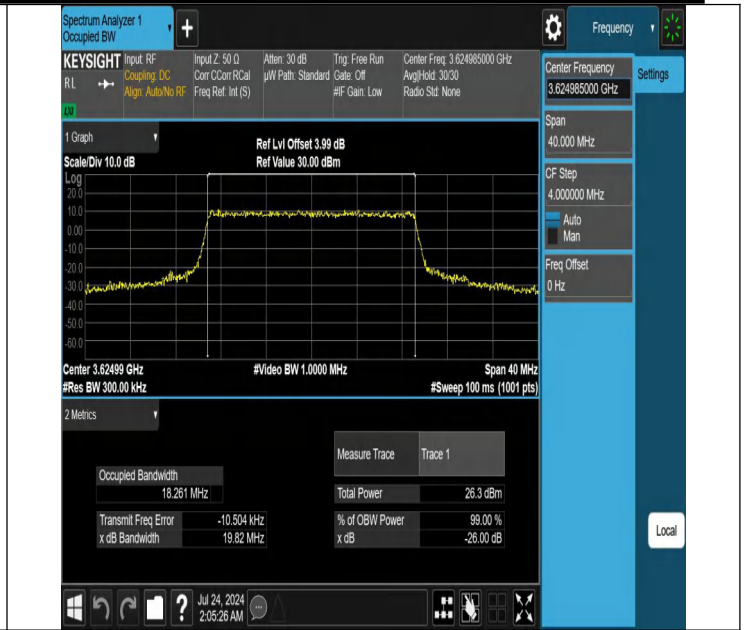
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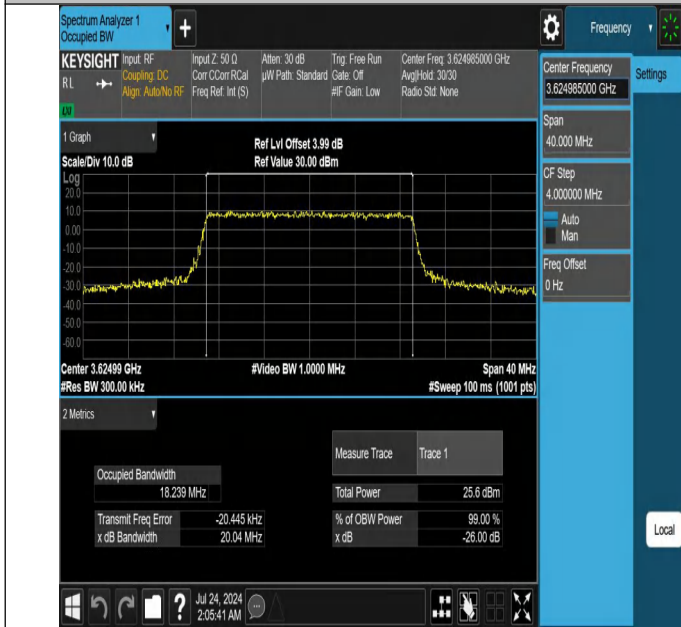
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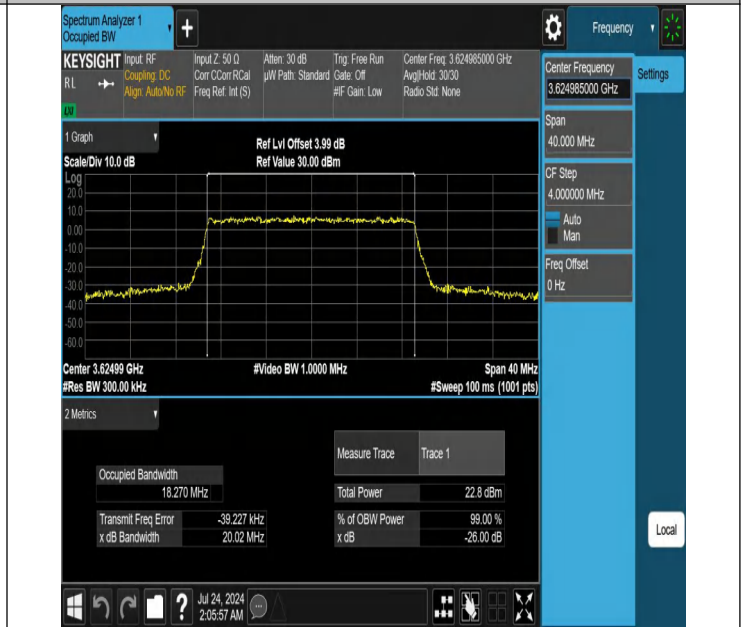
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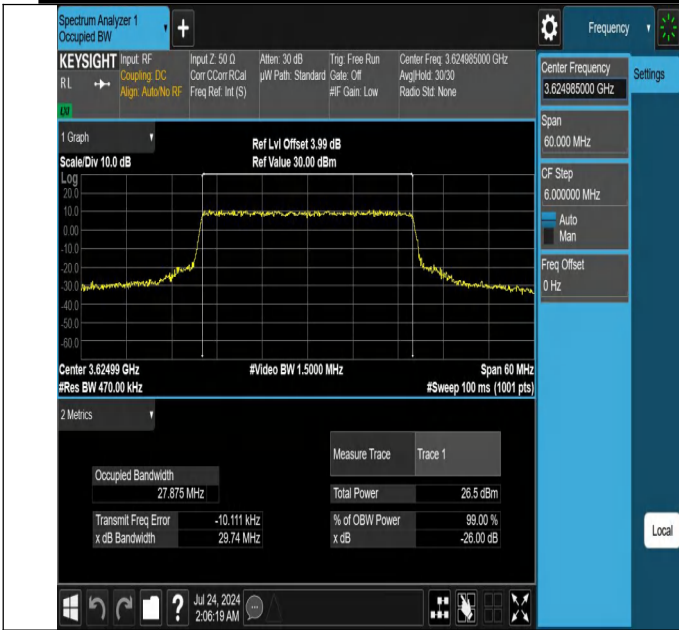
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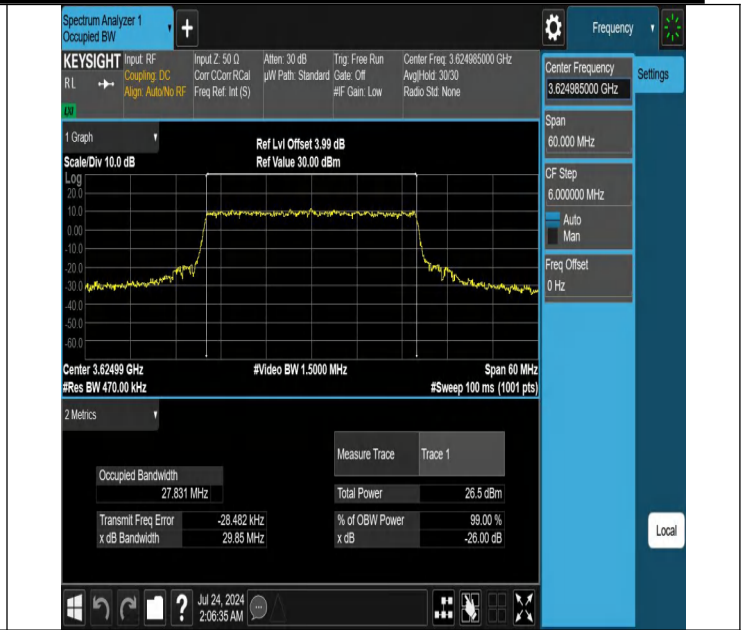
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NTNV-N48-PC3-30-20-M-CP-256QAM-Outer_Full-Ant1-PASS



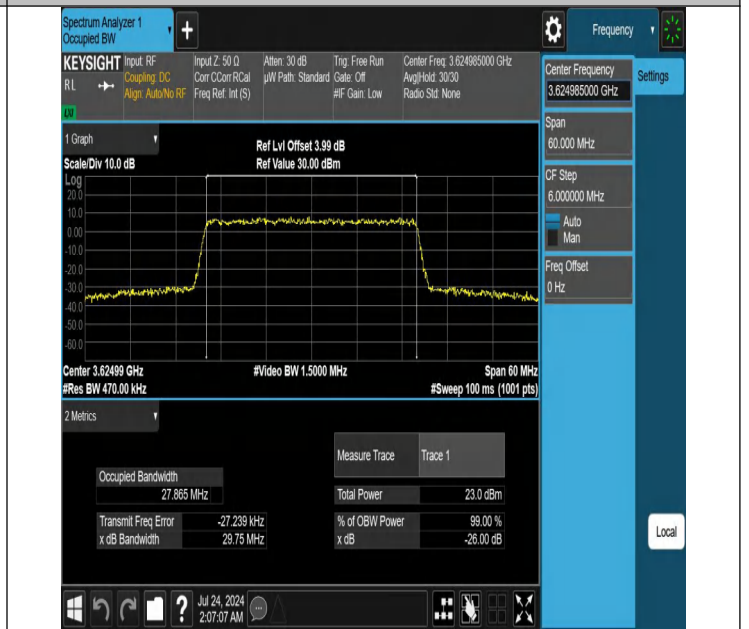
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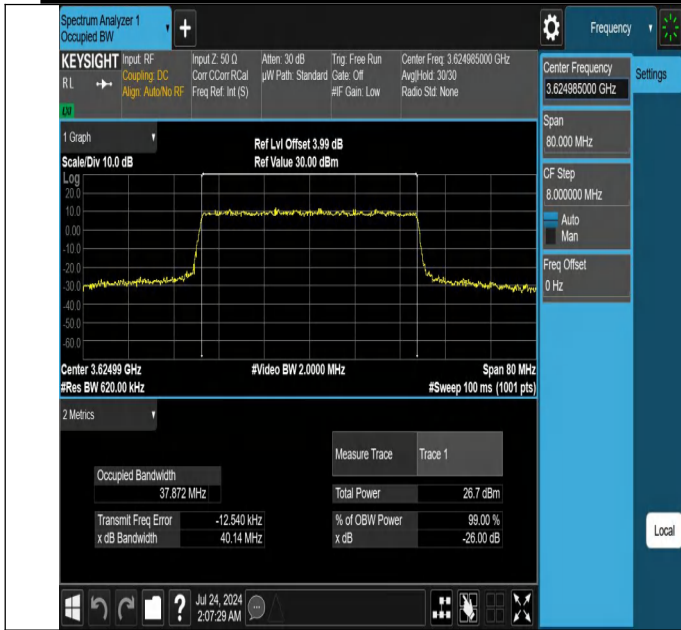
NTNV-N48-PC3-30-30-M-CP-16QAM-Outer_Full-Ant1-PASS



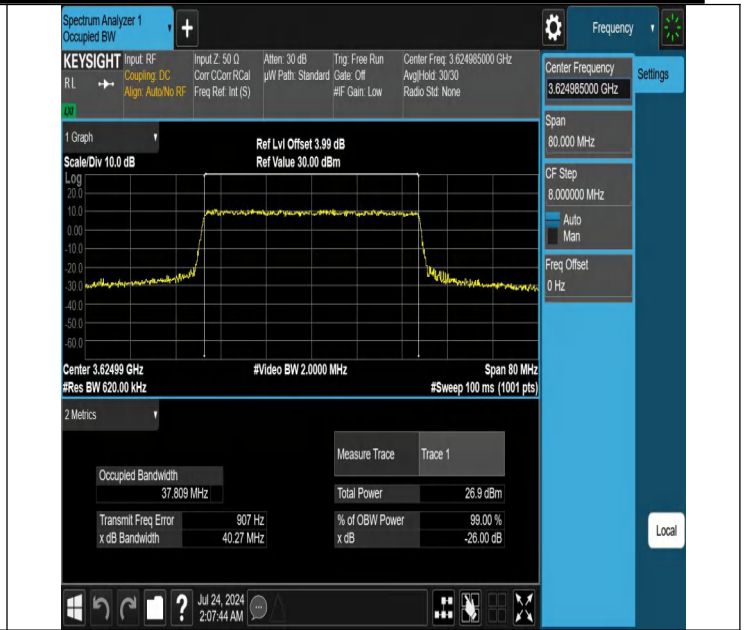
NTNV-N48-PC3-30-30-M-CP-64QAM-Outer_Full-Ant1-PASS



NTNV-N48-PC3-30-30-M-CP-256QAM-Outer_Full-Ant1-PASS



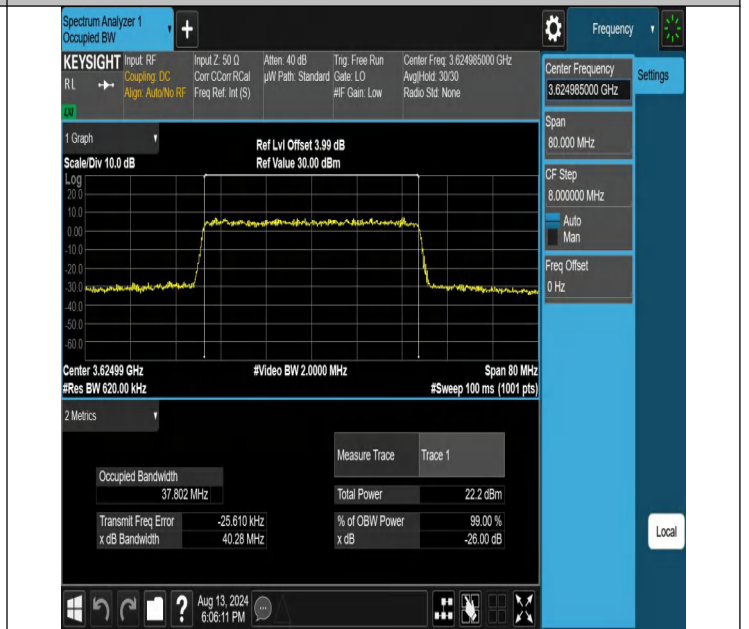
NTNV-N48-PC3-30-40-M-CP-QPSK-Outer_Full-Ant1-PASS



NTNV-N48-PC3-30-40-M-CP-16QAM-Outer_Full-Ant1-PASS



NTNV-N48-PC3-30-40-M-CP-64QAM-Outer_Full-Ant1-PASS



NTNV-N48-PC3-30-40-M-CP-256QAM-Outer_Full-Ant1-PASS

Maximum eirp Density

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (dBm)	Limit (dBm)	Verdict
N48	30	10	DFT-BPSK	L	Edge_1RB_Left	21.5	23	PASS
N48	30	10	DFT-BPSK	L	Outer_Full	20.8	23	PASS
N48	30	10	DFT-BPSK	L	Inner_Full	21.92	23	PASS
N48	30	10	DFT-BPSK	L	Edge_1RB_Right	21.57	23	PASS
N48	30	10	DFT-QPSK	L	Edge_1RB_Left	20.91	23	PASS
N48	30	10	DFT-QPSK	L	Outer_Full	20.35	23	PASS
N48	30	10	DFT-QPSK	L	Inner_Full	21.75	23	PASS
N48	30	10	DFT-QPSK	L	Edge_1RB_Right	20.95	23	PASS
N48	30	10	DFT-16QAM	L	Edge_1RB_Left	19.25	23	PASS
N48	30	10	DFT-16QAM	L	Outer_Full	19.36	23	PASS
N48	30	10	DFT-16QAM	L	Inner_Full	20.82	23	PASS
N48	30	10	DFT-16QAM	L	Edge_1RB_Right	19.22	23	PASS
N48	30	10	DFT-64QAM	L	Edge_1RB_Left	18.91	23	PASS
N48	30	10	DFT-64QAM	L	Outer_Full	18.84	23	PASS
N48	30	10	DFT-64QAM	L	Inner_Full	19.37	23	PASS
N48	30	10	DFT-64QAM	L	Edge_1RB_Right	19.74	23	PASS
N48	30	10	DFT-256QAM	L	Edge_1RB_Left	17.61	23	PASS
N48	30	10	DFT-256QAM	L	Outer_Full	16.76	23	PASS
N48	30	10	DFT-256QAM	L	Inner_Full	17.37	23	PASS
N48	30	10	DFT-256QAM	L	Edge_1RB_Right	17.37	23	PASS
N48	30	10	CP-QPSK	L	Edge_1RB_Left	18.86	23	PASS
N48	30	10	CP-QPSK	L	Outer_Full	18.27	23	PASS
N48	30	10	CP-QPSK	L	Inner_Full	20.42	23	PASS
N48	30	10	CP-QPSK	L	Edge_1RB_Right	19.02	23	PASS
N48	30	10	CP-16QAM	L	Edge_1RB_Left	18.3	23	PASS
N48	30	10	CP-16QAM	L	Outer_Full	18.44	23	PASS
N48	30	10	CP-16QAM	L	Inner_Full	19.89	23	PASS

N48	30	10	CP-16QAM	L	Edge_1RB_Right	18.26	23	PASS
N48	30	10	CP-64QAM	L	Edge_1RB_Left	17.99	23	PASS
N48	30	10	CP-64QAM	L	Outer_Full	17.74	23	PASS
N48	30	10	CP-64QAM	L	Inner_Full	18.36	23	PASS
N48	30	10	CP-64QAM	L	Edge_1RB_Right	18.67	23	PASS
N48	30	10	CP-256QAM	L	Edge_1RB_Left	15.7	23	PASS
N48	30	10	CP-256QAM	L	Outer_Full	14.83	23	PASS
N48	30	10	CP-256QAM	L	Inner_Full	15.37	23	PASS
N48	30	10	CP-256QAM	L	Edge_1RB_Right	15.33	23	PASS
N48	30	10	DFT-BPSK	M	Edge_1RB_Left	21.2	23	PASS
N48	30	10	DFT-BPSK	M	Outer_Full	20.58	23	PASS
N48	30	10	DFT-BPSK	M	Inner_Full	21.53	23	PASS
N48	30	10	DFT-BPSK	M	Edge_1RB_Right	21.14	23	PASS
N48	30	10	DFT-QPSK	M	Edge_1RB_Left	20.62	23	PASS
N48	30	10	DFT-QPSK	M	Outer_Full	20.13	23	PASS
N48	30	10	DFT-QPSK	M	Inner_Full	21.6	23	PASS
N48	30	10	DFT-QPSK	M	Edge_1RB_Right	20.73	23	PASS
N48	30	10	DFT-16QAM	M	Edge_1RB_Left	19.76	23	PASS
N48	30	10	DFT-16QAM	M	Outer_Full	19.07	23	PASS
N48	30	10	DFT-16QAM	M	Inner_Full	20.47	23	PASS
N48	30	10	DFT-16QAM	M	Edge_1RB_Right	19.68	23	PASS
N48	30	10	DFT-64QAM	M	Edge_1RB_Left	19.76	23	PASS
N48	30	10	DFT-64QAM	M	Outer_Full	18.56	23	PASS
N48	30	10	DFT-64QAM	M	Inner_Full	18.93	23	PASS
N48	30	10	DFT-64QAM	M	Edge_1RB_Right	19.68	23	PASS
N48	30	10	DFT-256QAM	M	Edge_1RB_Left	17.05	23	PASS
N48	30	10	DFT-256QAM	M	Outer_Full	16.51	23	PASS
N48	30	10	DFT-256QAM	M	Inner_Full	16.87	23	PASS
N48	30	10	DFT-256QAM	M	Edge_1RB_Right	17.03	23	PASS
N48	30	10	CP-QPSK	M	Edge_1RB_Left	18.77	23	PASS
N48	30	10	CP-QPSK	M	Outer_Full	18.06	23	PASS

N48	30	10	CP-QPSK	M	Inner_Full	20.07	23	PASS
N48	30	10	CP-QPSK	M	Edge_1RB_Right	18.86	23	PASS
N48	30	10	CP-16QAM	M	Edge_1RB_Left	18.84	23	PASS
N48	30	10	CP-16QAM	M	Outer_Full	18.19	23	PASS
N48	30	10	CP-16QAM	M	Inner_Full	19.35	23	PASS
N48	30	10	CP-16QAM	M	Edge_1RB_Right	18.71	23	PASS
N48	30	10	CP-64QAM	M	Edge_1RB_Left	18.66	23	PASS
N48	30	10	CP-64QAM	M	Outer_Full	17.53	23	PASS
N48	30	10	CP-64QAM	M	Inner_Full	17.97	23	PASS
N48	30	10	CP-64QAM	M	Edge_1RB_Right	18.6	23	PASS
N48	30	10	CP-256QAM	M	Edge_1RB_Left	15.13	23	PASS
N48	30	10	CP-256QAM	M	Outer_Full	14.55	23	PASS
N48	30	10	CP-256QAM	M	Inner_Full	15.03	23	PASS
N48	30	10	CP-256QAM	M	Edge_1RB_Right	14.97	23	PASS
N48	30	10	DFT-BPSK	H	Edge_1RB_Left	20.97	23	PASS
N48	30	10	DFT-BPSK	H	Outer_Full	20.42	23	PASS
N48	30	10	DFT-BPSK	H	Inner_Full	21.39	23	PASS
N48	30	10	DFT-BPSK	H	Edge_1RB_Right	21.02	23	PASS
N48	30	10	DFT-QPSK	H	Edge_1RB_Left	20.49	23	PASS
N48	30	10	DFT-QPSK	H	Outer_Full	19.93	23	PASS
N48	30	10	DFT-QPSK	H	Inner_Full	21.35	23	PASS
N48	30	10	DFT-QPSK	H	Edge_1RB_Right	20.61	23	PASS
N48	30	10	DFT-16QAM	H	Edge_1RB_Left	19.26	23	PASS
N48	30	10	DFT-16QAM	H	Outer_Full	18.79	23	PASS
N48	30	10	DFT-16QAM	H	Inner_Full	20.47	23	PASS
N48	30	10	DFT-16QAM	H	Edge_1RB_Right	19.8	23	PASS
N48	30	10	DFT-64QAM	H	Edge_1RB_Left	18.85	23	PASS
N48	30	10	DFT-64QAM	H	Outer_Full	18.36	23	PASS
N48	30	10	DFT-64QAM	H	Inner_Full	18.83	23	PASS
N48	30	10	DFT-64QAM	H	Edge_1RB_Right	18.91	23	PASS
N48	30	10	DFT-256QAM	H	Edge_1RB_Left	17.03	23	PASS
N48	30	10	DFT-256QAM	H	Outer_Full	16.3	23	PASS

N48	30	10	DFT-256QAM	H	Inner_Full	16.79	23	PASS
N48	30	10	DFT-256QAM	H	Edge_1RB_Right	16.97	23	PASS
N48	30	10	CP-QPSK	H	Edge_1RB_Left	18.45	23	PASS
N48	30	10	CP-QPSK	H	Outer_Full	17.91	23	PASS
N48	30	10	CP-QPSK	H	Inner_Full	19.85	23	PASS
N48	30	10	CP-QPSK	H	Edge_1RB_Right	18.52	23	PASS
N48	30	10	CP-16QAM	H	Edge_1RB_Left	18.12	23	PASS
N48	30	10	CP-16QAM	H	Outer_Full	17.86	23	PASS
N48	30	10	CP-16QAM	H	Inner_Full	19.32	23	PASS
N48	30	10	CP-16QAM	H	Edge_1RB_Right	18.84	23	PASS
N48	30	10	CP-64QAM	H	Edge_1RB_Left	18.1	23	PASS
N48	30	10	CP-64QAM	H	Outer_Full	17.36	23	PASS
N48	30	10	CP-64QAM	H	Inner_Full	17.91	23	PASS
N48	30	10	CP-64QAM	H	Edge_1RB_Right	18.1	23	PASS
N48	30	10	CP-256QAM	H	Edge_1RB_Left	15.04	23	PASS
N48	30	10	CP-256QAM	H	Outer_Full	14.33	23	PASS
N48	30	10	CP-256QAM	H	Inner_Full	14.7	23	PASS
N48	30	10	CP-256QAM	H	Edge_1RB_Right	15.08	23	PASS
N48	30	15	DFT-BPSK	L	Edge_1RB_Left	20.82	23	PASS
N48	30	15	DFT-BPSK	L	Outer_Full	20.3	23	PASS
N48	30	15	DFT-BPSK	L	Inner_Full	21.91	23	PASS
N48	30	15	DFT-BPSK	L	Edge_1RB_Right	21.7	23	PASS
N48	30	15	DFT-QPSK	L	Edge_1RB_Left	21.18	23	PASS
N48	30	15	DFT-QPSK	L	Outer_Full	19.84	23	PASS
N48	30	15	DFT-QPSK	L	Inner_Full	21.9	23	PASS
N48	30	15	DFT-QPSK	L	Edge_1RB_Right	21.23	23	PASS
N48	30	15	DFT-16QAM	L	Edge_1RB_Left	20.29	23	PASS
N48	30	15	DFT-16QAM	L	Outer_Full	18.83	23	PASS
N48	30	15	DFT-16QAM	L	Inner_Full	20.97	23	PASS
N48	30	15	DFT-16QAM	L	Edge_1RB_Right	20.32	23	PASS
N48	30	15	DFT-64QAM	L	Edge_1RB_Left	19.89	23	PASS
N48	30	15	DFT-64QAM	L	Outer_Full	18.31	23	PASS

N48	30	15	DFT-64QAM	L	Inner_Full	19.27	23	PASS
N48	30	15	DFT-64QAM	L	Edge_1RB_Right	19.93	23	PASS
N48	30	15	DFT-256QAM	L	Edge_1RB_Left	17.74	23	PASS
N48	30	15	DFT-256QAM	L	Outer_Full	16.21	23	PASS
N48	30	15	DFT-256QAM	L	Inner_Full	17.19	23	PASS
N48	30	15	DFT-256QAM	L	Edge_1RB_Right	17.76	23	PASS
N48	30	15	CP-QPSK	L	Edge_1RB_Left	19.32	23	PASS
N48	30	15	CP-QPSK	L	Outer_Full	17.67	23	PASS
N48	30	15	CP-QPSK	L	Inner_Full	20.35	23	PASS
N48	30	15	CP-QPSK	L	Edge_1RB_Right	19.28	23	PASS
N48	30	15	CP-16QAM	L	Edge_1RB_Left	19.33	23	PASS
N48	30	15	CP-16QAM	L	Outer_Full	17.59	23	PASS
N48	30	15	CP-16QAM	L	Inner_Full	19.72	23	PASS
N48	30	15	CP-16QAM	L	Edge_1RB_Right	18.77	23	PASS
N48	30	15	CP-64QAM	L	Edge_1RB_Left	19.61	23	PASS
N48	30	15	CP-64QAM	L	Outer_Full	17.09	23	PASS
N48	30	15	CP-64QAM	L	Inner_Full	18.35	23	PASS
N48	30	15	CP-64QAM	L	Edge_1RB_Right	19.61	23	PASS
N48	30	15	CP-256QAM	L	Edge_1RB_Left	15.6	23	PASS
N48	30	15	CP-256QAM	L	Outer_Full	14.15	23	PASS
N48	30	15	CP-256QAM	L	Inner_Full	15.38	23	PASS
N48	30	15	CP-256QAM	L	Edge_1RB_Right	15.58	23	PASS
N48	30	15	DFT-BPSK	M	Edge_1RB_Left	21.33	23	PASS
N48	30	15	DFT-BPSK	M	Outer_Full	20.09	23	PASS
N48	30	15	DFT-BPSK	M	Inner_Full	21.54	23	PASS
N48	30	15	DFT-BPSK	M	Edge_1RB_Right	21.25	23	PASS
N48	30	15	DFT-QPSK	M	Edge_1RB_Left	20.76	23	PASS
N48	30	15	DFT-QPSK	M	Outer_Full	19.59	23	PASS
N48	30	15	DFT-QPSK	M	Inner_Full	21.58	23	PASS
N48	30	15	DFT-QPSK	M	Edge_1RB_Right	20.69	23	PASS
N48	30	15	DFT-16QAM	M	Edge_1RB_Left	20.25	23	PASS

N48	30	15	DFT-16QAM	M	Outer_Full	18.56	23	PASS
N48	30	15	DFT-16QAM	M	Inner_Full	20.49	23	PASS
N48	30	15	DFT-16QAM	M	Edge_1RB_Right	19.77	23	PASS
N48	30	15	DFT-64QAM	M	Edge_1RB_Left	19.25	23	PASS
N48	30	15	DFT-64QAM	M	Outer_Full	18.05	23	PASS
N48	30	15	DFT-64QAM	M	Inner_Full	18.93	23	PASS
N48	30	15	DFT-64QAM	M	Edge_1RB_Right	18.68	23	PASS
N48	30	15	DFT-256QAM	M	Edge_1RB_Left	17.26	23	PASS
N48	30	15	DFT-256QAM	M	Outer_Full	16.05	23	PASS
N48	30	15	DFT-256QAM	M	Inner_Full	16.82	23	PASS
N48	30	15	DFT-256QAM	M	Edge_1RB_Right	17.08	23	PASS
N48	30	15	CP-QPSK	M	Edge_1RB_Left	19.08	23	PASS
N48	30	15	CP-QPSK	M	Outer_Full	17.34	23	PASS
N48	30	15	CP-QPSK	M	Inner_Full	20	23	PASS
N48	30	15	CP-QPSK	M	Edge_1RB_Right	19.06	23	PASS
N48	30	15	CP-16QAM	M	Edge_1RB_Left	18.96	23	PASS
N48	30	15	CP-16QAM	M	Outer_Full	17.45	23	PASS
N48	30	15	CP-16QAM	M	Inner_Full	19.45	23	PASS
N48	30	15	CP-16QAM	M	Edge_1RB_Right	18.88	23	PASS
N48	30	15	CP-64QAM	M	Edge_1RB_Left	17.64	23	PASS
N48	30	15	CP-64QAM	M	Outer_Full	16.95	23	PASS
N48	30	15	CP-64QAM	M	Inner_Full	17.88	23	PASS
N48	30	15	CP-64QAM	M	Edge_1RB_Right	17.59	23	PASS
N48	30	15	CP-256QAM	M	Edge_1RB_Left	15.36	23	PASS
N48	30	15	CP-256QAM	M	Outer_Full	13.96	23	PASS
N48	30	15	CP-256QAM	M	Inner_Full	14.92	23	PASS
N48	30	15	CP-256QAM	M	Edge_1RB_Right	14.95	23	PASS
N48	30	15	DFT-BPSK	H	Edge_1RB_Left	21.12	23	PASS
N48	30	15	DFT-BPSK	H	Outer_Full	19.83	23	PASS
N48	30	15	DFT-BPSK	H	Inner_Full	21.39	23	PASS
N48	30	15	DFT-BPSK	H	Edge_1RB_Right	21.24	23	PASS

N48	30	15	DFT-QPSK	H	Edge_1RB_Left	20.54	23	PASS
N48	30	15	DFT-QPSK	H	Outer_Full	19.41	23	PASS
N48	30	15	DFT-QPSK	H	Inner_Full	21.37	23	PASS
N48	30	15	DFT-QPSK	H	Edge_1RB_Right	20.61	23	PASS
N48	30	15	DFT-16QAM	H	Edge_1RB_Left	19.78	23	PASS
N48	30	15	DFT-16QAM	H	Outer_Full	18.45	23	PASS
N48	30	15	DFT-16QAM	H	Inner_Full	20.39	23	PASS
N48	30	15	DFT-16QAM	H	Edge_1RB_Right	19.83	23	PASS
N48	30	15	DFT-64QAM	H	Edge_1RB_Left	19.05	23	PASS
N48	30	15	DFT-64QAM	H	Outer_Full	17.82	23	PASS
N48	30	15	DFT-64QAM	H	Inner_Full	18.73	23	PASS
N48	30	15	DFT-64QAM	H	Edge_1RB_Right	18.91	23	PASS
N48	30	15	DFT-256QAM	H	Edge_1RB_Left	16.85	23	PASS
N48	30	15	DFT-256QAM	H	Outer_Full	15.89	23	PASS
N48	30	15	DFT-256QAM	H	Inner_Full	16.71	23	PASS
N48	30	15	DFT-256QAM	H	Edge_1RB_Right	17.49	23	PASS
N48	30	15	CP-QPSK	H	Edge_1RB_Left	18.6	23	PASS
N48	30	15	CP-QPSK	H	Outer_Full	17.26	23	PASS
N48	30	15	CP-QPSK	H	Inner_Full	19.8	23	PASS
N48	30	15	CP-QPSK	H	Edge_1RB_Right	18.82	23	PASS
N48	30	15	CP-16QAM	H	Edge_1RB_Left	18.67	23	PASS
N48	30	15	CP-16QAM	H	Outer_Full	17.31	23	PASS
N48	30	15	CP-16QAM	H	Inner_Full	19.28	23	PASS
N48	30	15	CP-16QAM	H	Edge_1RB_Right	19.21	23	PASS
N48	30	15	CP-64QAM	H	Edge_1RB_Left	17.92	23	PASS
N48	30	15	CP-64QAM	H	Outer_Full	16.78	23	PASS
N48	30	15	CP-64QAM	H	Inner_Full	17.84	23	PASS
N48	30	15	CP-64QAM	H	Edge_1RB_Right	17.98	23	PASS
N48	30	15	CP-256QAM	H	Edge_1RB_Left	15.6	23	PASS
N48	30	15	CP-256QAM	H	Outer_Full	13.76	23	PASS
N48	30	15	CP-256QAM	H	Inner_Full	14.88	23	PASS

N48	30	15	CP-256QAM	H	Edge_1RB_Right	15.67	23	PASS
N48	30	20	DFT-BPSK	L	Edge_1RB_Left	21.87	23	PASS
N48	30	20	DFT-BPSK	L	Outer_Full	19.15	23	PASS
N48	30	20	DFT-BPSK	L	Inner_Full	21.55	23	PASS
N48	30	20	DFT-BPSK	L	Edge_1RB_Right	21.93	23	PASS
N48	30	20	DFT-QPSK	L	Edge_1RB_Left	21.19	23	PASS
N48	30	20	DFT-QPSK	L	Outer_Full	18.65	23	PASS
N48	30	20	DFT-QPSK	L	Inner_Full	21.61	23	PASS
N48	30	20	DFT-QPSK	L	Edge_1RB_Right	21.29	23	PASS
N48	30	20	DFT-16QAM	L	Edge_1RB_Left	20.25	23	PASS
N48	30	20	DFT-16QAM	L	Outer_Full	17.7	23	PASS
N48	30	20	DFT-16QAM	L	Inner_Full	20.65	23	PASS
N48	30	20	DFT-16QAM	L	Edge_1RB_Right	20.33	23	PASS
N48	30	20	DFT-64QAM	L	Edge_1RB_Left	19.59	23	PASS
N48	30	20	DFT-64QAM	L	Outer_Full	17.18	23	PASS
N48	30	20	DFT-64QAM	L	Inner_Full	19.12	23	PASS
N48	30	20	DFT-64QAM	L	Edge_1RB_Right	19.77	23	PASS
N48	30	20	DFT-256QAM	L	Edge_1RB_Left	17.85	23	PASS
N48	30	20	DFT-256QAM	L	Outer_Full	15.22	23	PASS
N48	30	20	DFT-256QAM	L	Inner_Full	17.08	23	PASS
N48	30	20	DFT-256QAM	L	Edge_1RB_Right	18.13	23	PASS
N48	30	20	CP-QPSK	L	Edge_1RB_Left	19.32	23	PASS
N48	30	20	CP-QPSK	L	Outer_Full	16.69	23	PASS
N48	30	20	CP-QPSK	L	Inner_Full	20.11	23	PASS
N48	30	20	CP-QPSK	L	Edge_1RB_Right	19.31	23	PASS
N48	30	20	CP-16QAM	L	Edge_1RB_Left	19.34	23	PASS
N48	30	20	CP-16QAM	L	Outer_Full	16.65	23	PASS
N48	30	20	CP-16QAM	L	Inner_Full	19.5	23	PASS
N48	30	20	CP-16QAM	L	Edge_1RB_Right	19.37	23	PASS
N48	30	20	CP-64QAM	L	Edge_1RB_Left	18.62	23	PASS
N48	30	20	CP-64QAM	L	Outer_Full	16.18	23	PASS

N48	30	20	CP-64QAM	L	Inner_Full	18.02	23	PASS
N48	30	20	CP-64QAM	L	Edge_1RB_Right	18.67	23	PASS
N48	30	20	CP-256QAM	L	Edge_1RB_Left	15.83	23	PASS
N48	30	20	CP-256QAM	L	Outer_Full	13.11	23	PASS
N48	30	20	CP-256QAM	L	Inner_Full	15.05	23	PASS
N48	30	20	CP-256QAM	L	Edge_1RB_Right	15.96	23	PASS
N48	30	20	DFT-BPSK	M	Edge_1RB_Left	21.49	23	PASS
N48	30	20	DFT-BPSK	M	Outer_Full	19.04	23	PASS
N48	30	20	DFT-BPSK	M	Inner_Full	21.28	23	PASS
N48	30	20	DFT-BPSK	M	Edge_1RB_Right	21.32	23	PASS
N48	30	20	DFT-QPSK	M	Edge_1RB_Left	20.92	23	PASS
N48	30	20	DFT-QPSK	M	Outer_Full	18.53	23	PASS
N48	30	20	DFT-QPSK	M	Inner_Full	21.26	23	PASS
N48	30	20	DFT-QPSK	M	Edge_1RB_Right	20.69	23	PASS
N48	30	20	DFT-16QAM	M	Edge_1RB_Left	19.87	23	PASS
N48	30	20	DFT-16QAM	M	Outer_Full	17.46	23	PASS
N48	30	20	DFT-16QAM	M	Inner_Full	20.25	23	PASS
N48	30	20	DFT-16QAM	M	Edge_1RB_Right	19.8	23	PASS
N48	30	20	DFT-64QAM	M	Edge_1RB_Left	19.95	23	PASS
N48	30	20	DFT-64QAM	M	Outer_Full	16.96	23	PASS
N48	30	20	DFT-64QAM	M	Inner_Full	18.77	23	PASS
N48	30	20	DFT-64QAM	M	Edge_1RB_Right	19.76	23	PASS
N48	30	20	DFT-256QAM	M	Edge_1RB_Left	17.45	23	PASS
N48	30	20	DFT-256QAM	M	Outer_Full	14.8	23	PASS
N48	30	20	DFT-256QAM	M	Inner_Full	16.85	23	PASS
N48	30	20	DFT-256QAM	M	Edge_1RB_Right	17.33	23	PASS
N48	30	20	CP-QPSK	M	Edge_1RB_Left	18.89	23	PASS
N48	30	20	CP-QPSK	M	Outer_Full	16.3	23	PASS
N48	30	20	CP-QPSK	M	Inner_Full	19.75	23	PASS
N48	30	20	CP-QPSK	M	Edge_1RB_Right	18.79	23	PASS
N48	30	20	CP-16QAM	M	Edge_1RB_Left	19.03	23	PASS

N48	30	20	CP-16QAM	M	Outer_Full	16.25	23	PASS
N48	30	20	CP-16QAM	M	Inner_Full	19.26	23	PASS
N48	30	20	CP-16QAM	M	Edge_1RB_Right	18.5	23	PASS
N48	30	20	CP-64QAM	M	Edge_1RB_Left	18.36	23	PASS
N48	30	20	CP-64QAM	M	Outer_Full	15.86	23	PASS
N48	30	20	CP-64QAM	M	Inner_Full	17.78	23	PASS
N48	30	20	CP-64QAM	M	Edge_1RB_Right	18.8	23	PASS
N48	30	20	CP-256QAM	M	Edge_1RB_Left	15.39	23	PASS
N48	30	20	CP-256QAM	M	Outer_Full	12.82	23	PASS
N48	30	20	CP-256QAM	M	Inner_Full	14.78	23	PASS
N48	30	20	CP-256QAM	M	Edge_1RB_Right	15.37	23	PASS
N48	30	20	DFT-BPSK	H	Edge_1RB_Left	21.29	23	PASS
N48	30	20	DFT-BPSK	H	Outer_Full	18.69	23	PASS
N48	30	20	DFT-BPSK	H	Inner_Full	21.01	23	PASS
N48	30	20	DFT-BPSK	H	Edge_1RB_Right	21.17	23	PASS
N48	30	20	DFT-QPSK	H	Edge_1RB_Left	20.63	23	PASS
N48	30	20	DFT-QPSK	H	Outer_Full	18.21	23	PASS
N48	30	20	DFT-QPSK	H	Inner_Full	21.04	23	PASS
N48	30	20	DFT-QPSK	H	Edge_1RB_Right	20.69	23	PASS
N48	30	20	DFT-16QAM	H	Edge_1RB_Left	19.9	23	PASS
N48	30	20	DFT-16QAM	H	Outer_Full	17.22	23	PASS
N48	30	20	DFT-16QAM	H	Inner_Full	19.99	23	PASS
N48	30	20	DFT-16QAM	H	Edge_1RB_Right	19.94	23	PASS
N48	30	20	DFT-64QAM	H	Edge_1RB_Left	19.5	23	PASS
N48	30	20	DFT-64QAM	H	Outer_Full	16.78	23	PASS
N48	30	20	DFT-64QAM	H	Inner_Full	18.53	23	PASS
N48	30	20	DFT-64QAM	H	Edge_1RB_Right	18.58	23	PASS
N48	30	20	DFT-256QAM	H	Edge_1RB_Left	17.12	23	PASS
N48	30	20	DFT-256QAM	H	Outer_Full	14.69	23	PASS
N48	30	20	DFT-256QAM	H	Inner_Full	16.42	23	PASS
N48	30	20	DFT-256QAM	H	Edge_1RB_Right	17.09	23	PASS

N48	30	20	CP-QPSK	H	Edge_1RB_Left	18.69	23	PASS
N48	30	20	CP-QPSK	H	Outer_Full	16.17	23	PASS
N48	30	20	CP-QPSK	H	Inner_Full	19.45	23	PASS
N48	30	20	CP-QPSK	H	Edge_1RB_Right	18.69	23	PASS
N48	30	20	CP-16QAM	H	Edge_1RB_Left	18.02	23	PASS
N48	30	20	CP-16QAM	H	Outer_Full	16.13	23	PASS
N48	30	20	CP-16QAM	H	Inner_Full	19.04	23	PASS
N48	30	20	CP-16QAM	H	Edge_1RB_Right	18.94	23	PASS
N48	30	20	CP-64QAM	H	Edge_1RB_Left	17.95	23	PASS
N48	30	20	CP-64QAM	H	Outer_Full	15.68	23	PASS
N48	30	20	CP-64QAM	H	Inner_Full	17.56	23	PASS
N48	30	20	CP-64QAM	H	Edge_1RB_Right	18.52	23	PASS
N48	30	20	CP-256QAM	H	Edge_1RB_Left	15.11	23	PASS
N48	30	20	CP-256QAM	H	Outer_Full	12.69	23	PASS
N48	30	20	CP-256QAM	H	Inner_Full	14.4	23	PASS
N48	30	20	CP-256QAM	H	Edge_1RB_Right	15.47	23	PASS
N48	30	30	DFT-BPSK	L	Edge_1RB_Left	21.96	23	PASS
N48	30	30	DFT-BPSK	L	Outer_Full	17.54	23	PASS
N48	30	30	DFT-BPSK	L	Inner_Full	20.92	23	PASS
N48	30	30	DFT-BPSK	L	Edge_1RB_Right	21.98	23	PASS
N48	30	30	DFT-QPSK	L	Edge_1RB_Left	21.28	23	PASS
N48	30	30	DFT-QPSK	L	Outer_Full	17.02	23	PASS
N48	30	30	DFT-QPSK	L	Inner_Full	20.94	23	PASS
N48	30	30	DFT-QPSK	L	Edge_1RB_Right	21.54	23	PASS
N48	30	30	DFT-16QAM	L	Edge_1RB_Left	20.41	23	PASS
N48	30	30	DFT-16QAM	L	Outer_Full	16.15	23	PASS
N48	30	30	DFT-16QAM	L	Inner_Full	19.9	23	PASS
N48	30	30	DFT-16QAM	L	Edge_1RB_Right	20.54	23	PASS
N48	30	30	DFT-64QAM	L	Edge_1RB_Left	20.39	23	PASS
N48	30	30	DFT-64QAM	L	Outer_Full	15.6	23	PASS
N48	30	30	DFT-64QAM	L	Inner_Full	18.54	23	PASS
N48	30	30	DFT-64QAM	L	Edge_1RB_Right	20.5	23	PASS

N48	30	30	DFT-256QAM	L	Edge_1RB_Left	17.99	23	PASS
N48	30	30	DFT-256QAM	L	Outer_Full	13.57	23	PASS
N48	30	30	DFT-256QAM	L	Inner_Full	16.4	23	PASS
N48	30	30	DFT-256QAM	L	Edge_1RB_Right	17.84	23	PASS
N48	30	30	CP-QPSK	L	Edge_1RB_Left	19.32	23	PASS
N48	30	30	CP-QPSK	L	Outer_Full	14.84	23	PASS
N48	30	30	CP-QPSK	L	Inner_Full	19.21	23	PASS
N48	30	30	CP-QPSK	L	Edge_1RB_Right	19.47	23	PASS
N48	30	30	CP-16QAM	L	Edge_1RB_Left	19.31	23	PASS
N48	30	30	CP-16QAM	L	Outer_Full	14.89	23	PASS
N48	30	30	CP-16QAM	L	Inner_Full	18.73	23	PASS
N48	30	30	CP-16QAM	L	Edge_1RB_Right	19.41	23	PASS
N48	30	30	CP-64QAM	L	Edge_1RB_Left	19.37	23	PASS
N48	30	30	CP-64QAM	L	Outer_Full	14.41	23	PASS
N48	30	30	CP-64QAM	L	Inner_Full	17.26	23	PASS
N48	30	30	CP-64QAM	L	Edge_1RB_Right	19.2	23	PASS
N48	30	30	CP-256QAM	L	Edge_1RB_Left	15.57	23	PASS
N48	30	30	CP-256QAM	L	Outer_Full	11.35	23	PASS
N48	30	30	CP-256QAM	L	Inner_Full	14.21	23	PASS
N48	30	30	CP-256QAM	L	Edge_1RB_Right	15.9	23	PASS
N48	30	30	DFT-BPSK	M	Edge_1RB_Left	21.61	23	PASS
N48	30	30	DFT-BPSK	M	Outer_Full	17.41	23	PASS
N48	30	30	DFT-BPSK	M	Inner_Full	20.73	23	PASS
N48	30	30	DFT-BPSK	M	Edge_1RB_Right	21.32	23	PASS
N48	30	30	DFT-QPSK	M	Edge_1RB_Left	20.99	23	PASS
N48	30	30	DFT-QPSK	M	Outer_Full	16.82	23	PASS
N48	30	30	DFT-QPSK	M	Inner_Full	20.68	23	PASS
N48	30	30	DFT-QPSK	M	Edge_1RB_Right	20.91	23	PASS
N48	30	30	DFT-16QAM	M	Edge_1RB_Left	19.9	23	PASS
N48	30	30	DFT-16QAM	M	Outer_Full	15.74	23	PASS
N48	30	30	DFT-16QAM	M	Inner_Full	19.75	23	PASS

N48	30	30	DFT-16QAM	M	Edge_1RB_Right	20.1	23	PASS
N48	30	30	DFT-64QAM	M	Edge_1RB_Left	19.48	23	PASS
N48	30	30	DFT-64QAM	M	Outer_Full	15.29	23	PASS
N48	30	30	DFT-64QAM	M	Inner_Full	18.24	23	PASS
N48	30	30	DFT-64QAM	M	Edge_1RB_Right	19.25	23	PASS
N48	30	30	DFT-256QAM	M	Edge_1RB_Left	17.49	23	PASS
N48	30	30	DFT-256QAM	M	Outer_Full	13.22	23	PASS
N48	30	30	DFT-256QAM	M	Inner_Full	16.17	23	PASS
N48	30	30	DFT-256QAM	M	Edge_1RB_Right	17.33	23	PASS
N48	30	30	CP-QPSK	M	Edge_1RB_Left	19.07	23	PASS
N48	30	30	CP-QPSK	M	Outer_Full	14.65	23	PASS
N48	30	30	CP-QPSK	M	Inner_Full	18.96	23	PASS
N48	30	30	CP-QPSK	M	Edge_1RB_Right	18.87	23	PASS
N48	30	30	CP-16QAM	M	Edge_1RB_Left	19.44	23	PASS
N48	30	30	CP-16QAM	M	Outer_Full	14.72	23	PASS
N48	30	30	CP-16QAM	M	Inner_Full	18.37	23	PASS
N48	30	30	CP-16QAM	M	Edge_1RB_Right	18.57	23	PASS
N48	30	30	CP-64QAM	M	Edge_1RB_Left	18.79	23	PASS
N48	30	30	CP-64QAM	M	Outer_Full	14.27	23	PASS
N48	30	30	CP-64QAM	M	Inner_Full	16.93	23	PASS
N48	30	30	CP-64QAM	M	Edge_1RB_Right	18.51	23	PASS
N48	30	30	CP-256QAM	M	Edge_1RB_Left	15.54	23	PASS
N48	30	30	CP-256QAM	M	Outer_Full	11.12	23	PASS
N48	30	30	CP-256QAM	M	Inner_Full	13.92	23	PASS
N48	30	30	CP-256QAM	M	Edge_1RB_Right	15.41	23	PASS
N48	30	30	DFT-BPSK	H	Edge_1RB_Left	21.38	23	PASS
N48	30	30	DFT-BPSK	H	Outer_Full	17.33	23	PASS
N48	30	30	DFT-BPSK	H	Inner_Full	20.57	23	PASS
N48	30	30	DFT-BPSK	H	Edge_1RB_Right	21.37	23	PASS
N48	30	30	DFT-QPSK	H	Edge_1RB_Left	-41.11	23	PASS
N48	30	30	DFT-QPSK	H	Outer_Full	16.74	23	PASS

N48	30	30	DFT-QPSK	H	Inner_Full	20.41	23	PASS
N48	30	30	DFT-QPSK	H	Edge_1RB_Right	20.78	23	PASS
N48	30	30	DFT-16QAM	H	Edge_1RB_Left	19.43	23	PASS
N48	30	30	DFT-16QAM	H	Outer_Full	15.65	23	PASS
N48	30	30	DFT-16QAM	H	Inner_Full	19.45	23	PASS
N48	30	30	DFT-16QAM	H	Edge_1RB_Right	19.39	23	PASS
N48	30	30	DFT-64QAM	H	Edge_1RB_Left	20.3	23	PASS
N48	30	30	DFT-64QAM	H	Outer_Full	15.11	23	PASS
N48	30	30	DFT-64QAM	H	Inner_Full	17.92	23	PASS
N48	30	30	DFT-64QAM	H	Edge_1RB_Right	19.51	23	PASS
N48	30	30	DFT-256QAM	H	Edge_1RB_Left	17.25	23	PASS
N48	30	30	DFT-256QAM	H	Outer_Full	13.18	23	PASS
N48	30	30	DFT-256QAM	H	Inner_Full	15.86	23	PASS
N48	30	30	DFT-256QAM	H	Edge_1RB_Right	17.47	23	PASS
N48	30	30	CP-QPSK	H	Edge_1RB_Left	18.82	23	PASS
N48	30	30	CP-QPSK	H	Outer_Full	14.5	23	PASS
N48	30	30	CP-QPSK	H	Inner_Full	18.79	23	PASS
N48	30	30	CP-QPSK	H	Edge_1RB_Right	18.76	23	PASS
N48	30	30	CP-16QAM	H	Edge_1RB_Left	18.85	23	PASS
N48	30	30	CP-16QAM	H	Outer_Full	14.48	23	PASS
N48	30	30	CP-16QAM	H	Inner_Full	18.33	23	PASS
N48	30	30	CP-16QAM	H	Edge_1RB_Right	18.86	23	PASS
N48	30	30	CP-64QAM	H	Edge_1RB_Left	19.28	23	PASS
N48	30	30	CP-64QAM	H	Outer_Full	14.1	23	PASS
N48	30	30	CP-64QAM	H	Inner_Full	16.92	23	PASS
N48	30	30	CP-64QAM	H	Edge_1RB_Right	19.15	23	PASS
N48	30	30	CP-256QAM	H	Edge_1RB_Left	15.21	23	PASS
N48	30	30	CP-256QAM	H	Outer_Full	11	23	PASS
N48	30	30	CP-256QAM	H	Inner_Full	13.75	23	PASS
N48	30	30	CP-256QAM	H	Edge_1RB_Right	15.15	23	PASS
N48	30	40	DFT-BPSK	L	Edge_1RB_Left	21.82	23	PASS

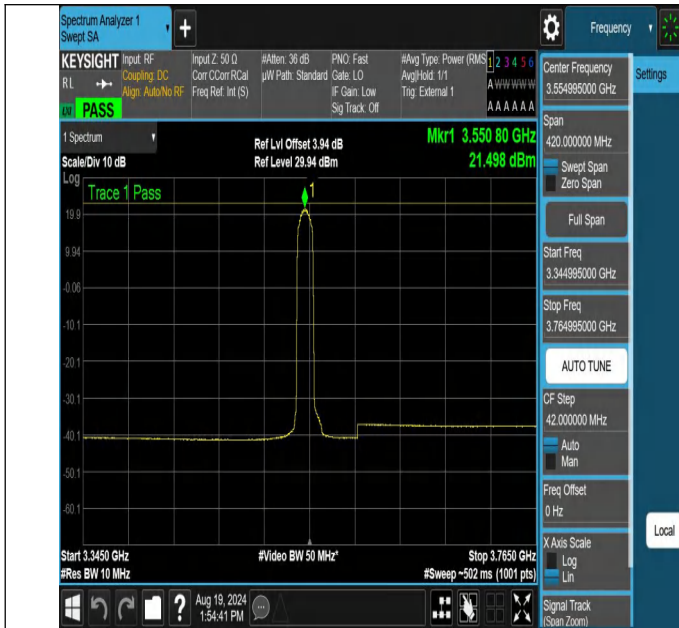
N48	30	40	DFT-BPSK	L	Outer_Full	16.47	23	PASS
N48	30	40	DFT-BPSK	L	Inner_Full	19.75	23	PASS
N48	30	40	DFT-BPSK	L	Edge_1RB_Right	22.03	23	PASS
N48	30	40	DFT-QPSK	L	Edge_1RB_Left	21.31	23	PASS
N48	30	40	DFT-QPSK	L	Outer_Full	16	23	PASS
N48	30	40	DFT-QPSK	L	Inner_Full	19.82	23	PASS
N48	30	40	DFT-QPSK	L	Edge_1RB_Right	21.57	23	PASS
N48	30	40	DFT-16QAM	L	Edge_1RB_Left	20.42	23	PASS
N48	30	40	DFT-16QAM	L	Outer_Full	14.91	23	PASS
N48	30	40	DFT-16QAM	L	Inner_Full	18.83	23	PASS
N48	30	40	DFT-16QAM	L	Edge_1RB_Right	20.89	23	PASS
N48	30	40	DFT-64QAM	L	Edge_1RB_Left	19.79	23	PASS
N48	30	40	DFT-64QAM	L	Outer_Full	14.53	23	PASS
N48	30	40	DFT-64QAM	L	Inner_Full	17.28	23	PASS
N48	30	40	DFT-64QAM	L	Edge_1RB_Right	19.95	23	PASS
N48	30	40	DFT-256QAM	L	Edge_1RB_Left	17.73	23	PASS
N48	30	40	DFT-256QAM	L	Outer_Full	12.41	23	PASS
N48	30	40	DFT-256QAM	L	Inner_Full	15.34	23	PASS
N48	30	40	DFT-256QAM	L	Edge_1RB_Right	18.03	23	PASS
N48	30	40	CP-QPSK	L	Edge_1RB_Left	19.42	23	PASS
N48	30	40	CP-QPSK	L	Outer_Full	13.76	23	PASS
N48	30	40	CP-QPSK	L	Inner_Full	18	23	PASS
N48	30	40	CP-QPSK	L	Edge_1RB_Right	19.52	23	PASS
N48	30	40	CP-16QAM	L	Edge_1RB_Left	19.64	23	PASS
N48	30	40	CP-16QAM	L	Outer_Full	13.6	23	PASS
N48	30	40	CP-16QAM	L	Inner_Full	17.62	23	PASS
N48	30	40	CP-16QAM	L	Edge_1RB_Right	20.09	23	PASS
N48	30	40	CP-64QAM	L	Edge_1RB_Left	18.31	23	PASS
N48	30	40	CP-64QAM	L	Outer_Full	13.28	23	PASS
N48	30	40	CP-64QAM	L	Inner_Full	15.96	23	PASS
N48	30	40	CP-64QAM	L	Edge_1RB_Right	19.19	23	PASS

N48	30	40	CP-256QAM	L	Edge_1RB_Left	15.74	23	PASS
N48	30	40	CP-256QAM	L	Outer_Full	10.22	23	PASS
N48	30	40	CP-256QAM	L	Inner_Full	12.93	23	PASS
N48	30	40	CP-256QAM	L	Edge_1RB_Right	16.08	23	PASS
N48	30	40	DFT-BPSK	M	Edge_1RB_Left	21.78	23	PASS
N48	30	40	DFT-BPSK	M	Outer_Full	16.35	23	PASS
N48	30	40	DFT-BPSK	M	Inner_Full	19.59	23	PASS
N48	30	40	DFT-BPSK	M	Edge_1RB_Right	21.47	23	PASS
N48	30	40	DFT-QPSK	M	Edge_1RB_Left	21.3	23	PASS
N48	30	40	DFT-QPSK	M	Outer_Full	15.64	23	PASS
N48	30	40	DFT-QPSK	M	Inner_Full	19.49	23	PASS
N48	30	40	DFT-QPSK	M	Edge_1RB_Right	20.82	23	PASS
N48	30	40	DFT-16QAM	M	Edge_1RB_Left	20.35	23	PASS
N48	30	40	DFT-16QAM	M	Outer_Full	14.76	23	PASS
N48	30	40	DFT-16QAM	M	Inner_Full	18.5	23	PASS
N48	30	40	DFT-16QAM	M	Edge_1RB_Right	19.81	23	PASS
N48	30	40	DFT-64QAM	M	Edge_1RB_Left	19.56	23	PASS
N48	30	40	DFT-64QAM	M	Outer_Full	14.12	23	PASS
N48	30	40	DFT-64QAM	M	Inner_Full	16.94	23	PASS
N48	30	40	DFT-64QAM	M	Edge_1RB_Right	19.2	23	PASS
N48	30	40	DFT-256QAM	M	Edge_1RB_Left	17.4	23	PASS
N48	30	40	DFT-256QAM	M	Outer_Full	12.23	23	PASS
N48	30	40	DFT-256QAM	M	Inner_Full	15.04	23	PASS
N48	30	40	DFT-256QAM	M	Edge_1RB_Right	17.71	23	PASS
N48	30	40	CP-QPSK	M	Edge_1RB_Left	19.09	23	PASS
N48	30	40	CP-QPSK	M	Outer_Full	13.54	23	PASS
N48	30	40	CP-QPSK	M	Inner_Full	17.75	23	PASS
N48	30	40	CP-QPSK	M	Edge_1RB_Right	19.07	23	PASS
N48	30	40	CP-16QAM	M	Edge_1RB_Left	19.66	23	PASS
N48	30	40	CP-16QAM	M	Outer_Full	13.58	23	PASS
N48	30	40	CP-16QAM	M	Inner_Full	17.35	23	PASS

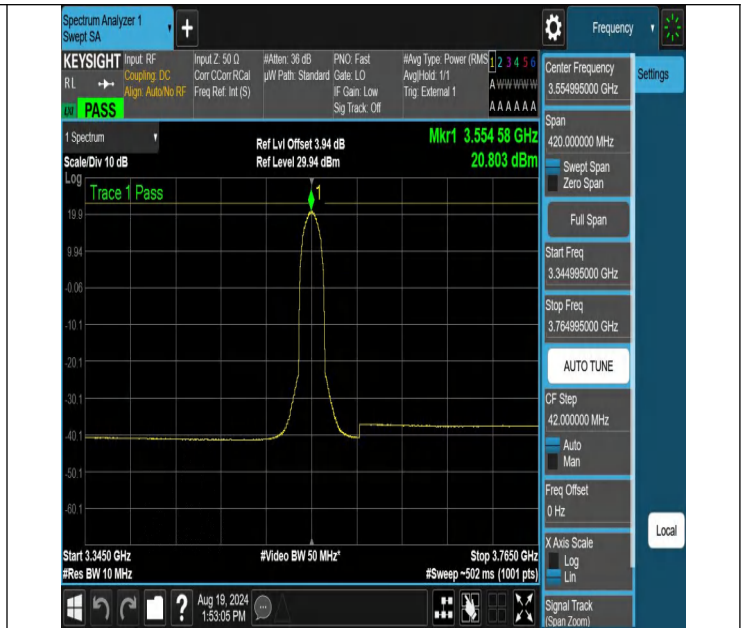
N48	30	40	CP-16QAM	M	Edge_1RB_Right	19.39	23	PASS
N48	30	40	CP-64QAM	M	Edge_1RB_Left	18.77	23	PASS
N48	30	40	CP-64QAM	M	Outer_Full	12.97	23	PASS
N48	30	40	CP-64QAM	M	Inner_Full	15.75	23	PASS
N48	30	40	CP-64QAM	M	Edge_1RB_Right	18.05	23	PASS
N48	30	40	CP-256QAM	M	Edge_1RB_Left	16.12	23	PASS
N48	30	40	CP-256QAM	M	Outer_Full	9.96	23	PASS
N48	30	40	CP-256QAM	M	Inner_Full	12.78	23	PASS
N48	30	40	CP-256QAM	M	Edge_1RB_Right	15.86	23	PASS
N48	30	40	DFT-BPSK	H	Edge_1RB_Left	21.47	23	PASS
N48	30	40	DFT-BPSK	H	Outer_Full	16.02	23	PASS
N48	30	40	DFT-BPSK	H	Inner_Full	19.36	23	PASS
N48	30	40	DFT-BPSK	H	Edge_1RB_Right	21.49	23	PASS
N48	30	40	DFT-QPSK	H	Edge_1RB_Left	20.86	23	PASS
N48	30	40	DFT-QPSK	H	Outer_Full	15.57	23	PASS
N48	30	40	DFT-QPSK	H	Inner_Full	19.35	23	PASS
N48	30	40	DFT-QPSK	H	Edge_1RB_Right	20.92	23	PASS
N48	30	40	DFT-16QAM	H	Edge_1RB_Left	19.89	23	PASS
N48	30	40	DFT-16QAM	H	Outer_Full	14.42	23	PASS
N48	30	40	DFT-16QAM	H	Inner_Full	18.37	23	PASS
N48	30	40	DFT-16QAM	H	Edge_1RB_Right	20.19	23	PASS
N48	30	40	DFT-64QAM	H	Edge_1RB_Left	19.26	23	PASS
N48	30	40	DFT-64QAM	H	Outer_Full	13.97	23	PASS
N48	30	40	DFT-64QAM	H	Inner_Full	16.77	23	PASS
N48	30	40	DFT-64QAM	H	Edge_1RB_Right	19.32	23	PASS
N48	30	40	DFT-256QAM	H	Edge_1RB_Left	17.64	23	PASS
N48	30	40	DFT-256QAM	H	Outer_Full	11.93	23	PASS
N48	30	40	DFT-256QAM	H	Inner_Full	14.79	23	PASS
N48	30	40	DFT-256QAM	H	Edge_1RB_Right	17.7	23	PASS
N48	30	40	CP-QPSK	H	Edge_1RB_Left	18.91	23	PASS
N48	30	40	CP-QPSK	H	Outer_Full	13.2	23	PASS

N48	30	40	CP-QPSK	H	Inner_Full	17.55	23	PASS
N48	30	40	CP-QPSK	H	Edge_1RB_Right	18.93	23	PASS
N48	30	40	CP-16QAM	H	Edge_1RB_Left	19.02	23	PASS
N48	30	40	CP-16QAM	H	Outer_Full	13.19	23	PASS
N48	30	40	CP-16QAM	H	Inner_Full	17.06	23	PASS
N48	30	40	CP-16QAM	H	Edge_1RB_Right	18.93	23	PASS
N48	30	40	CP-64QAM	H	Edge_1RB_Left	18.23	23	PASS
N48	30	40	CP-64QAM	H	Outer_Full	12.62	23	PASS
N48	30	40	CP-64QAM	H	Inner_Full	15.56	23	PASS
N48	30	40	CP-64QAM	H	Edge_1RB_Right	18.31	23	PASS
N48	30	40	CP-256QAM	H	Edge_1RB_Left	15.47	23	PASS
N48	30	40	CP-256QAM	H	Outer_Full	9.71	23	PASS
N48	30	40	CP-256QAM	H	Inner_Full	12.55	23	PASS
N48	30	40	CP-256QAM	H	Edge_1RB_Right	15.57	23	PASS

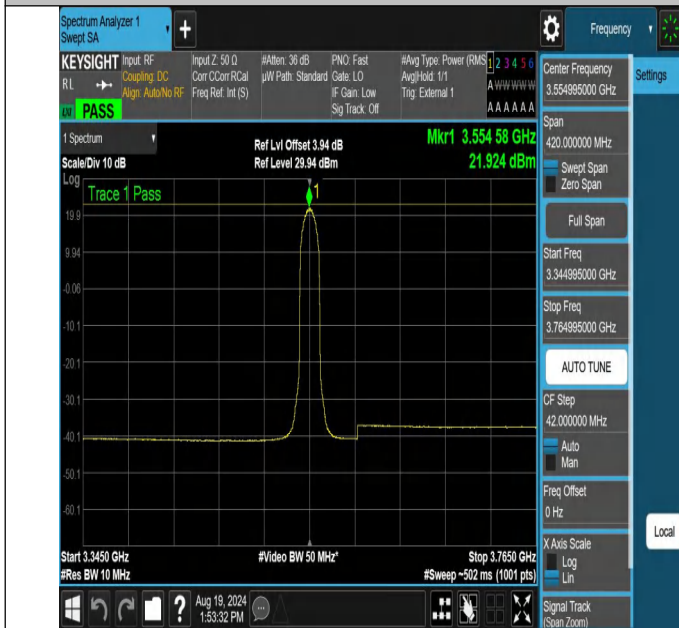
Test Graphs



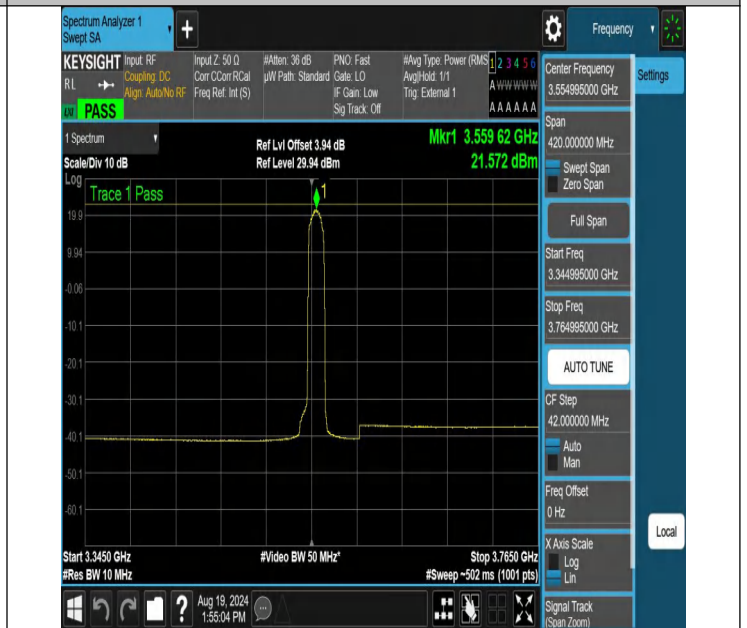
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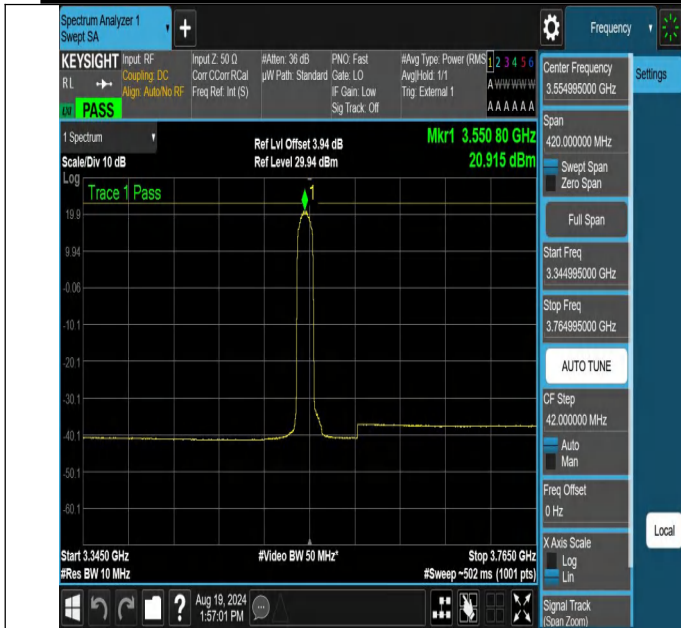
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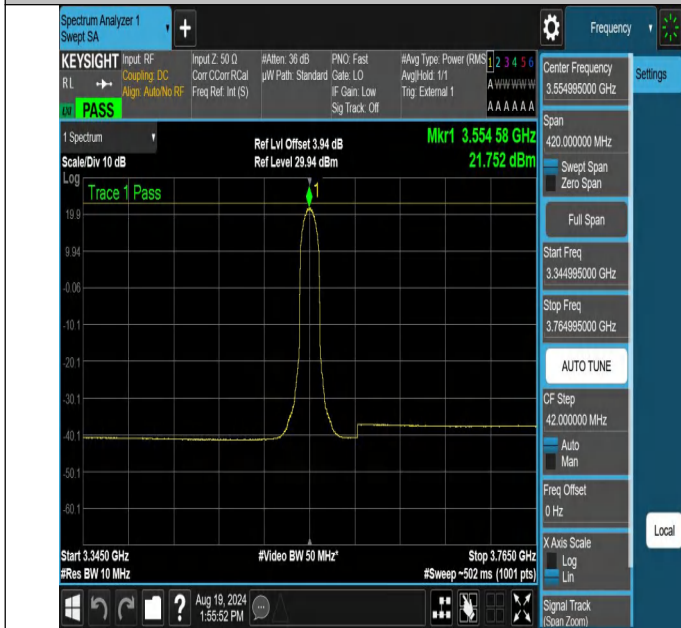
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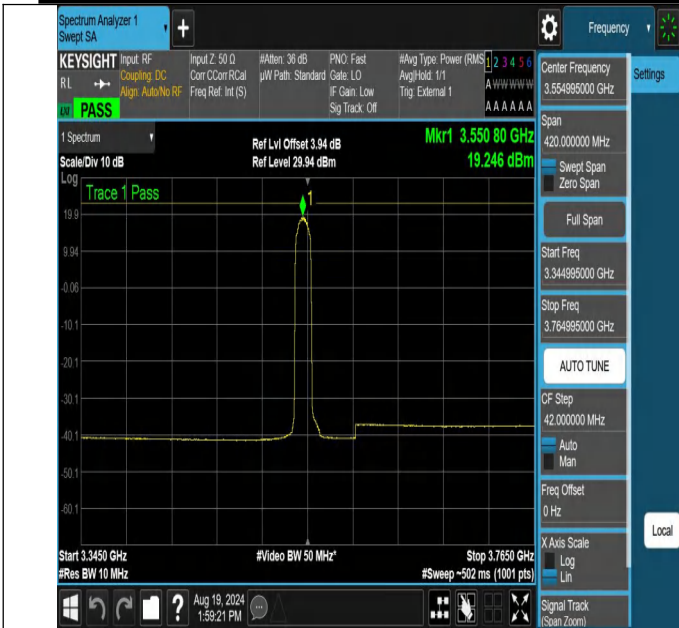
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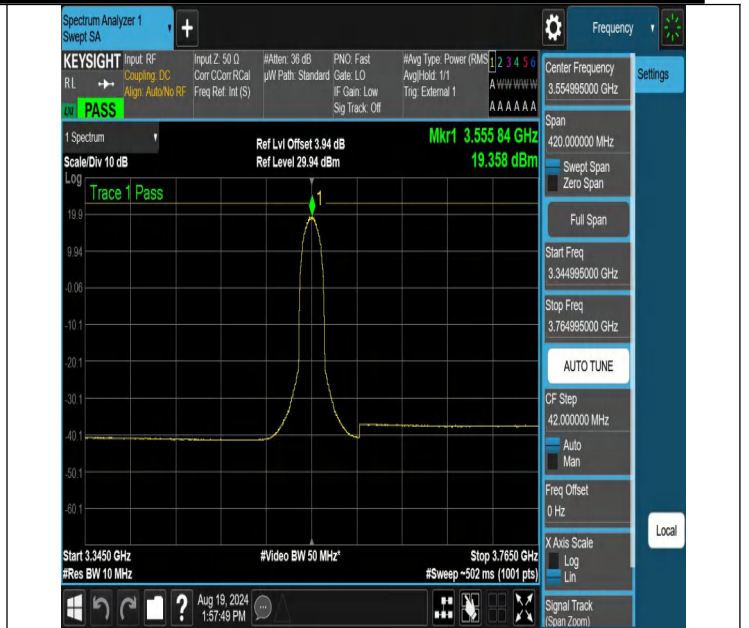
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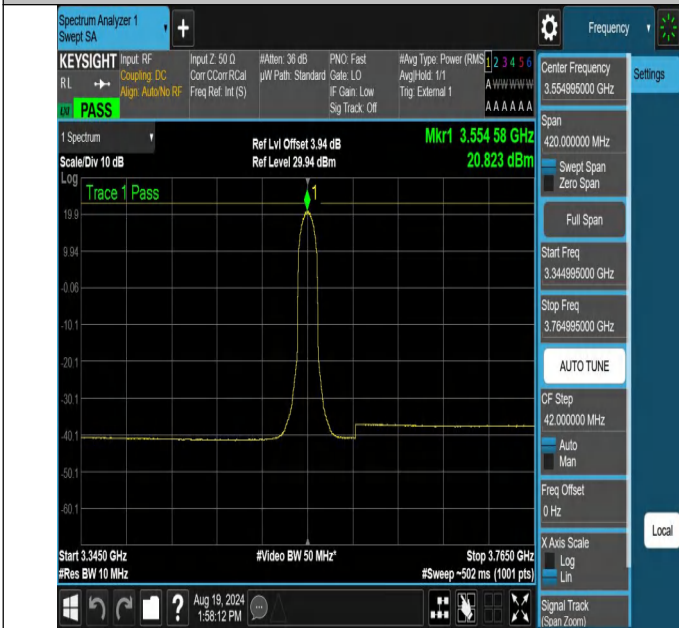
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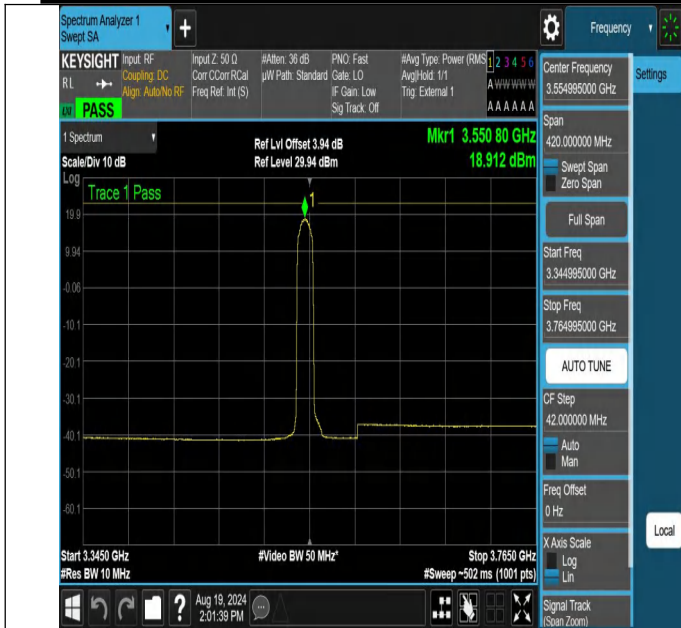
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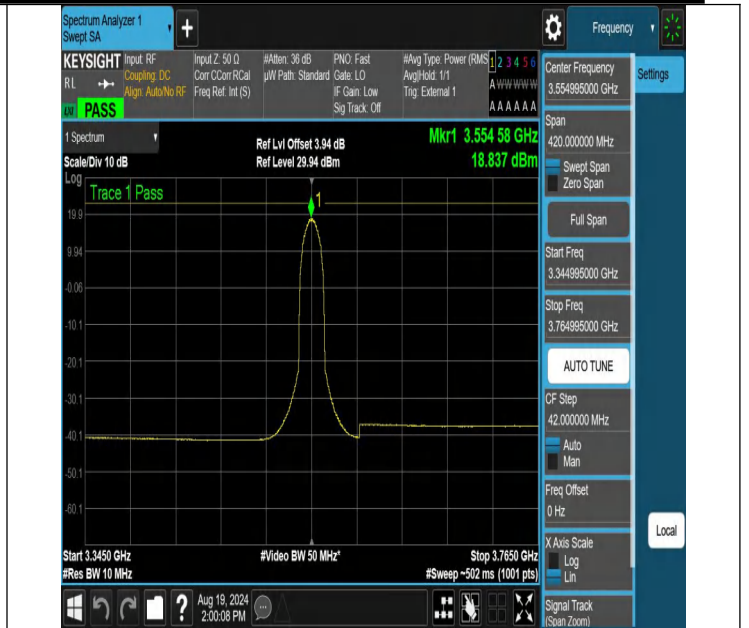
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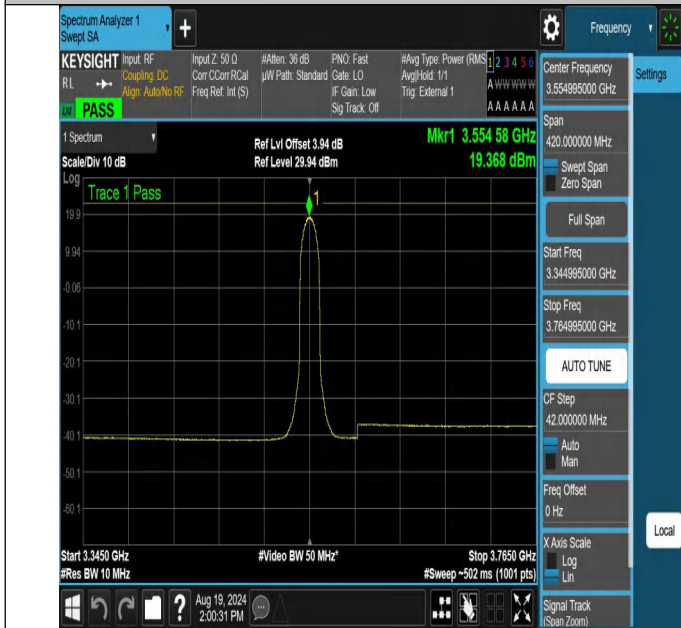
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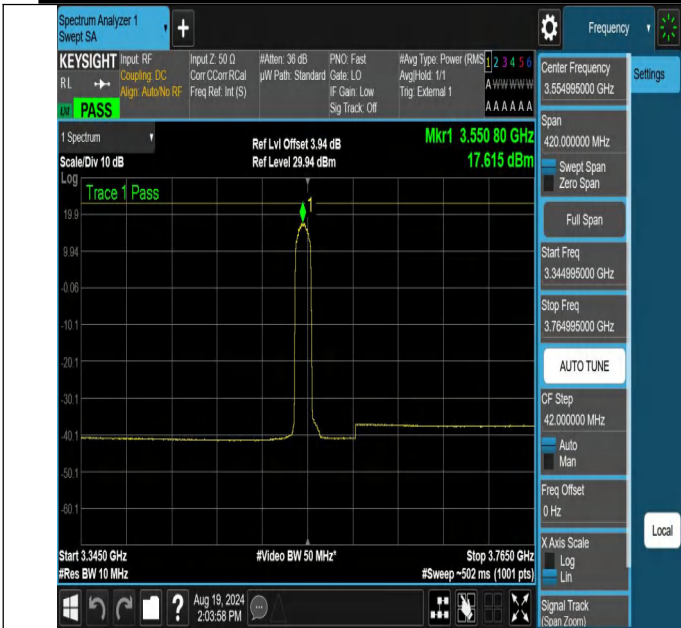
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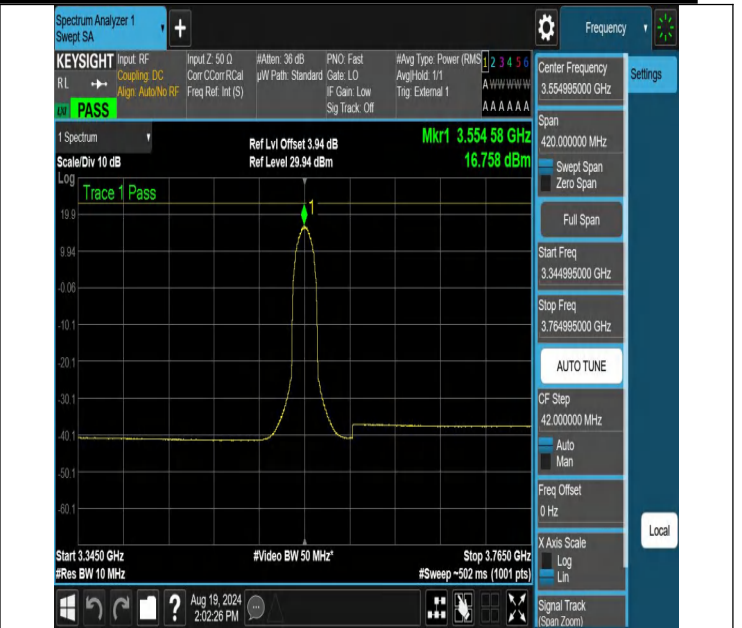
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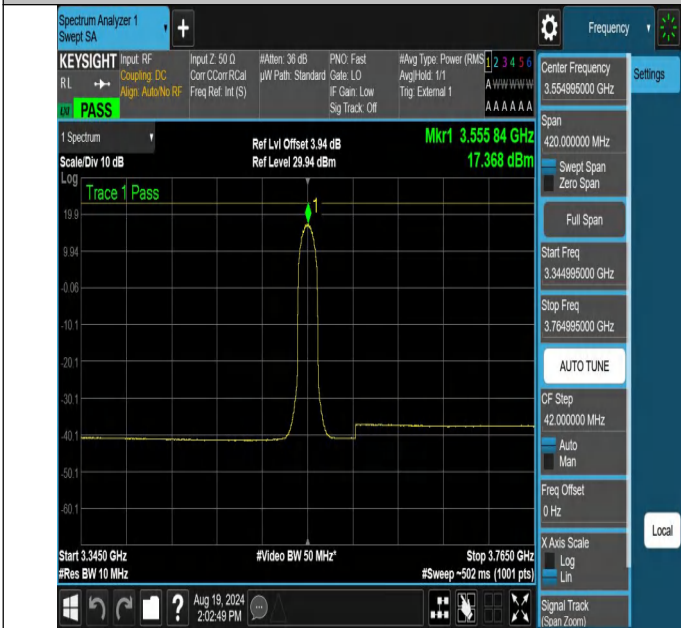
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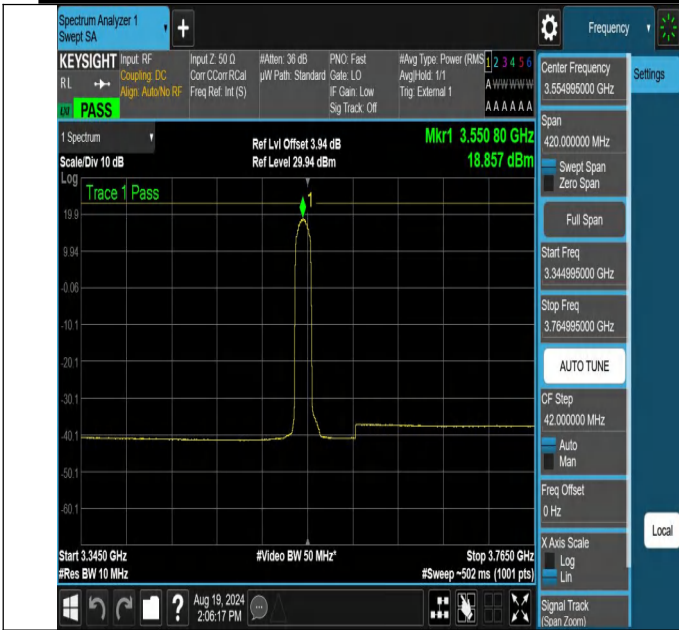
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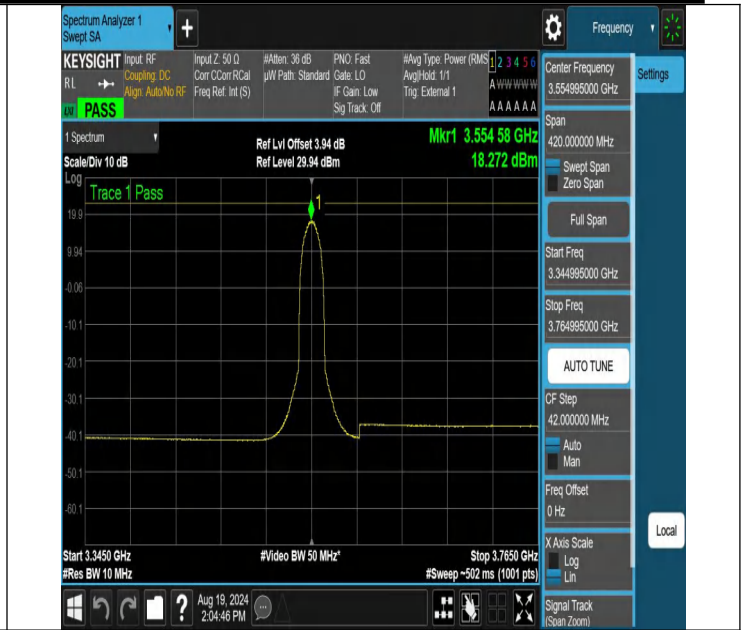
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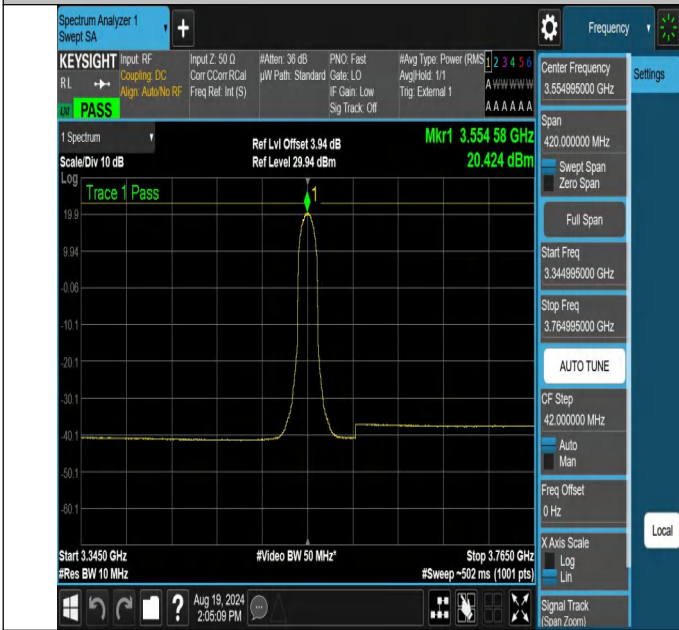
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64.995-Ant1-PASS



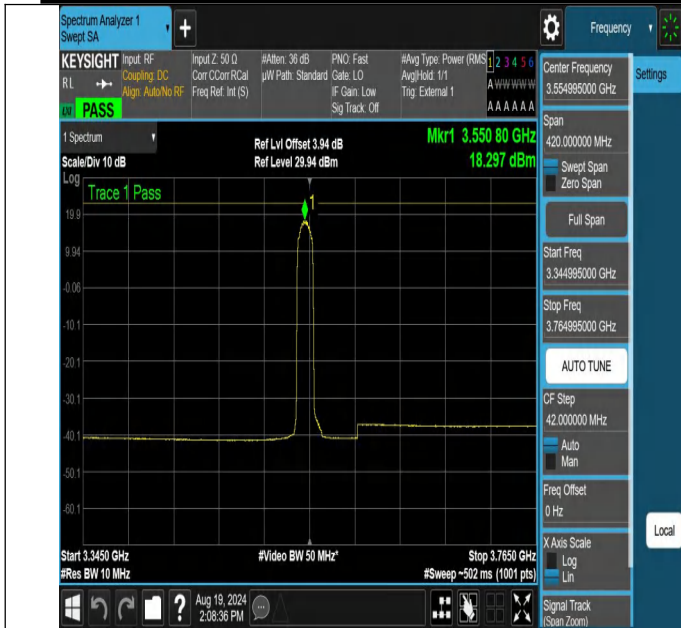
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5-Ant1-PASS



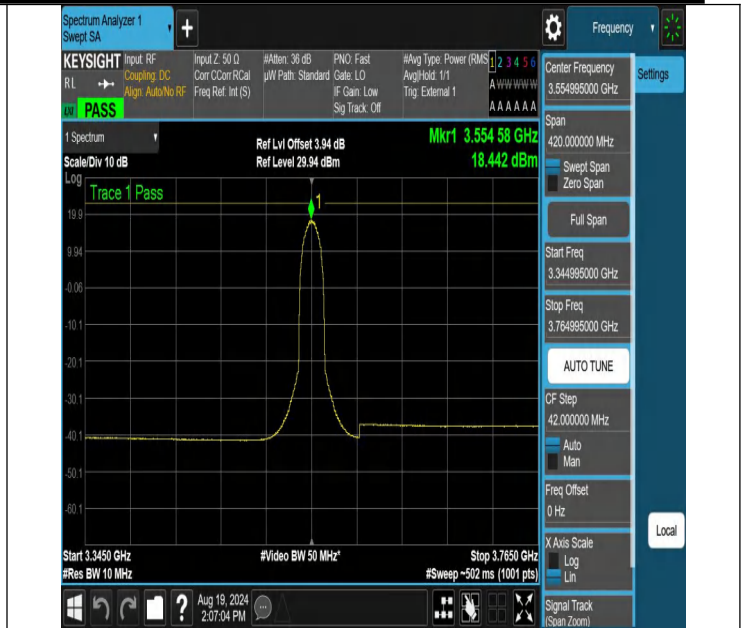
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5-Ant1-PASS



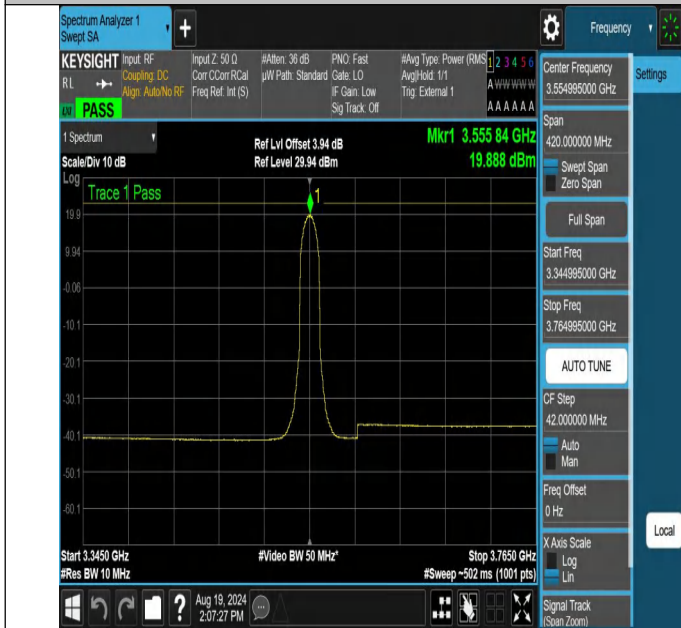
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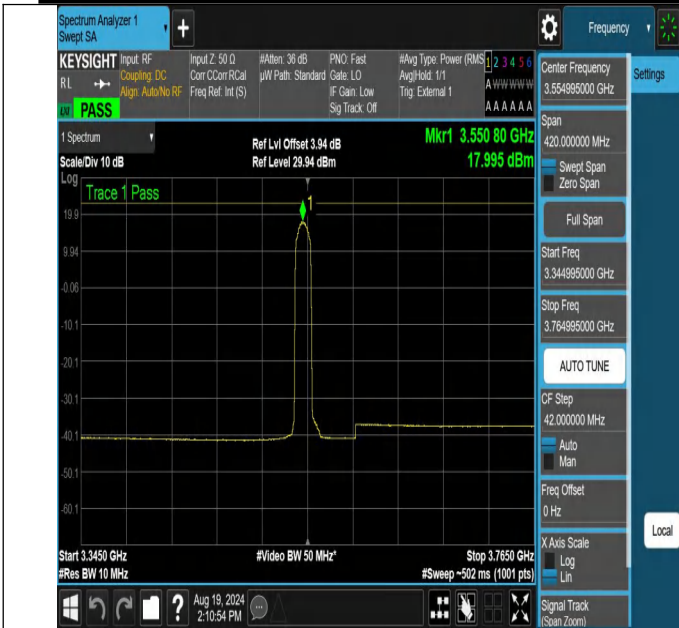
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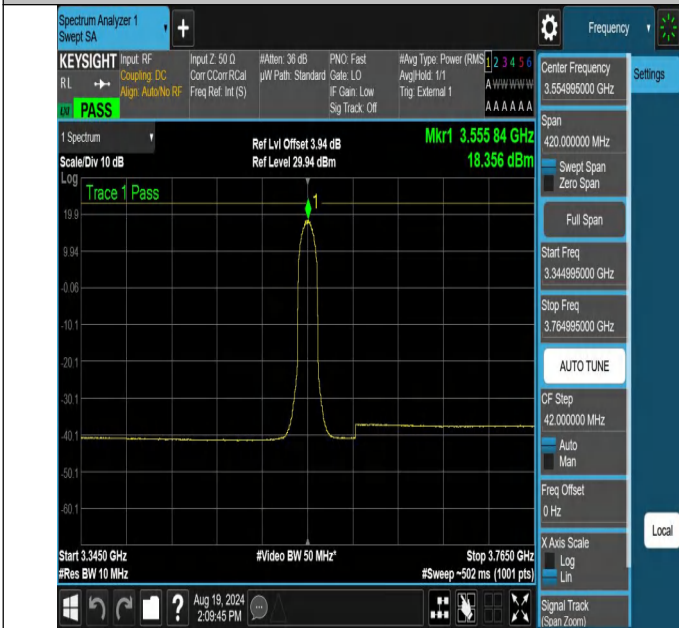
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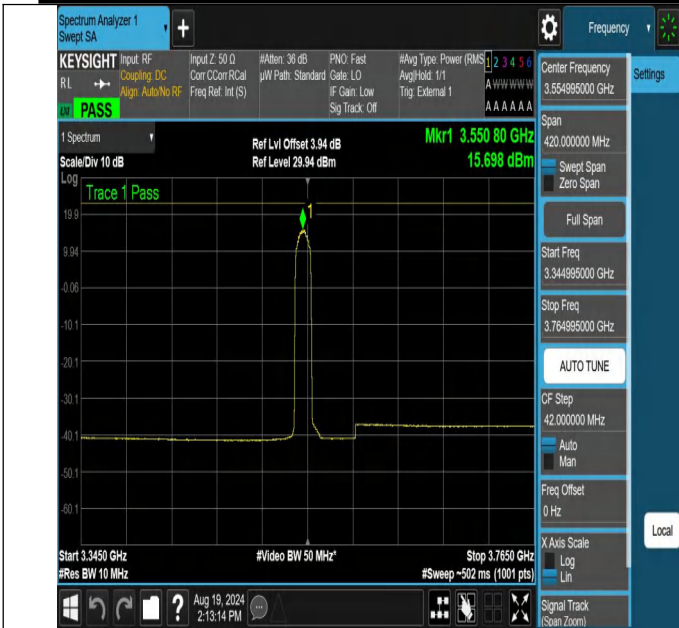
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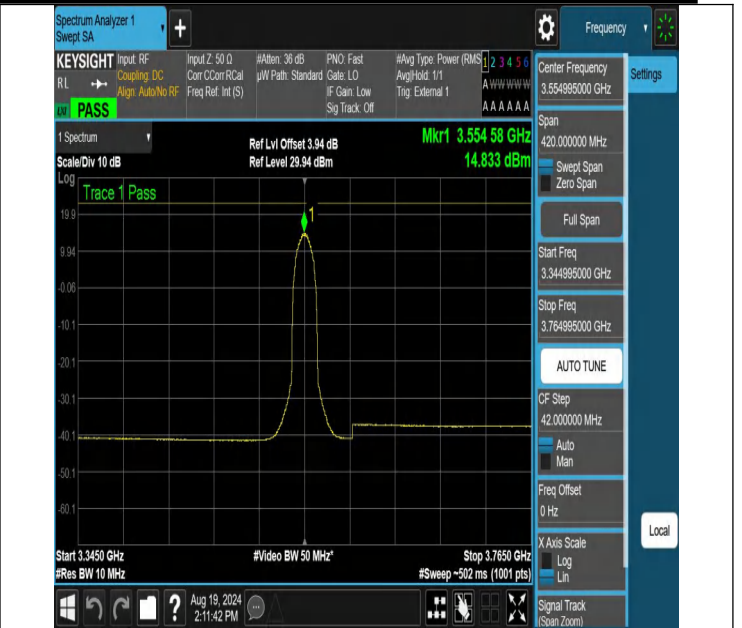
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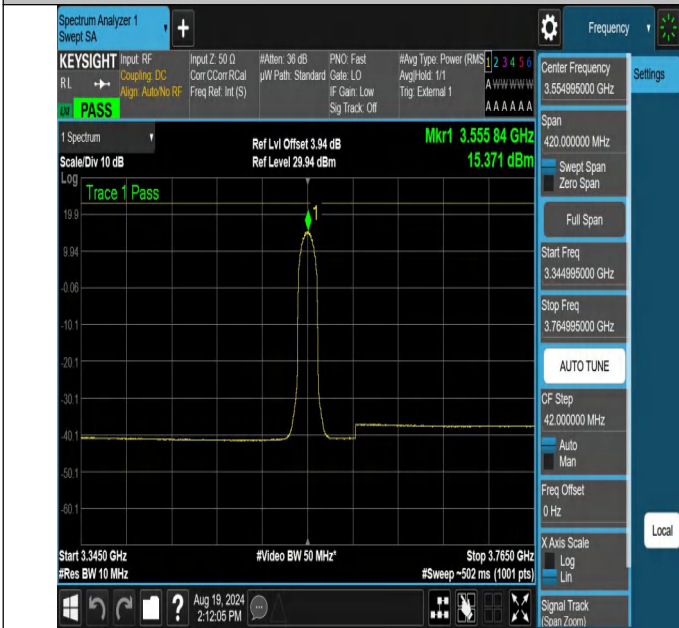
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NTNV-N48-PC3-30-10-L-CP-256QAM-Edge_1RB_Left-3344.995-3764.995-Ant1-PASS



NTNV-N48-PC3-30-10-L-CP-256QAM-Outer_Full-3344.995-3764.995-Ant1-PASS



NTNV-N48-PC3-30-10-L-CP-256QAM-Inner_Full-3344.995-3764.995-Ant1-PASS



NTNV-N48-PC3-30-10-L-CP-256QAM-Edge_1RB_Right-3344.995-3764.995-Ant1-PASS