

Effective (Isotropic) Radiated Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Limit (dBm)	Verdict
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	22.26	23.63	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	22.1	23.47	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	21.57	22.94	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+L	Inner_Full	22.76	24.13	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+M	Inner_Full	22.59	23.96	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+H	Inner_Full	22.07	23.44	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.33	23.7	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.57	22.94	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.53	22.9	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.37	23.74	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.62	22.99	30	PASS
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.55	22.92	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+L	Outer_Full	21.79	23.16	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+M	Outer_Full	21.53	22.9	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+H	Outer_Full	21.29	22.66	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+L	Inner_Full	22.87	24.24	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+M	Inner_Full	22.5	23.87	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+H	Inner_Full	22.25	23.62	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	21.68	23.05	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	21.62	22.99	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	21.02	22.39	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	22.26	23.63	30	PASS

DC_12A_n66A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	21.61	22.98	30	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	21.05	22.42	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+L	Outer_Full	20.7	22.07	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+M	Outer_Full	20.44	21.81	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+H	Outer_Full	20.36	21.73	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+L	Inner_Full	21.72	23.09	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+M	Inner_Full	21.54	22.91	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+H	Inner_Full	21.37	22.74	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	20.51	21.88	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	20.48	21.85	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	20.1	21.47	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	20.5	21.87	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	20.44	21.81	30	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	20.14	21.51	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+L	Outer_Full	20.14	21.51	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+M	Outer_Full	19.95	21.32	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+H	Outer_Full	19.65	21.02	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+L	Inner_Full	20.15	21.52	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+M	Inner_Full	20.18	21.55	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+H	Inner_Full	19.82	21.19	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	20.29	21.66	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	20.37	21.74	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	19.91	21.28	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	20.32	21.69	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	20.37	21.74	30	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	19.94	21.31	30	PASS

DC_12A_n66A	15	5+5	DFT-256QAM	M+L	Outer_Full	18.18	19.55	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+M	Outer_Full	18.05	19.42	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+H	Outer_Full	17.84	19.21	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+L	Inner_Full	18.17	19.54	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+M	Inner_Full	18.11	19.48	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+H	Inner_Full	17.85	19.22	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	18.49	19.86	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	17.86	19.23	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	17.59	18.96	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	18.5	19.87	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	17.84	19.21	30	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	17.65	19.02	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+L	Outer_Full	21.99	23.36	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+M	Outer_Full	21.76	23.13	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+H	Outer_Full	21.57	22.94	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+L	Inner_Full	22.52	23.89	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+M	Inner_Full	22.32	23.69	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+H	Inner_Full	22.03	23.4	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.97	23.34	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.68	23.05	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.46	22.83	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.93	23.3	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.63	23	30	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.55	22.92	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+L	Outer_Full	21.73	23.1	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+M	Outer_Full	21.54	22.91	30	PASS

DC_12A_n66A	15	5+10	DFT-QPSK	M+H	Outer_Full	21.29	22.66	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+L	Inner_Full	22.74	24.11	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+M	Inner_Full	22.54	23.91	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+H	Inner_Full	22.36	23.73	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.57	22.94	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	21.31	22.68	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	21.38	22.75	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	21.57	22.94	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	21.31	22.68	30	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	21.46	22.83	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+L	Outer_Full	20.75	22.12	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+M	Outer_Full	20.56	21.93	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+H	Outer_Full	20.29	21.66	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+L	Inner_Full	21.72	23.09	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+M	Inner_Full	21.62	22.99	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+H	Inner_Full	21.33	22.7	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.78	22.15	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.64	22.01	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	20.35	21.72	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.69	22.06	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.64	22.01	30	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	20.42	21.79	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+L	Outer_Full	20.2	21.57	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+M	Outer_Full	19.96	21.33	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+H	Outer_Full	19.86	21.23	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+L	Inner_Full	20.17	21.54	30	PASS

DC_12A_n66A	15	5+10	DFT-64QAM	M+M	Inner_Full	20.01	21.38	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+H	Inner_Full	19.84	21.21	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Left	20.58	21.95	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Left	19.64	21.01	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Left	20.09	21.46	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Right	20.49	21.86	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Right	19.6	20.97	30	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Right	20.17	21.54	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+L	Outer_Full	18.1	19.47	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+M	Outer_Full	17.91	19.28	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+H	Outer_Full	17.73	19.1	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+L	Inner_Full	18.04	19.41	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+M	Inner_Full	18	19.37	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+H	Inner_Full	17.86	19.23	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Left	18.07	19.44	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Left	18.14	19.51	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Left	17.71	19.08	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Right	18.01	19.38	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Right	18.05	19.42	30	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Right	17.72	19.09	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+L	Outer_Full	22.01	23.38	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+M	Outer_Full	21.96	23.33	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+H	Outer_Full	21.69	23.06	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+L	Inner_Full	22.52	23.89	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+M	Inner_Full	22.42	23.79	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+H	Inner_Full	22.26	23.63	30	PASS

DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.94	23.31	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.94	23.31	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.72	23.09	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.94	23.31	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.84	23.21	30	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.76	23.13	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+L	Outer_Full	21.8	23.17	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+M	Outer_Full	21.69	23.06	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+H	Outer_Full	21.42	22.79	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+L	Inner_Full	22.73	24.1	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+M	Inner_Full	22.55	23.92	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+H	Inner_Full	22.42	23.79	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	21.9	23.27	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	21.68	23.05	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	21.17	22.54	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	21.9	23.27	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	21.58	22.95	30	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	21.14	22.51	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+L	Outer_Full	20.75	22.12	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+M	Outer_Full	20.55	21.92	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+H	Outer_Full	20.54	21.91	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+L	Inner_Full	21.73	23.1	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+M	Inner_Full	21.62	22.99	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+H	Inner_Full	21.42	22.79	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.52	21.89	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.55	21.92	30	PASS

DC_12A_n66A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	20.31	21.68	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	20.52	21.89	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.47	21.84	30	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	20.24	21.61	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+L	Outer_Full	20.26	21.63	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+M	Outer_Full	20.11	21.48	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+H	Outer_Full	19.89	21.26	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+L	Inner_Full	20.27	21.64	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+M	Inner_Full	20.11	21.48	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+H	Inner_Full	19.93	21.3	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Left	19.87	21.24	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Left	19.99	21.36	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Left	19.81	21.18	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Right	19.94	21.31	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Right	19.96	21.33	30	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Right	19.77	21.14	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+L	Outer_Full	18.26	19.63	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+M	Outer_Full	18.03	19.4	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+H	Outer_Full	17.9	19.27	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+L	Inner_Full	18.18	19.55	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+M	Inner_Full	18.01	19.38	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+H	Inner_Full	17.86	19.23	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Left	18.46	19.83	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Left	18.61	19.98	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Left	18.08	19.45	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Right	18.57	19.94	30	PASS

DC_12A_n66A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Right	18.62	19.99	30	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Right	18.12	19.49	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	22.03	23.4	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	21.96	23.33	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	21.71	23.08	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+L	Inner_Full	22.53	23.9	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+M	Inner_Full	22.41	23.78	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+H	Inner_Full	22.15	23.52	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.01	23.38	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.93	23.3	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.84	23.21	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.98	23.35	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.77	23.14	30	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.73	23.1	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+L	Outer_Full	21.74	23.11	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+M	Outer_Full	21.65	23.02	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+H	Outer_Full	21.37	22.74	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+L	Inner_Full	22.75	24.12	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+M	Inner_Full	22.69	24.06	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+H	Inner_Full	22.42	23.79	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.66	23.03	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.75	23.12	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	21.19	22.56	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.7	23.07	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	21.62	22.99	30	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.2	22.57	30	PASS

DC_12A_n66A	15	5+20	DFT-16QAM	M+L	Outer_Full	20.74	22.11	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+M	Outer_Full	20.73	22.1	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+H	Outer_Full	20.48	21.85	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+L	Inner_Full	21.73	23.1	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+M	Inner_Full	21.6	22.97	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+H	Inner_Full	21.4	22.77	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	20.93	22.3	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.01	22.38	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	20.75	22.12	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	20.81	22.18	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.77	22.14	30	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	20.65	22.02	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+L	Outer_Full	20.24	21.61	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+M	Outer_Full	20.01	21.38	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+H	Outer_Full	19.9	21.27	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+L	Inner_Full	20.2	21.57	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+M	Inner_Full	20.12	21.49	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+H	Inner_Full	19.85	21.22	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.44	21.81	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	20.44	21.81	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	20.01	21.38	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	20.45	21.82	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	20.38	21.75	30	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	19.99	21.36	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+L	Outer_Full	18.14	19.51	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+M	Outer_Full	17.96	19.33	30	PASS

DC_12A_n66A	15	5+20	DFT-256QAM	M+H	Outer_Full	17.88	19.25	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+L	Inner_Full	18.14	19.51	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+M	Inner_Full	18.18	19.55	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+H	Inner_Full	17.94	19.31	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	18.15	19.52	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	18.35	19.72	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.06	19.43	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	18.13	19.5	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	18.15	19.52	30	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	17.99	19.36	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+L	Outer_Full	22.04	23.41	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+M	Outer_Full	21.86	23.23	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+H	Outer_Full	21.74	23.11	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+L	Inner_Full	22.49	23.86	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+M	Inner_Full	22.36	23.73	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+H	Inner_Full	22.3	23.67	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.22	23.59	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.08	23.45	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.83	23.2	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.11	23.48	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.03	23.4	30	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.82	23.19	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+L	Outer_Full	21.82	23.19	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+M	Outer_Full	21.6	22.97	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+H	Outer_Full	21.54	22.91	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+L	Inner_Full	22.73	24.1	30	PASS

DC_12A_n66A	15	5+25	DFT-QPSK	M+M	Inner_Full	22.56	23.93	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+H	Inner_Full	22.4	23.77	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+L	Edge_1RB_Left	21.9	23.27	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+M	Edge_1RB_Left	21.84	23.21	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+H	Edge_1RB_Left	21.58	22.95	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+L	Edge_1RB_Right	21.79	23.16	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+M	Edge_1RB_Right	21.75	23.12	30	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+H	Edge_1RB_Right	21.59	22.96	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+L	Outer_Full	20.71	22.08	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+M	Outer_Full	20.63	22	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+H	Outer_Full	20.42	21.79	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+L	Inner_Full	21.73	23.1	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+M	Inner_Full	21.59	22.96	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+H	Inner_Full	21.39	22.76	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+L	Edge_1RB_Left	20.99	22.36	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+M	Edge_1RB_Left	20.88	22.25	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+H	Edge_1RB_Left	20.85	22.22	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+L	Edge_1RB_Right	20.89	22.26	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+M	Edge_1RB_Right	20.67	22.04	30	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+H	Edge_1RB_Right	20.76	22.13	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+L	Outer_Full	20.29	21.66	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+M	Outer_Full	20.04	21.41	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+H	Outer_Full	20.02	21.39	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+L	Inner_Full	20.24	21.61	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+M	Inner_Full	20.13	21.5	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+H	Inner_Full	19.9	21.27	30	PASS

DC_12A_n66A	15	5+25	DFT-64QAM	M+L	Edge_1RB_Left	20.28	21.65	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+M	Edge_1RB_Left	20.54	21.91	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+H	Edge_1RB_Left	20.1	21.47	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+L	Edge_1RB_Right	20.25	21.62	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+M	Edge_1RB_Right	20.45	21.82	30	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+H	Edge_1RB_Right	20.19	21.56	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+L	Outer_Full	18.25	19.62	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+M	Outer_Full	18.07	19.44	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+H	Outer_Full	17.94	19.31	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+L	Inner_Full	18.14	19.51	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+M	Inner_Full	18.01	19.38	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+H	Inner_Full	17.95	19.32	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+L	Edge_1RB_Left	18.6	19.97	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+M	Edge_1RB_Left	18.48	19.85	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+H	Edge_1RB_Left	18.35	19.72	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+L	Edge_1RB_Right	18.53	19.9	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+M	Edge_1RB_Right	18.38	19.75	30	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+H	Edge_1RB_Right	18.29	19.66	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+L	Outer_Full	22.02	23.39	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+M	Outer_Full	21.89	23.26	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+H	Outer_Full	21.81	23.18	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+L	Inner_Full	22.53	23.9	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+M	Inner_Full	22.29	23.66	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+H	Inner_Full	22.22	23.59	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.19	23.56	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.94	23.31	30	PASS

DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.85	23.22	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.87	23.24	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.76	23.13	30	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.63	23	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+L	Outer_Full	21.8	23.17	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+M	Outer_Full	21.69	23.06	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+H	Outer_Full	21.45	22.82	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+L	Inner_Full	22.66	24.03	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+M	Inner_Full	22.51	23.88	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+H	Inner_Full	22.38	23.75	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+L	Edge_1RB_Left	21.89	23.26	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+M	Edge_1RB_Left	21.93	23.3	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+H	Edge_1RB_Left	21.44	22.81	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+L	Edge_1RB_Right	21.69	23.06	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+M	Edge_1RB_Right	21.6	22.97	30	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+H	Edge_1RB_Right	21.21	22.58	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+L	Outer_Full	20.75	22.12	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+M	Outer_Full	20.66	22.03	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+H	Outer_Full	20.51	21.88	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+L	Inner_Full	21.75	23.12	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+M	Inner_Full	21.5	22.87	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+H	Inner_Full	21.4	22.77	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+L	Edge_1RB_Left	21.03	22.4	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+M	Edge_1RB_Left	20.83	22.2	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+H	Edge_1RB_Left	20.68	22.05	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+L	Edge_1RB_Right	20.84	22.21	30	PASS

DC_12A_n66A	15	5+30	DFT-16QAM	M+M	Edge_1RB_Right	20.61	21.98	30	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+H	Edge_1RB_Right	20.48	21.85	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+L	Outer_Full	20.3	21.67	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+M	Outer_Full	20.15	21.52	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+H	Outer_Full	20.06	21.43	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+L	Inner_Full	20.27	21.64	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+M	Inner_Full	20.02	21.39	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+H	Inner_Full	19.92	21.29	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+L	Edge_1RB_Left	20.18	21.55	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+M	Edge_1RB_Left	20.21	21.58	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+H	Edge_1RB_Left	20.08	21.45	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+L	Edge_1RB_Right	19.99	21.36	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+M	Edge_1RB_Right	19.87	21.24	30	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+H	Edge_1RB_Right	19.9	21.27	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+L	Outer_Full	18.29	19.66	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+M	Outer_Full	18.03	19.4	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+H	Outer_Full	18.02	19.39	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+L	Inner_Full	18.29	19.66	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+M	Inner_Full	17.99	19.36	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+H	Inner_Full	17.84	19.21	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+L	Edge_1RB_Left	18.84	20.21	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+M	Edge_1RB_Left	18.46	19.83	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+H	Edge_1RB_Left	17.98	19.35	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+L	Edge_1RB_Right	18.74	20.11	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+M	Edge_1RB_Right	18.33	19.7	30	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+H	Edge_1RB_Right	17.82	19.19	30	PASS

DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+L	Outer_Full	21.98	23.35	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+M	Outer_Full	21.95	23.32	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+H	Outer_Full	21.82	23.19	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+L	Inner_Full	22.47	23.84	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+M	Inner_Full	22.34	23.71	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+H	Inner_Full	22.27	23.64	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.16	23.53	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.11	23.48	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.92	23.29	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.68	23.05	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.79	23.16	30	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	21.62	22.99	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+L	Outer_Full	21.79	23.16	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+M	Outer_Full	21.63	23	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+H	Outer_Full	21.53	22.9	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+L	Inner_Full	22.68	24.05	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+M	Inner_Full	22.65	24.02	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+H	Inner_Full	22.5	23.87	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.84	23.21	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.71	23.08	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.52	22.89	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.58	22.95	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.47	22.84	30	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.35	22.72	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+L	Outer_Full	20.81	22.18	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+M	Outer_Full	20.6	21.97	30	PASS

DC_12A_n66A	15	5+40	DFT-16QAM	M+H	Outer_Full	20.51	21.88	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+L	Inner_Full	21.72	23.09	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+M	Inner_Full	21.63	23	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+H	Inner_Full	21.52	22.89	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+L	Edge_1RB_Left	21.09	22.46	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.99	22.36	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.66	22.03	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.78	22.15	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.56	21.93	30	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.42	21.79	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+L	Outer_Full	20.28	21.65	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+M	Outer_Full	20.14	21.51	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+H	Outer_Full	19.99	21.36	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+L	Inner_Full	20.3	21.67	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+M	Inner_Full	20.13	21.5	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+H	Inner_Full	19.91	21.28	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+L	Edge_1RB_Left	20.68	22.05	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+M	Edge_1RB_Left	20.5	21.87	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+H	Edge_1RB_Left	20.06	21.43	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+L	Edge_1RB_Right	20.29	21.66	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+M	Edge_1RB_Right	20.15	21.52	30	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+H	Edge_1RB_Right	19.85	21.22	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+L	Outer_Full	18.22	19.59	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+M	Outer_Full	18.12	19.49	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+H	Outer_Full	17.98	19.35	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+L	Inner_Full	18.28	19.65	30	PASS

DC_12A_n66A	15	5+40	DFT-256QAM	M+M	Inner_Full	18.09	19.46	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+H	Inner_Full	18.04	19.41	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+L	Edge_1RB_Left	18.28	19.65	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+M	Edge_1RB_Left	18.32	19.69	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+H	Edge_1RB_Left	18.19	19.56	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+L	Edge_1RB_Right	18.08	19.45	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+M	Edge_1RB_Right	18.11	19.48	30	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+H	Edge_1RB_Right	18.16	19.53	30	PASS
DC_12A-n66A	15	5+35	DFT-BPSK	M+L	Edge_1RB_Left	22.63	24	30	PASS
DC_12A-n66A	15	5+35	DFT-BPSK	M+L	Outer_Full	22.76	24.13	30	PASS
DC_12A-n66A	15	5+35	DFT-BPSK	M+L	Inner_Full	23.23	24.6	30	PASS
DC_12A-n66A	15	5+35	DFT-BPSK	M+L	Edge_1RB_Right	22.76	24.13	30	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+L	Edge_1RB_Left	22.03	23.4	30	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+L	Outer_Full	22.15	23.52	30	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+L	Inner_Full	23.16	24.53	30	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+L	Edge_1RB_Right	22.26	23.63	30	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+L	Edge_1RB_Left	21.2	22.57	30	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+L	Outer_Full	21.21	22.58	30	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+L	Inner_Full	22.14	23.51	30	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+L	Edge_1RB_Right	21.34	22.71	30	PASS
DC_12A-n66A	15	5+35	DFT-64QAM	M+L	Edge_1RB_Left	20.59	21.96	30	PASS
DC_12A-n66A	15	5+35	DFT-64QAM	M+L	Outer_Full	20.72	22.09	30	PASS
DC_12A-n66A	15	5+35	DFT-64QAM	M+L	Inner_Full	20.68	22.05	30	PASS
DC_12A-n66A	15	5+35	DFT-64QAM	M+L	Edge_1RB_Right	20.81	22.18	30	PASS
DC_12A-n66A	15	5+35	DFT-256QAM	M+L	Edge_1RB_Left	18.31	19.68	30	PASS
DC_12A-n66A	15	5+35	DFT-256QAM	M+L	Outer_Full	18.65	20.02	30	PASS

DC_12A-n66A	15	5+35	DFT-256QAM	M+L	Inner_Full	18.62	19.99	30	PASS
DC_12A-n66A	15	5+35	DFT-256QAM	M+L	Edge_1RB_Right	18.39	19.76	30	PASS
DC_12A-n66A	15	5+35	DFT-BPSK	M+M	Edge_1RB_Left	22.67	24.04	30	PASS
DC_12A-n66A	15	5+35	DFT-BPSK	M+M	Outer_Full	22.74	24.11	30	PASS
DC_12A-n66A	15	5+35	DFT-BPSK	M+M	Inner_Full	23.27	24.64	30	PASS
DC_12A-n66A	15	5+35	DFT-BPSK	M+M	Edge_1RB_Right	22.71	24.08	30	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+M	Edge_1RB_Left	22.11	23.48	30	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+M	Outer_Full	22.31	23.68	30	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+M	Inner_Full	23.28	24.65	30	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+M	Edge_1RB_Right	22.22	23.59	30	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+M	Edge_1RB_Left	21.23	22.6	30	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+M	Outer_Full	21.29	22.66	30	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+M	Inner_Full	22.27	23.64	30	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+M	Edge_1RB_Right	21.35	22.72	30	PASS
DC_12A-n66A	15	5+35	DFT-64QAM	M+M	Edge_1RB_Left	20.61	21.98	30	PASS
DC_12A-n66A	15	5+35	DFT-64QAM	M+M	Outer_Full	20.84	22.21	30	PASS

Peak-to-Average Ratio for NSA

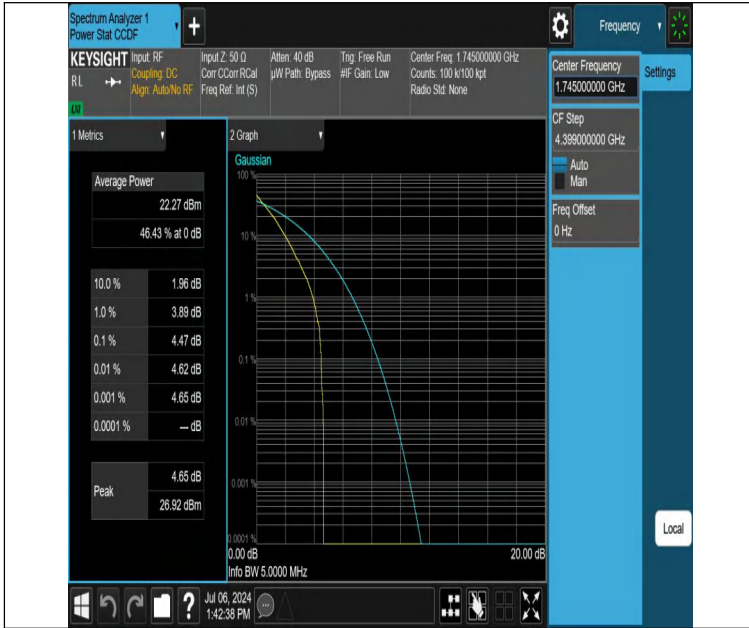
Peak-to-Average Ratio(CCDF)

Test Result

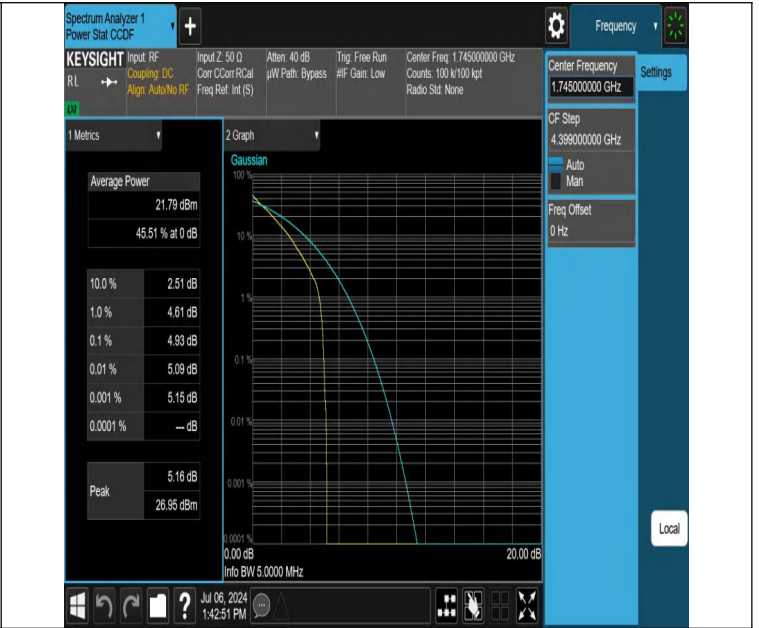
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (dBm)	Limit (dBm)	Verdict
DC_12A_n66A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	4.47	≤13	PASS
DC_12A_n66A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.93	≤13	PASS
DC_12A_n66A	15	5+5	DFT-16QAM	M+M	Outer_Full	6.09	≤13	PASS
DC_12A_n66A	15	5+5	DFT-64QAM	M+M	Outer_Full	6.63	≤13	PASS
DC_12A_n66A	15	5+5	DFT-256QAM	M+M	Outer_Full	6.60	≤13	PASS
DC_12A_n66A	15	5+10	DFT-PI2BPSK	M+M	Outer_Full	4.48	≤13	PASS
DC_12A_n66A	15	5+10	DFT-QPSK	M+M	Outer_Full	5.21	≤13	PASS
DC_12A_n66A	15	5+10	DFT-16QAM	M+M	Outer_Full	6.17	≤13	PASS
DC_12A_n66A	15	5+10	DFT-64QAM	M+M	Outer_Full	6.60	≤13	PASS
DC_12A_n66A	15	5+10	DFT-256QAM	M+M	Outer_Full	6.63	≤13	PASS
DC_12A_n66A	15	5+15	DFT-PI2BPSK	M+M	Outer_Full	4.41	≤13	PASS
DC_12A_n66A	15	5+15	DFT-QPSK	M+M	Outer_Full	4.97	≤13	PASS
DC_12A_n66A	15	5+15	DFT-16QAM	M+M	Outer_Full	5.85	≤13	PASS
DC_12A_n66A	15	5+15	DFT-64QAM	M+M	Outer_Full	6.36	≤13	PASS
DC_12A_n66A	15	5+15	DFT-256QAM	M+M	Outer_Full	6.76	≤13	PASS
DC_12A_n66A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	4.53	≤13	PASS
DC_12A_n66A	15	5+20	DFT-QPSK	M+M	Outer_Full	4.96	≤13	PASS
DC_12A_n66A	15	5+20	DFT-16QAM	M+M	Outer_Full	5.94	≤13	PASS
DC_12A_n66A	15	5+20	DFT-64QAM	M+M	Outer_Full	6.37	≤13	PASS
DC_12A_n66A	15	5+20	DFT-256QAM	M+M	Outer_Full	6.83	≤13	PASS
DC_12A_n66A	15	5+25	DFT-PI2BPSK	M+M	Outer_Full	4.46	≤13	PASS
DC_12A_n66A	15	5+25	DFT-QPSK	M+M	Outer_Full	4.97	≤13	PASS
DC_12A_n66A	15	5+25	DFT-16QAM	M+M	Outer_Full	5.92	≤13	PASS
DC_12A_n66A	15	5+25	DFT-64QAM	M+M	Outer_Full	6.39	≤13	PASS
DC_12A_n66A	15	5+25	DFT-256QAM	M+M	Outer_Full	6.71	≤13	PASS
DC_12A_n66A	15	5+30	DFT-PI2BPSK	M+M	Outer_Full	4.45	≤13	PASS
DC_12A_n66A	15	5+30	DFT-QPSK	M+M	Outer_Full	4.95	≤13	PASS
DC_12A_n66A	15	5+30	DFT-16QAM	M+M	Outer_Full	5.92	≤13	PASS
DC_12A_n66A	15	5+30	DFT-64QAM	M+M	Outer_Full	6.39	≤13	PASS
DC_12A_n66A	15	5+30	DFT-256QAM	M+M	Outer_Full	6.75	≤13	PASS
DC_12A_n66A	15	5+40	DFT-PI2BPSK	M+M	Outer_Full	4.46	≤13	PASS
DC_12A_n66A	15	5+40	DFT-QPSK	M+M	Outer_Full	4.96	≤13	PASS
DC_12A_n66A	15	5+40	DFT-16QAM	M+M	Outer_Full	5.87	≤13	PASS
DC_12A_n66A	15	5+40	DFT-64QAM	M+M	Outer_Full	6.35	≤13	PASS
DC_12A_n66A	15	5+40	DFT-256QAM	M+M	Outer_Full	6.80	≤13	PASS

DC_12A-n66A	15	5+35	DFT-BPSK	M+M	Outer_Full	8.44	≤13dB	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+M	Outer_Full	8.44	≤13dB	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+M	Outer_Full	8.44	≤13dB	PASS
DC_12A-n66A	15	5+35	DFT-64QAM	M+M	Outer_Full	8.44	≤13dB	PASS
DC_12A-n66A	15	5+35	DFT-256QAM	M+M	Outer_Full	8.44	≤13dB	PASS

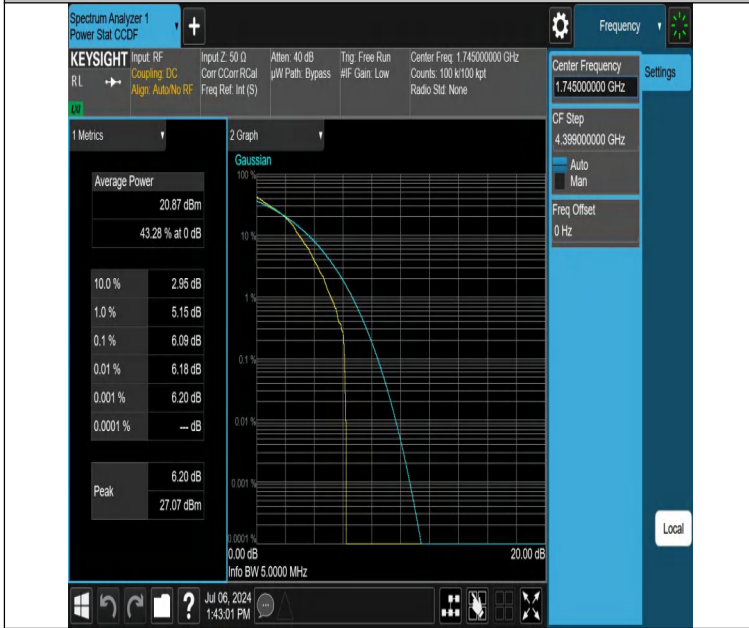
Test Graphs



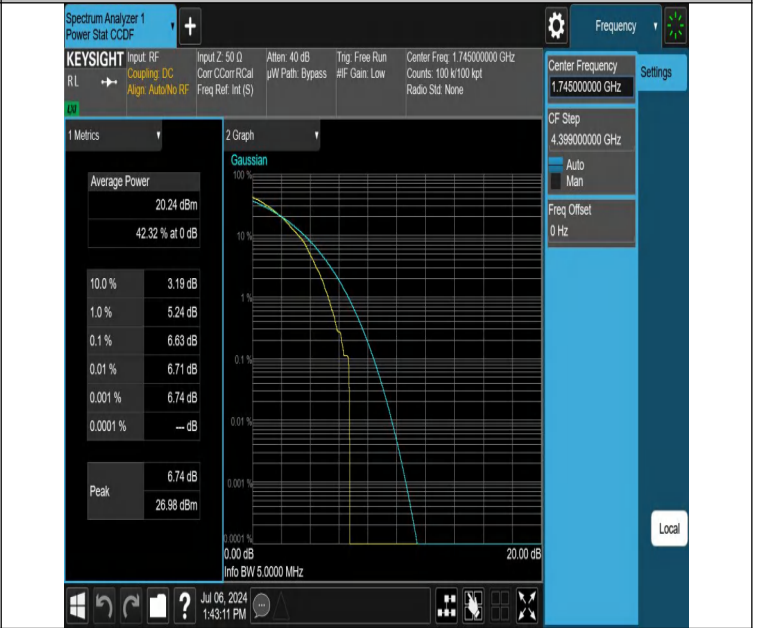
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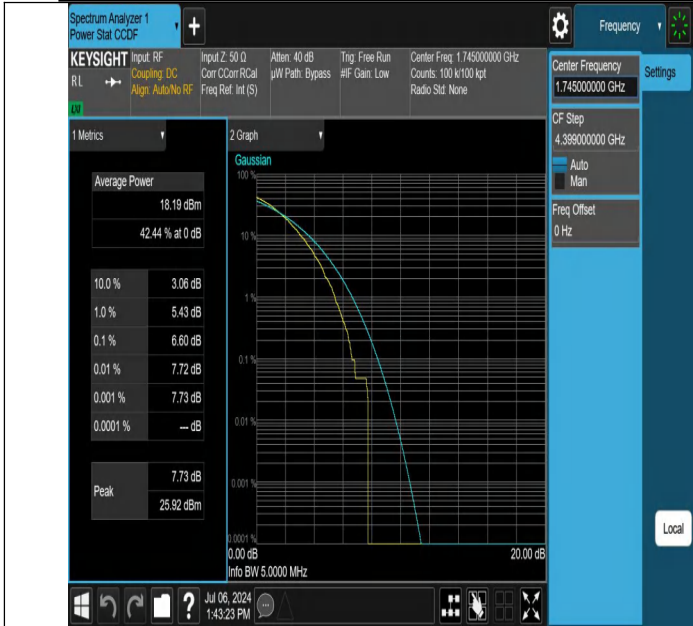
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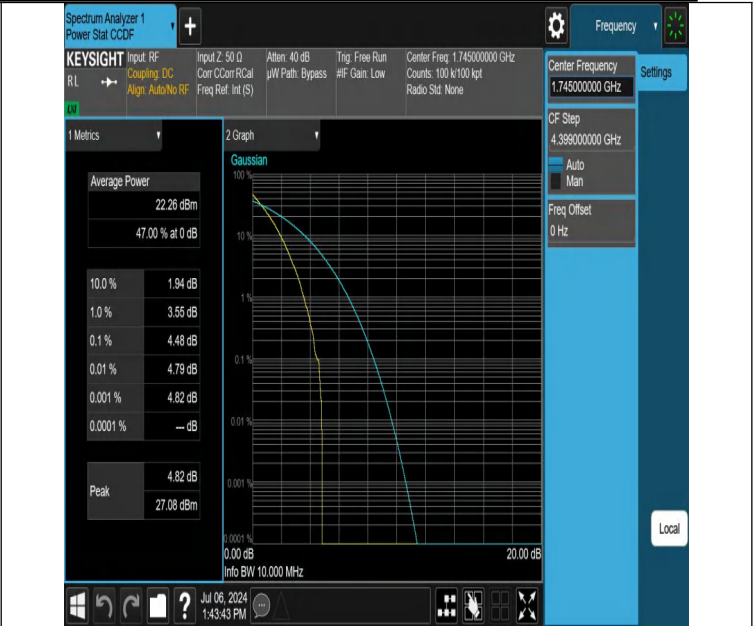
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NTNV-DC_12A_n66A-PC3-15-5+5-M+M-N/A-N/A-DFT-64QAM-Outer_Full-PASS



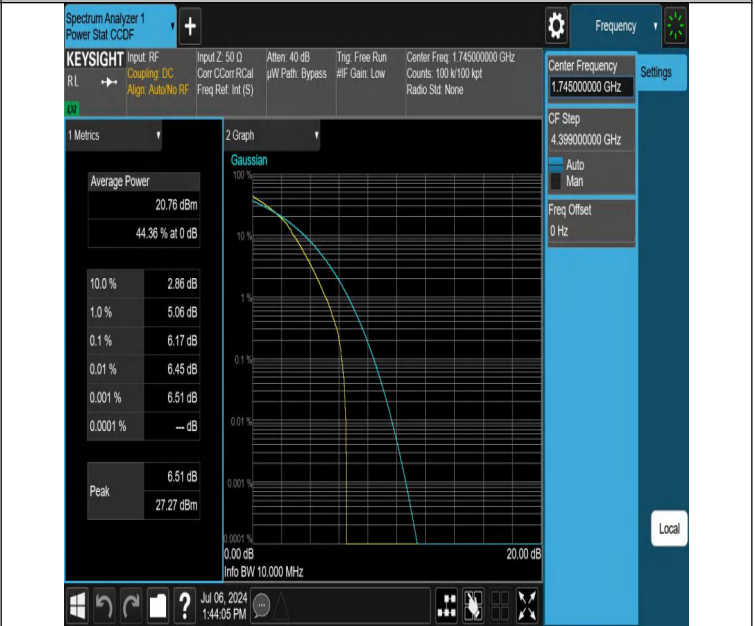
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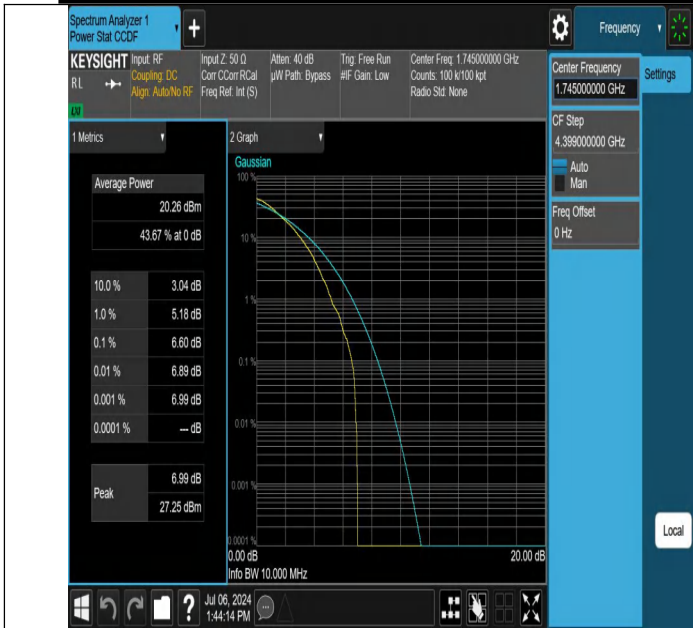
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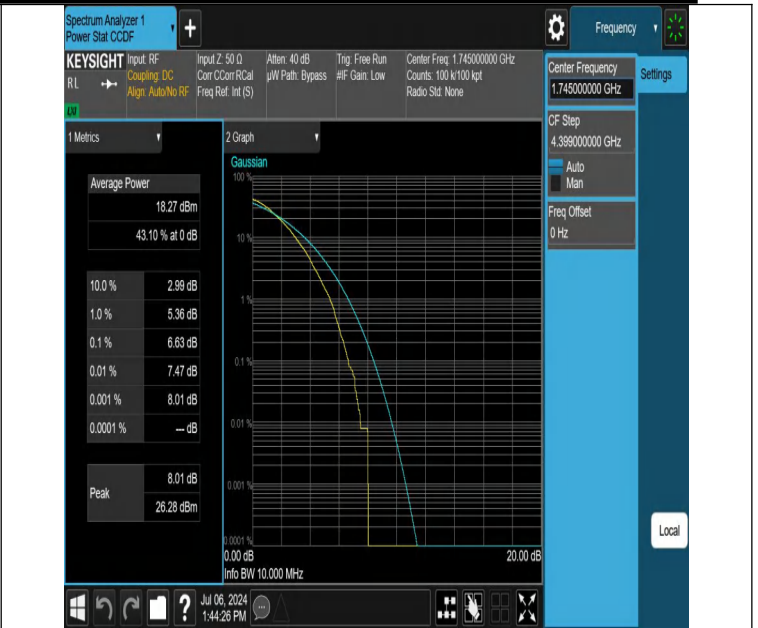
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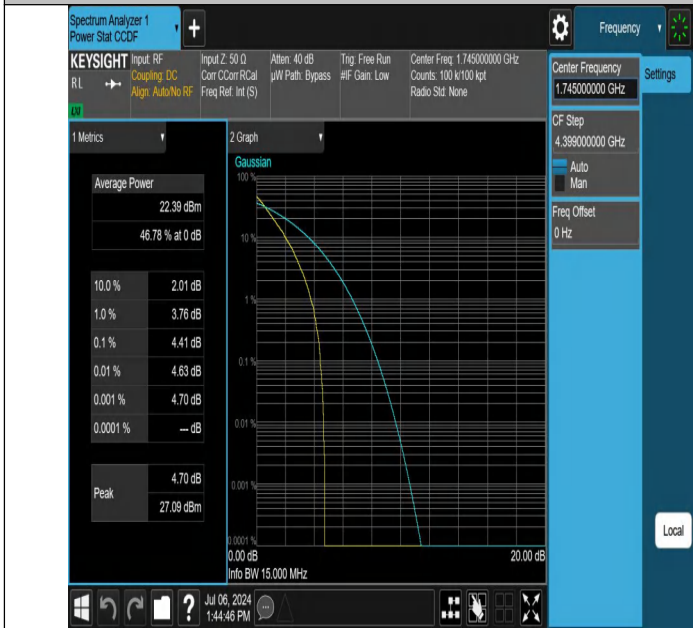
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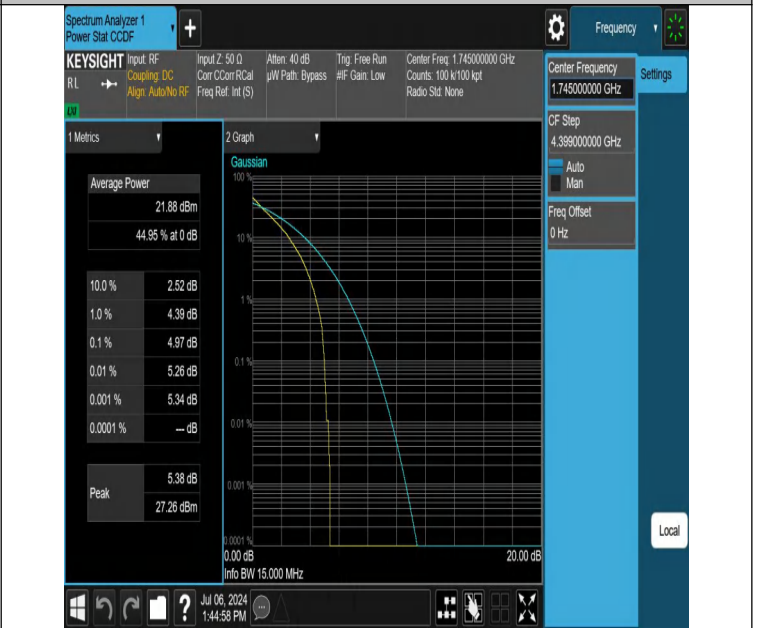
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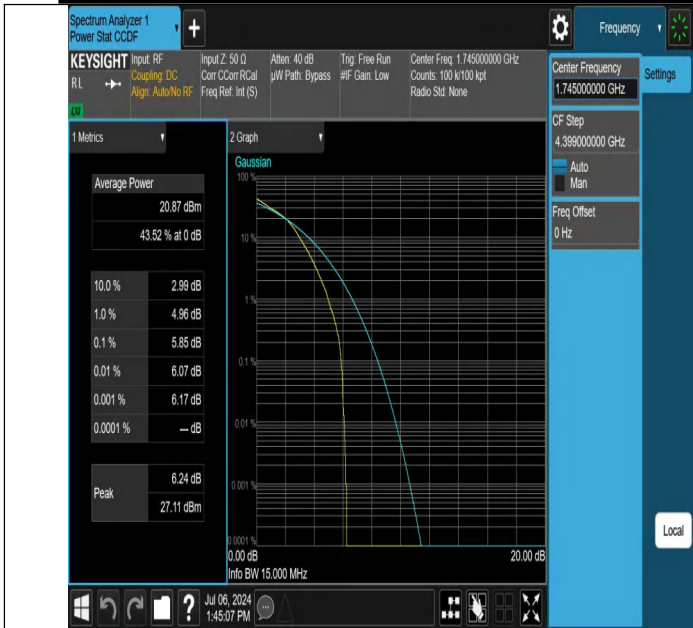
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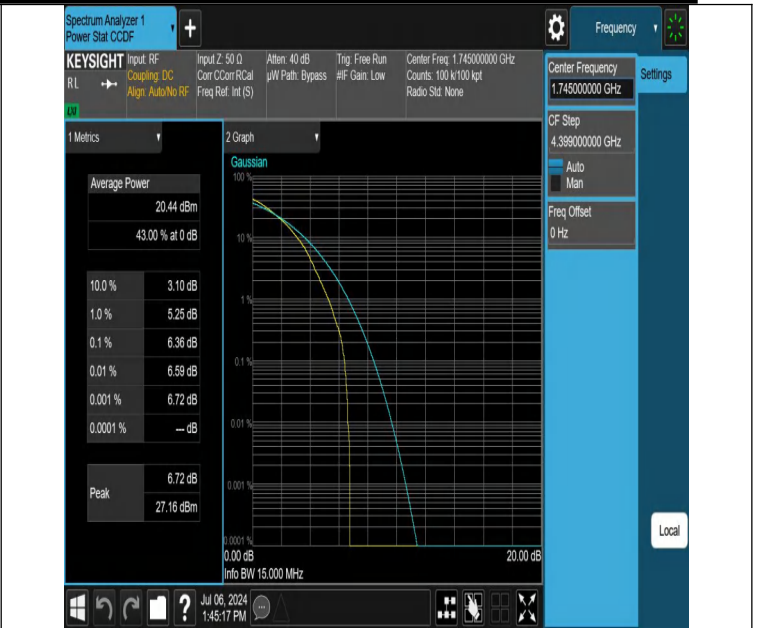
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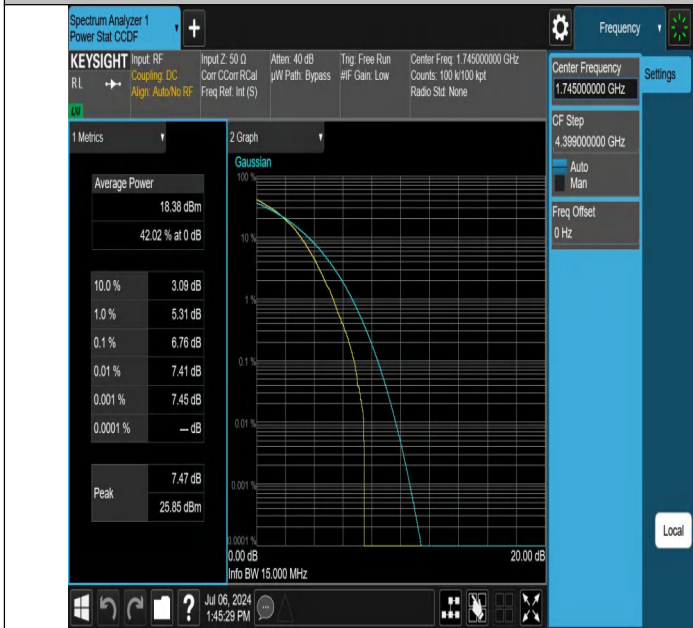
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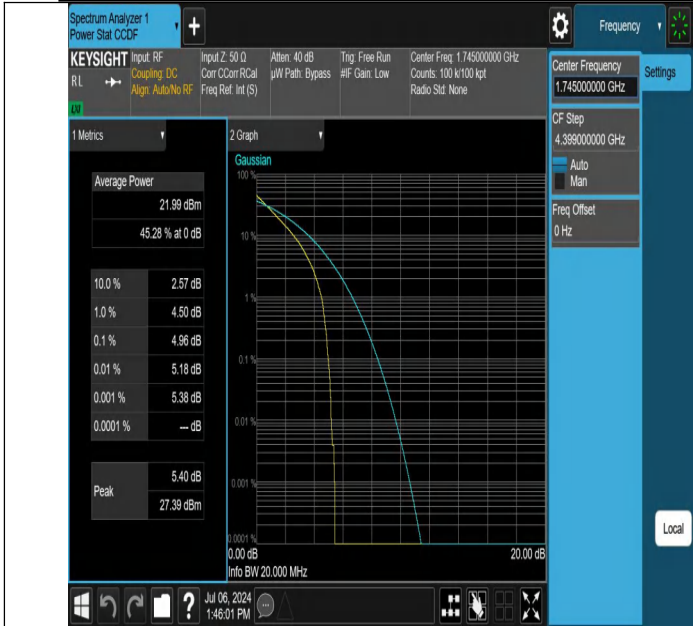
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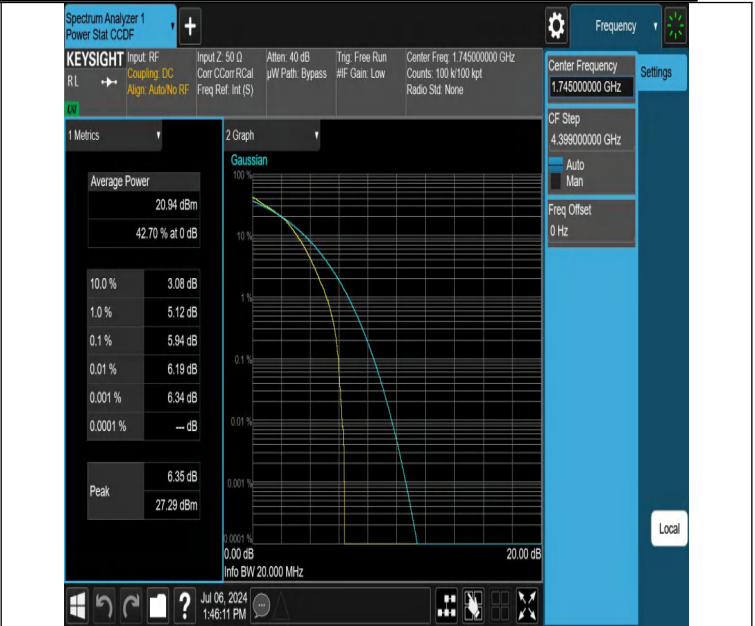
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Outer_Full-PASS



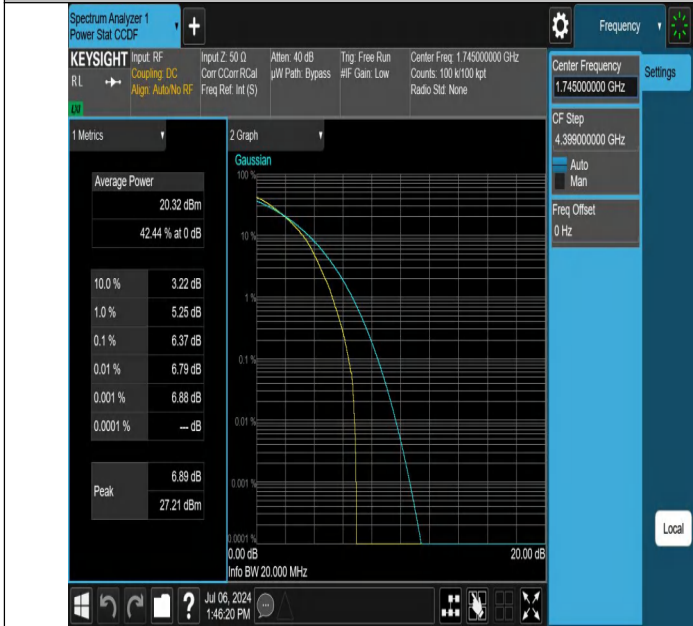
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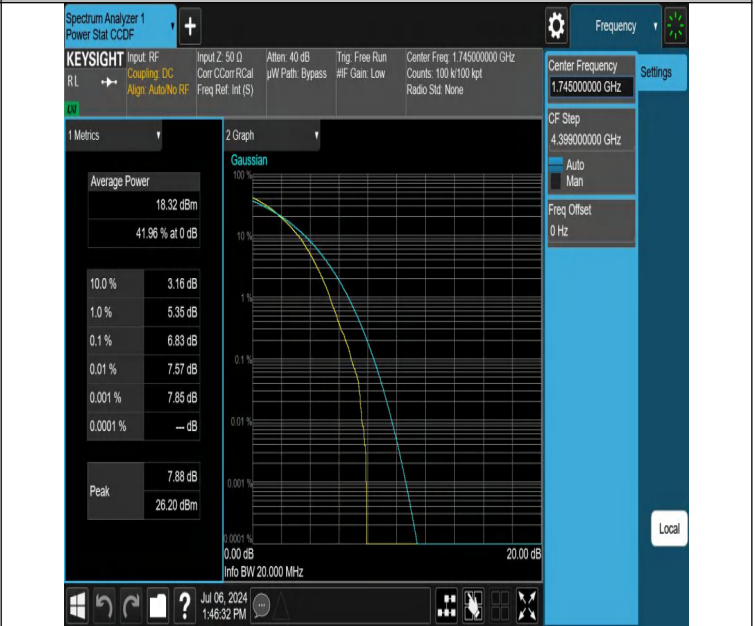
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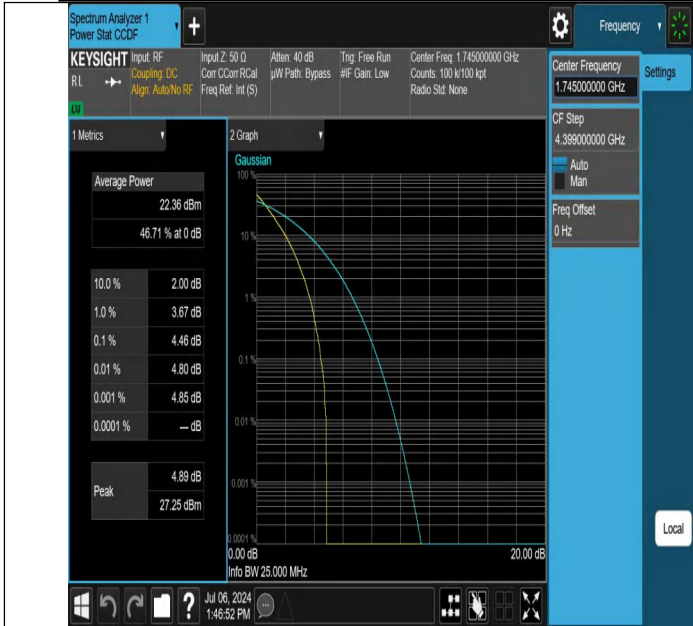
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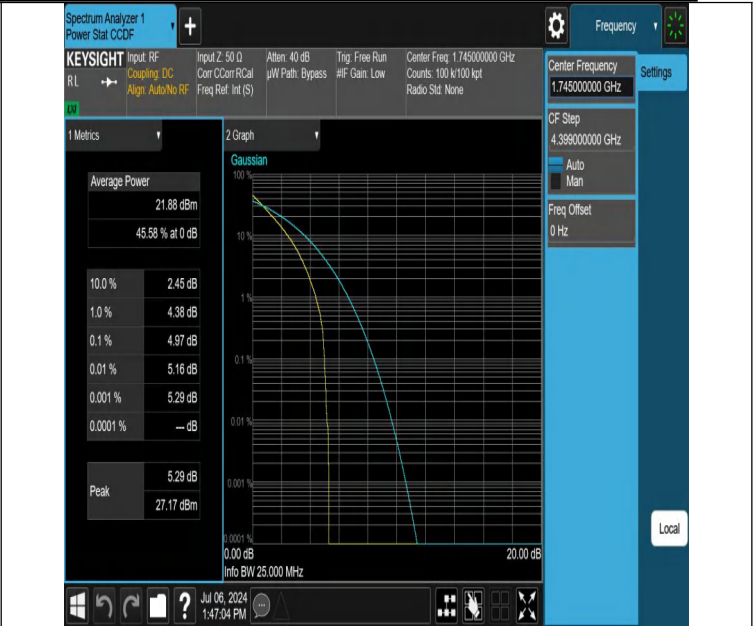
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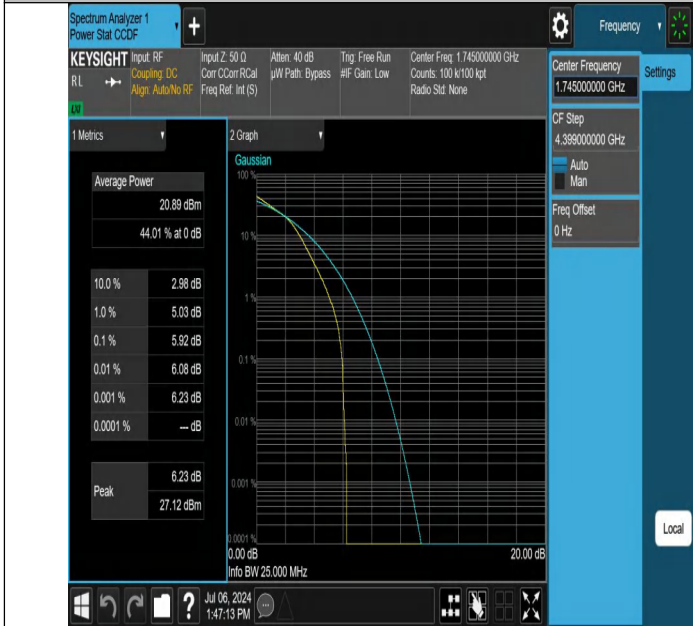
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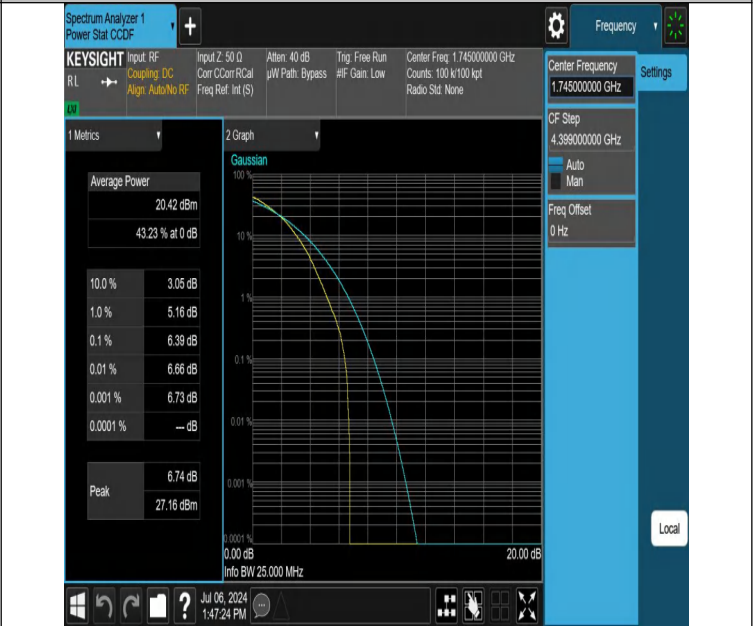
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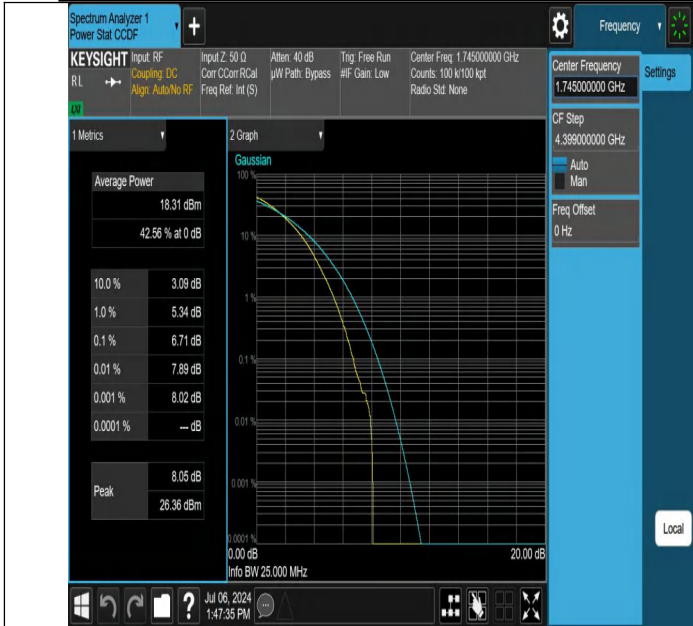
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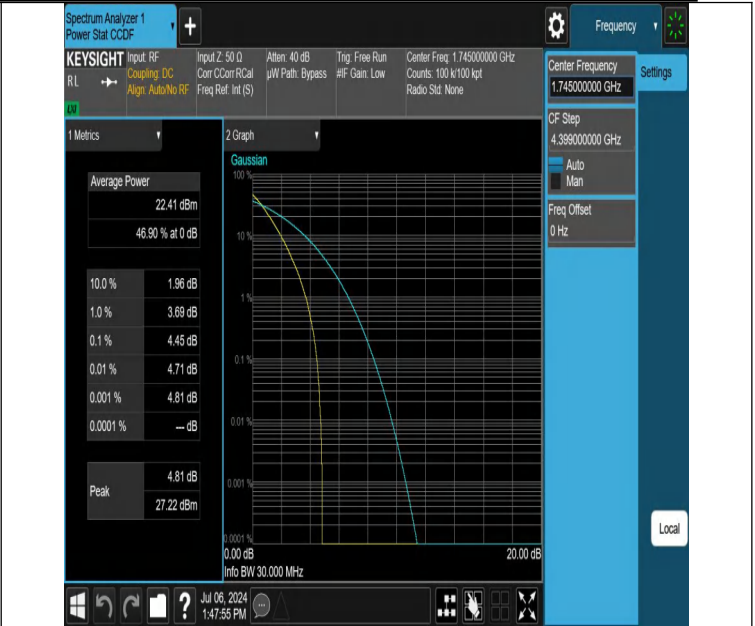
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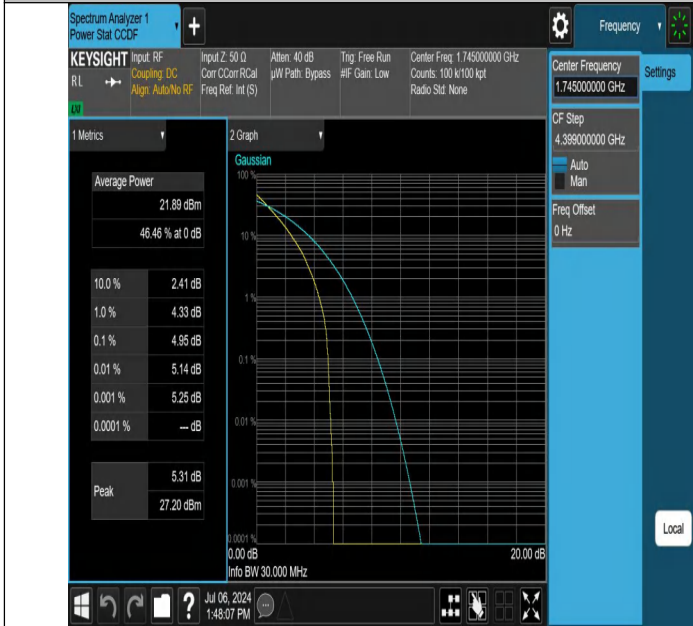
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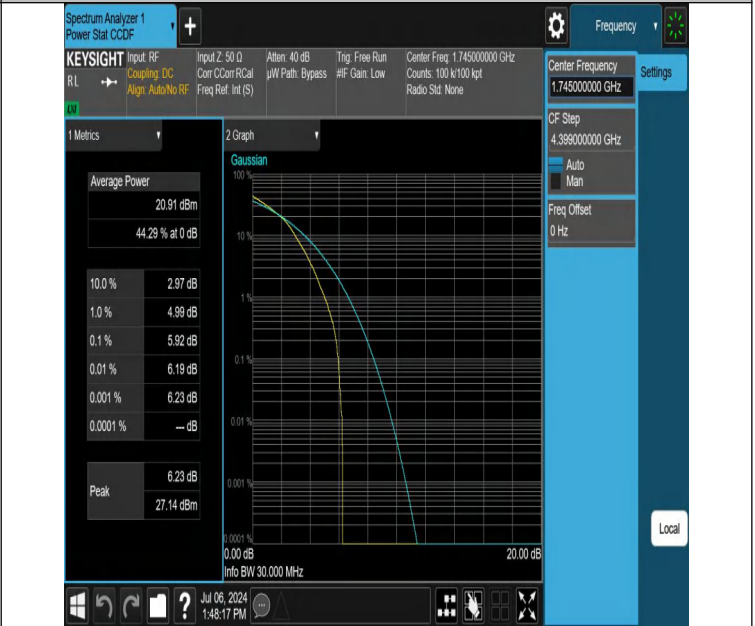
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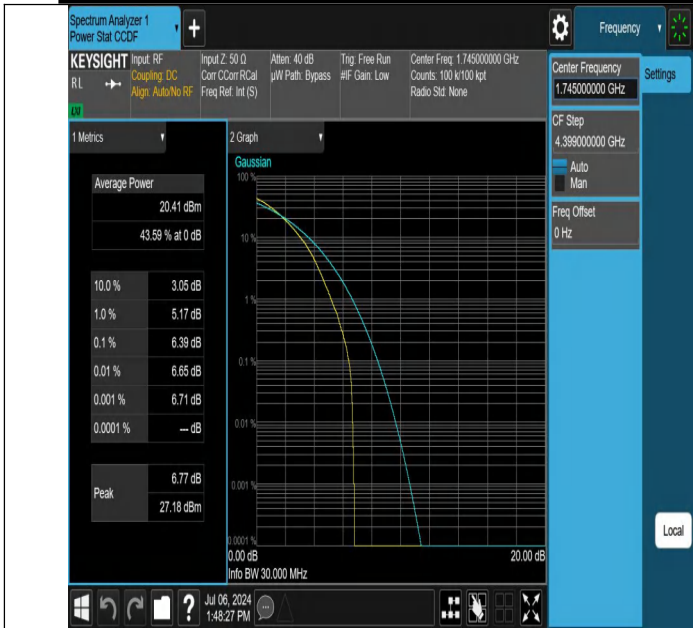
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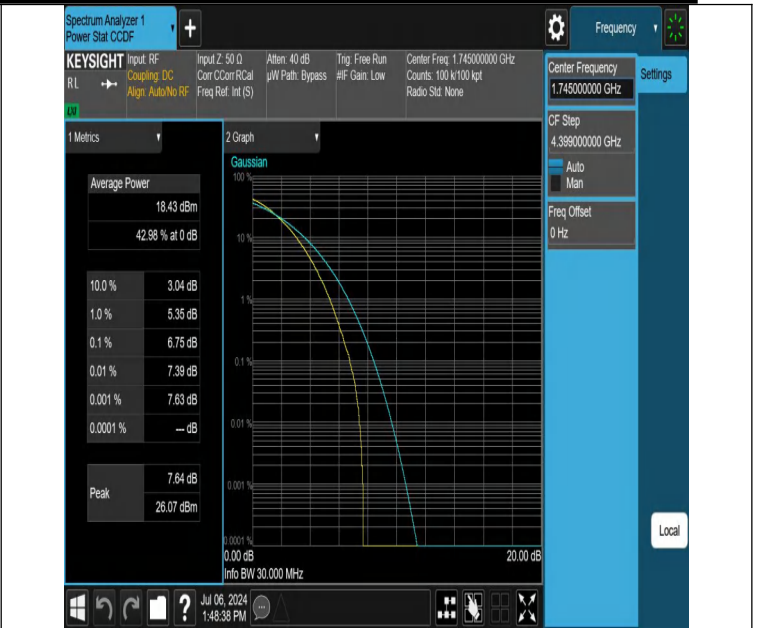
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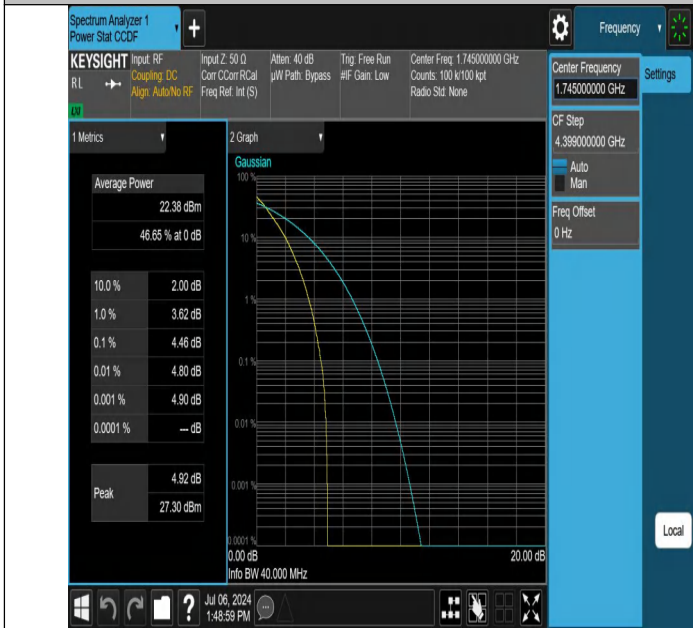
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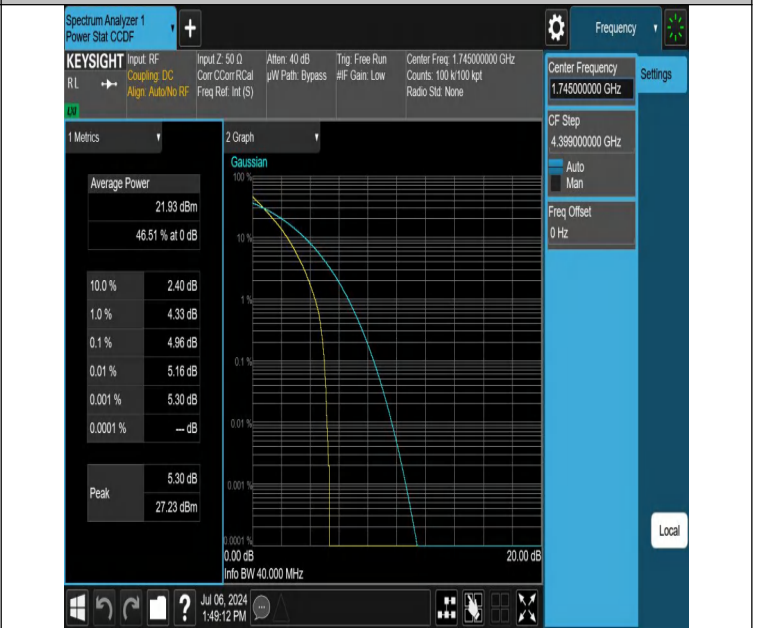
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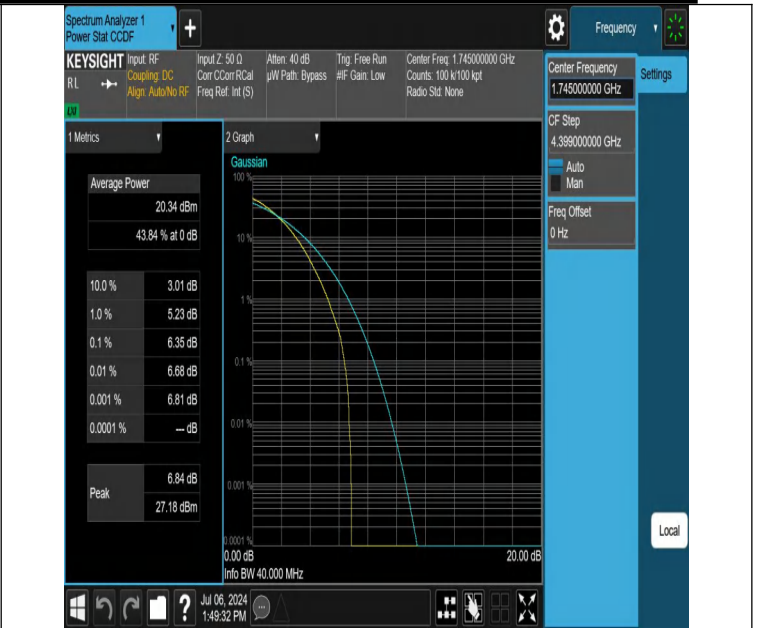
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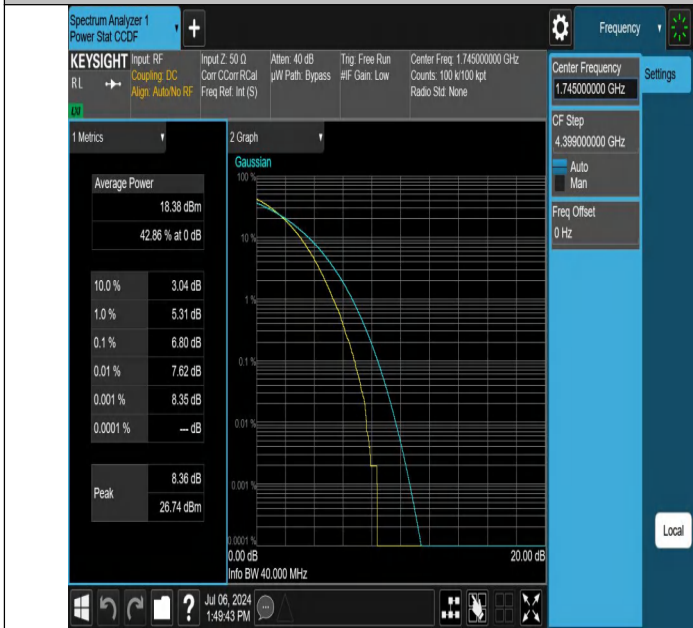
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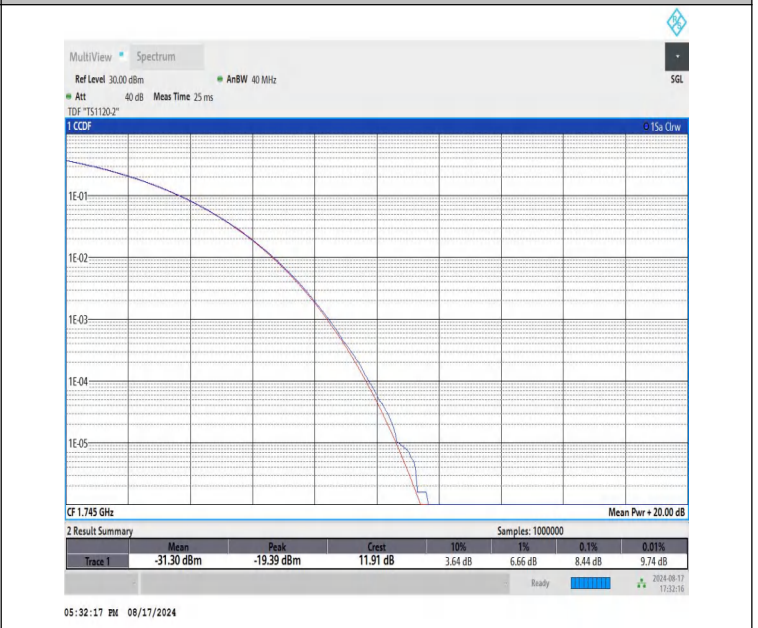
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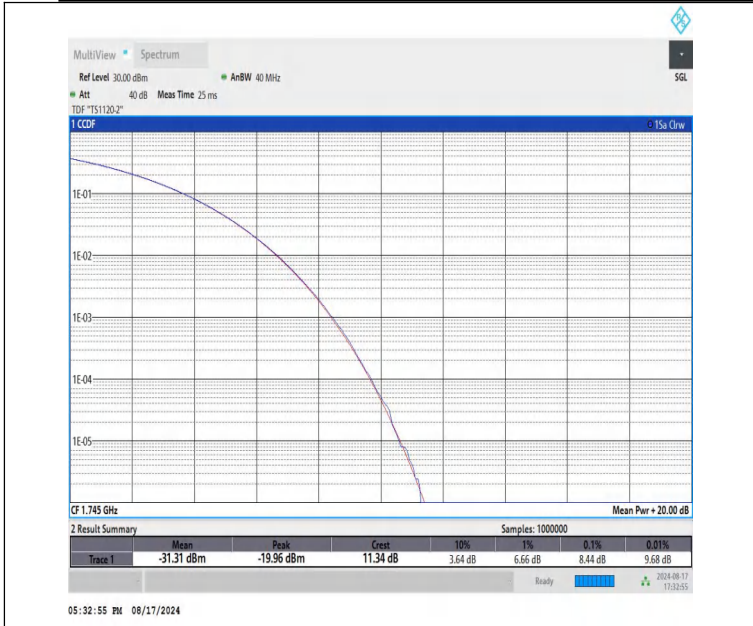
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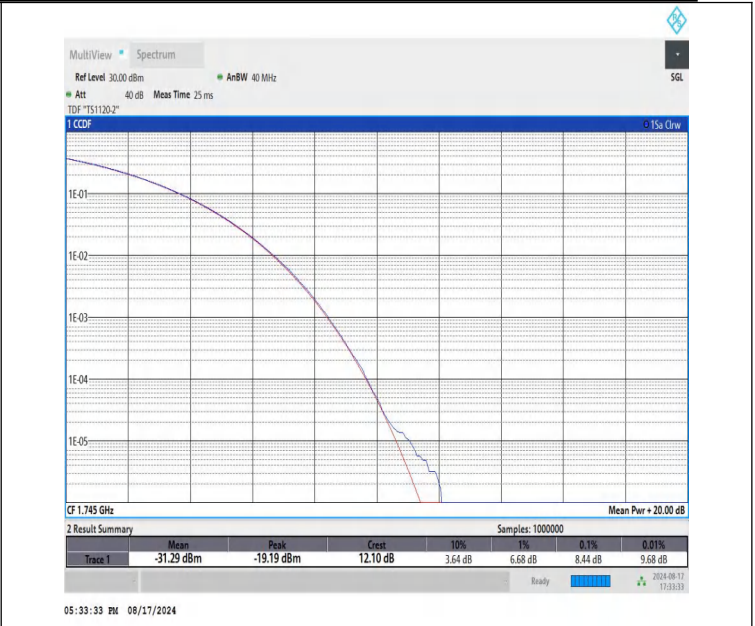
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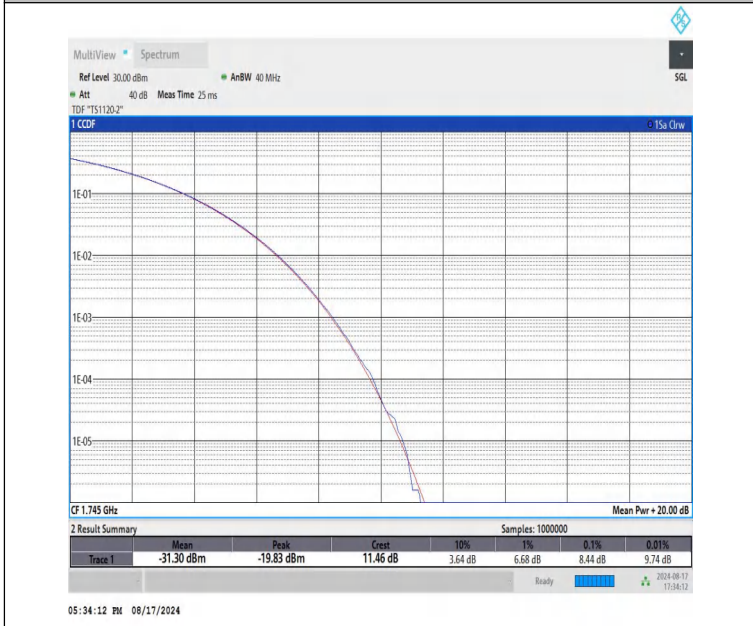
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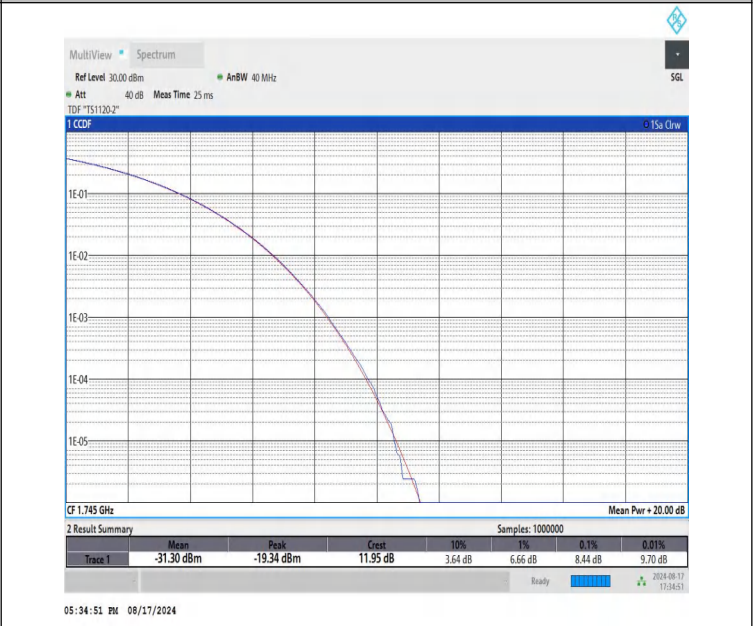
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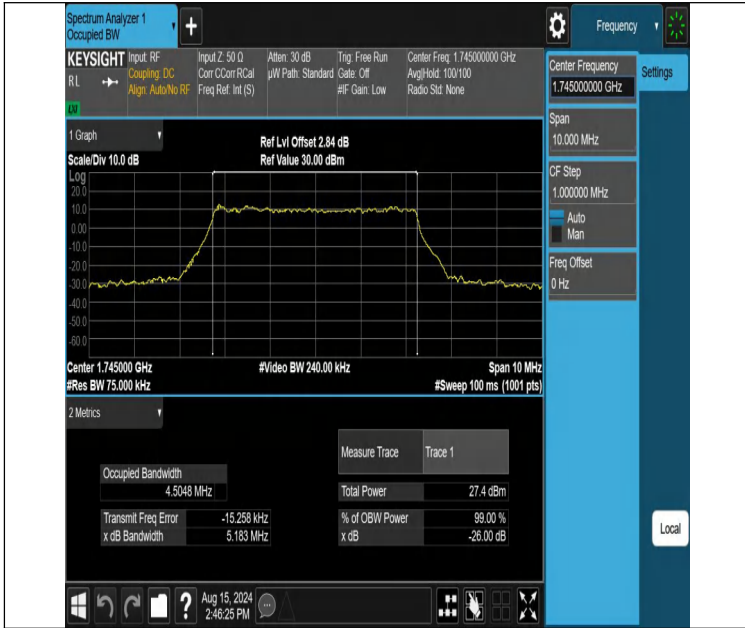
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26dB Bandwidth and Occupied Bandwidth for NSA

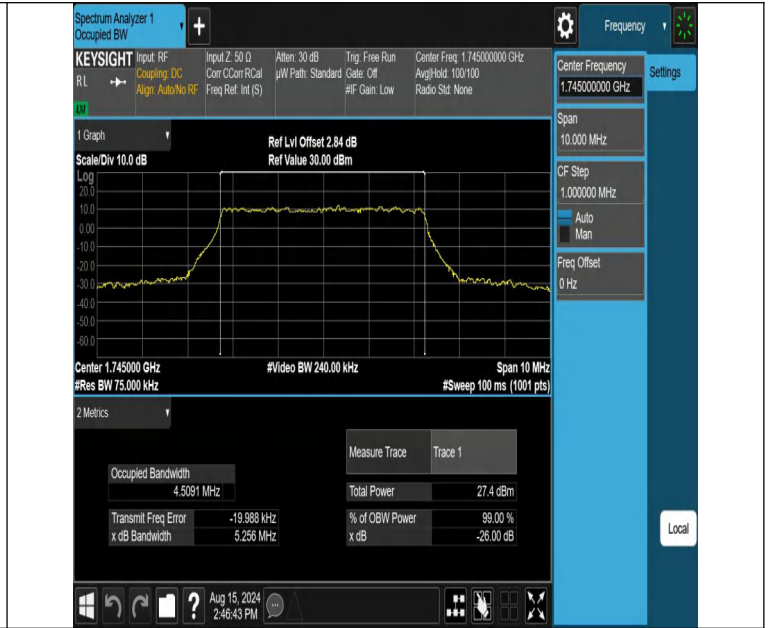
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_12A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	4.5048	5.183	PASS
DC_12A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	4.5091	5.256	PASS
DC_12A_n66A	15	5+5	CP-64QAM	M+M	Outer_Full	4.4976	5.276	PASS
DC_12A_n66A	15	5+5	CP-256QAM	M+M	Outer_Full	4.4957	5.128	PASS
DC_12A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	9.3213	10.20	PASS
DC_12A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	9.3183	10.17	PASS
DC_12A_n66A	15	5+10	CP-64QAM	M+M	Outer_Full	9.2894	10.22	PASS
DC_12A_n66A	15	5+10	CP-256QAM	M+M	Outer_Full	9.3191	10.18	PASS
DC_12A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	14.134	15.21	PASS
DC_12A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	14.103	15.16	PASS
DC_12A_n66A	15	5+15	CP-64QAM	M+M	Outer_Full	14.111	15.10	PASS
DC_12A_n66A	15	5+15	CP-256QAM	M+M	Outer_Full	14.141	15.16	PASS
DC_12A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	18.916	20.17	PASS
DC_12A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	18.944	20.07	PASS
DC_12A_n66A	15	5+20	CP-64QAM	M+M	Outer_Full	18.967	20.15	PASS
DC_12A_n66A	15	5+20	CP-256QAM	M+M	Outer_Full	18.941	20.19	PASS
DC_12A_n66A	15	5+25	CP-QPSK	M+M	Outer_Full	23.738	25.00	PASS
DC_12A_n66A	15	5+25	CP-16QAM	M+M	Outer_Full	23.790	25.10	PASS
DC_12A_n66A	15	5+25	CP-64QAM	M+M	Outer_Full	23.770	25.07	PASS
DC_12A_n66A	15	5+25	CP-256QAM	M+M	Outer_Full	23.733	25.14	PASS
DC_12A_n66A	15	5+30	CP-QPSK	M+M	Outer_Full	28.586	30.14	PASS
DC_12A_n66A	15	5+30	CP-16QAM	M+M	Outer_Full	28.640	30.23	PASS
DC_12A_n66A	15	5+30	CP-64QAM	M+M	Outer_Full	28.644	30.21	PASS
DC_12A_n66A	15	5+30	CP-256QAM	M+M	Outer_Full	28.501	30.11	PASS
DC_12A_n66A	15	5+40	CP-QPSK	M+M	Outer_Full	38.498	40.34	PASS
DC_12A_n66A	15	5+40	CP-16QAM	M+M	Outer_Full	38.575	40.47	PASS
DC_12A_n66A	15	5+40	CP-64QAM	M+M	Outer_Full	38.564	40.45	PASS
DC_12A_n66A	15	5+40	CP-256QAM	M+M	Outer_Full	38.614	40.46	PASS
DC_12A-n66A	15	5+35	DFT-QPSK	M+M	Outer_Full	32.1	33.670	PASS
DC_12A-n66A	15	5+35	DFT-16QAM	M+M	Outer_Full	32.161	33.810	PASS
DC_12A-n66A	15	5+35	DFT-64QAM	M+M	Outer_Full	32.131	33.670	PASS
DC_12A-n66A	15	5+35	DFT-256QAM	M+M	Outer_Full	32.143	33.740	PASS

Test Graphs



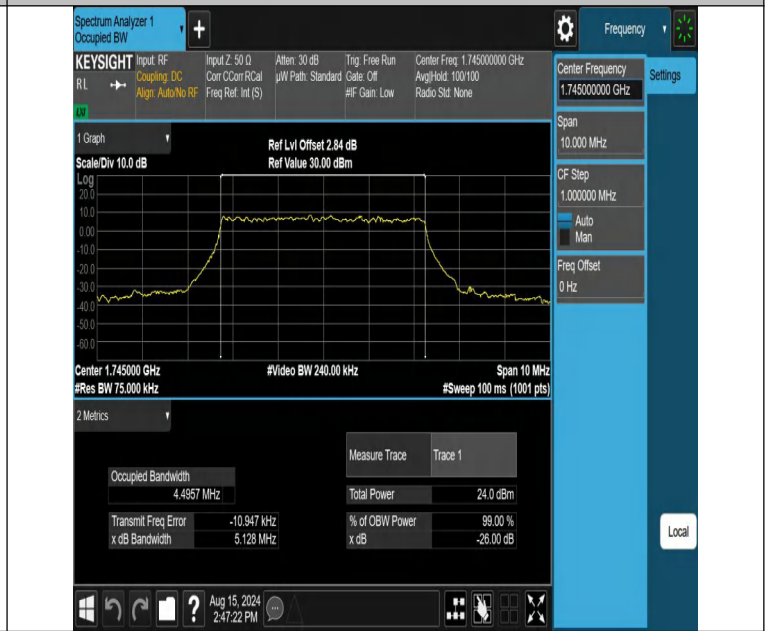
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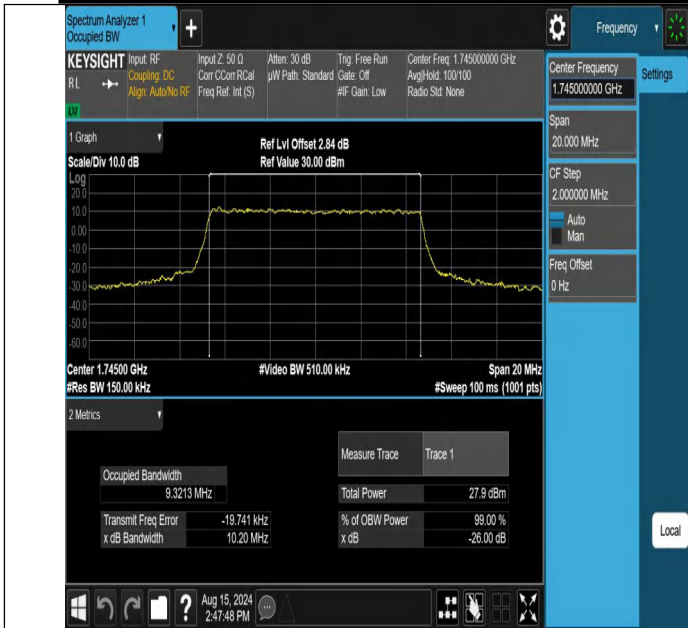
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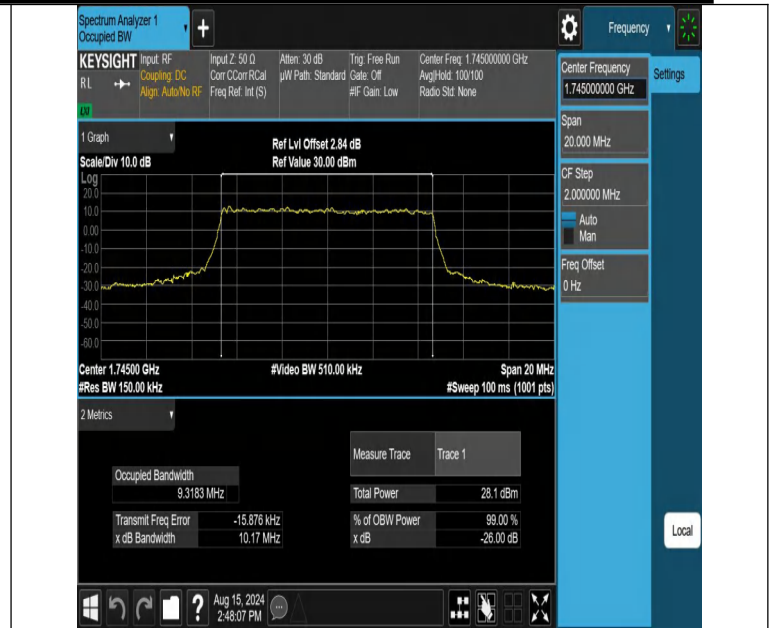
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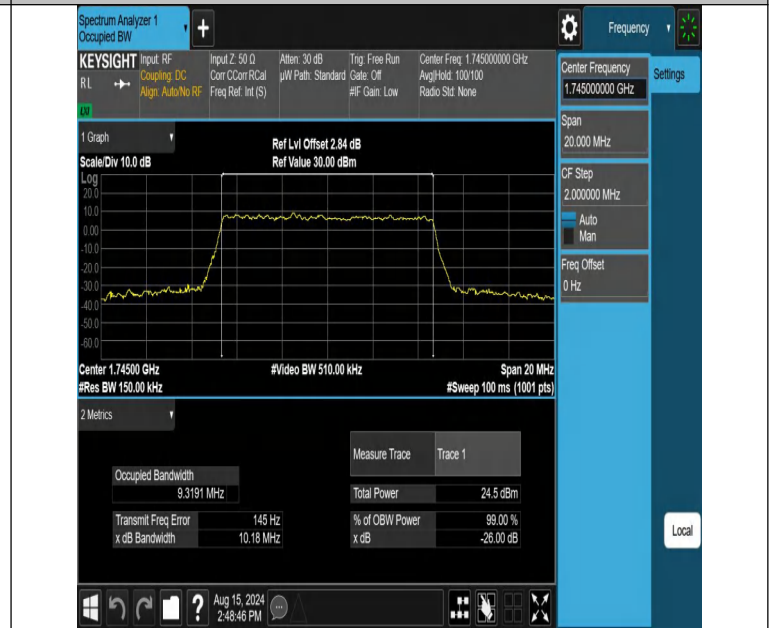
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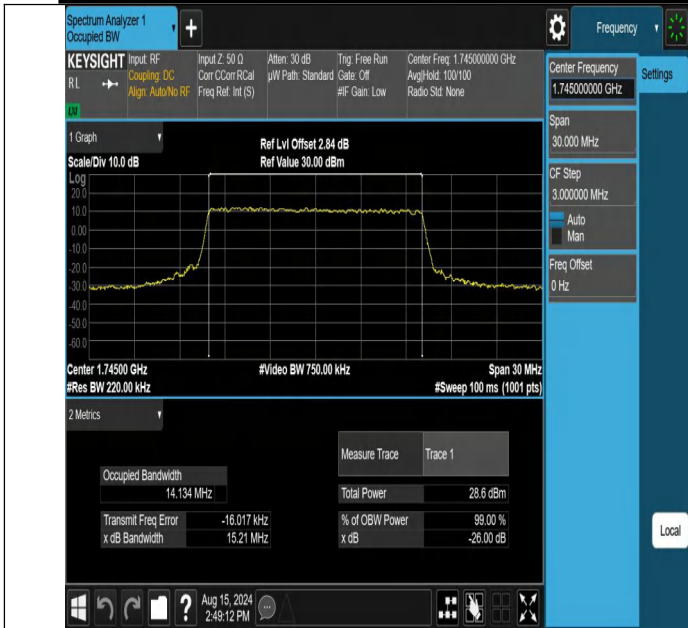
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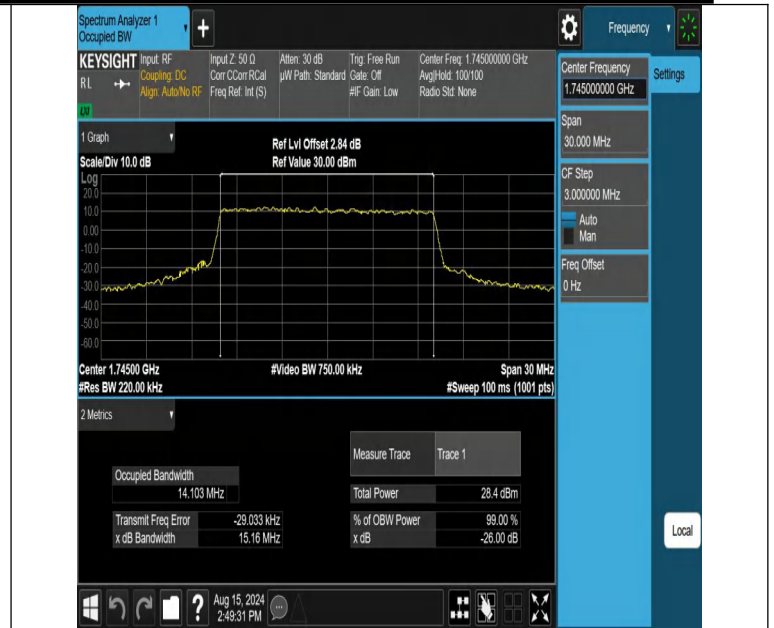
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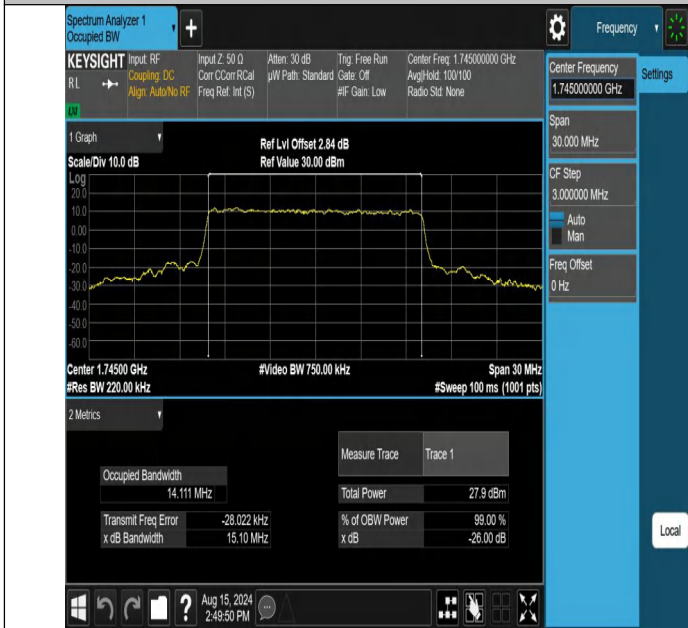
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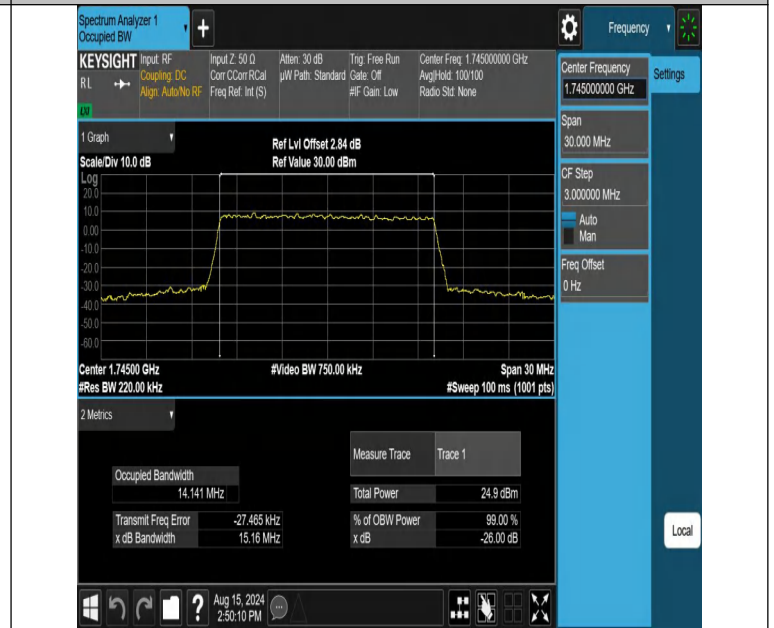
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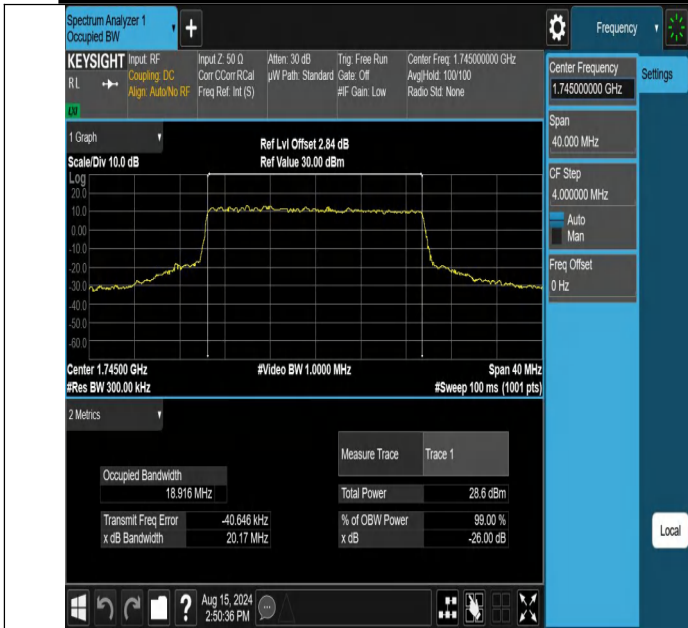
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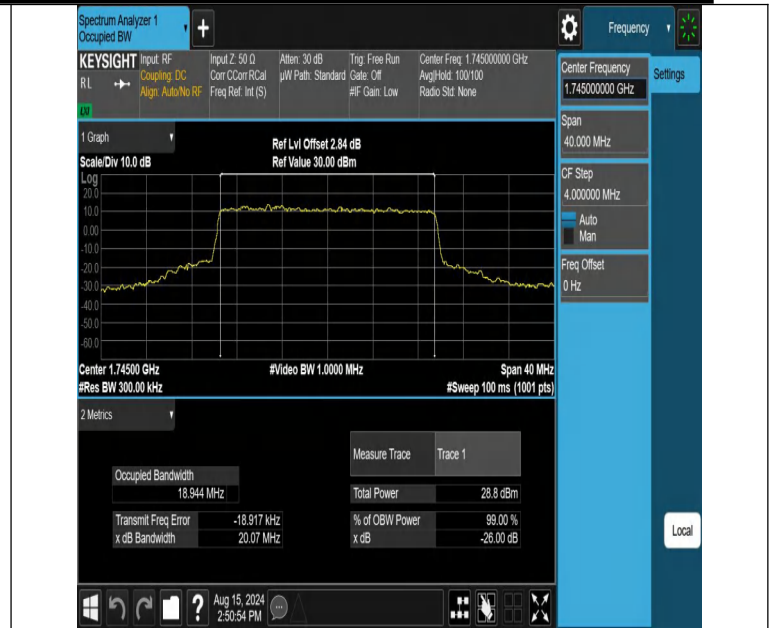
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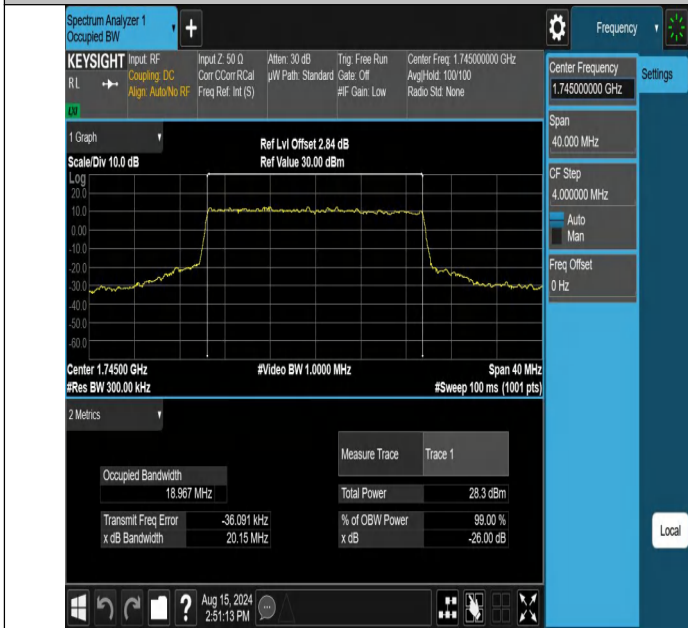
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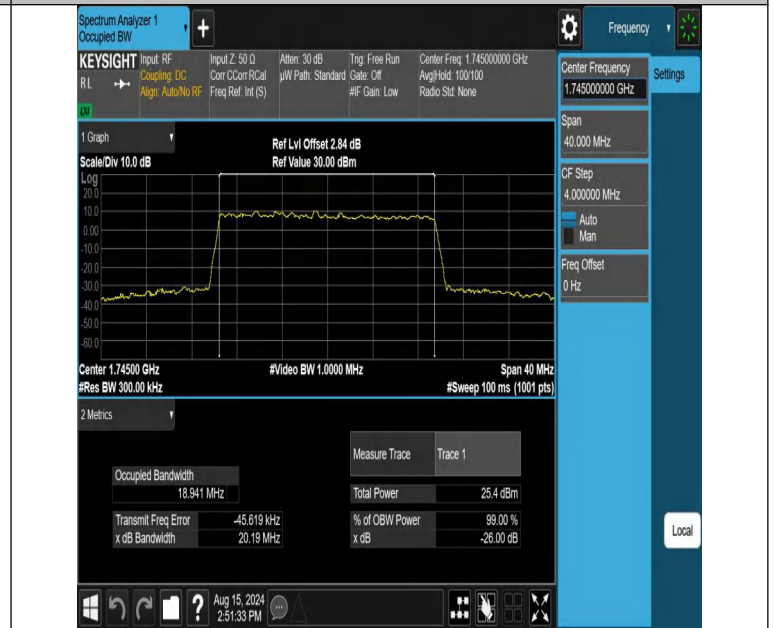
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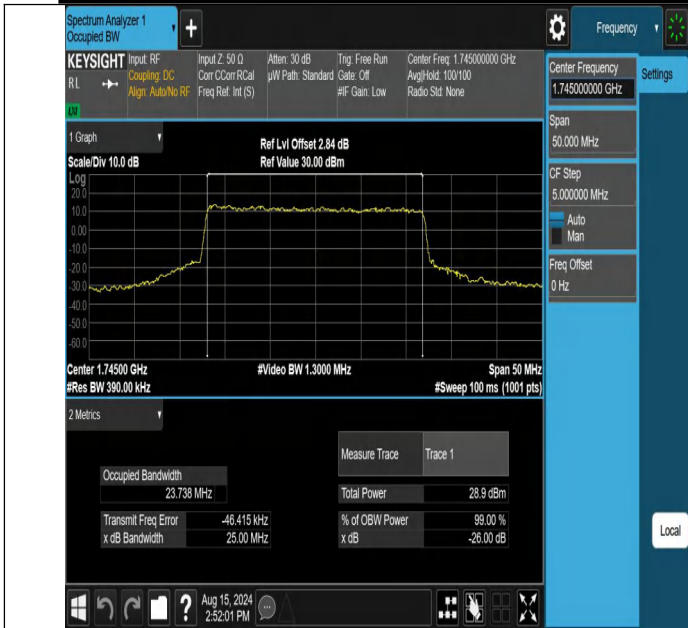
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NTNV-DC_12A_n66A-PC3-15-5+20-M+M-N/A-N/A-CP-256QAM-Outer_Full-Ant1-PASS



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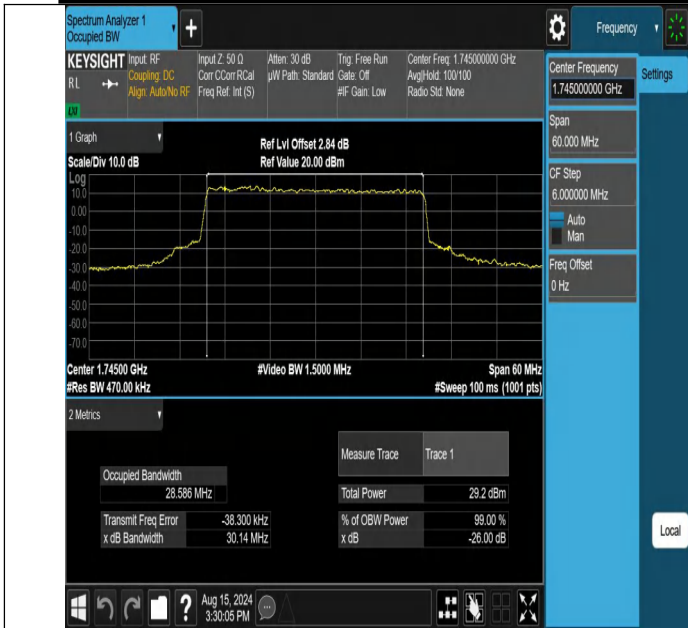
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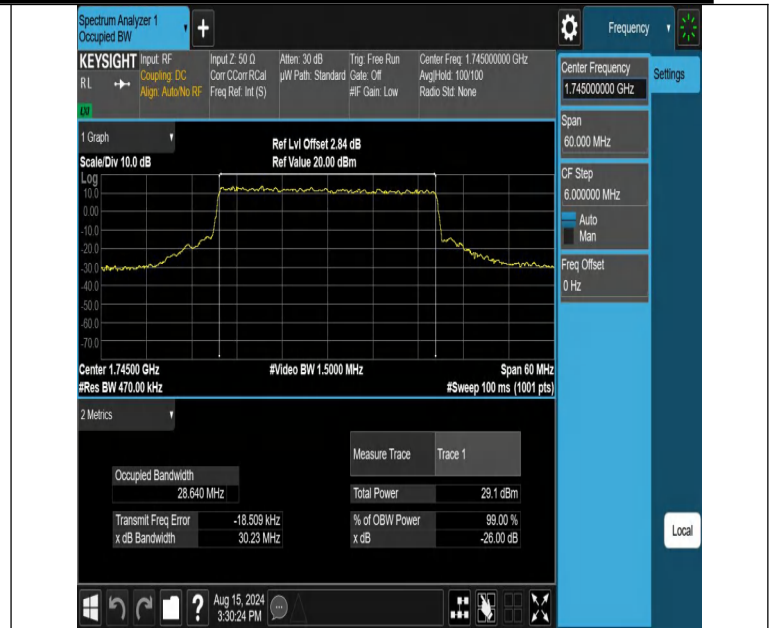
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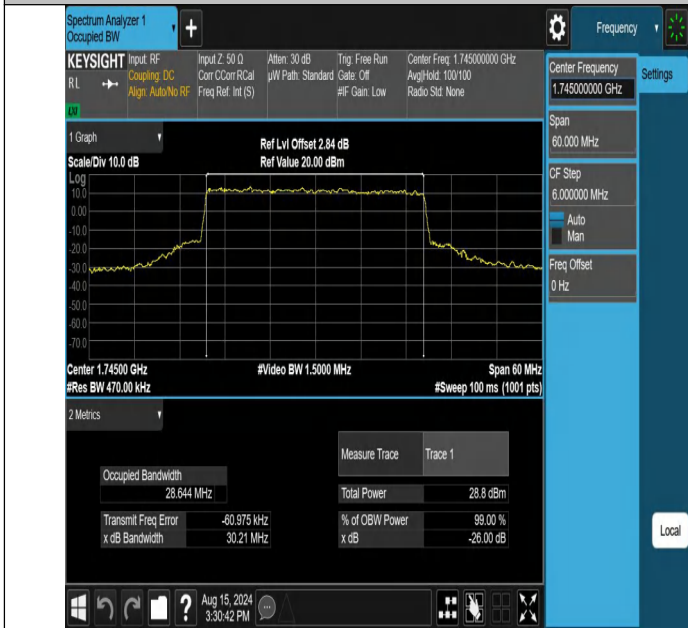
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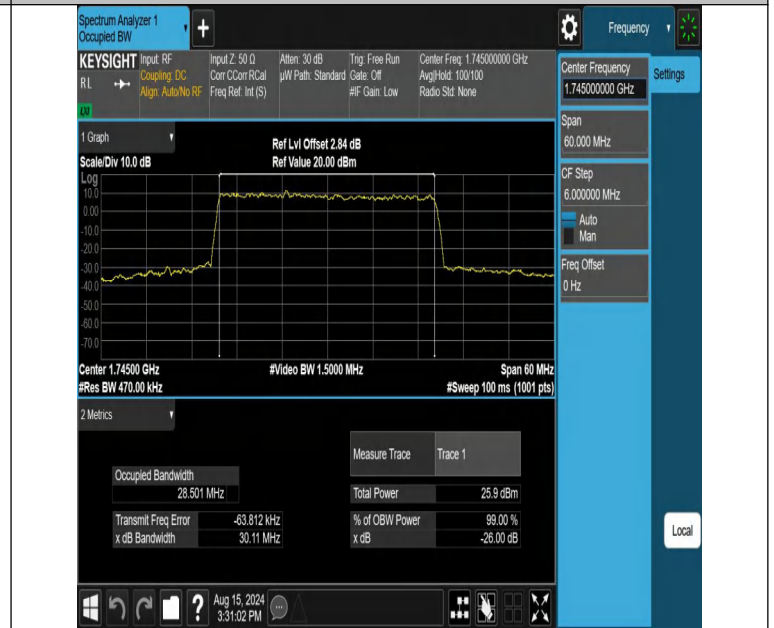
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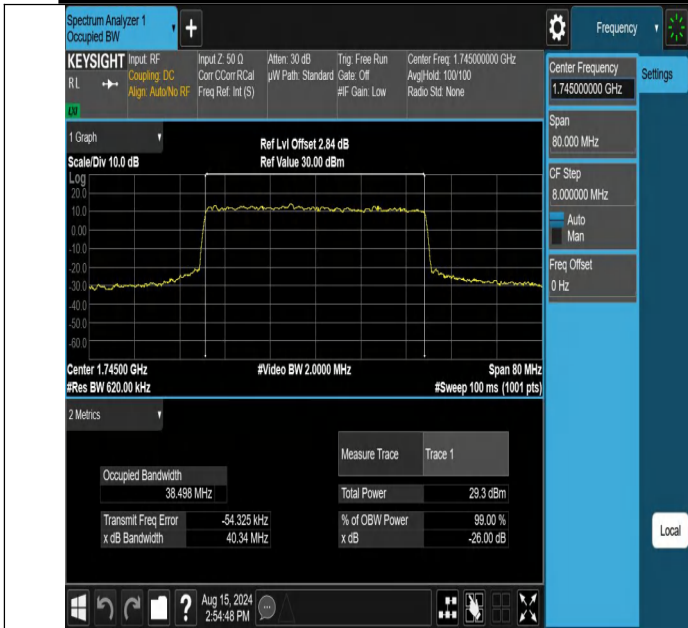
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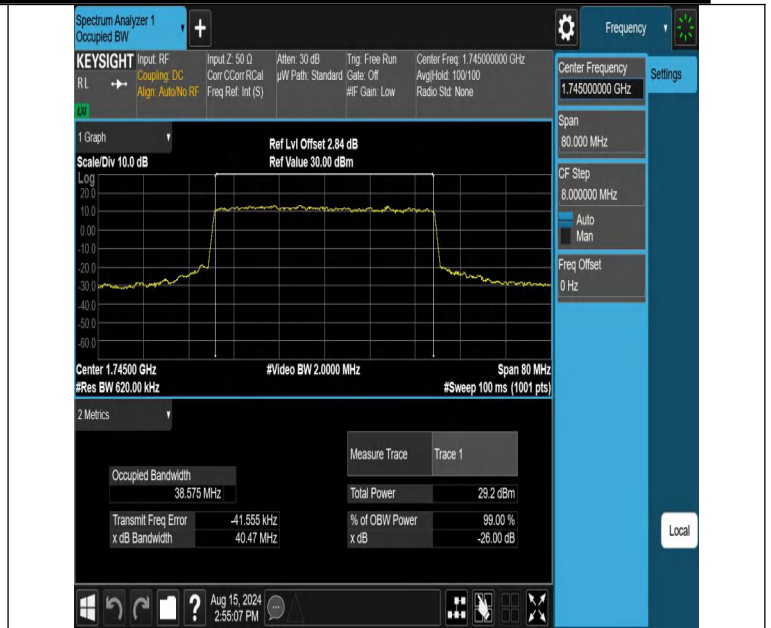
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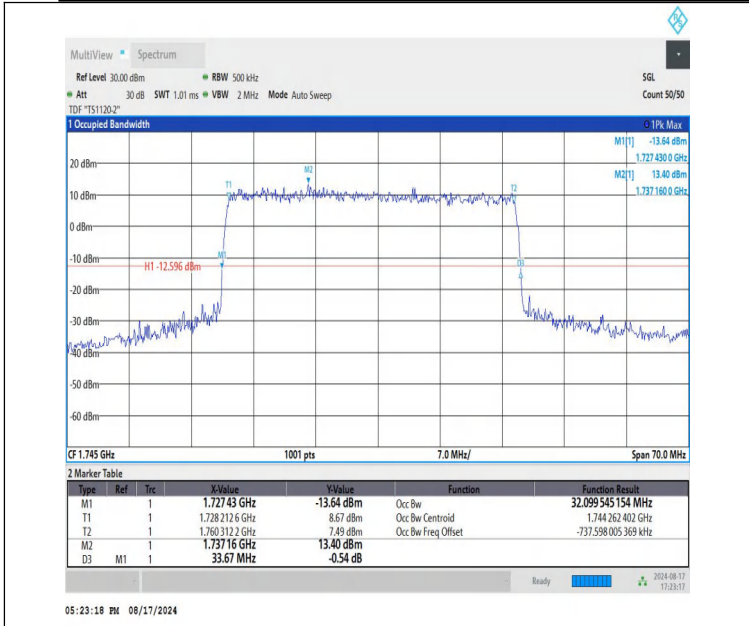
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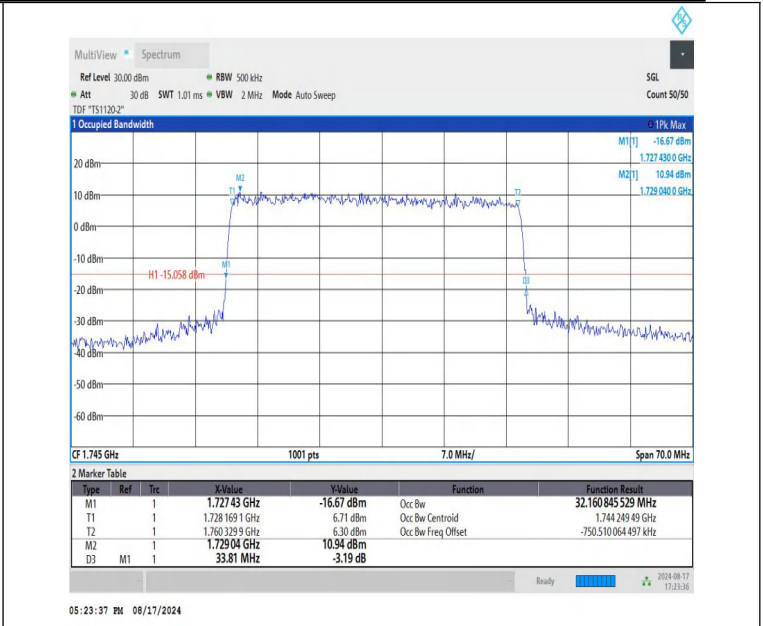
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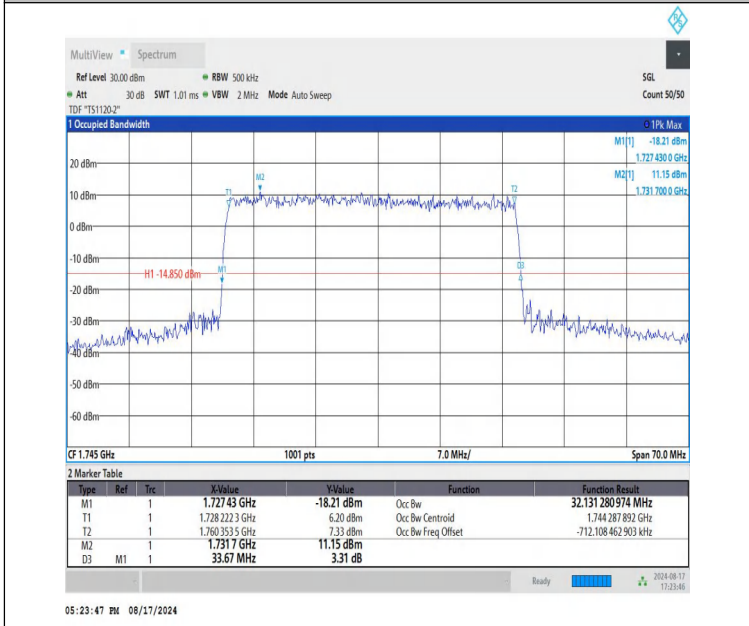
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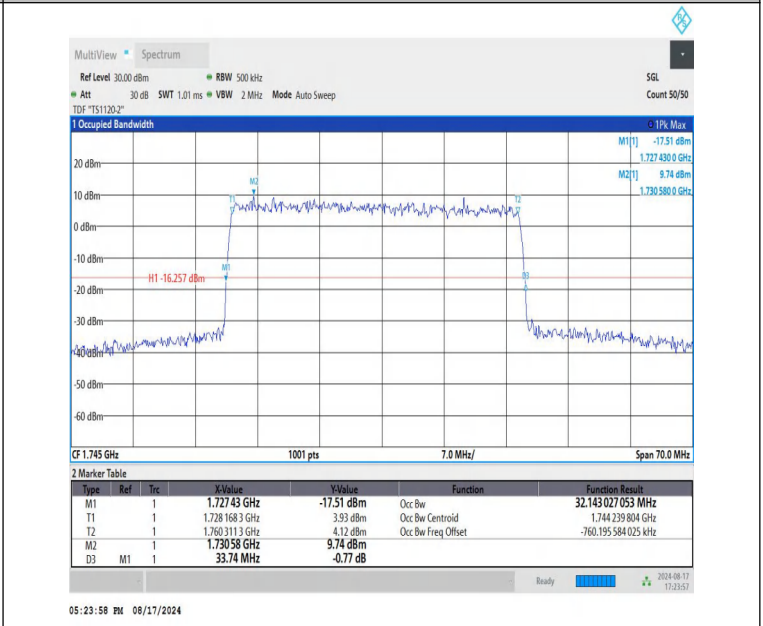
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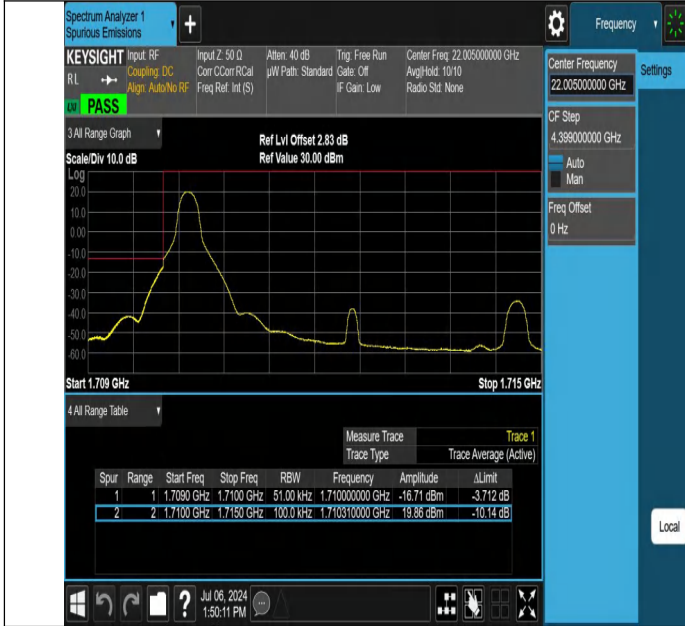
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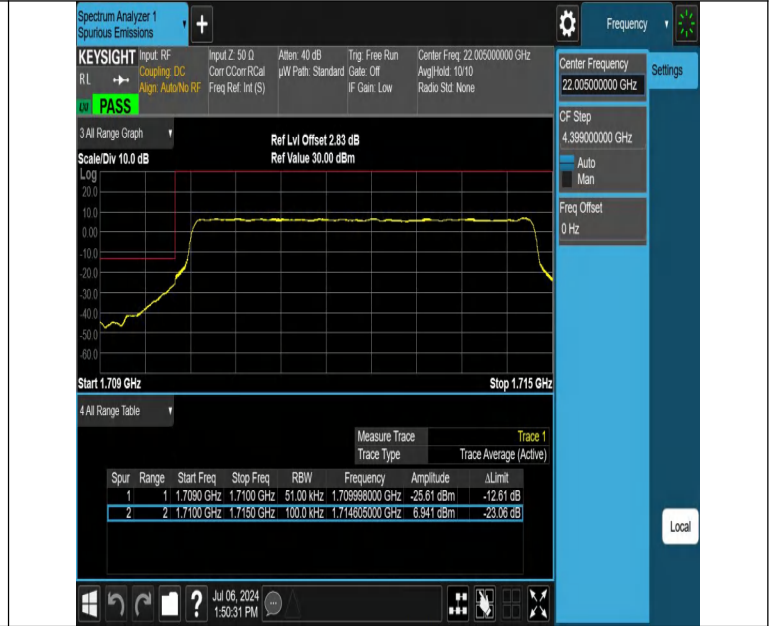
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Band Edge for NSA

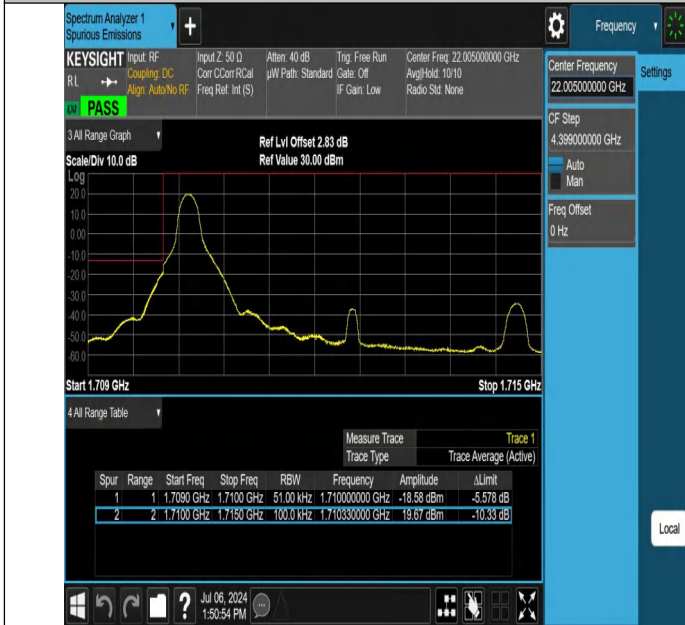
Test Graphs



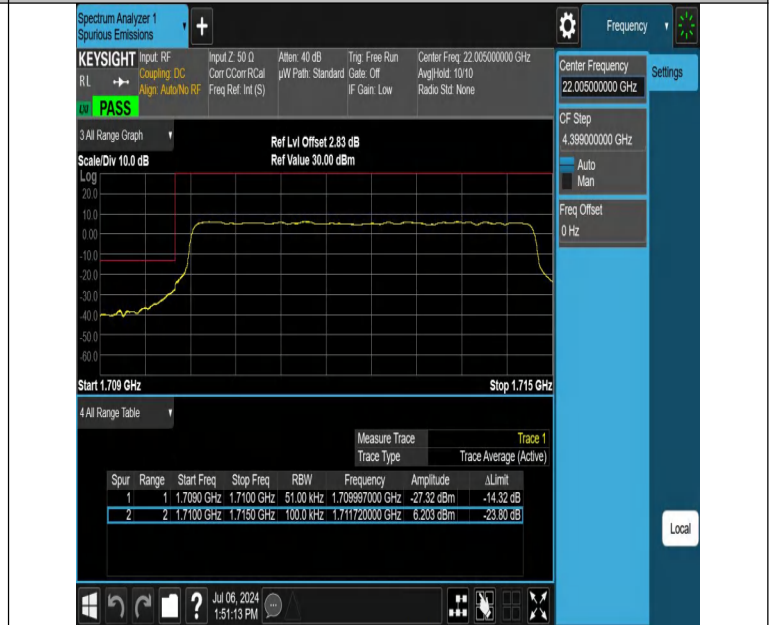
NTNV-DC_12A_n66A-PC3-15-5+5-M+L-N/A-N/A-DFT-PI2BPSK-E dge_1RB_Left-PASS



NTNV-DC_12A_n66A-PC3-15-5+5-M+L-N/A-N/A-DFT-PI2BPSK-Out er_Full-PASS



NTNV-DC_12A_n66A-PC3-15-5+5-M+L-N/A-N/A-DFT-QPSK-Edge _1RB_Left-PASS



NTNV-DC_12A_n66A-PC3-15-5+5-M+L-N/A-N/A-DFT-QPSK-Outer _Full-PASS