

## Effective (Isotropic) Radiated Power Output Data for NSA

### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Limit (dBm)	Verdict
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	22.46	23.83	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	22.23	23.6	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	22.13	23.5	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+L	Inner_Full	22.84	24.21	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+M	Inner_Full	22.69	24.06	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+H	Inner_Full	22.61	23.98	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.5	23.87	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.12	23.49	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.01	23.38	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.46	23.83	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.14	23.51	33	PASS
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.1	23.47	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+L	Outer_Full	21.96	23.33	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+M	Outer_Full	21.77	23.14	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+H	Outer_Full	21.78	23.15	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+L	Inner_Full	23.05	24.42	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+M	Inner_Full	22.8	24.17	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+H	Inner_Full	22.64	24.01	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	21.94	23.31	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	21.99	23.36	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	21.52	22.89	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	21.91	23.28	33	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	21.98	23.35	33	PASS

DC_12A_n25A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	21.44	22.81	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+L	Outer_Full	21	22.37	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+M	Outer_Full	20.77	22.14	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+H	Outer_Full	20.7	22.07	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+L	Inner_Full	22.04	23.41	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+M	Inner_Full	21.91	23.28	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+H	Inner_Full	21.86	23.23	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	20.89	22.26	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	20.82	22.19	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	20.53	21.9	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	20.86	22.23	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	20.74	22.11	33	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	20.53	21.9	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+L	Outer_Full	20.47	21.84	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+M	Outer_Full	20.22	21.59	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+H	Outer_Full	20.09	21.46	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+L	Inner_Full	20.46	21.83	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+M	Inner_Full	20.51	21.88	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+H	Inner_Full	20.25	21.62	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	20.63	22	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	20.68	22.05	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	20.34	21.71	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	20.53	21.9	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	20.66	22.03	33	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	20.34	21.71	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+L	Outer_Full	18.56	19.93	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+M	Outer_Full	18.36	19.73	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+H	Outer_Full	18.4	19.77	33	PASS

DC_12A_n25A	15	5+5	DFT-256QAM	M+L	Inner_Full	18.54	19.91	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+M	Inner_Full	18.42	19.79	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+H	Inner_Full	18.13	19.5	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	18.8	20.17	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	18.18	19.55	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	18.12	19.49	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	18.81	20.18	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	18.22	19.59	33	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	18.15	19.52	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+L	Outer_Full	22.37	23.74	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+M	Outer_Full	22.2	23.57	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+H	Outer_Full	22.11	23.48	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+L	Inner_Full	22.94	24.31	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+M	Inner_Full	22.84	24.21	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+H	Inner_Full	22.64	24.01	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.38	23.75	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.16	23.53	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.98	23.35	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.36	23.73	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.21	23.58	33	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.08	23.45	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+L	Outer_Full	22.01	23.38	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+M	Outer_Full	21.87	23.24	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+H	Outer_Full	21.71	23.08	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+L	Inner_Full	23.02	24.39	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+M	Inner_Full	22.92	24.29	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+H	Inner_Full	22.74	24.11	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.78	23.15	33	PASS

DC_12A_n25A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	21.62	22.99	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	21.86	23.23	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	21.76	23.13	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	21.68	23.05	33	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	21.77	23.14	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+L	Outer_Full	21.1	22.47	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+M	Outer_Full	20.94	22.31	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+H	Outer_Full	20.72	22.09	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+L	Inner_Full	22.02	23.39	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+M	Inner_Full	21.98	23.35	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+H	Inner_Full	21.84	23.21	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.91	22.28	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.95	22.32	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	20.75	22.12	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.89	22.26	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	21	22.37	33	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	20.83	22.2	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+L	Outer_Full	20.44	21.81	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+M	Outer_Full	20.34	21.71	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+H	Outer_Full	20.29	21.66	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+L	Inner_Full	20.44	21.81	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+M	Inner_Full	20.32	21.69	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+H	Inner_Full	20.23	21.6	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Left	20.73	22.1	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Left	20.02	21.39	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Left	20.51	21.88	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+L	Edge_1RB_Right	20.7	22.07	33	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+M	Edge_1RB_Right	20.02	21.39	33	PASS

DC_12A_n25A	15	5+10	DFT-64QAM	M+H	Edge_1RB_Right	20.59	21.96	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+L	Outer_Full	18.48	19.85	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+M	Outer_Full	18.3	19.67	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+H	Outer_Full	18.18	19.55	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+L	Inner_Full	18.43	19.8	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+M	Inner_Full	18.25	19.62	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+H	Inner_Full	18.32	19.69	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Left	18.4	19.77	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Left	18.39	19.76	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Left	18.17	19.54	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+L	Edge_1RB_Right	18.36	19.73	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+M	Edge_1RB_Right	18.36	19.73	33	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+H	Edge_1RB_Right	18.21	19.58	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+L	Outer_Full	22.34	23.71	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+M	Outer_Full	22.45	23.82	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+H	Outer_Full	22.25	23.62	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+L	Inner_Full	22.87	24.24	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+M	Inner_Full	22.9	24.27	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+H	Inner_Full	22.73	24.1	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.47	23.84	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.49	23.86	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.32	23.69	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.33	23.7	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.33	23.7	33	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.29	23.66	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+L	Outer_Full	22.07	23.44	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+M	Outer_Full	22.07	23.44	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+H	Outer_Full	21.87	23.24	33	PASS

DC_12A_n25A	15	5+15	DFT-QPSK	M+L	Inner_Full	23.04	24.41	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+M	Inner_Full	23.05	24.42	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+H	Inner_Full	22.81	24.18	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	22.24	23.61	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	22.03	23.4	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	21.72	23.09	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	22.04	23.41	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	22.03	23.4	33	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	21.96	23.33	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+L	Outer_Full	21.1	22.47	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+M	Outer_Full	21.05	22.42	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+H	Outer_Full	20.91	22.28	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+L	Inner_Full	22.02	23.39	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+M	Inner_Full	22.03	23.4	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+H	Inner_Full	21.78	23.15	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.91	22.28	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.88	22.25	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	20.82	22.19	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	20.73	22.1	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.92	22.29	33	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	20.85	22.22	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+L	Outer_Full	20.52	21.89	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+M	Outer_Full	20.45	21.82	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+H	Outer_Full	20.36	21.73	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+L	Inner_Full	20.54	21.91	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+M	Inner_Full	20.5	21.87	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+H	Inner_Full	20.34	21.71	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Left	20.13	21.5	33	PASS

DC_12A_n25A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Left	20.39	21.76	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Left	20.13	21.5	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+L	Edge_1RB_Right	20.02	21.39	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+M	Edge_1RB_Right	20.35	21.72	33	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+H	Edge_1RB_Right	20.17	21.54	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+L	Outer_Full	18.51	19.88	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+M	Outer_Full	18.51	19.88	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+H	Outer_Full	18.36	19.73	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+L	Inner_Full	18.53	19.9	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+M	Inner_Full	18.52	19.89	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+H	Inner_Full	18.3	19.67	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Left	18.82	20.19	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Left	18.96	20.33	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Left	18.55	19.92	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+L	Edge_1RB_Right	18.56	19.93	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+M	Edge_1RB_Right	18.9	20.27	33	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+H	Edge_1RB_Right	18.56	19.93	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	22.4	23.77	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	22.48	23.85	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	22.29	23.66	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+L	Inner_Full	22.86	24.23	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+M	Inner_Full	22.9	24.27	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+H	Inner_Full	22.74	24.11	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.56	23.93	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.3	23.67	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.35	23.72	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.28	23.65	33	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.13	23.5	33	PASS

DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.41	23.78	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+L	Outer_Full	21.98	23.35	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+M	Outer_Full	22.05	23.42	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+H	Outer_Full	21.84	23.21	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+L	Inner_Full	22.99	24.36	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+M	Inner_Full	23.02	24.39	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+H	Inner_Full	22.84	24.21	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.98	23.35	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.01	23.38	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	21.77	23.14	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.8	23.17	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.04	23.41	33	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.78	23.15	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+L	Outer_Full	21	22.37	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+M	Outer_Full	21.03	22.4	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+H	Outer_Full	20.91	22.28	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+L	Inner_Full	21.95	23.32	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+M	Inner_Full	21.92	23.29	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+H	Inner_Full	21.84	23.21	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.21	22.58	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.19	22.56	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.08	22.45	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.08	22.45	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.23	22.6	33	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	20.9	22.27	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+L	Outer_Full	20.47	21.84	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+M	Outer_Full	20.43	21.8	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+H	Outer_Full	20.43	21.8	33	PASS



DC_12A_n25A	15	5+20	DFT-64QAM	M+L	Inner_Full	20.38	21.75	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+M	Inner_Full	20.43	21.8	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+H	Inner_Full	20.37	21.74	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.82	22.19	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	20.74	22.11	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	20.43	21.8	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	20.65	22.02	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	20.79	22.16	33	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	20.46	21.83	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+L	Outer_Full	18.4	19.77	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+M	Outer_Full	18.43	19.8	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+H	Outer_Full	18.34	19.71	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+L	Inner_Full	18.37	19.74	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+M	Inner_Full	18.52	19.89	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+H	Inner_Full	18.34	19.71	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	18.51	19.88	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	18.64	20.01	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.5	19.87	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	18.24	19.61	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	18.62	19.99	33	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	18.49	19.86	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+L	Outer_Full	22.53	23.9	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+M	Outer_Full	22.52	23.89	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+H	Outer_Full	22.15	23.52	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+L	Inner_Full	23.06	24.43	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+M	Inner_Full	22.87	24.24	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+H	Inner_Full	22.57	23.94	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.69	24.06	33	PASS

DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.47	23.84	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.12	23.49	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.42	23.79	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.39	23.76	33	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.15	23.52	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+L	Outer_Full	22.14	23.51	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+M	Outer_Full	22.04	23.41	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+H	Outer_Full	21.78	23.15	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+L	Inner_Full	23.1	24.47	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+M	Inner_Full	23.02	24.39	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+H	Inner_Full	22.73	24.1	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+L	Edge_1RB_Left	22.19	23.56	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+M	Edge_1RB_Left	22.16	23.53	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+H	Edge_1RB_Left	21.84	23.21	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+L	Edge_1RB_Right	21.94	23.31	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+M	Edge_1RB_Right	22.08	23.45	33	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+H	Edge_1RB_Right	21.66	23.03	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+L	Outer_Full	21.07	22.44	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+M	Outer_Full	21.08	22.45	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+H	Outer_Full	20.72	22.09	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+L	Inner_Full	22.06	23.43	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+M	Inner_Full	21.99	23.36	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+H	Inner_Full	21.71	23.08	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+L	Edge_1RB_Left	21.41	22.78	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+M	Edge_1RB_Left	21.04	22.41	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+H	Edge_1RB_Left	21.05	22.42	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+L	Edge_1RB_Right	21.16	22.53	33	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+M	Edge_1RB_Right	21.02	22.39	33	PASS

DC_12A_n25A	15	5+25	DFT-16QAM	M+H	Edge_1RB_Right	20.84	22.21	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+L	Outer_Full	20.54	21.91	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+M	Outer_Full	20.5	21.87	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+H	Outer_Full	20.28	21.65	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+L	Inner_Full	20.49	21.86	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+M	Inner_Full	20.49	21.86	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+H	Inner_Full	20.24	21.61	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+L	Edge_1RB_Left	20.71	22.08	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+M	Edge_1RB_Left	20.75	22.12	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+H	Edge_1RB_Left	20.23	21.6	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+L	Edge_1RB_Right	20.5	21.87	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+M	Edge_1RB_Right	20.82	22.19	33	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+H	Edge_1RB_Right	20.13	21.5	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+L	Outer_Full	18.52	19.89	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+M	Outer_Full	18.47	19.84	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+H	Outer_Full	18.23	19.6	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+L	Inner_Full	18.55	19.92	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+M	Inner_Full	18.44	19.81	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+H	Inner_Full	18.2	19.57	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+L	Edge_1RB_Left	19.02	20.39	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+M	Edge_1RB_Left	18.87	20.24	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+H	Edge_1RB_Left	18.59	19.96	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+L	Edge_1RB_Right	18.7	20.07	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+M	Edge_1RB_Right	18.79	20.16	33	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+H	Edge_1RB_Right	18.34	19.71	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+L	Outer_Full	22.59	23.96	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+M	Outer_Full	22.52	23.89	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+H	Outer_Full	22.12	23.49	33	PASS

DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+L	Inner_Full	23.05	24.42	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+M	Inner_Full	22.86	24.23	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+H	Inner_Full	22.52	23.89	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.54	23.91	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.41	23.78	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.23	23.6	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.39	23.76	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.28	23.65	33	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.08	23.45	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+L	Outer_Full	22.12	23.49	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+M	Outer_Full	22.03	23.4	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+H	Outer_Full	21.86	23.23	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+L	Inner_Full	23.12	24.49	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+M	Inner_Full	23	24.37	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+H	Inner_Full	22.7	24.07	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+L	Edge_1RB_Left	22.27	23.64	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+M	Edge_1RB_Left	22.19	23.56	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+H	Edge_1RB_Left	21.72	23.09	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+L	Edge_1RB_Right	22.01	23.38	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+M	Edge_1RB_Right	22.01	23.38	33	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+H	Edge_1RB_Right	21.53	22.9	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+L	Outer_Full	21.12	22.49	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+M	Outer_Full	21	22.37	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+H	Outer_Full	20.89	22.26	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+L	Inner_Full	22.12	23.49	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+M	Inner_Full	21.98	23.35	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+H	Inner_Full	21.76	23.13	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+L	Edge_1RB_Left	21.4	22.77	33	PASS

DC_12A_n25A	15	5+30	DFT-16QAM	M+M	Edge_1RB_Left	21.1	22.47	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+H	Edge_1RB_Left	20.88	22.25	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+L	Edge_1RB_Right	21.13	22.5	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+M	Edge_1RB_Right	20.95	22.32	33	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+H	Edge_1RB_Right	20.75	22.12	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+L	Outer_Full	20.57	21.94	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+M	Outer_Full	20.53	21.9	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+H	Outer_Full	20.36	21.73	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+L	Inner_Full	20.63	22	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+M	Inner_Full	20.49	21.86	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+H	Inner_Full	20.18	21.55	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+L	Edge_1RB_Left	20.6	21.97	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+M	Edge_1RB_Left	20.51	21.88	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+H	Edge_1RB_Left	20.35	21.72	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+L	Edge_1RB_Right	20.36	21.73	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+M	Edge_1RB_Right	20.32	21.69	33	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+H	Edge_1RB_Right	20.22	21.59	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+L	Outer_Full	18.63	20	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+M	Outer_Full	18.55	19.92	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+H	Outer_Full	18.31	19.68	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+L	Inner_Full	18.58	19.95	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+M	Inner_Full	18.5	19.87	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+H	Inner_Full	18.14	19.51	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+L	Edge_1RB_Left	19.27	20.64	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+M	Edge_1RB_Left	18.84	20.21	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+H	Edge_1RB_Left	18.22	19.59	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+L	Edge_1RB_Right	19.02	20.39	33	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+M	Edge_1RB_Right	18.65	20.02	33	PASS

DC_12A_n25A	15	5+30	DFT-256QAM	M+H	Edge_1RB_Right	18.08	19.45	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+L	Outer_Full	22.55	23.92	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+M	Outer_Full	22.26	23.63	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+H	Outer_Full	22.16	23.53	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+L	Inner_Full	22.97	24.34	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+M	Inner_Full	22.71	24.08	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+H	Inner_Full	22.6	23.97	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.53	23.9	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Left	22.33	23.7	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.1	23.47	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.33	23.7	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.13	23.5	33	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.12	23.49	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+L	Outer_Full	22.18	23.55	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+M	Outer_Full	21.82	23.19	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+H	Outer_Full	21.81	23.18	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+L	Inner_Full	23.06	24.43	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+M	Inner_Full	22.81	24.18	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+H	Inner_Full	22.81	24.18	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.26	23.63	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.81	23.18	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.73	23.1	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+L	Edge_1RB_Right	22.09	23.46	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.7	23.07	33	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.64	23.01	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+L	Outer_Full	21.12	22.49	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+M	Outer_Full	20.84	22.21	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+H	Outer_Full	20.72	22.09	33	PASS

DC_12A_n25A	15	5+40	DFT-16QAM	M+L	Inner_Full	22.1	23.47	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+M	Inner_Full	21.79	23.16	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+H	Inner_Full	21.77	23.14	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+L	Edge_1RB_Left	21.51	22.88	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.9	22.27	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.81	22.18	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+L	Edge_1RB_Right	21.28	22.65	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.82	22.19	33	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.66	22.03	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+L	Outer_Full	20.56	21.93	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+M	Outer_Full	20.33	21.7	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+H	Outer_Full	20.23	21.6	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+L	Inner_Full	20.63	22	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+M	Inner_Full	20.33	21.7	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+H	Inner_Full	20.24	21.61	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+L	Edge_1RB_Left	21.01	22.38	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+M	Edge_1RB_Left	20.51	21.88	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+H	Edge_1RB_Left	20.16	21.53	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+L	Edge_1RB_Right	20.79	22.16	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+M	Edge_1RB_Right	20.43	21.8	33	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+H	Edge_1RB_Right	20.1	21.47	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+L	Outer_Full	18.67	20.04	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+M	Outer_Full	18.37	19.74	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+H	Outer_Full	18.25	19.62	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+L	Inner_Full	18.61	19.98	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+M	Inner_Full	18.27	19.64	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+H	Inner_Full	18.33	19.7	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+L	Edge_1RB_Left	18.77	20.14	33	PASS

DC_12A_n25A	15	5+40	DFT-256QAM	M+M	Edge_1RB_Left	18.41	19.78	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+H	Edge_1RB_Left	18.43	19.8	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+L	Edge_1RB_Right	18.68	20.05	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+M	Edge_1RB_Right	18.17	19.54	33	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+H	Edge_1RB_Right	18.33	19.7	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+L	Edge_1RB_Left	22.71	24.08	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+L	Outer_Full	22.82	24.19	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+L	Inner_Full	23.31	24.68	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+L	Edge_1RB_Right	22.47	23.84	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+L	Edge_1RB_Left	22.2	23.57	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+L	Outer_Full	22.25	23.62	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+L	Inner_Full	23.3	24.67	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+L	Edge_1RB_Right	21.96	23.33	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+L	Edge_1RB_Left	21.35	22.72	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+L	Outer_Full	21.29	22.66	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+L	Inner_Full	22.29	23.66	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+L	Edge_1RB_Right	21.06	22.43	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+L	Edge_1RB_Left	20.69	22.06	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+L	Outer_Full	20.83	22.2	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+L	Inner_Full	20.78	22.15	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+L	Edge_1RB_Right	20.38	21.75	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+L	Edge_1RB_Left	18.54	19.91	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+L	Outer_Full	18.81	20.18	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+L	Inner_Full	18.71	20.08	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+L	Edge_1RB_Right	18.27	19.64	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+M	Edge_1RB_Left	22.28	23.65	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+M	Outer_Full	22.46	23.83	33	PASS



DC_12A-n25A	15	5+35	DFT-BPSK	M+M	Inner_Full	22.93	24.3	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+M	Edge_1RB_Right	22.18	23.55	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+M	Edge_1RB_Left	21.84	23.21	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+M	Outer_Full	21.98	23.35	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+M	Inner_Full	22.83	24.2	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+M	Edge_1RB_Right	21.74	23.11	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+M	Edge_1RB_Left	20.98	22.35	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+M	Outer_Full	20.83	22.2	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+M	Inner_Full	21.96	23.33	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+M	Edge_1RB_Right	20.86	22.23	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+M	Edge_1RB_Left	20.31	21.68	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+M	Outer_Full	20.49	21.86	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+M	Inner_Full	20.4	21.77	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+M	Edge_1RB_Right	20.24	21.61	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+M	Edge_1RB_Left	18.08	19.45	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+M	Outer_Full	18.42	19.79	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+M	Inner_Full	18.45	19.82	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+M	Edge_1RB_Right	17.99	19.36	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+H	Edge_1RB_Left	22.18	23.55	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+H	Outer_Full	22.34	23.71	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+H	Inner_Full	22.85	24.22	33	PASS
DC_12A-n25A	15	5+35	DFT-BPSK	M+H	Edge_1RB_Right	22.44	23.81	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+H	Edge_1RB_Left	21.67	23.04	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+H	Outer_Full	21.99	23.36	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+H	Inner_Full	22.9	24.27	33	PASS
DC_12A-n25A	15	5+35	DFT-QPSK	M+H	Edge_1RB_Right	21.89	23.26	33	PASS

DC_12A-n25A	15	5+35	DFT-16QAM	M+H	Edge_1RB_Left	20.81	22.18	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+H	Outer_Full	20.91	22.28	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+H	Inner_Full	21.94	23.31	33	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+H	Edge_1RB_Right	21.04	22.41	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+H	Edge_1RB_Left	20.14	21.51	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+H	Outer_Full	20.43	21.8	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+H	Inner_Full	20.41	21.78	33	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+H	Edge_1RB_Right	20.37	21.74	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+H	Edge_1RB_Left	17.99	19.36	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+H	Outer_Full	18.43	19.8	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+H	Inner_Full	18.39	19.76	33	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+H	Edge_1RB_Right	18.08	19.45	33	PASS

## Peak-to-Average Ratio for NSA

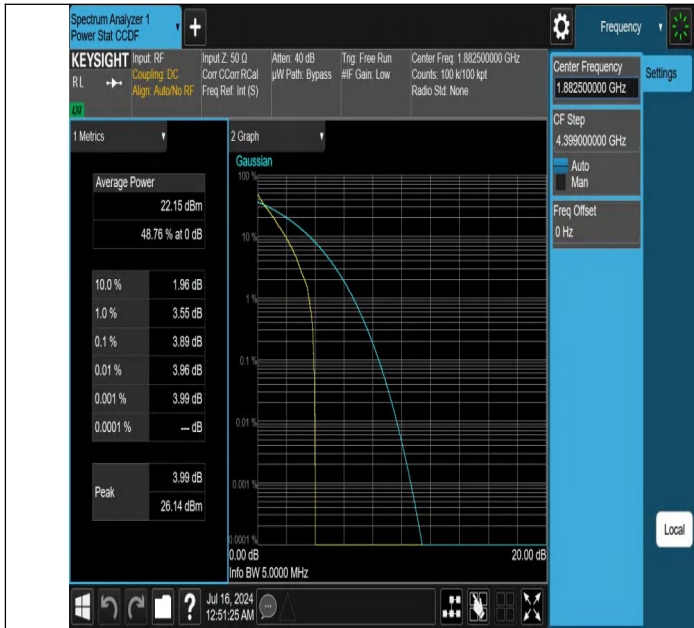
### Peak-to-Average Ratio(CCDF)

#### Test Result

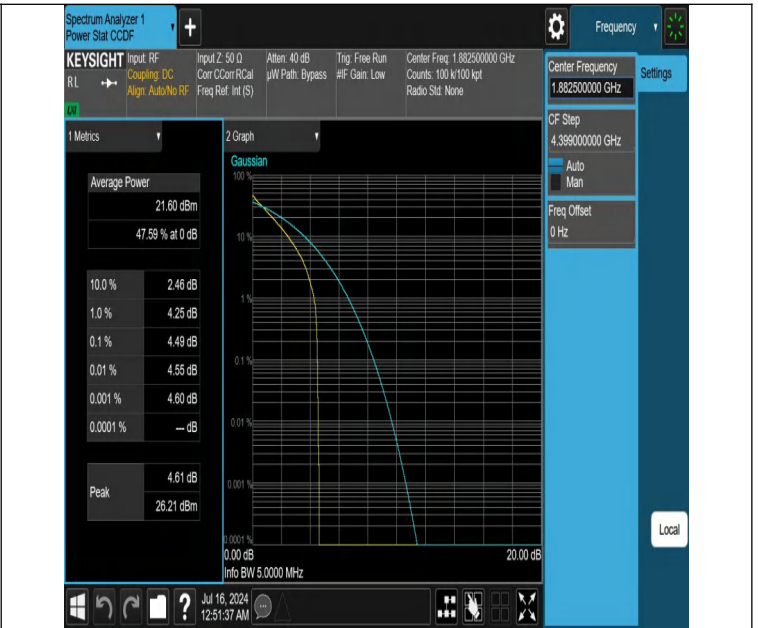
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result(dB)	Limit(dB)	Verdict
DC_12A_n25A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	3.89	≤13	PASS
DC_12A_n25A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.49	≤13	PASS
DC_12A_n25A	15	5+5	DFT-16QAM	M+M	Outer_Full	5.52	≤13	PASS
DC_12A_n25A	15	5+5	DFT-64QAM	M+M	Outer_Full	6.08	≤13	PASS
DC_12A_n25A	15	5+5	DFT-256QAM	M+M	Outer_Full	6.70	≤13	PASS
DC_12A_n25A	15	5+10	DFT-PI2BPSK	M+M	Outer_Full	4.03	≤13	PASS
DC_12A_n25A	15	5+10	DFT-QPSK	M+M	Outer_Full	4.63	≤13	PASS
DC_12A_n25A	15	5+10	DFT-16QAM	M+M	Outer_Full	5.58	≤13	PASS
DC_12A_n25A	15	5+10	DFT-64QAM	M+M	Outer_Full	6.15	≤13	PASS
DC_12A_n25A	15	5+10	DFT-256QAM	M+M	Outer_Full	6.53	≤13	PASS
DC_12A_n25A	15	5+15	DFT-PI2BPSK	M+M	Outer_Full	4.25	≤13	PASS
DC_12A_n25A	15	5+15	DFT-QPSK	M+M	Outer_Full	5.09	≤13	PASS
DC_12A_n25A	15	5+15	DFT-16QAM	M+M	Outer_Full	5.90	≤13	PASS
DC_12A_n25A	15	5+15	DFT-64QAM	M+M	Outer_Full	6.35	≤13	PASS
DC_12A_n25A	15	5+15	DFT-256QAM	M+M	Outer_Full	6.52	≤13	PASS
DC_12A_n25A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	4.62	≤13	PASS
DC_12A_n25A	15	5+20	DFT-QPSK	M+M	Outer_Full	5.09	≤13	PASS
DC_12A_n25A	15	5+20	DFT-16QAM	M+M	Outer_Full	5.87	≤13	PASS
DC_12A_n25A	15	5+20	DFT-64QAM	M+M	Outer_Full	6.31	≤13	PASS
DC_12A_n25A	15	5+20	DFT-256QAM	M+M	Outer_Full	6.59	≤13	PASS
DC_12A_n25A	15	5+25	DFT-PI2BPSK	M+M	Outer_Full	4.33	≤13	PASS
DC_12A_n25A	15	5+25	DFT-QPSK	M+M	Outer_Full	5.04	≤13	PASS
DC_12A_n25A	15	5+25	DFT-16QAM	M+M	Outer_Full	5.88	≤13	PASS
DC_12A_n25A	15	5+25	DFT-64QAM	M+M	Outer_Full	6.35	≤13	PASS
DC_12A_n25A	15	5+25	DFT-256QAM	M+M	Outer_Full	6.58	≤13	PASS
DC_12A_n25A	15	5+30	DFT-PI2BPSK	M+M	Outer_Full	4.27	≤13	PASS
DC_12A_n25A	15	5+30	DFT-QPSK	M+M	Outer_Full	5.03	≤13	PASS
DC_12A_n25A	15	5+30	DFT-16QAM	M+M	Outer_Full	5.94	≤13	PASS
DC_12A_n25A	15	5+30	DFT-64QAM	M+M	Outer_Full	6.18	≤13	PASS
DC_12A_n25A	15	5+30	DFT-256QAM	M+M	Outer_Full	6.65	≤13	PASS
DC_12A_n25A	15	5+40	DFT-PI2BPSK	M+M	Outer_Full	3.95	≤13	PASS
DC_12A_n25A	15	5+40	DFT-QPSK	M+M	Outer_Full	4.60	≤13	PASS
DC_12A_n25A	15	5+40	DFT-16QAM	M+M	Outer_Full	5.48	≤13	PASS
DC_12A_n25A	15	5+40	DFT-64QAM	M+M	Outer_Full	5.92	≤13	PASS
DC_12A_n25A	15	5+40	DFT-256QAM	M+M	Outer_Full	6.57	≤13	PASS
DC_12A_n25A	15	5+35	DFT-BPSK	M+M	Outer_Full	8.48	≤13dB	PASS

DC_12A-n25A	15	5+35	DFT-QPSK	M+M	Outer_Full	8.46	≤13dB	PASS
DC_12A-n25A	15	5+35	DFT-16QAM	M+M	Outer_Full	8.46	≤13dB	PASS
DC_12A-n25A	15	5+35	DFT-64QAM	M+M	Outer_Full	8.46	≤13dB	PASS
DC_12A-n25A	15	5+35	DFT-256QAM	M+M	Outer_Full	8.44	≤13dB	PASS

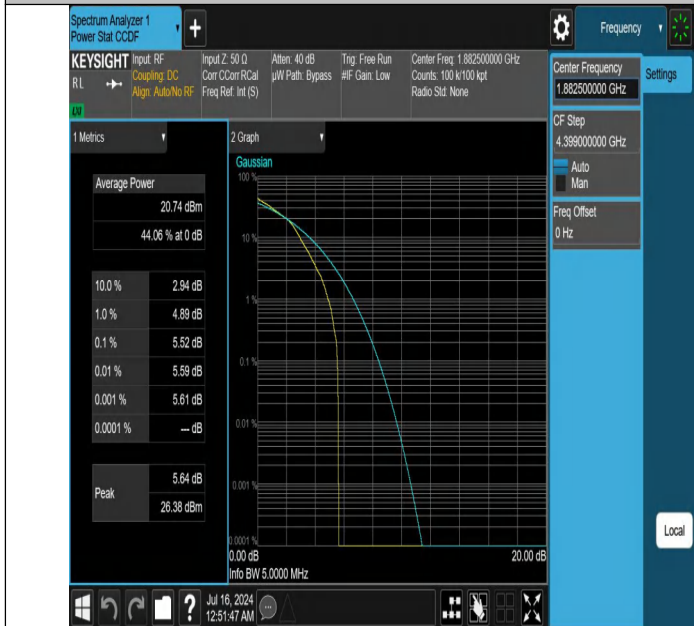
### Test Graphs



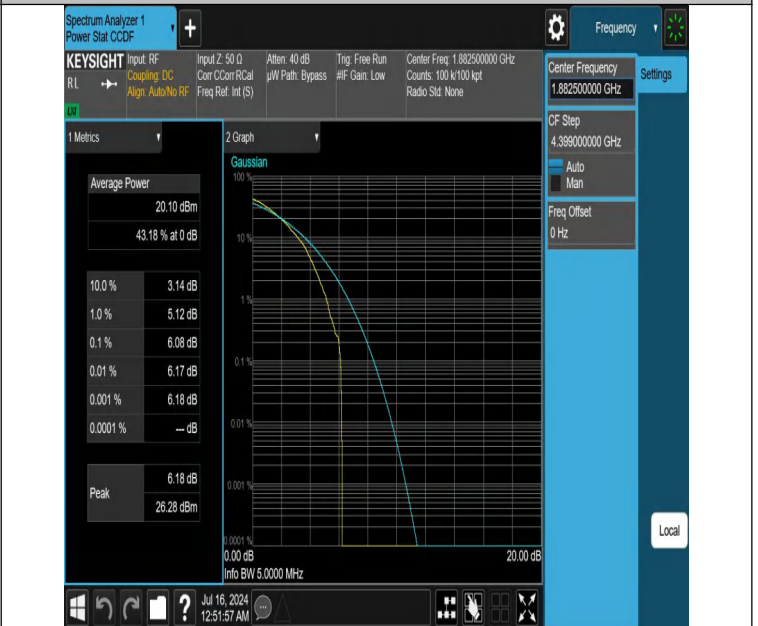
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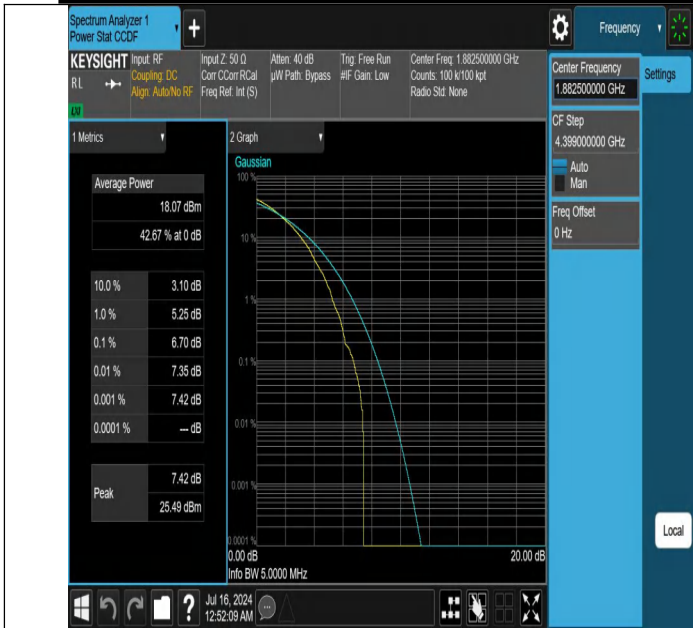
NTNV-DC\_12A\_n25A-PC3-15-5+5-M+M-N/A-N/A-DFT-QPSK-Outer\_Full-PASS



NTNV-DC\_12A\_n25A-PC3-15-5+5-M+M-N/A-N/A-DFT-16QAM-Outer\_Full-PASS



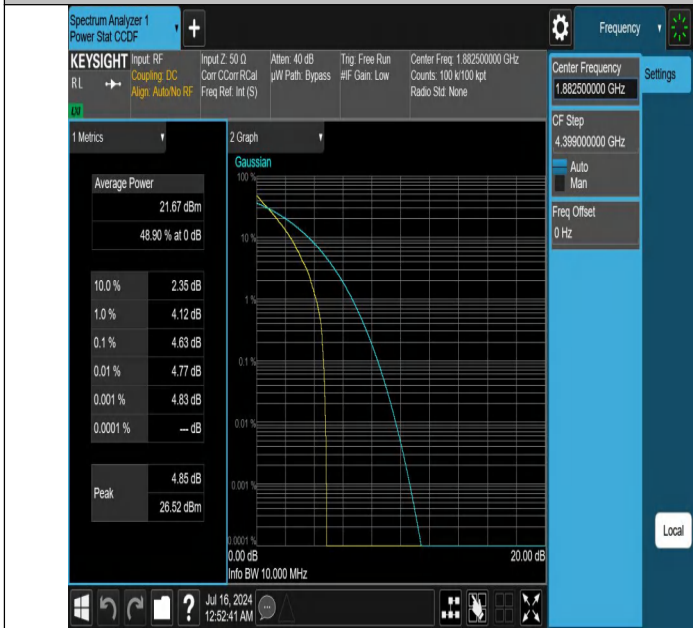
NTNV-DC\_12A\_n25A-PC3-15-5+5-M+M-N/A-N/A-DFT-64QAM-Outer\_Full-PASS



NTNV-DC\_12A\_n25A-PC3-15-5+5-M+M-N/A-N/A-DFT-256QAM-Outer\_Full-PASS



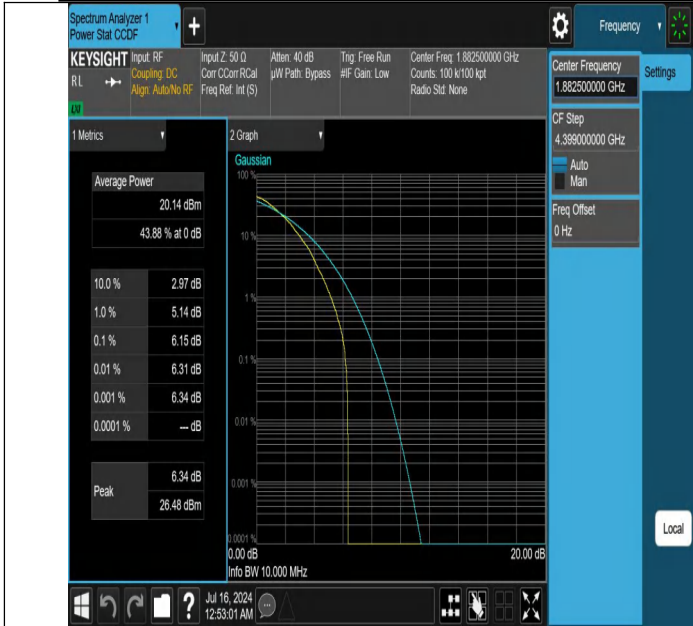
NTNV-DC\_12A\_n25A-PC3-15-5+10-M+M-N/A-N/A-DFT-PI2BPSK-Outer\_Full-PASS



NTNV-DC\_12A\_n25A-PC3-15-5+10-M+M-N/A-N/A-DFT-QPSK-Outer\_Full-PASS



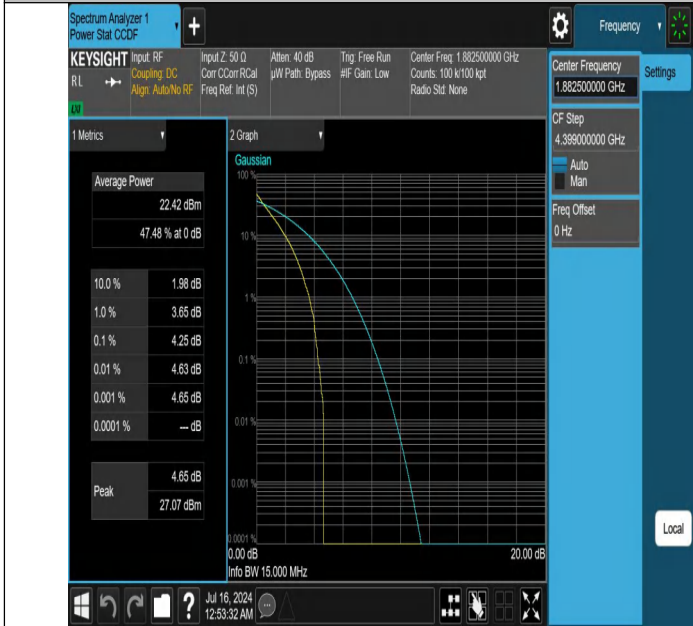
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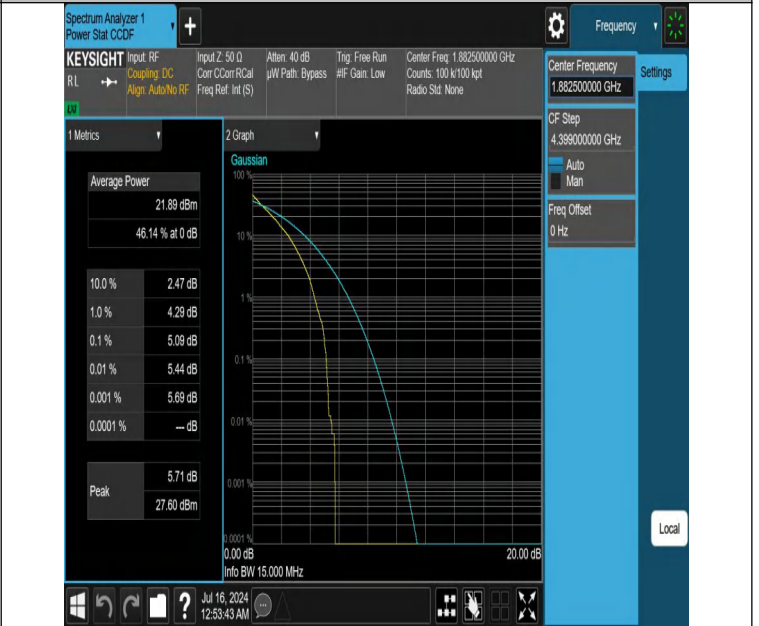
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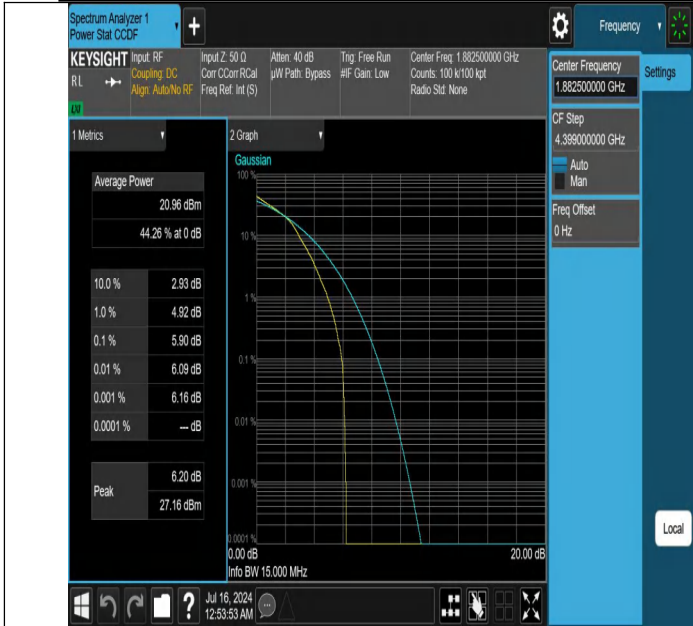
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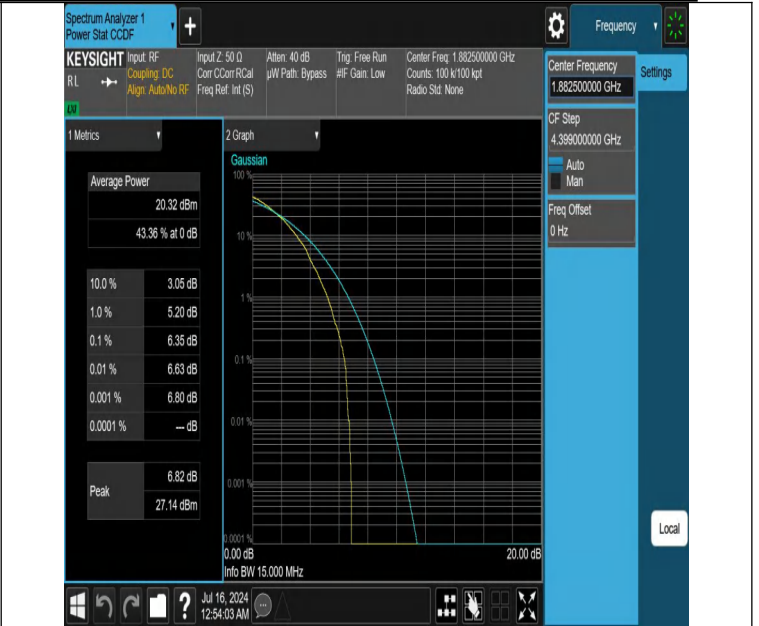
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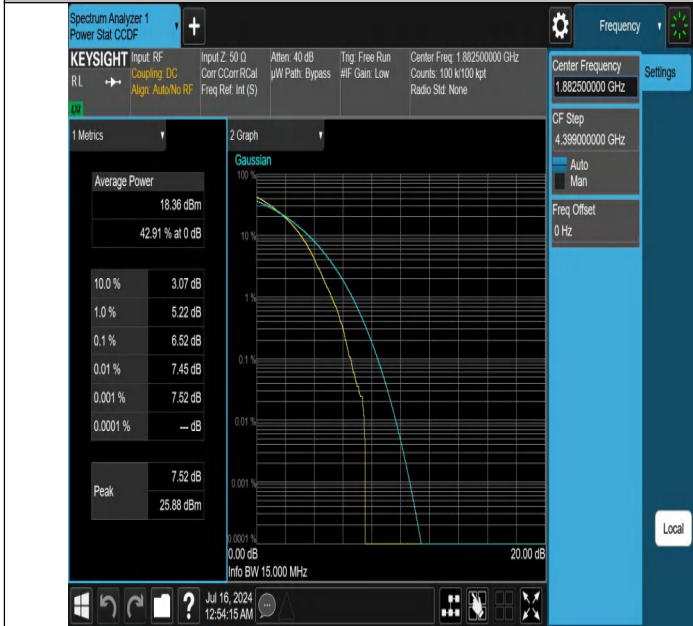
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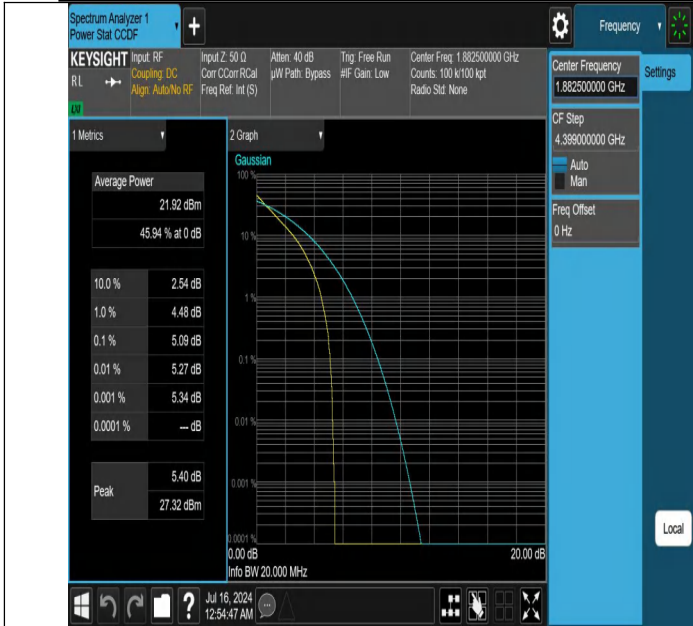


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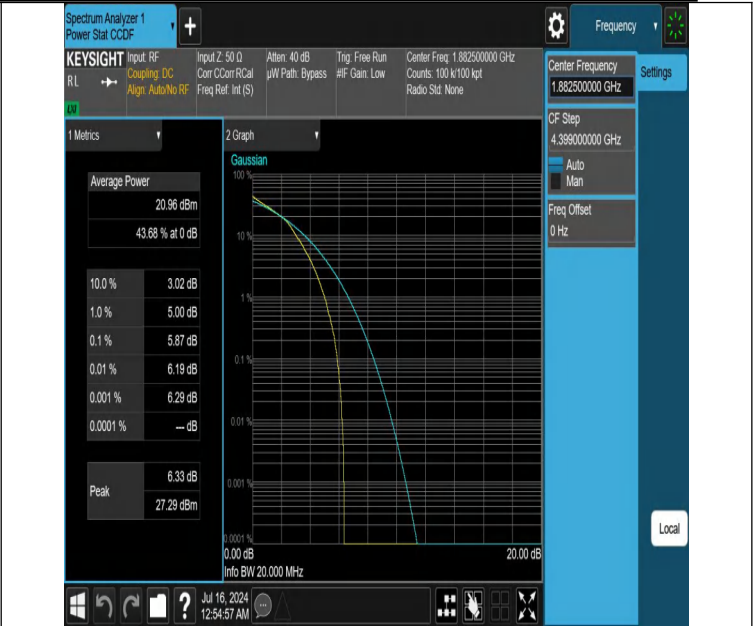


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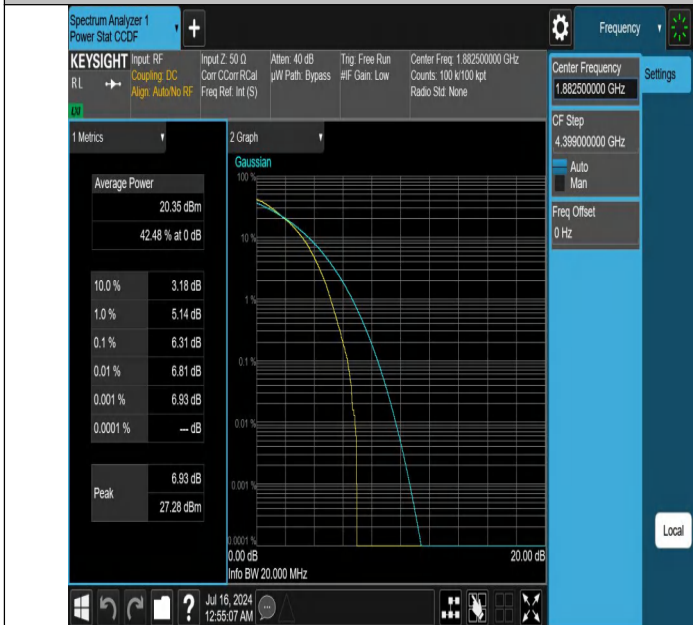




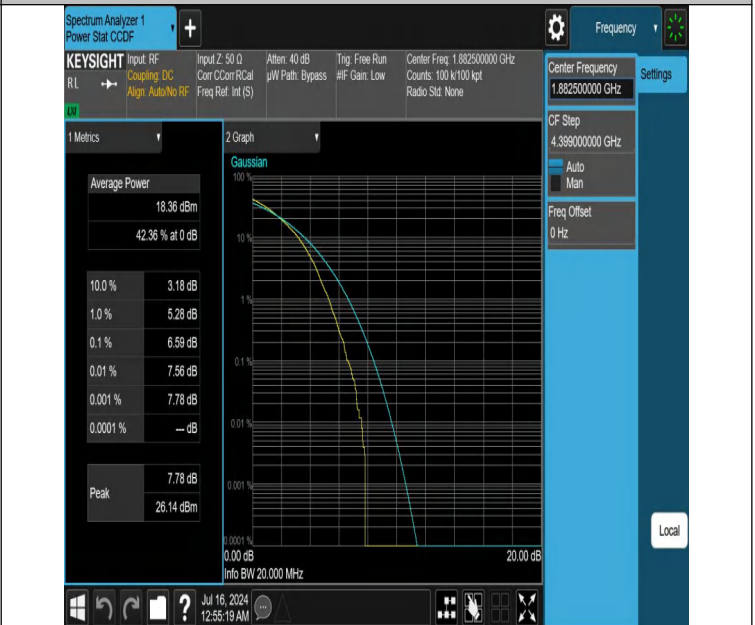
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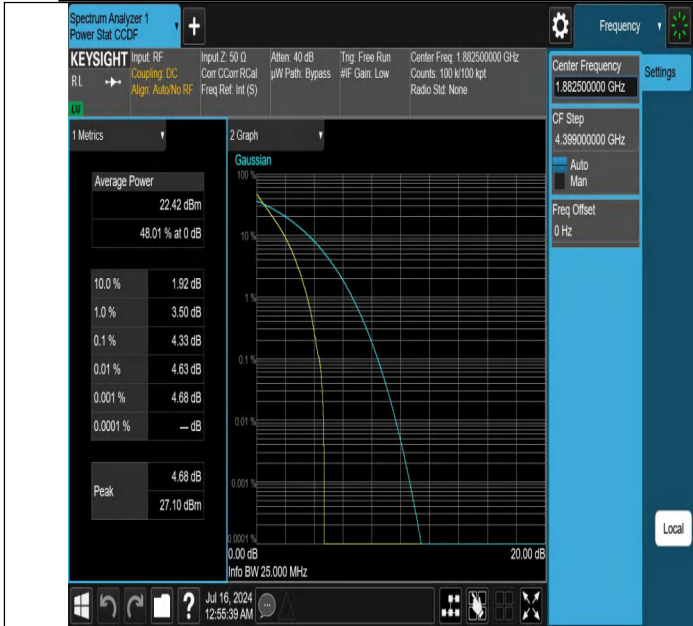
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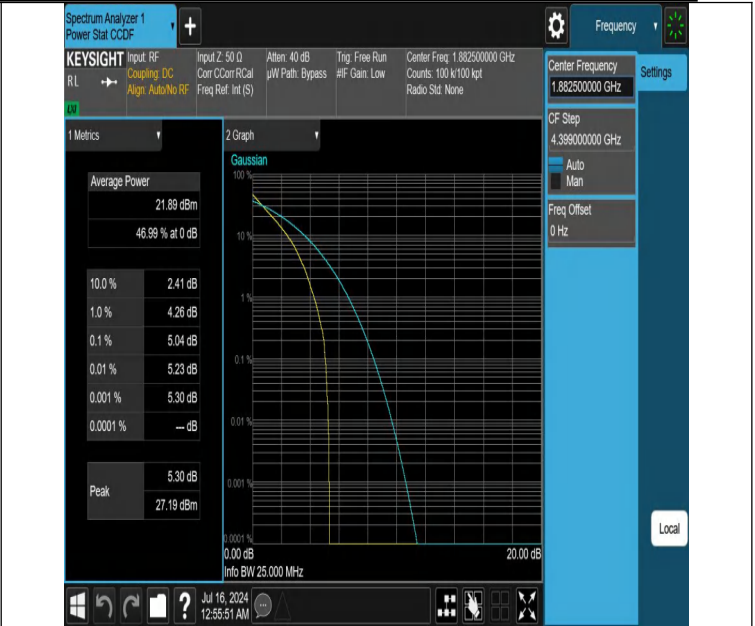
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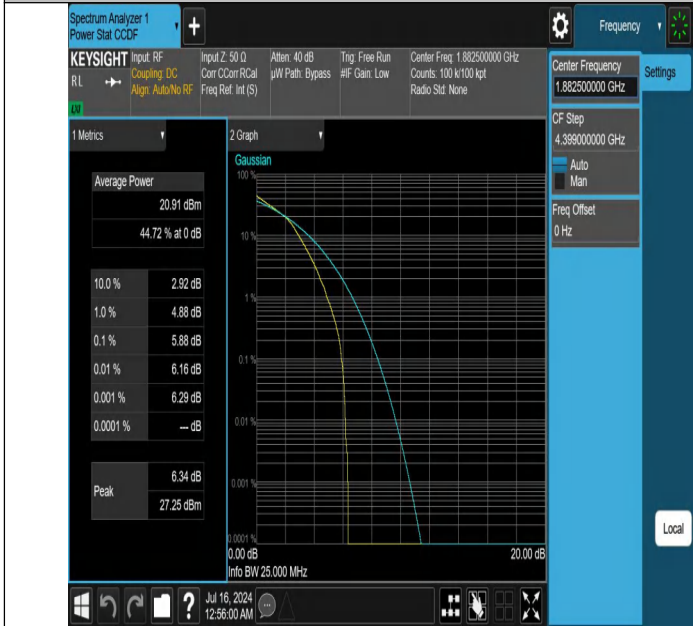
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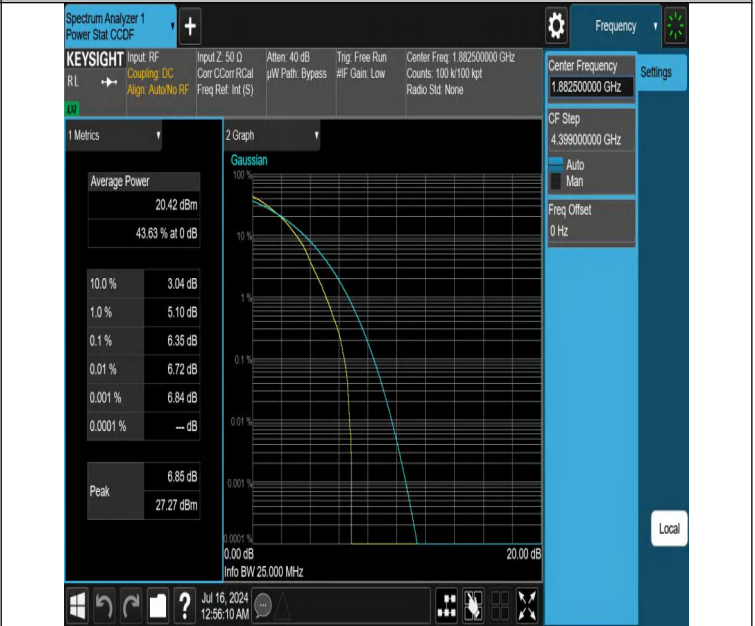
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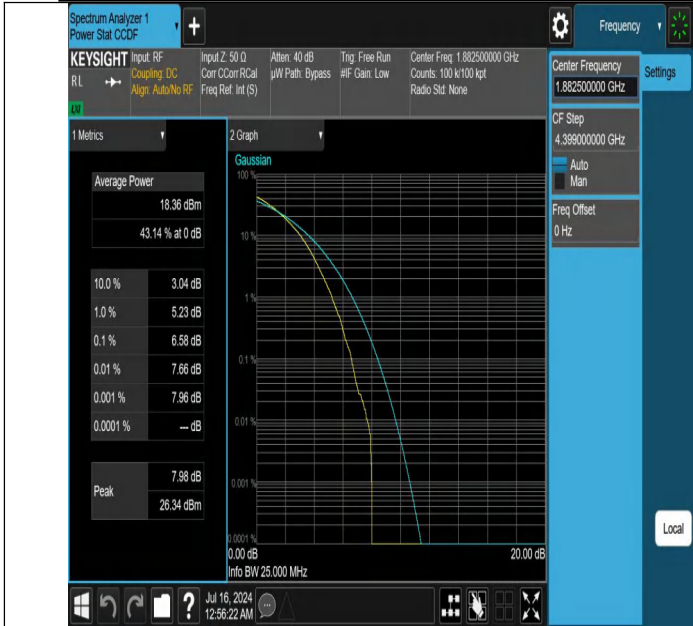
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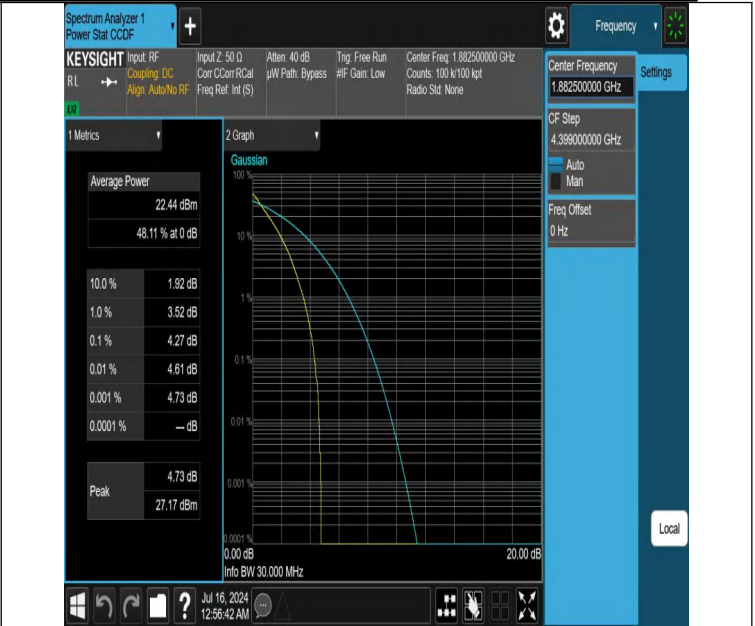
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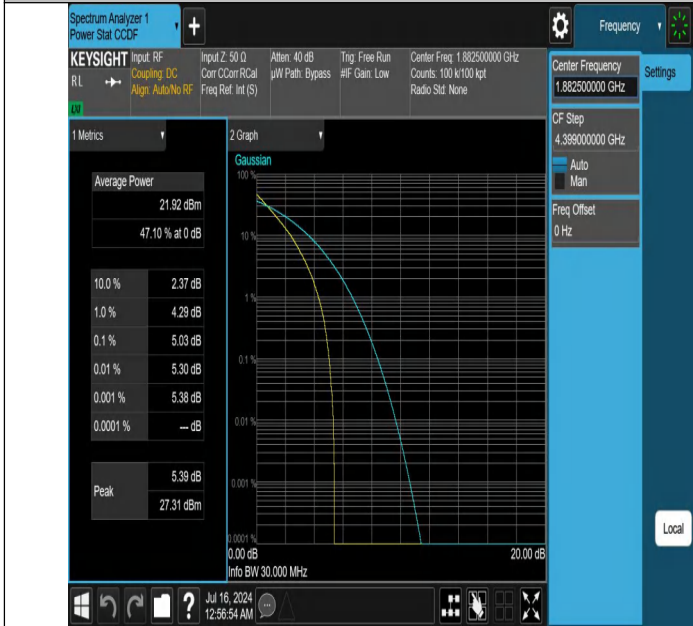
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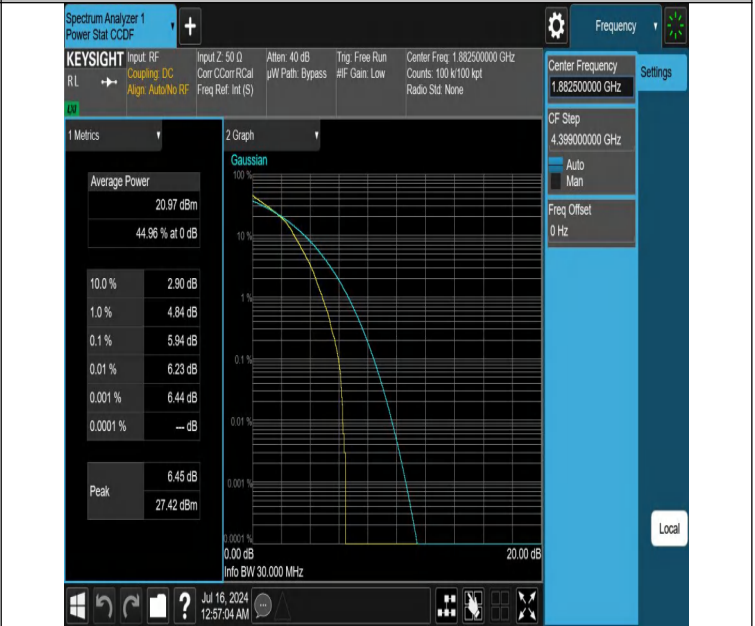
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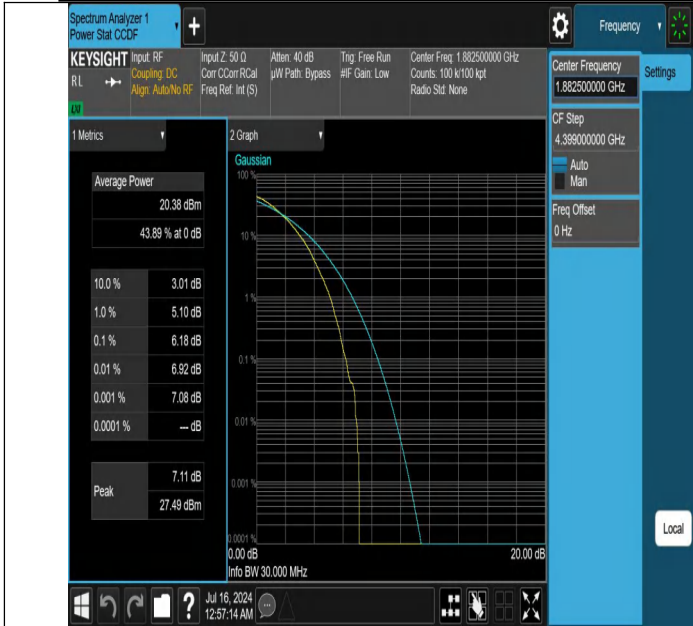
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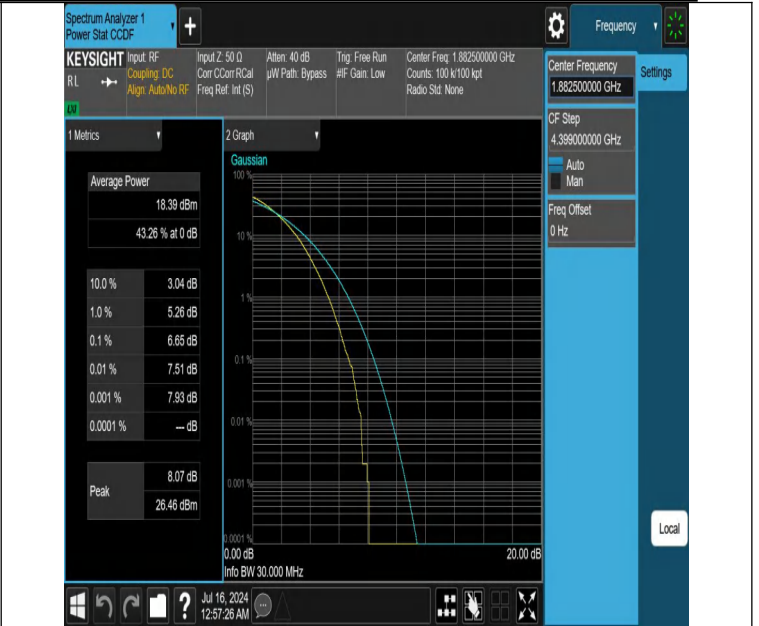
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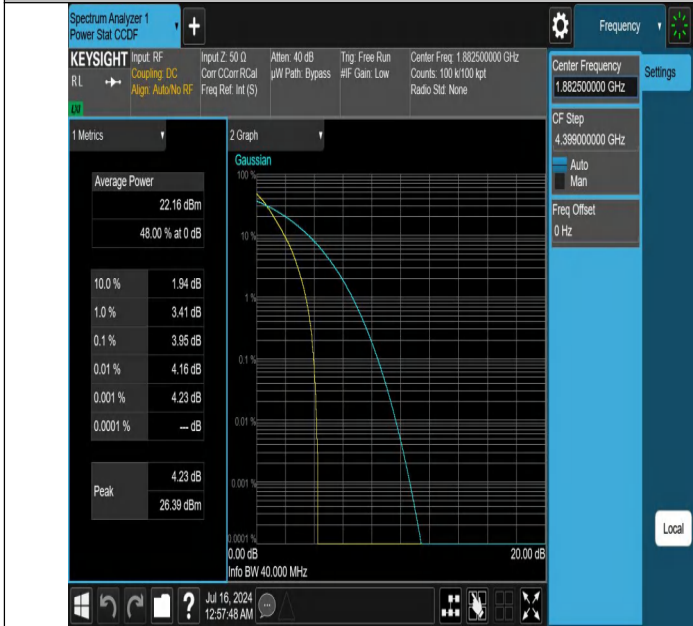
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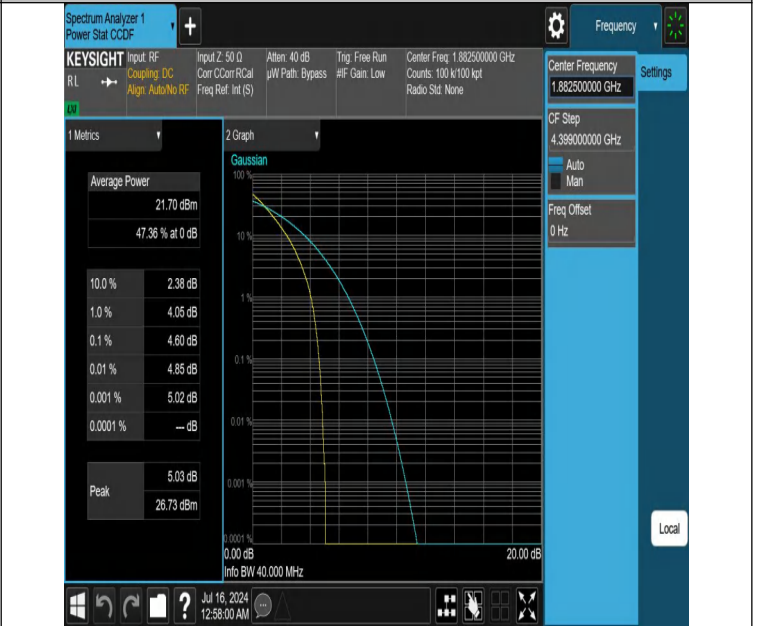
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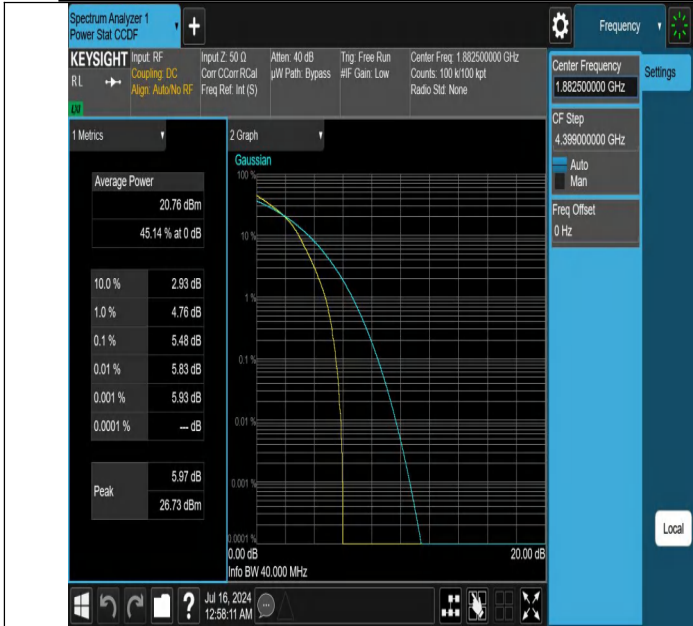
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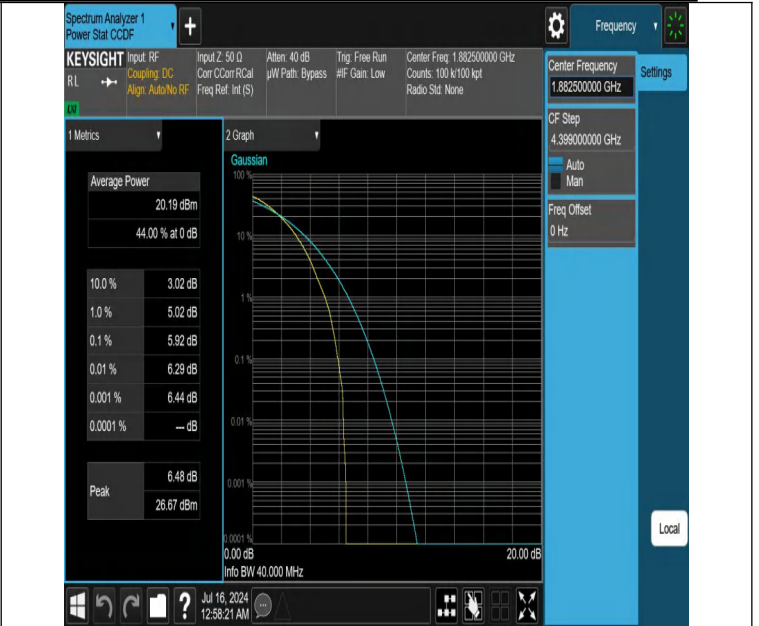
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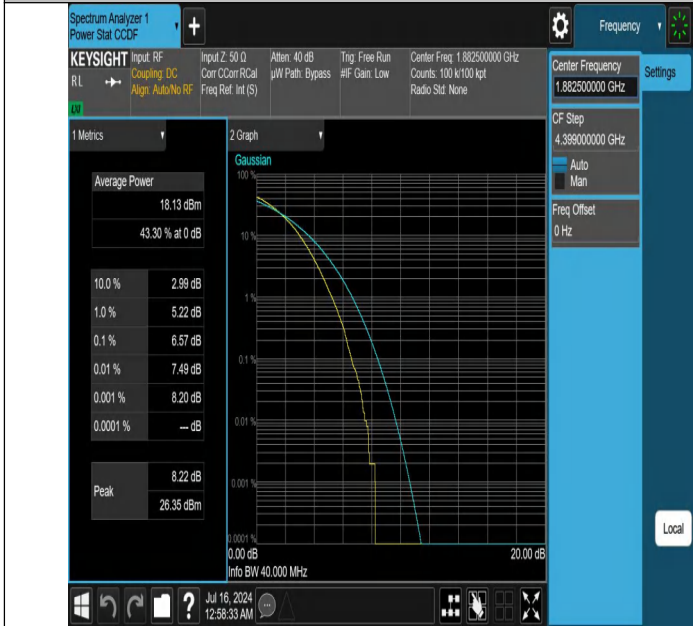
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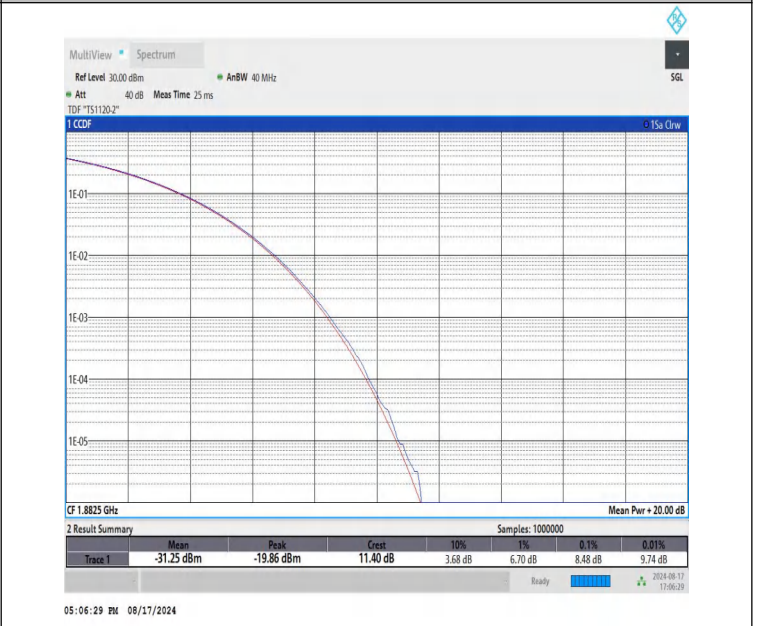
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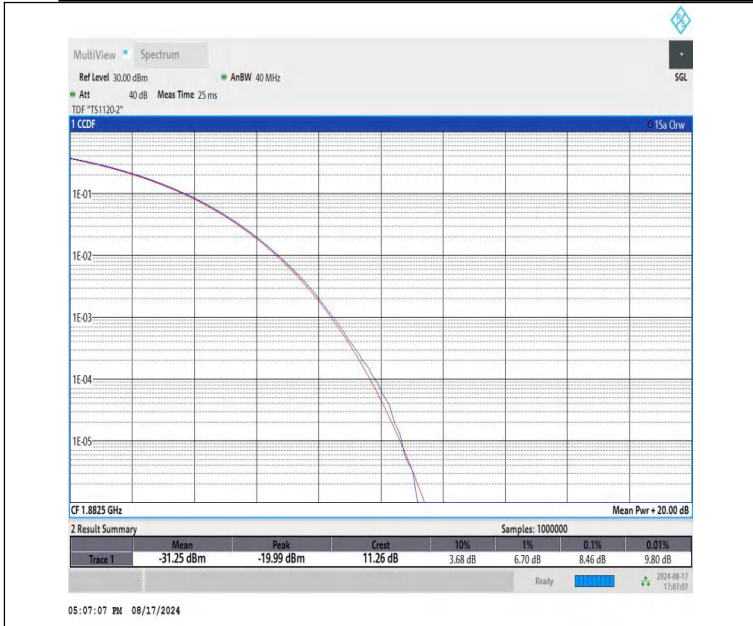
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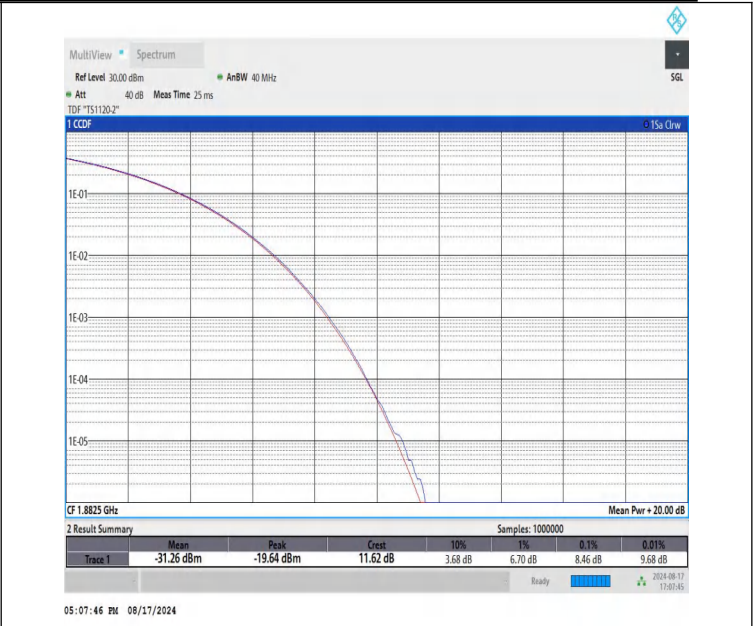
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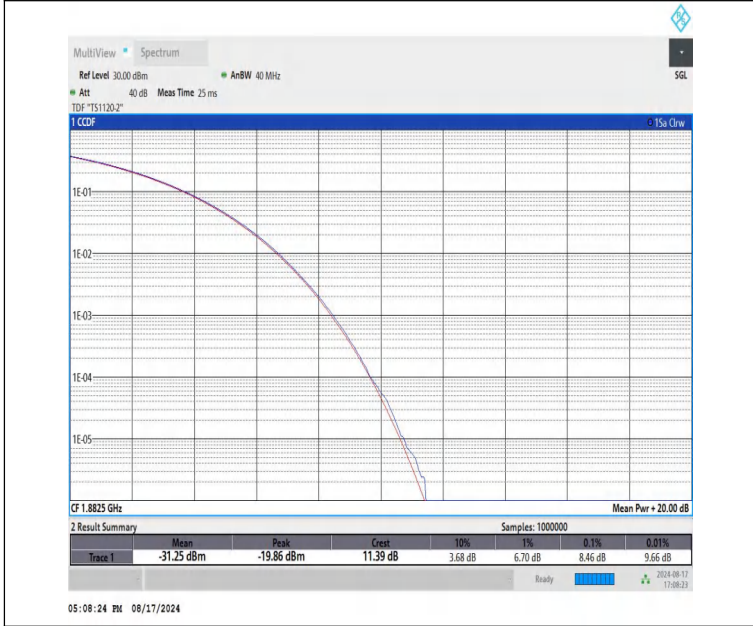
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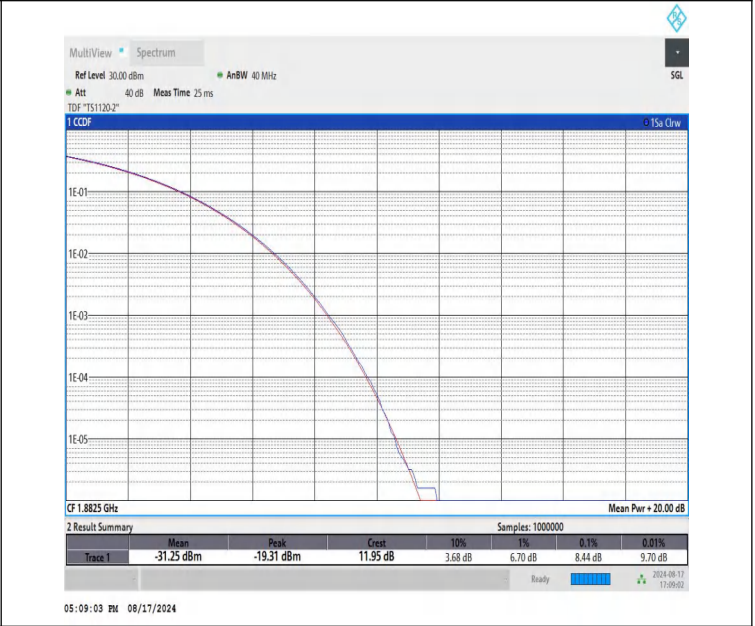
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NTNV-DC\_12A-n25A-PC3-15-5+35-M+M-QPSK-IDLE-DFT-64QA M-Outer\_Full-PASS



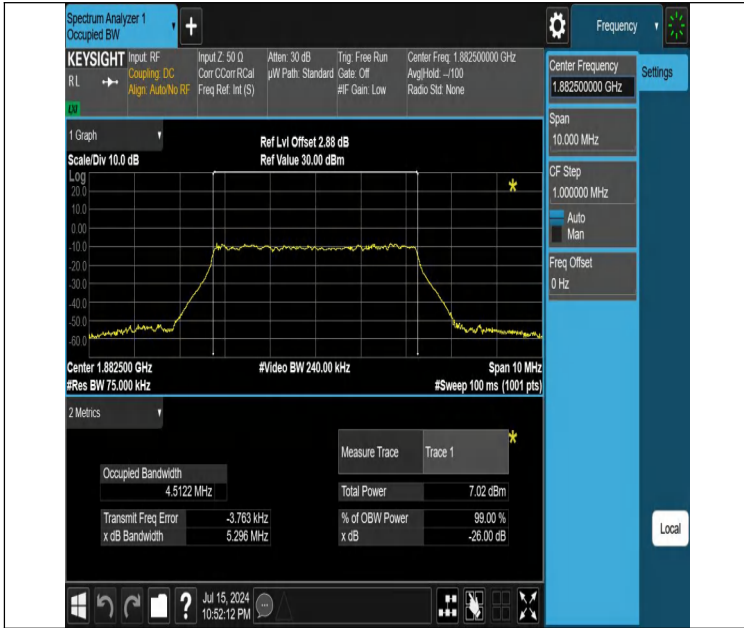
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## 26dB Bandwidth and Occupied Bandwidth for NSA

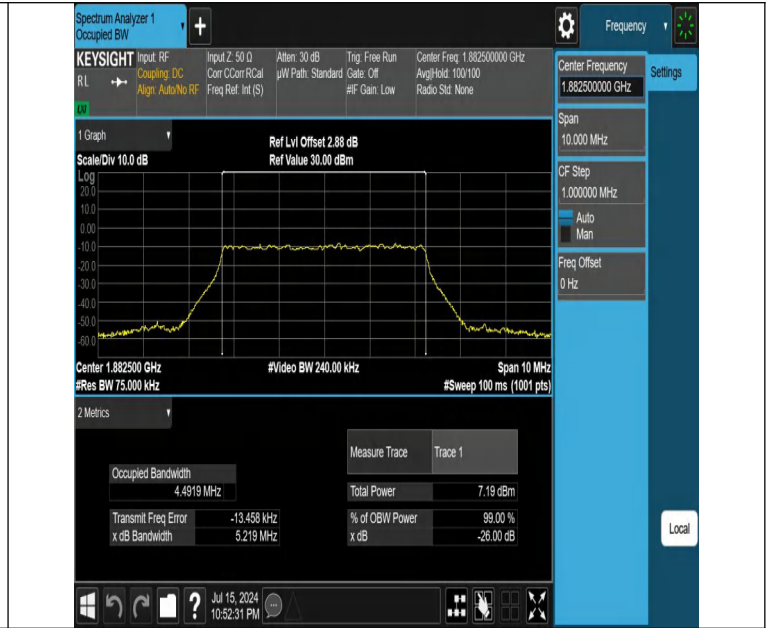
### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	4.5122	5.296	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4919	5.219	PASS
DC_12A_n25A	15	5+5	CP-64QAM	M+M	Outer_Full	4.4847	5.173	PASS
DC_12A_n25A	15	5+5	CP-256QAM	M+M	Outer_Full	4.4947	5.161	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	9.3115	10.27	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	9.3346	10.25	PASS
DC_12A_n25A	15	5+10	CP-64QAM	M+M	Outer_Full	9.2940	10.09	PASS
DC_12A_n25A	15	5+10	CP-256QAM	M+M	Outer_Full	9.3163	10.15	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	14.148	15.26	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	14.125	15.14	PASS
DC_12A_n25A	15	5+15	CP-64QAM	M+M	Outer_Full	14.131	15.15	PASS
DC_12A_n25A	15	5+15	CP-256QAM	M+M	Outer_Full	14.154	15.21	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	18.957	20.20	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	18.979	20.17	PASS
DC_12A_n25A	15	5+20	CP-64QAM	M+M	Outer_Full	18.999	20.25	PASS
DC_12A_n25A	15	5+20	CP-256QAM	M+M	Outer_Full	18.990	20.16	PASS
DC_12A_n25A	15	5+25	CP-QPSK	M+M	Outer_Full	23.798	25.15	PASS
DC_12A_n25A	15	5+25	CP-16QAM	M+M	Outer_Full	23.838	25.17	PASS
DC_12A_n25A	15	5+25	CP-64QAM	M+M	Outer_Full	23.816	25.11	PASS
DC_12A_n25A	15	5+25	CP-256QAM	M+M	Outer_Full	23.790	25.19	PASS
DC_12A_n25A	15	5+30	CP-QPSK	M+M	Outer_Full	28.620	30.15	PASS
DC_12A_n25A	15	5+30	CP-16QAM	M+M	Outer_Full	28.675	30.22	PASS
DC_12A_n25A	15	5+30	CP-64QAM	M+M	Outer_Full	28.603	30.19	PASS
DC_12A_n25A	15	5+30	CP-256QAM	M+M	Outer_Full	28.537	30.07	PASS
DC_12A_n25A	15	5+40	CP-QPSK	M+M	Outer_Full	38.474	40.46	PASS
DC_12A_n25A	15	5+40	CP-16QAM	M+M	Outer_Full	38.568	40.44	PASS
DC_12A_n25A	15	5+40	CP-64QAM	M+M	Outer_Full	38.559	40.39	PASS
DC_12A_n25A	15	5+40	CP-256QAM	M+M	Outer_Full	38.595	40.36	PASS
DC_12A-n25A	15	5+35	CP-QPSK	M+M	Outer_Full	32.219	33.600	PASS
DC_12A-n25A	15	5+35	CP-16QAM	M+M	Outer_Full	32.177	33.670	PASS
DC_12A-n25A	15	5+35	CP-64QAM	M+M	Outer_Full	32.197	33.670	PASS
DC_12A-n25A	15	5+35	CP-256QAM	M+M	Outer_Full	32.127	33.670	PASS

Test Graphs



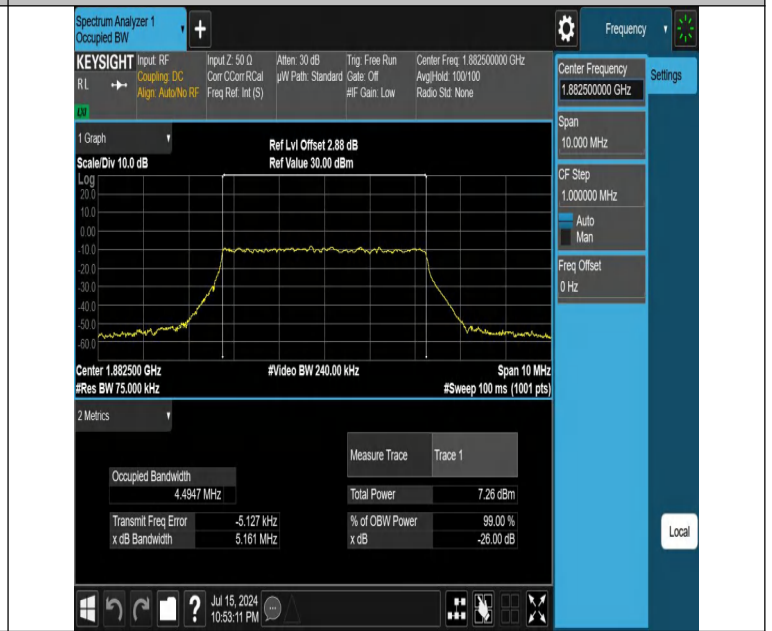
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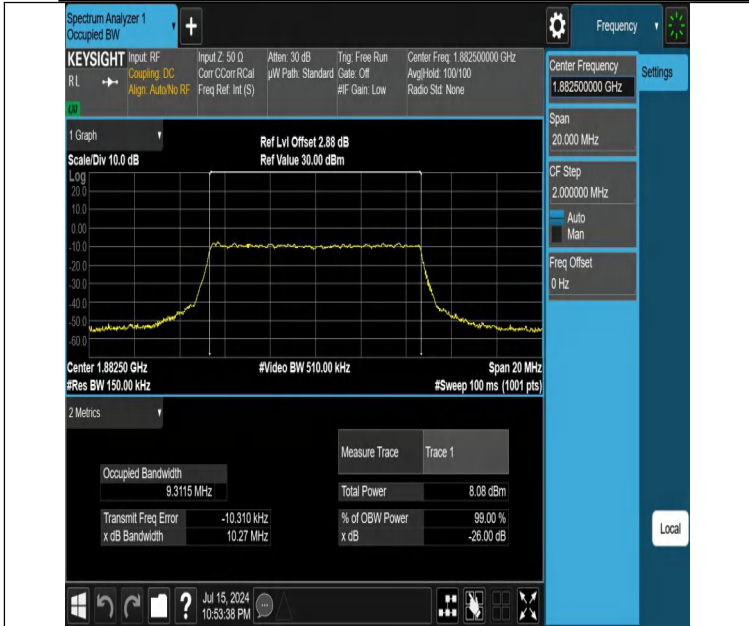


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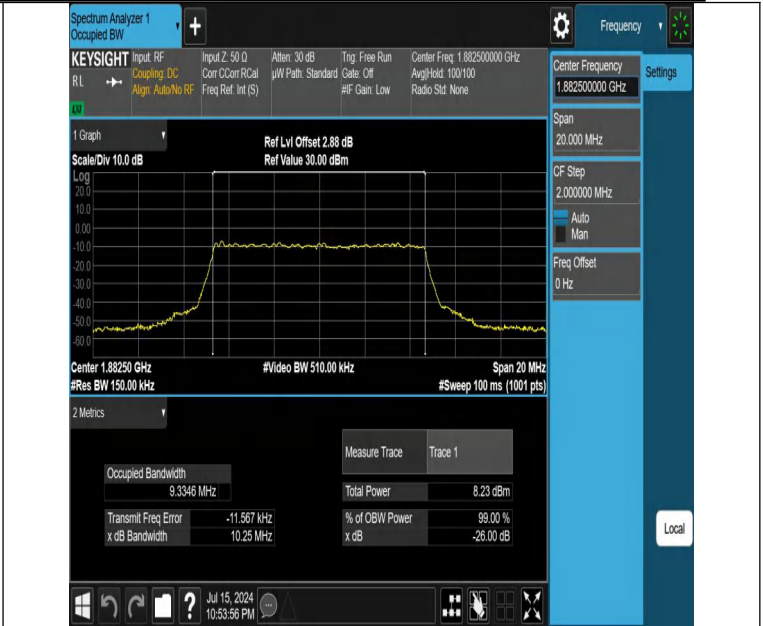


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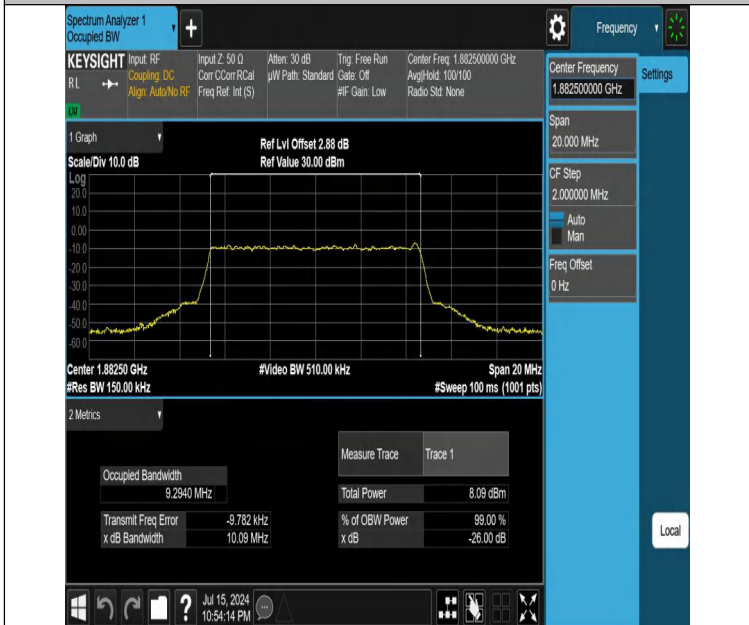




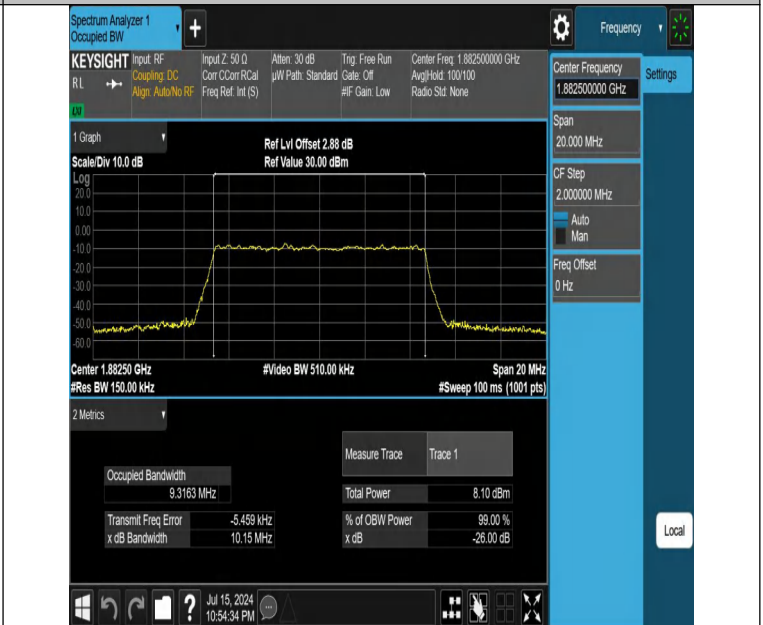
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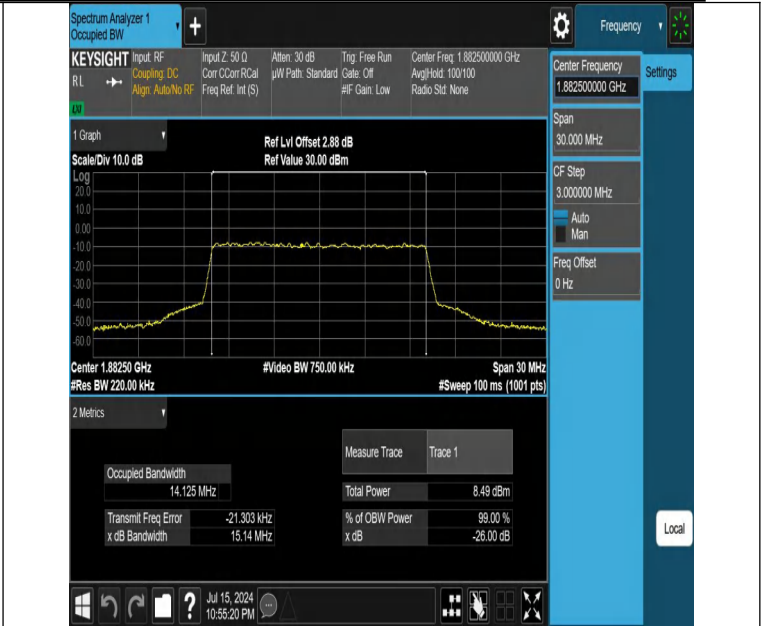
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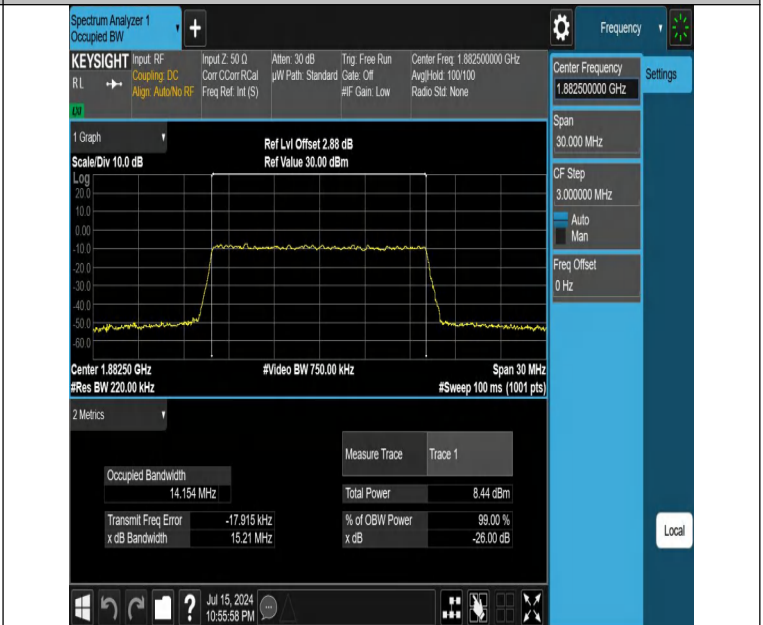
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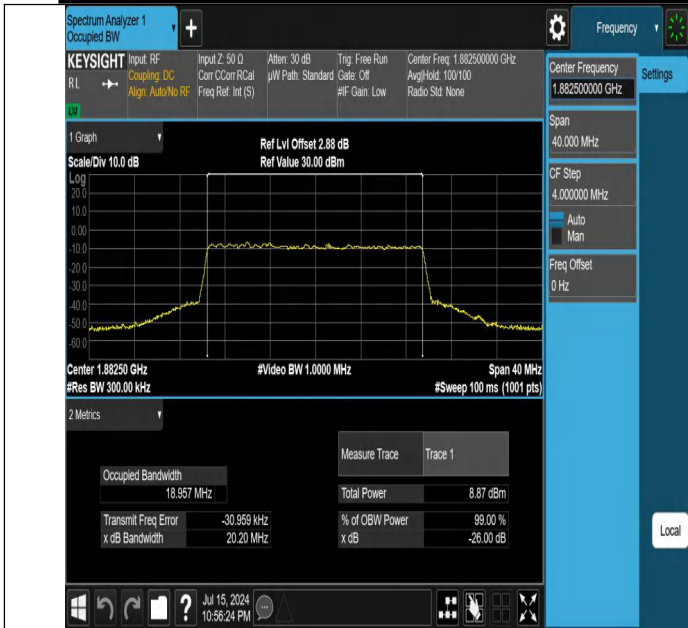
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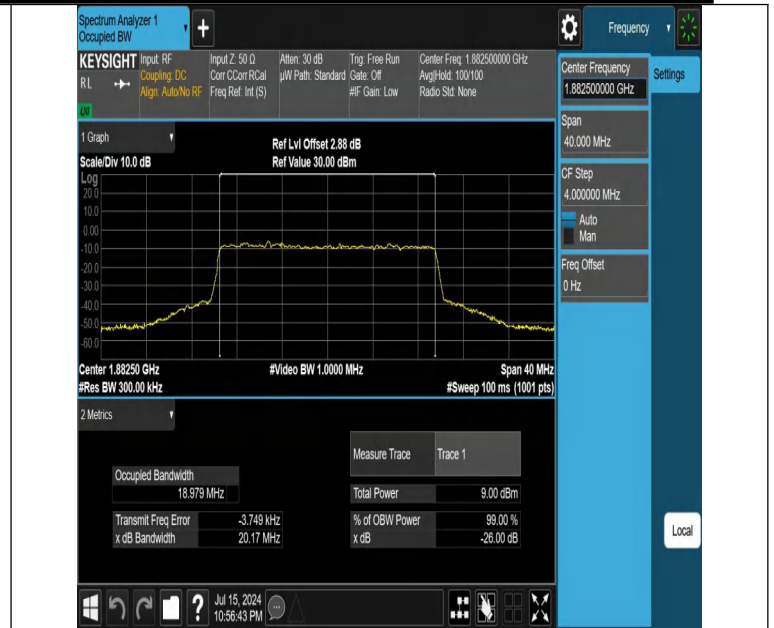
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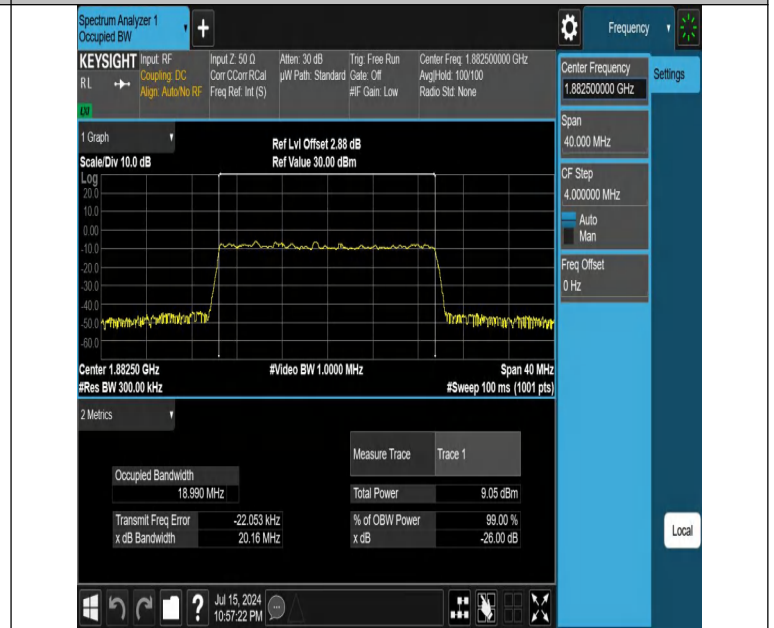
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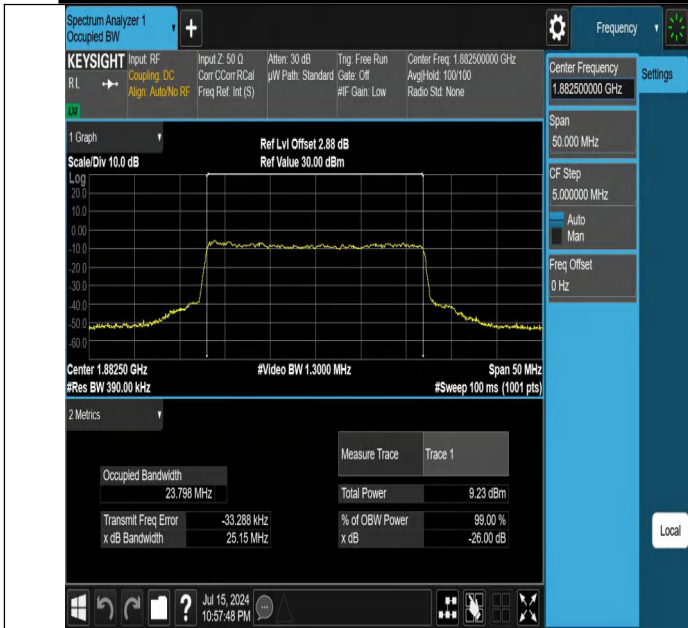
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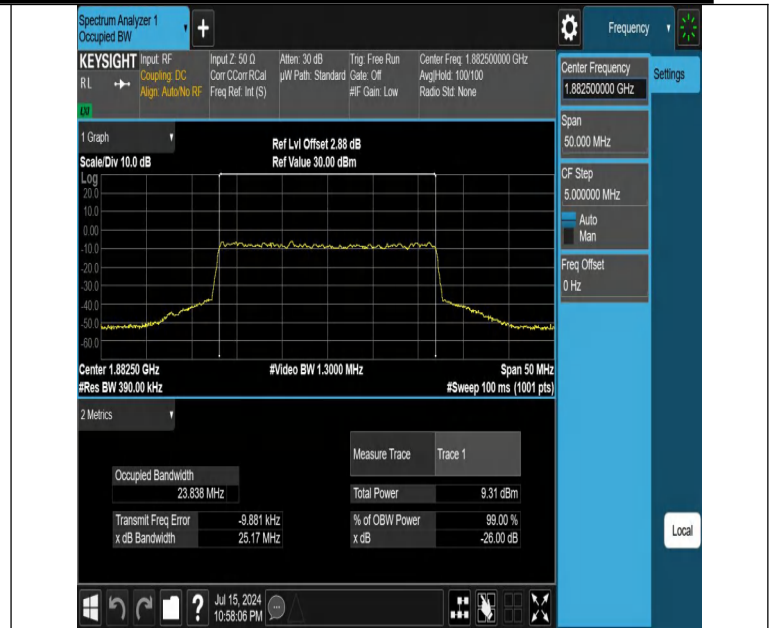
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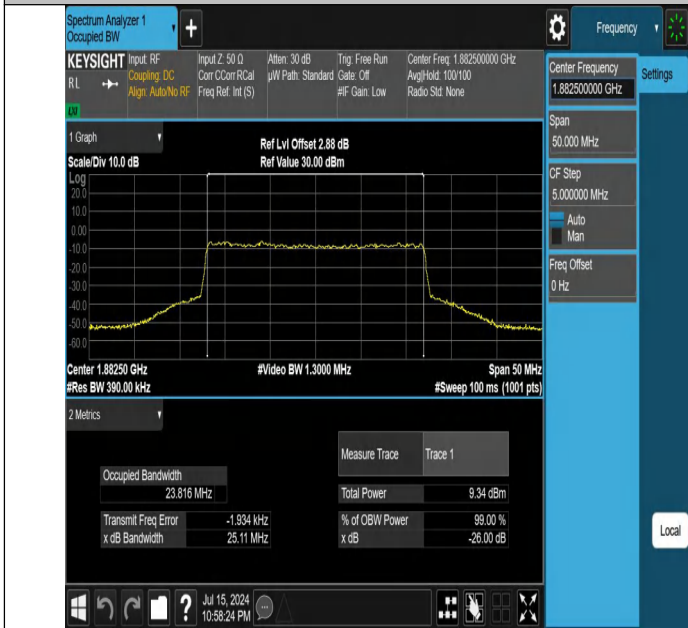
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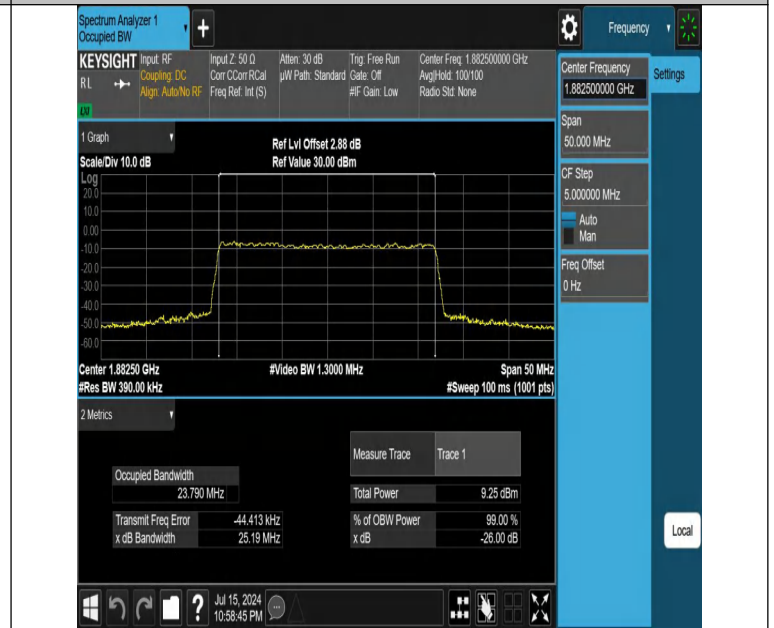
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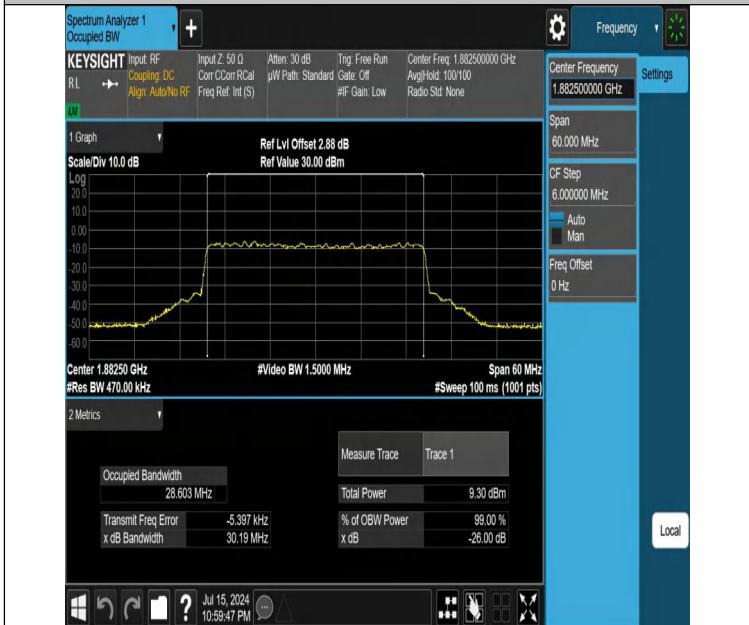
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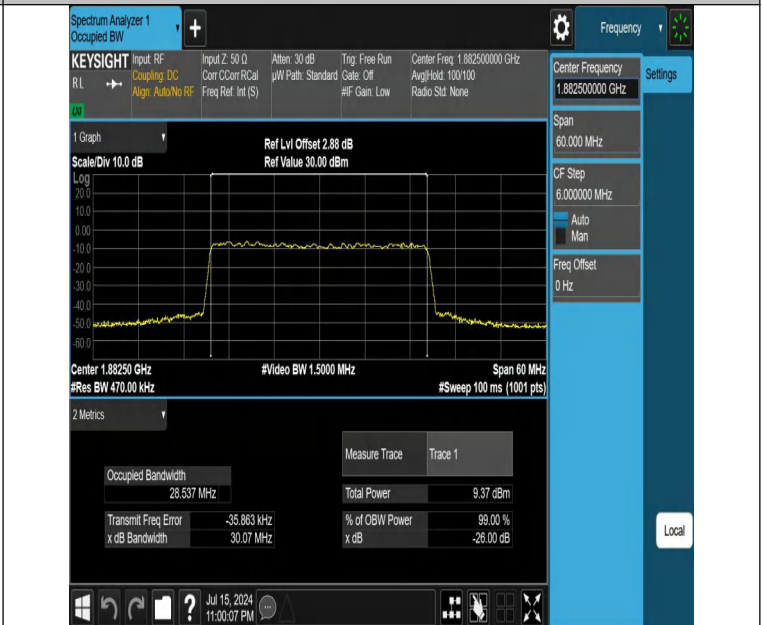
NTNV-DC\_12A\_n25A-PC3-15-5+30-M+M-N/A-N/A-CP-QPSK-Out\_r\_Full-Ant1-PASS



NTNV-DC\_12A\_n25A-PC3-15-5+30-M+M-N/A-N/A-CP-16QAM-Out\_r\_Full-Ant1-PASS



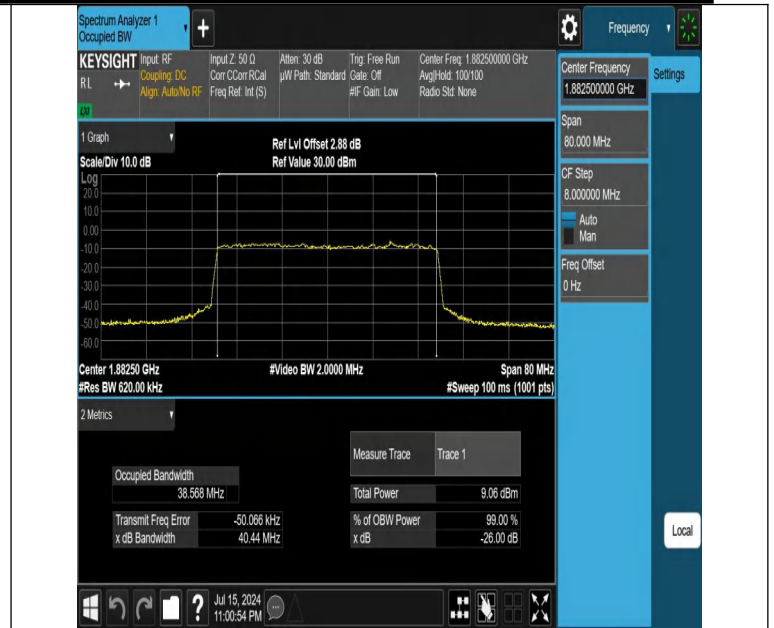
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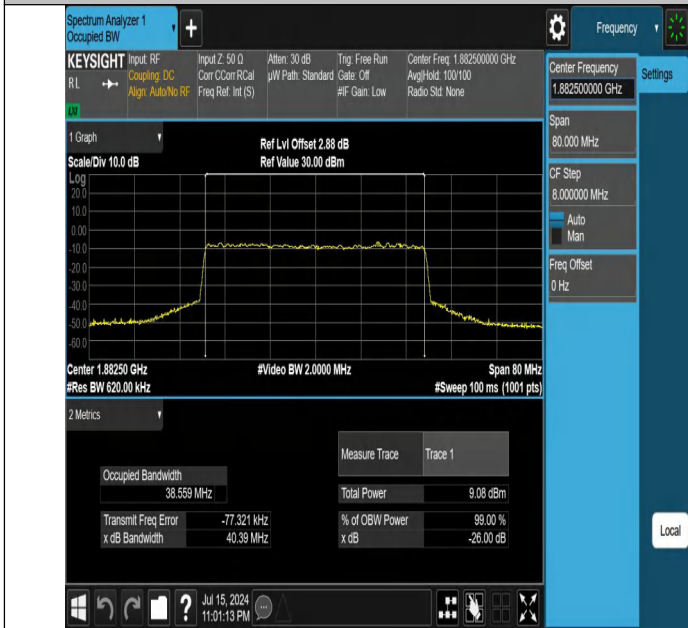
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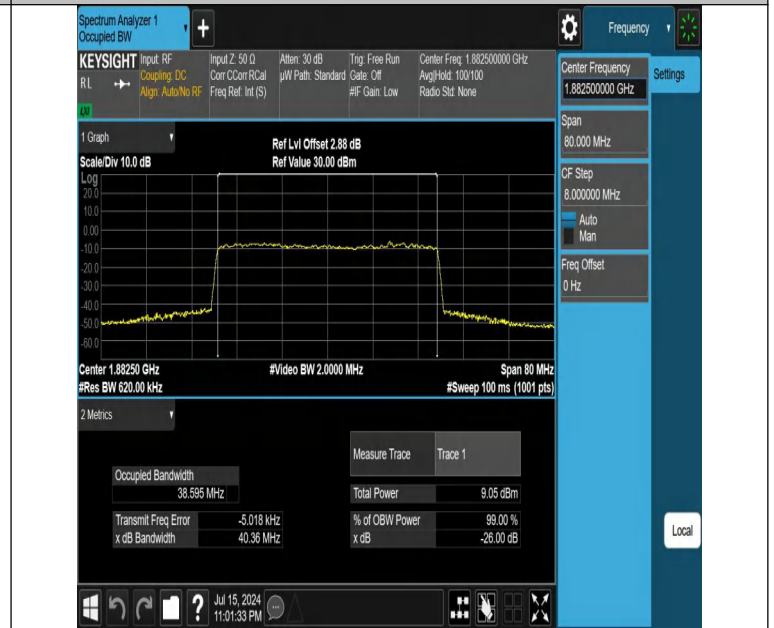
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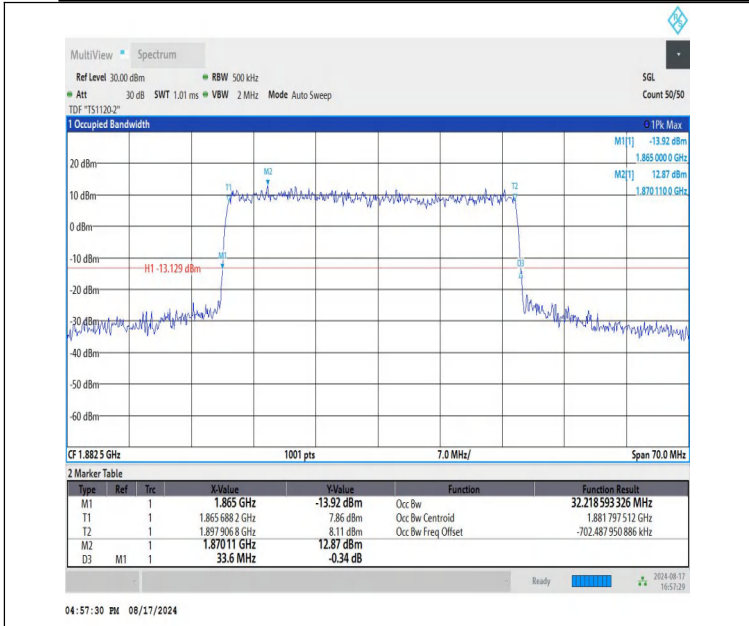
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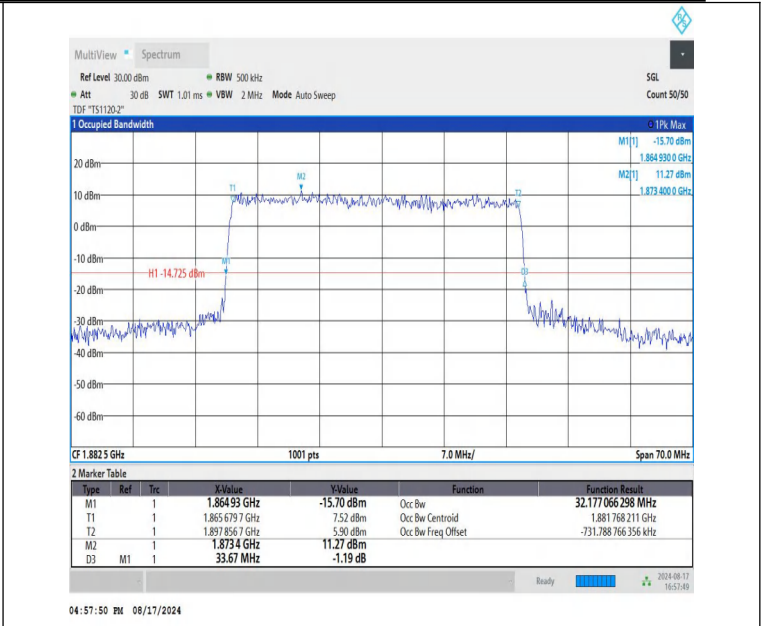
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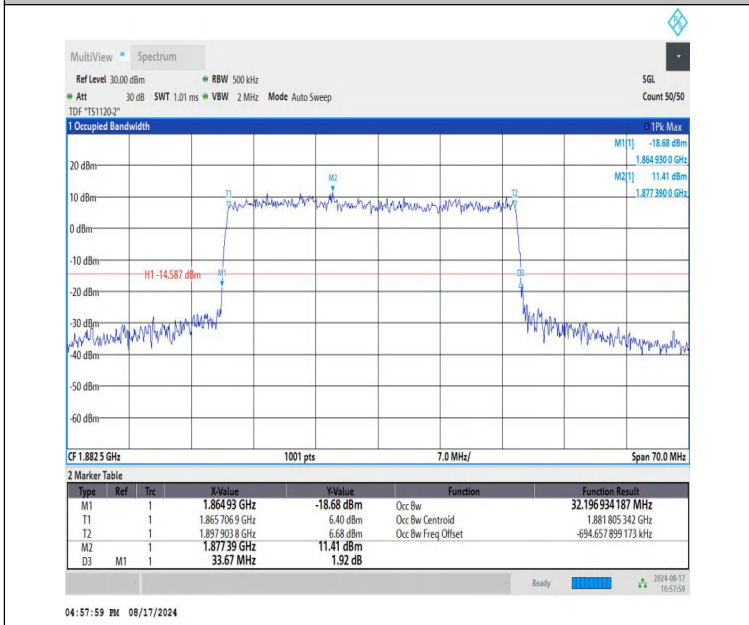
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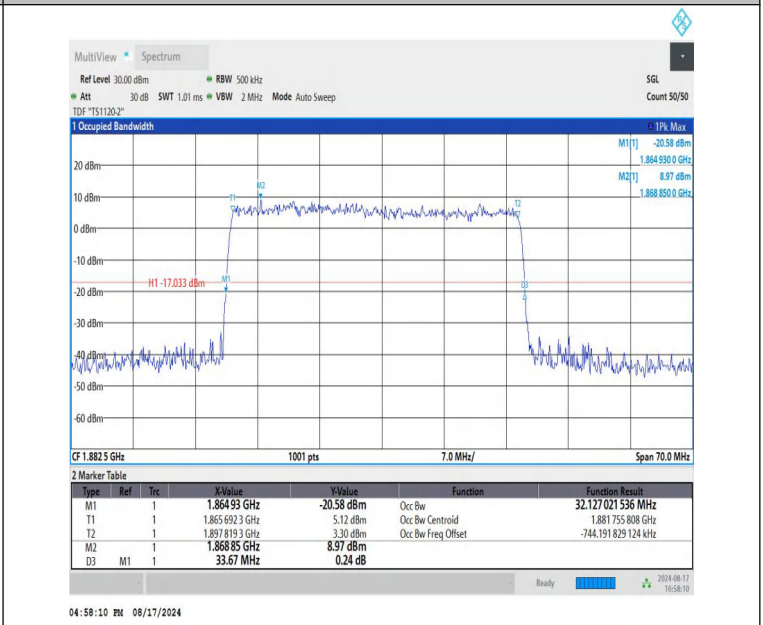
NTNV-DC\_12A-n25A-PC3-15-5+35-M+M-QPSK-IDLE-CP-QPSK-Outer\_Full-Ant1-PASS



NTNV-DC\_12A-n25A-PC3-15-5+35-M+M-QPSK-IDLE-CP-16QAM-Outer\_Full-Ant1-PASS



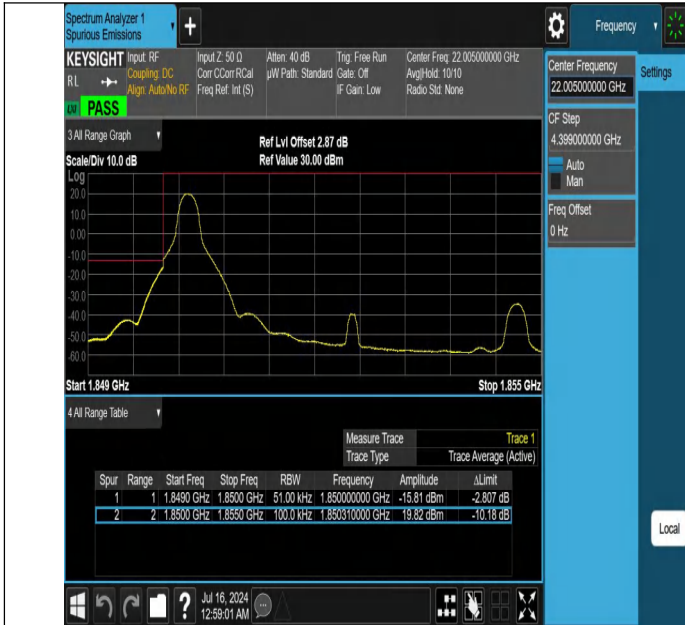
NTNV-DC\_12A-n25A-PC3-15-5+35-M+M-QPSK-IDLE-CP-64QAM-Outer\_Full-Ant1-PASS



NTNV-DC\_12A-n25A-PC3-15-5+35-M+M-QPSK-IDLE-CP-256QAM-Outer\_Full-Ant1-PASS

# Band Edge for NSA

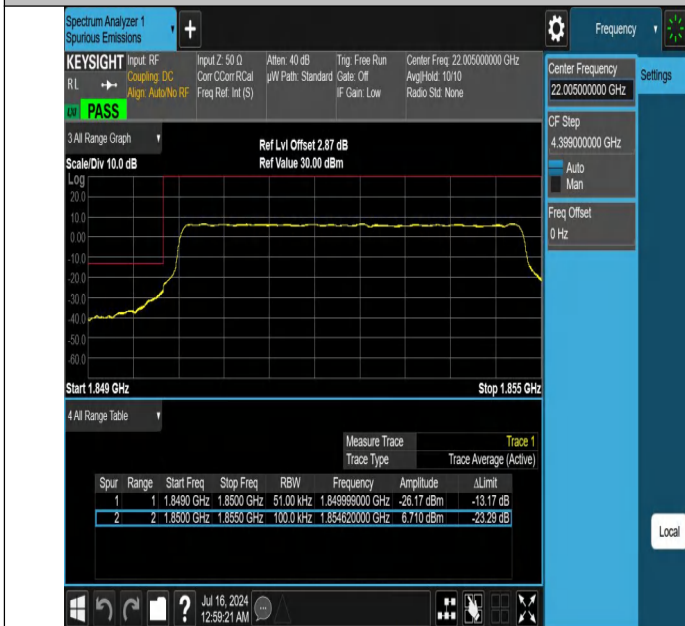
## Test Graphs



NTVN-DC\_12A\_n25A-PC3-15-5+5-M+L-N/A-N/A-DFT-PI2BPSK-E  
dge\_1RB\_Left-PASS



NTVN-DC\_12A\_n25A-PC3-15-5+5-M+H-N/A-N/A-DFT-PI2BPSK-Ed  
ge\_1RB\_Right-PASS



NTVN-DC\_12A\_n25A-PC3-15-5+5-M+L-N/A-N/A-DFT-PI2BPSK-O  
uter\_Full-PASS



NTVN-DC\_12A\_n25A-PC3-15-5+5-M+H-N/A-N/A-DFT-PI2BPSK-Ou  
ter\_Full-PASS