

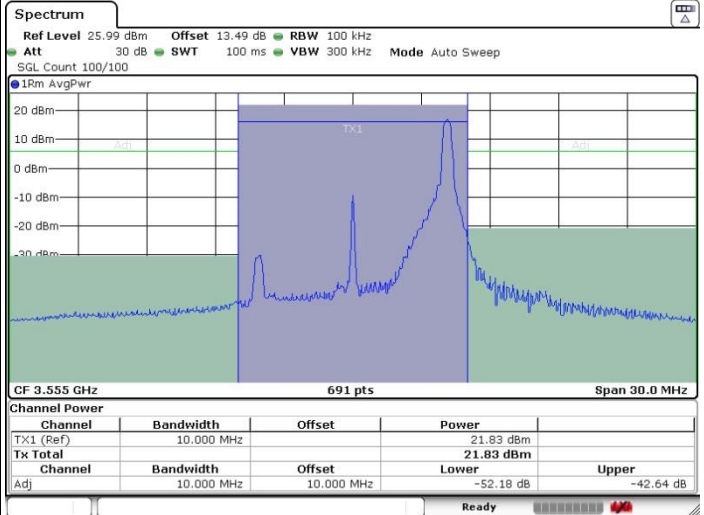
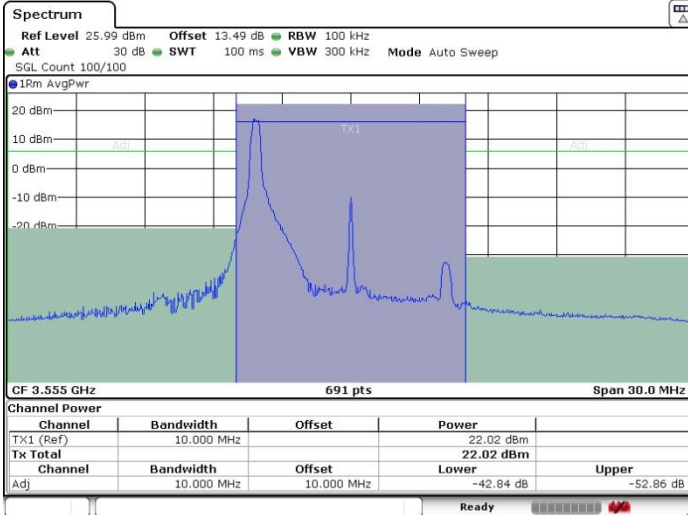


FR1 n78 / 10MHz

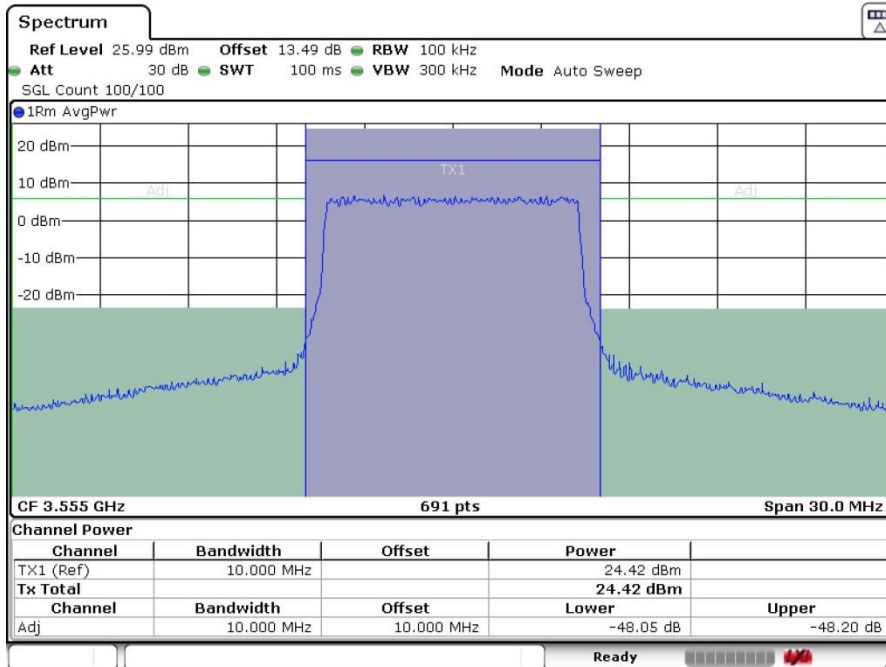
QPSK

Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB

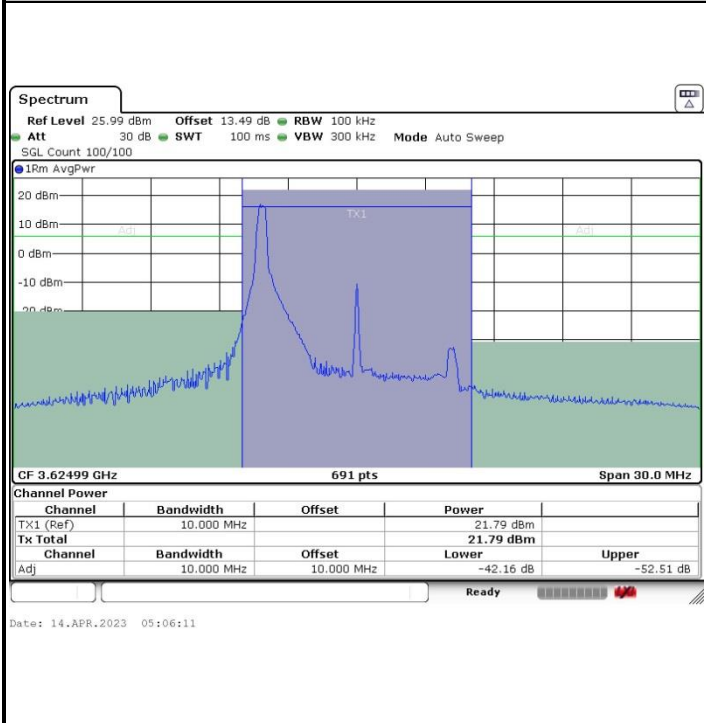




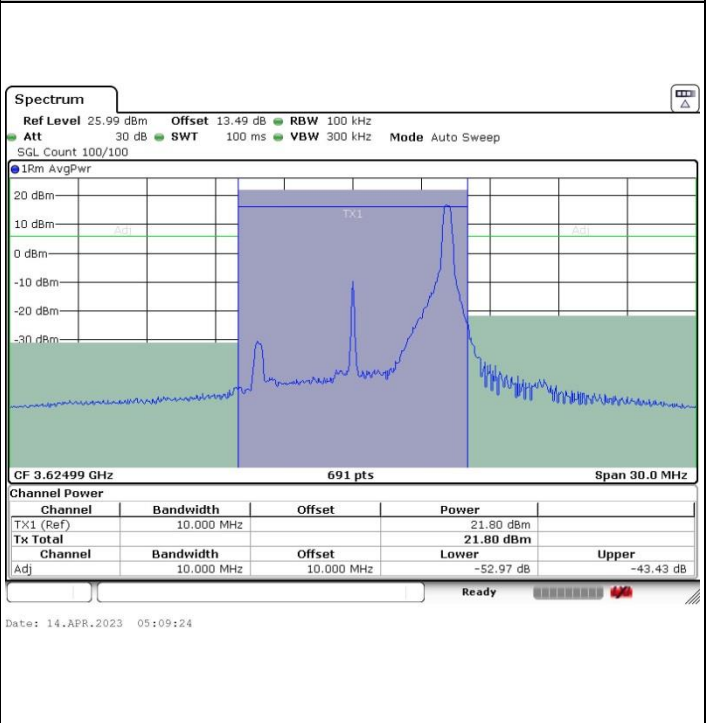
FR1 n78 / 10MHz

QPSK

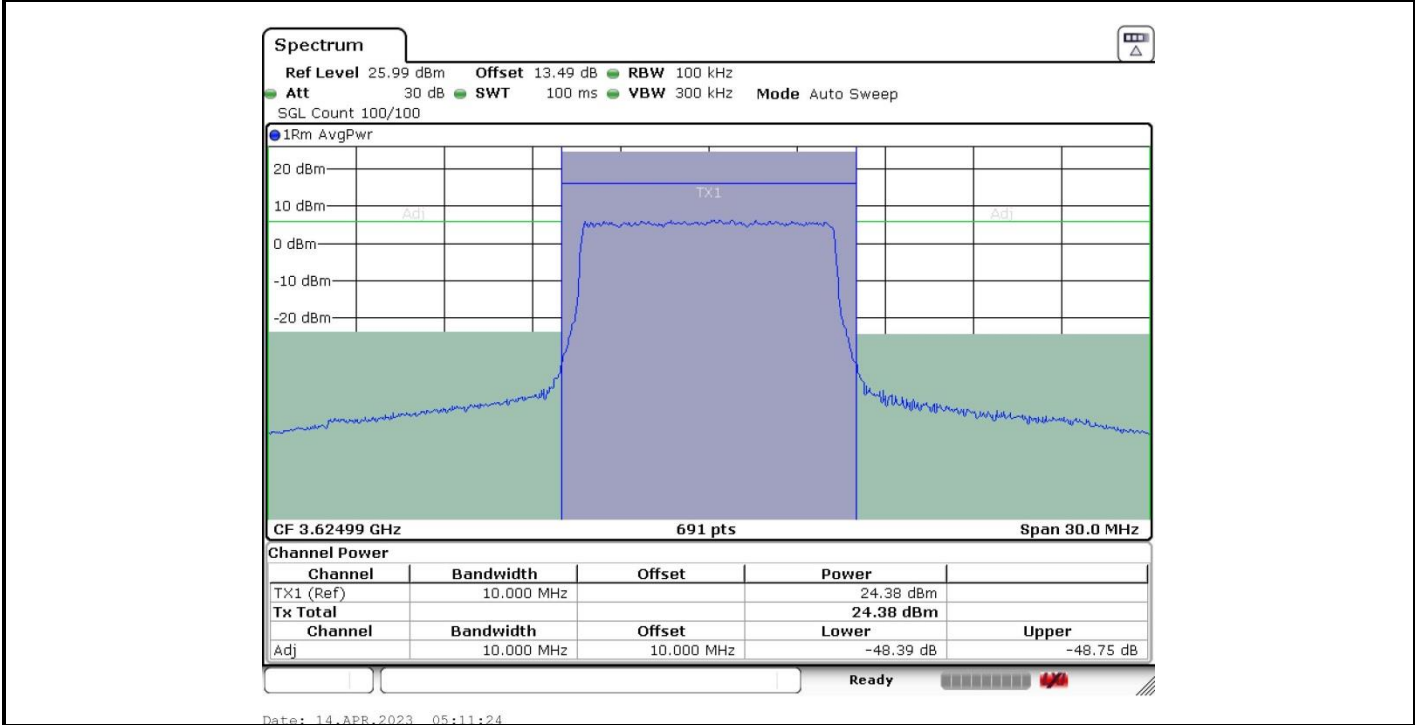
Middle Channel / 1RB0



Middle Channel / 1RBmax



Middle Channel / FullRB



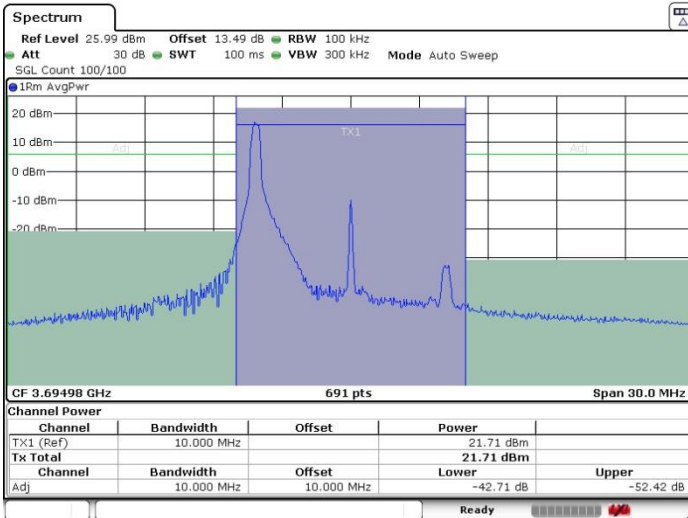


FR1 n78 / 10MHz

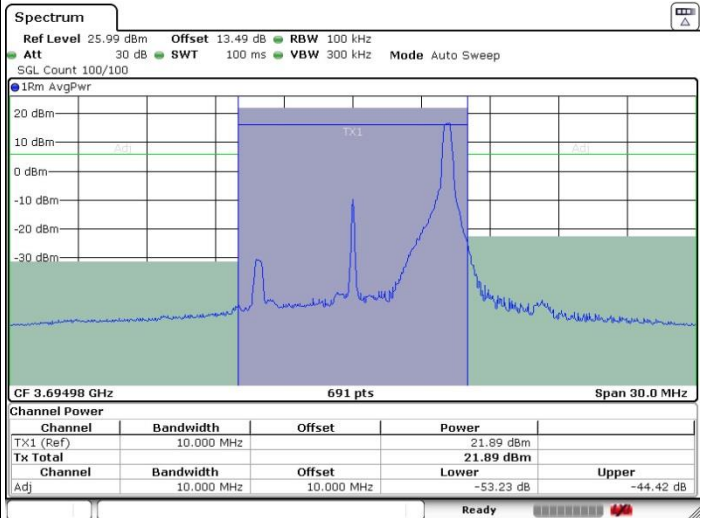
QPSK

Highest Channel / 1RB0

Highest Channel / 1RBmax

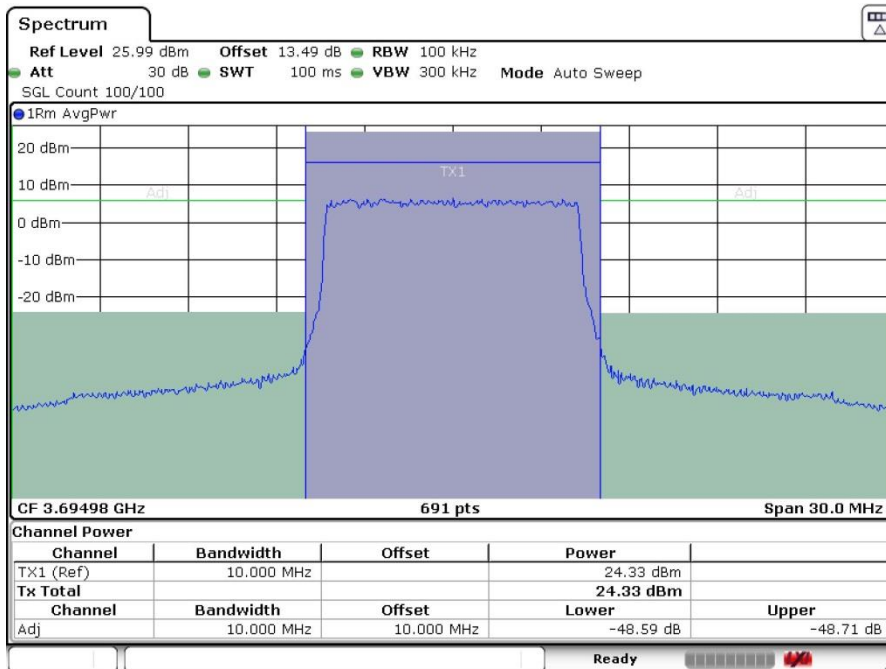


Date: 14.APR.2023 05:33:45



Date: 14.APR.2023 05:30:02

Highest Channel / FullRB



Date: 14.APR.2023 05:35:17

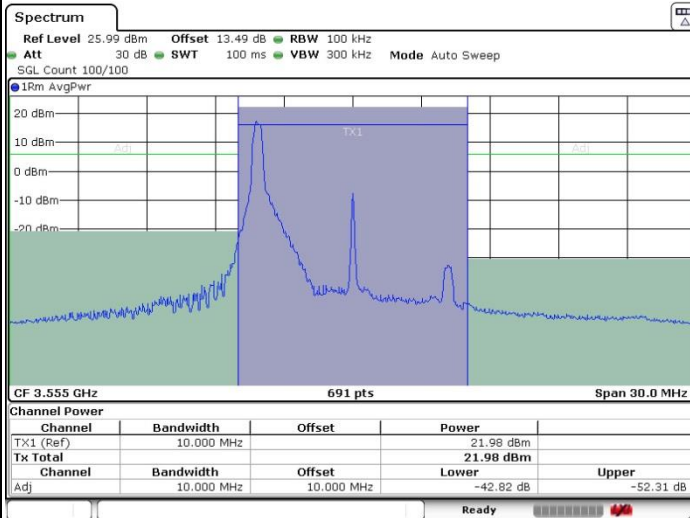


FR1 n78 / 10MHz

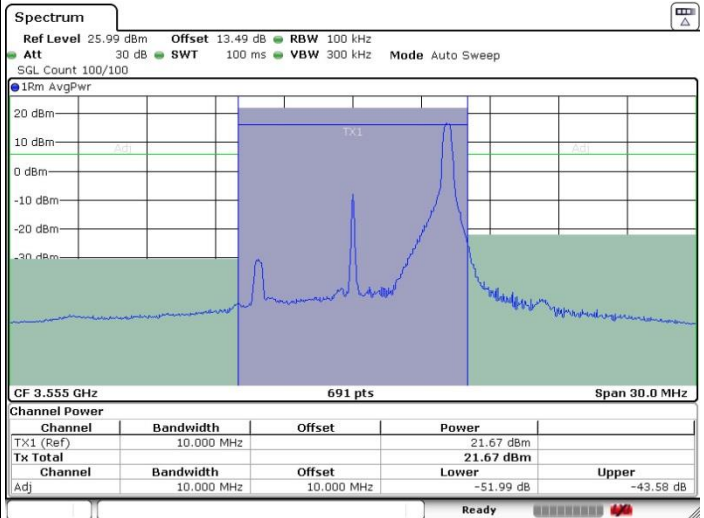
16QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

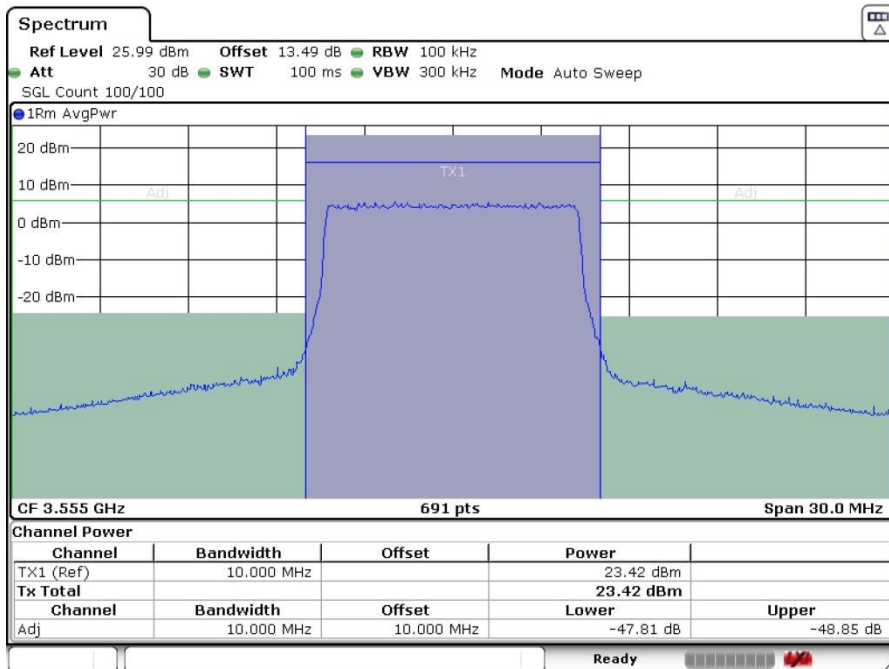


Date: 14.APR.2023 05:24:02



Date: 14.APR.2023 05:26:38

Lowest Channel / FullIRB



Date: 14.APR.2023 05:16:54

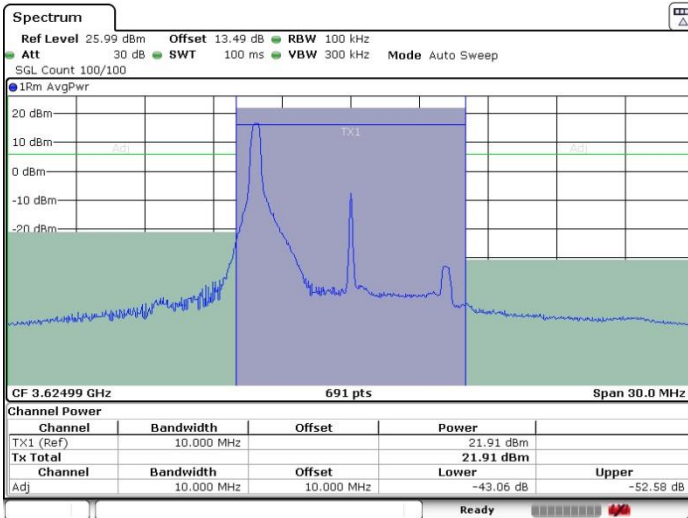


FR1 n78 / 10MHz

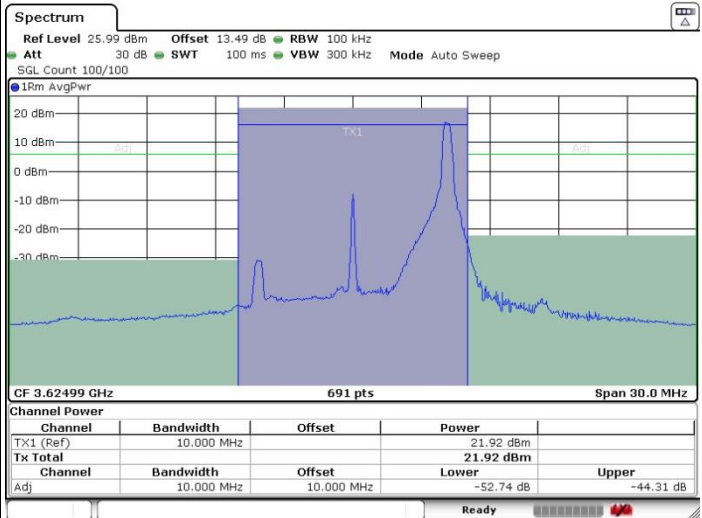
16QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

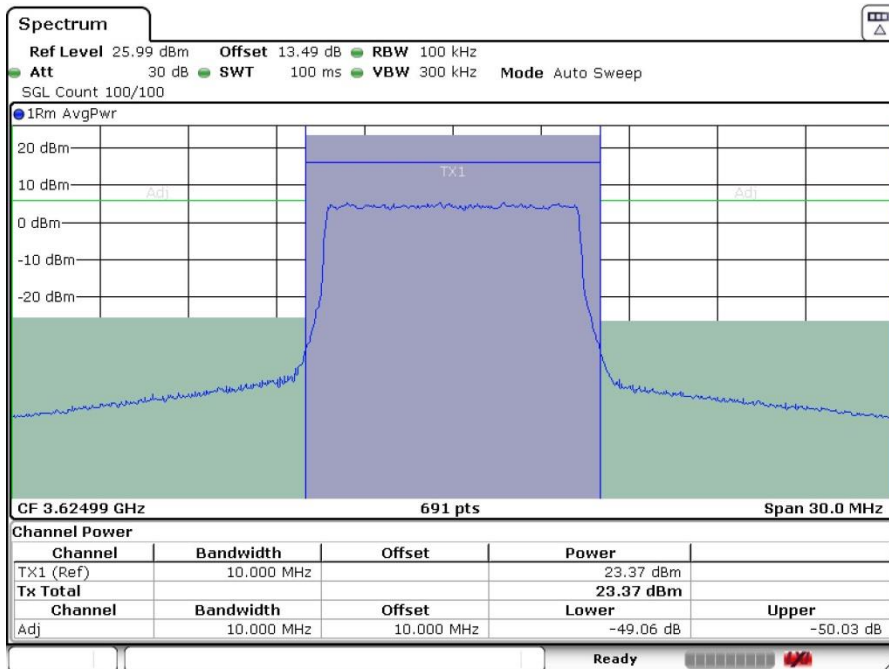


Date: 14.APR.2023 05:06:35



Date: 14.APR.2023 05:08:59

Middle Channel / FullRB



Date: 14.APR.2023 05:11:52

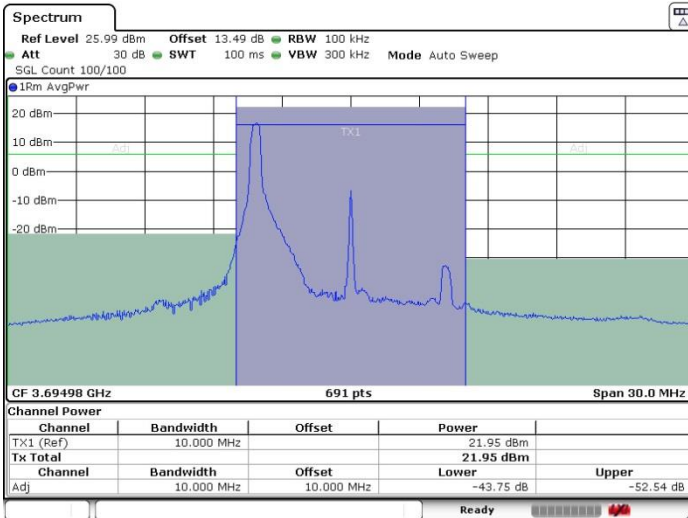


FR1 n78 / 10MHz

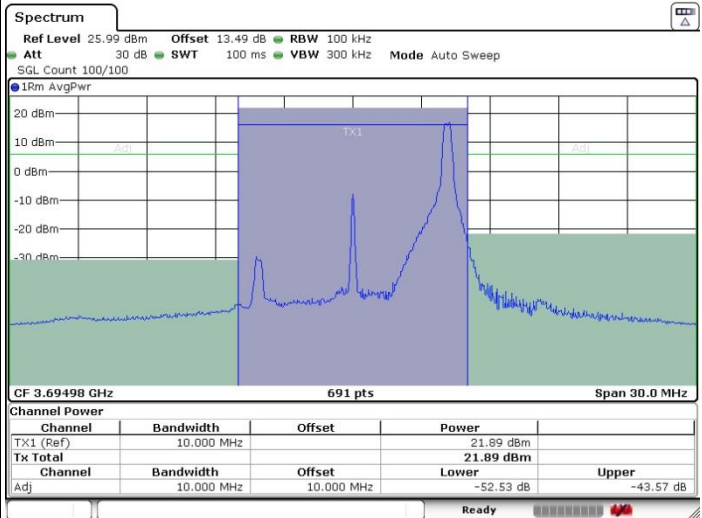
16QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

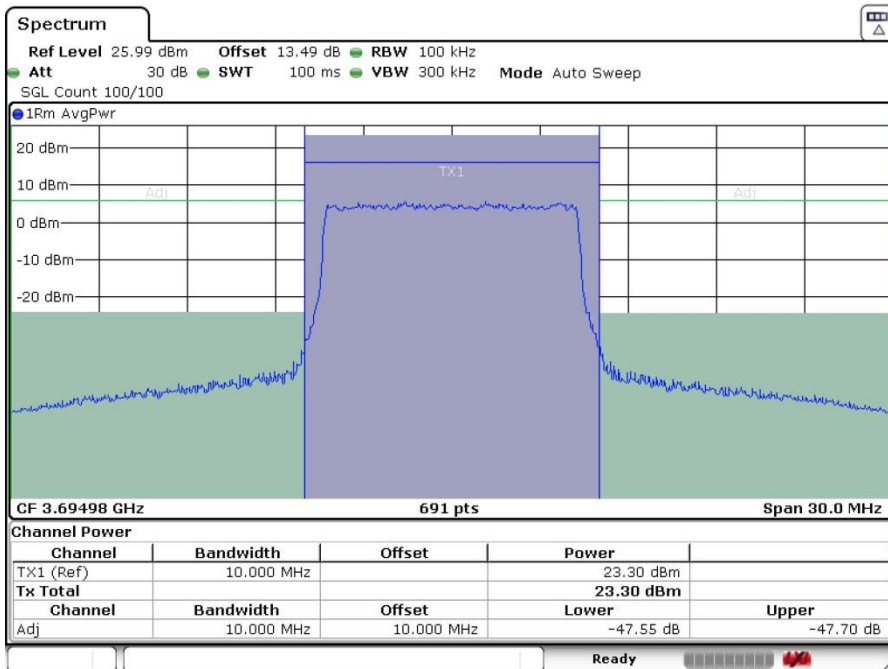


Date: 14.APR.2023 05:33:21



Date: 14.APR.2023 05:30:32

Highest Channel / FullRB



Date: 14.APR.2023 05:35:49

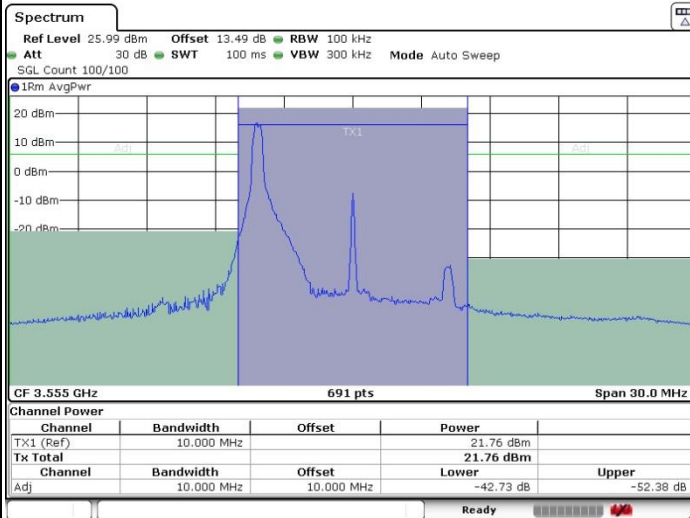


FR1 n78 / 10MHz

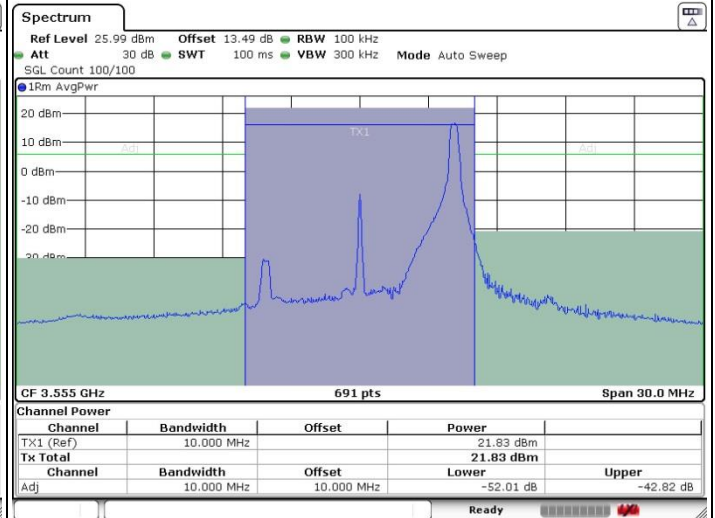
64QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

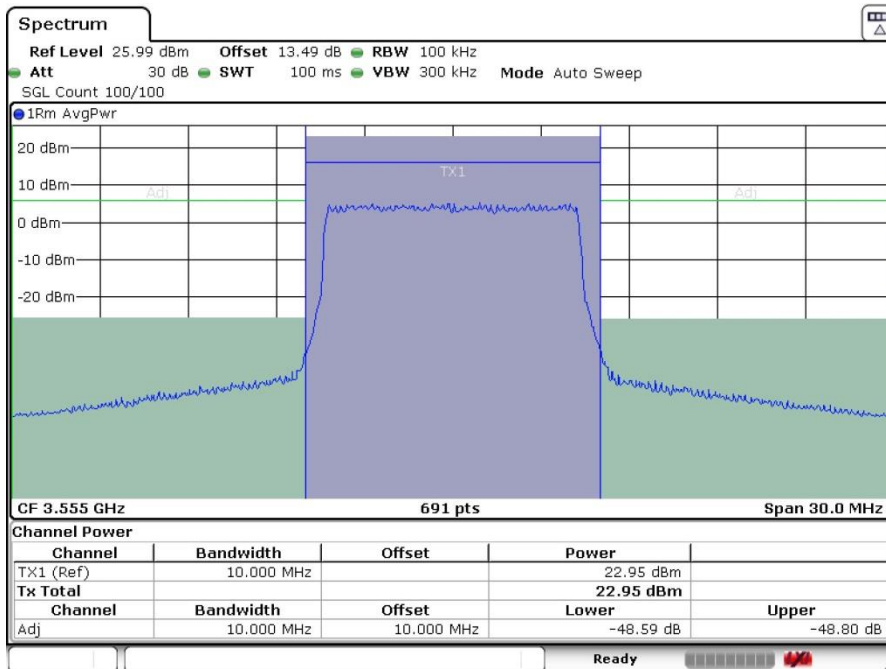


Date: 14.APR.2023 05:24:26



Date: 14.APR.2023 05:26:12

Lowest Channel / FullIRB



Date: 14.APR.2023 05:14:31

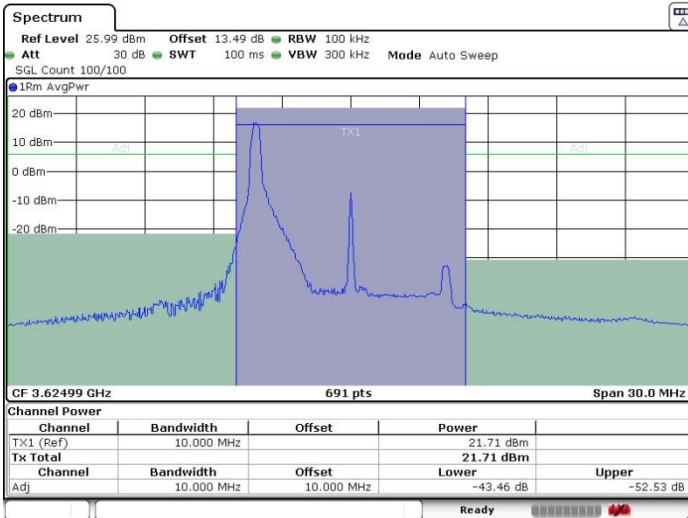


FR1 n78 / 10MHz

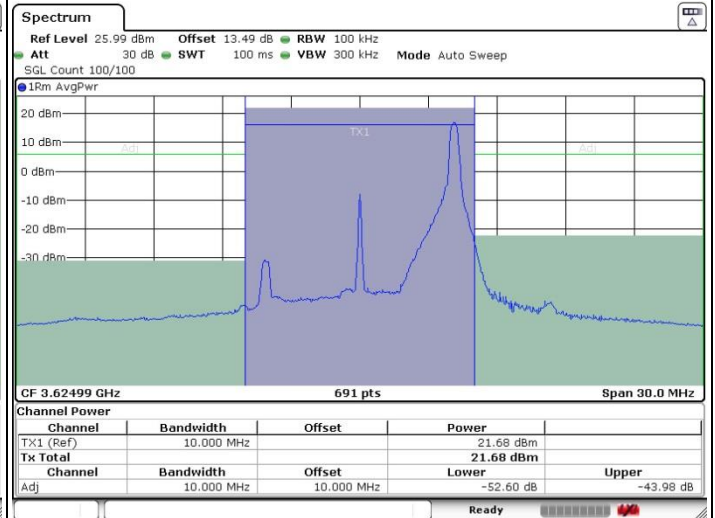
64QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

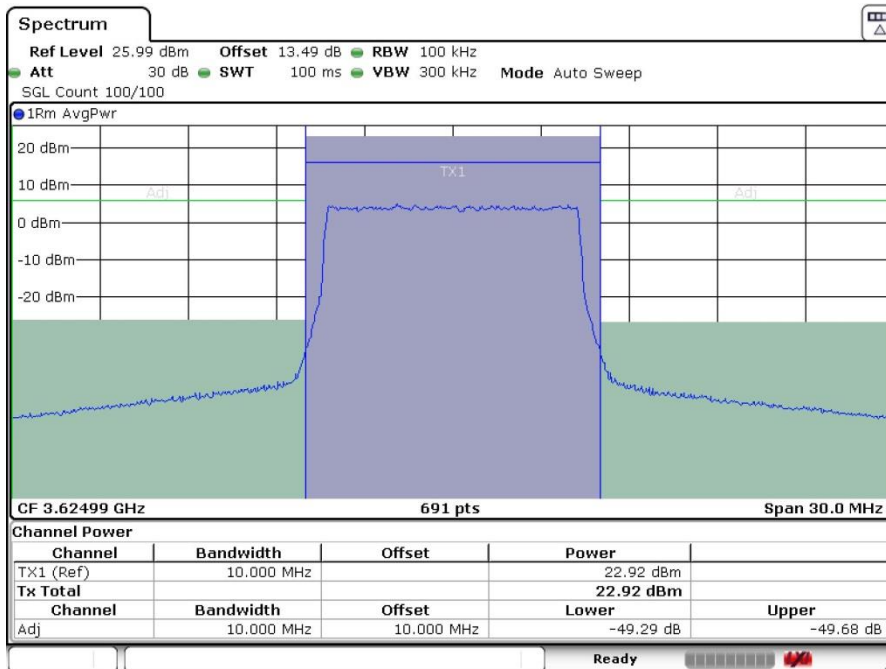


Date: 14.APR.2023 05:07:03



Date: 14.APR.2023 05:08:36

Middle Channel / FullRB



Date: 14.APR.2023 05:12:17



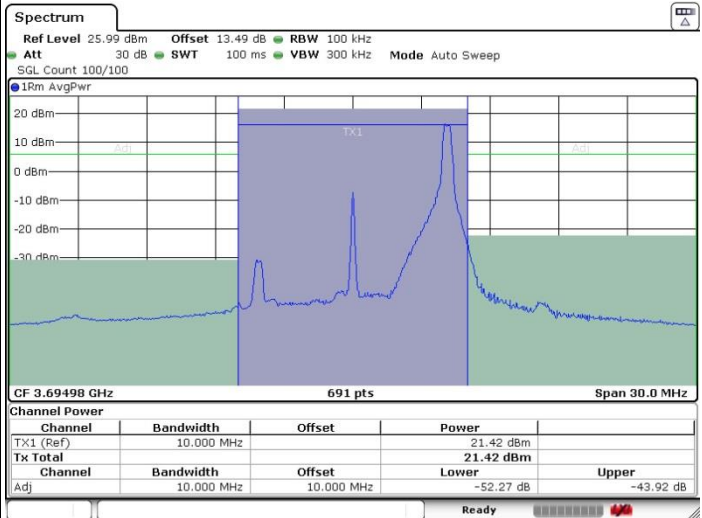
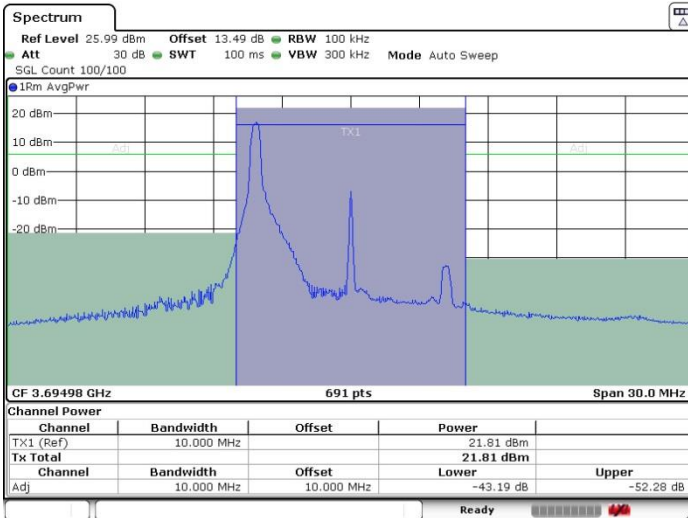


FR1 n78 / 10MHz

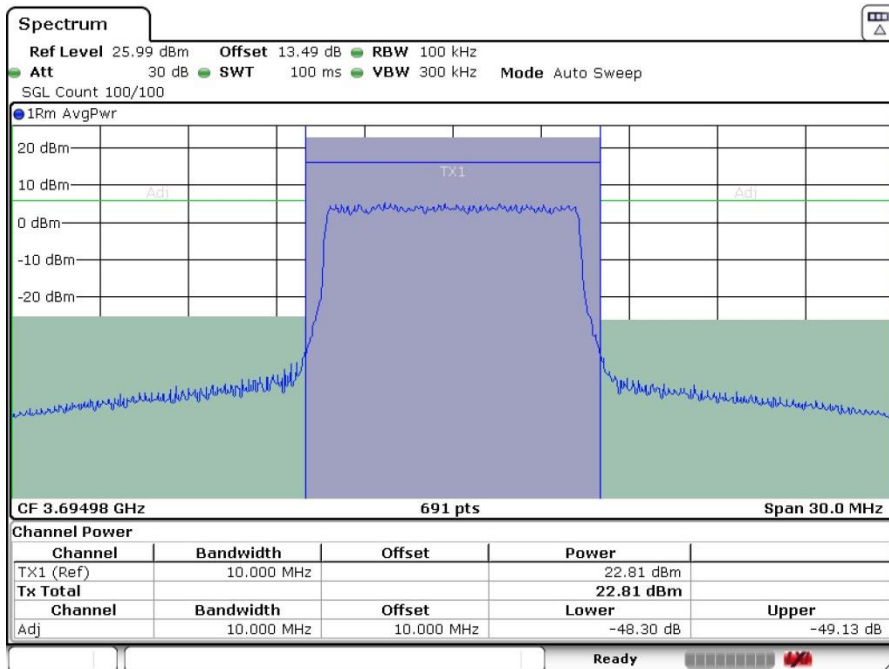
64QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB



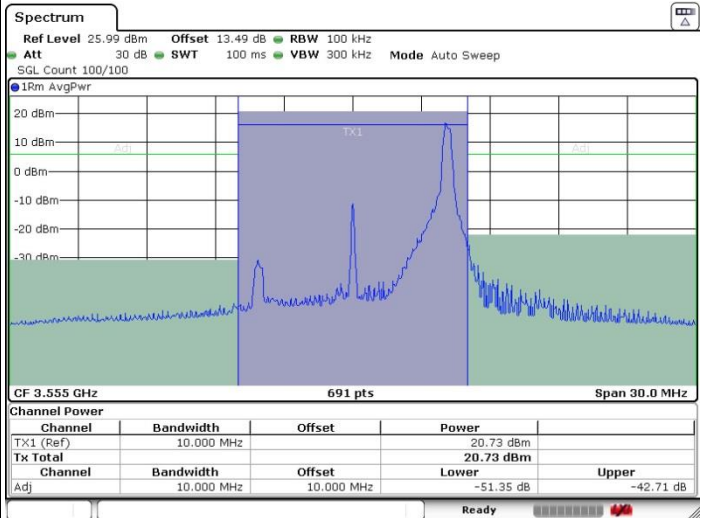
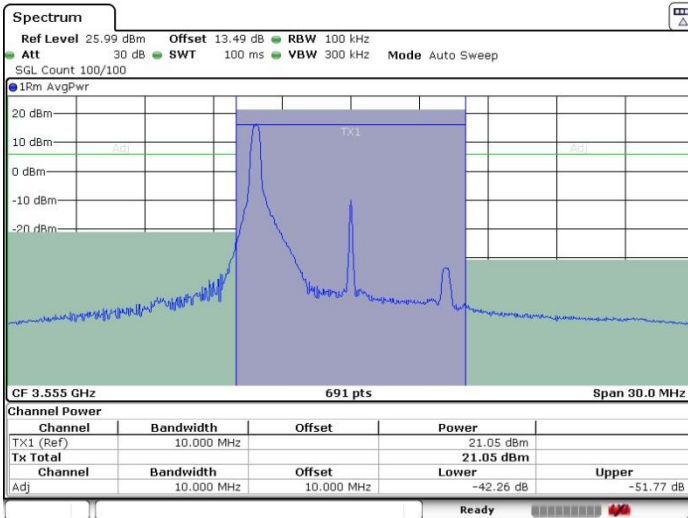


FR1 n78 / 10MHz

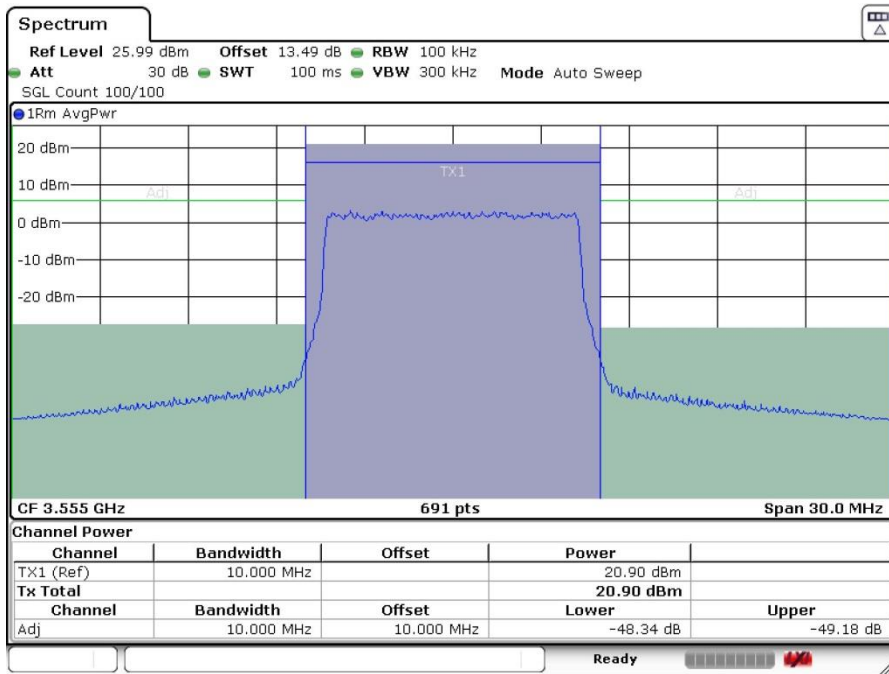
256QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB



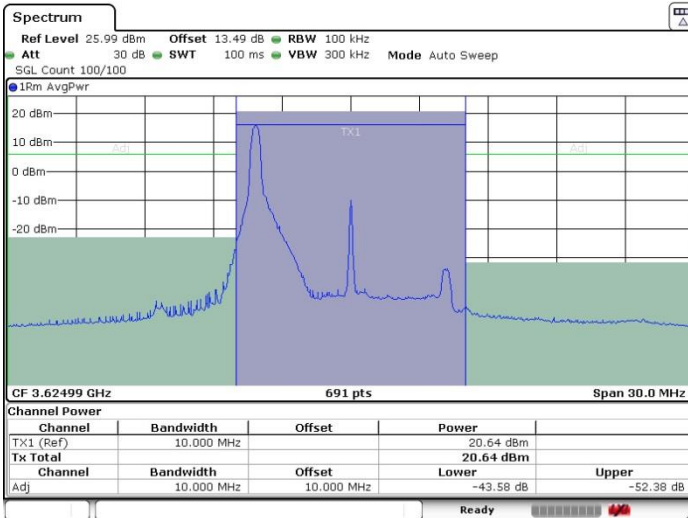


FR1 n78 / 10MHz

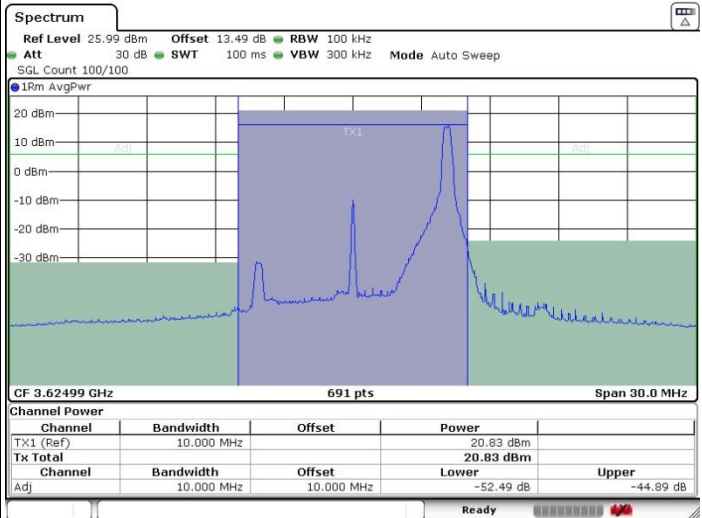
256QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

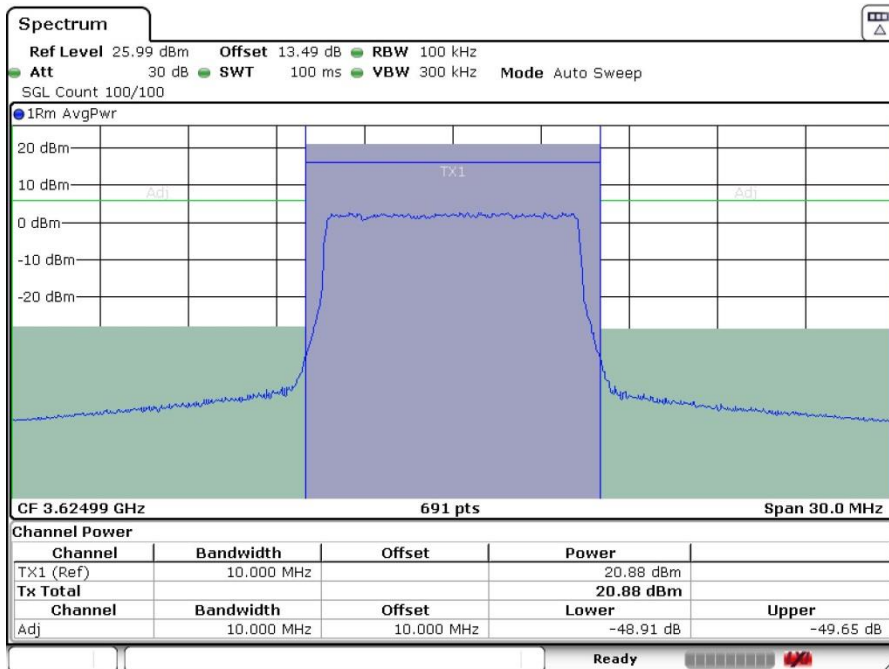


Date: 14.APR.2023 05:07:29



Date: 14.APR.2023 05:08:01

Middle Channel / FullRB



Date: 14.APR.2023 05:12:58

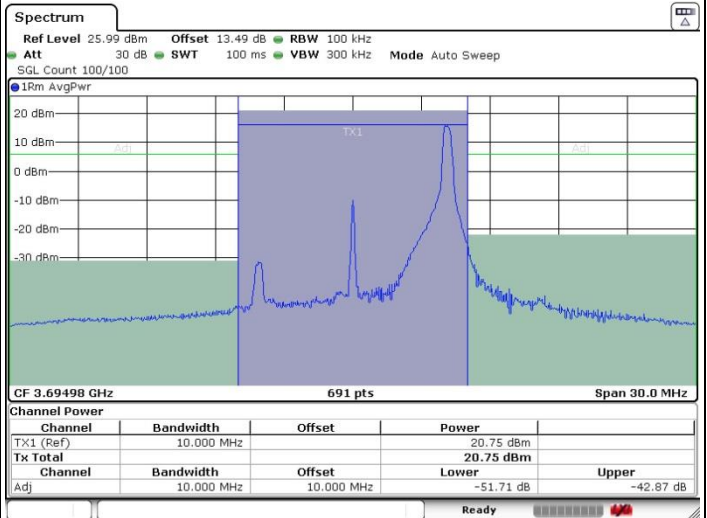
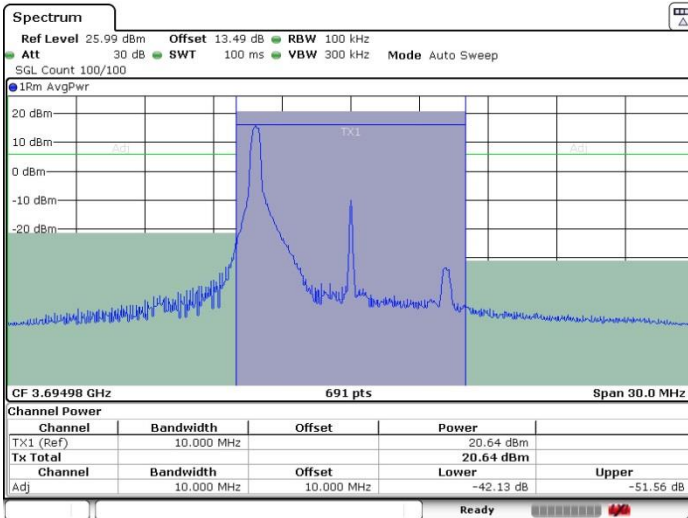


FR1 n78 / 10MHz

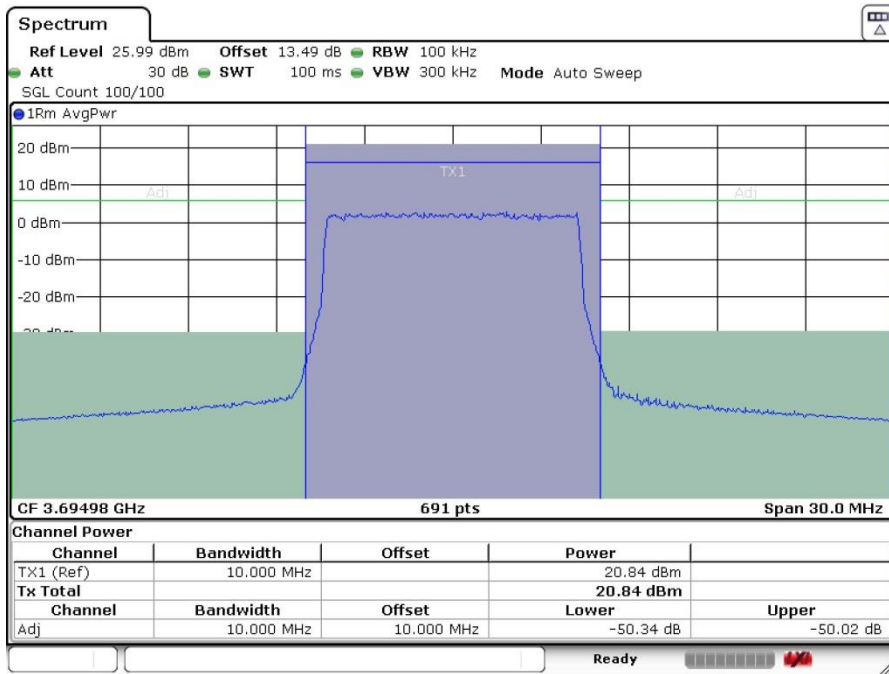
256QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB



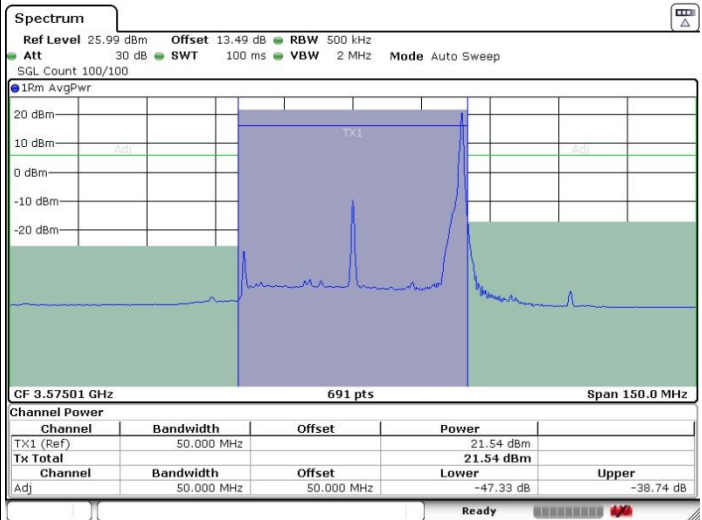
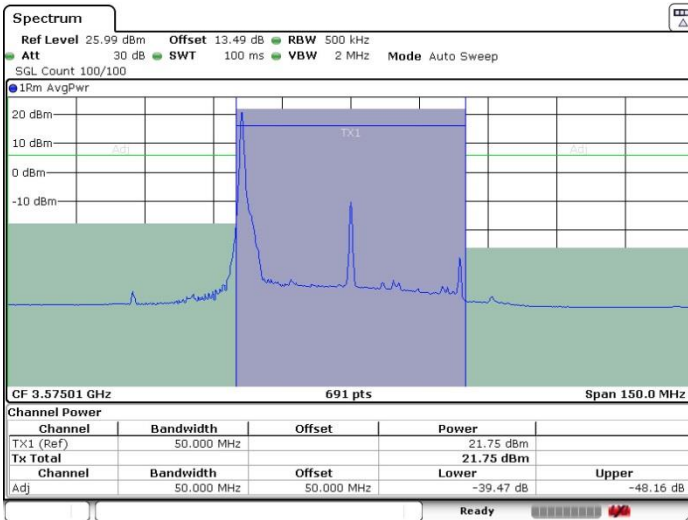


FR1 n78 / 50MHz

BPSK

Lowest Channel / 1RB0

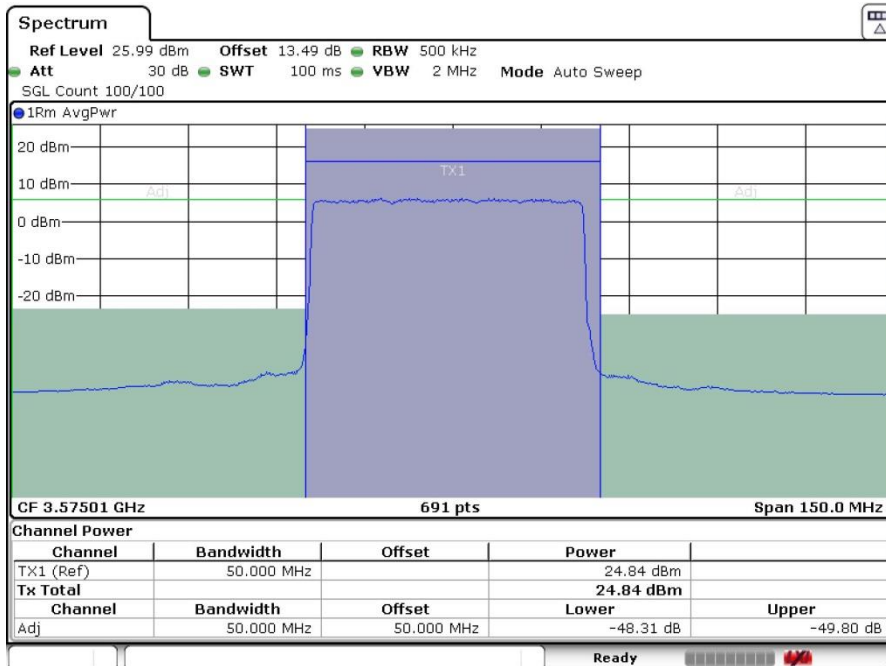
Lowest Channel / 1RBmax



Date: 14.APR.2023 04:38:49

Date: 14.APR.2023 04:44:53

Lowest Channel / FullRB



Date: 14.APR.2023 04:38:16

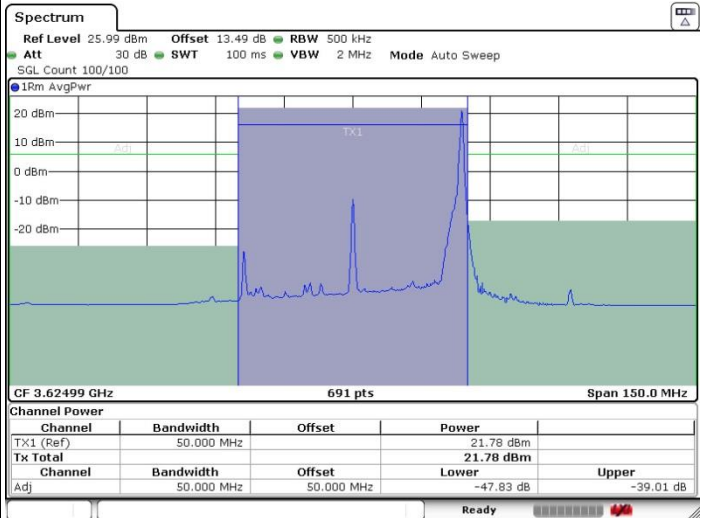
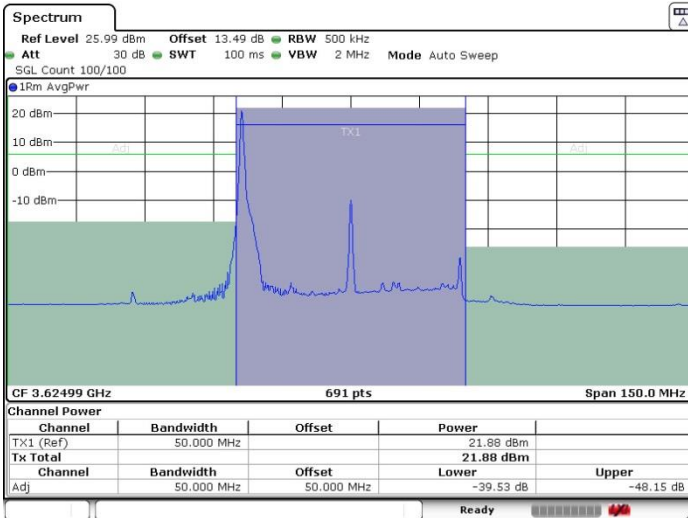


FR1 n78 / 50MHz

BPSK

Middle Channel / 1RB0

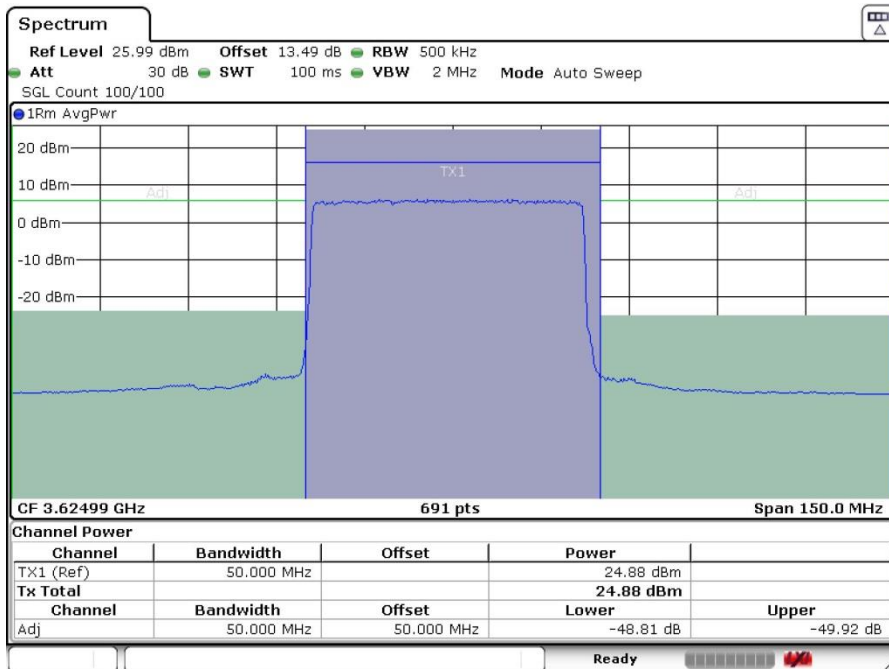
Middle Channel / 1RBmax



Date: 14.APR.2023 04:26:39

Date: 14.APR.2023 04:31:25

Middle Channel / FullRB



Date: 14.APR.2023 04:32:16

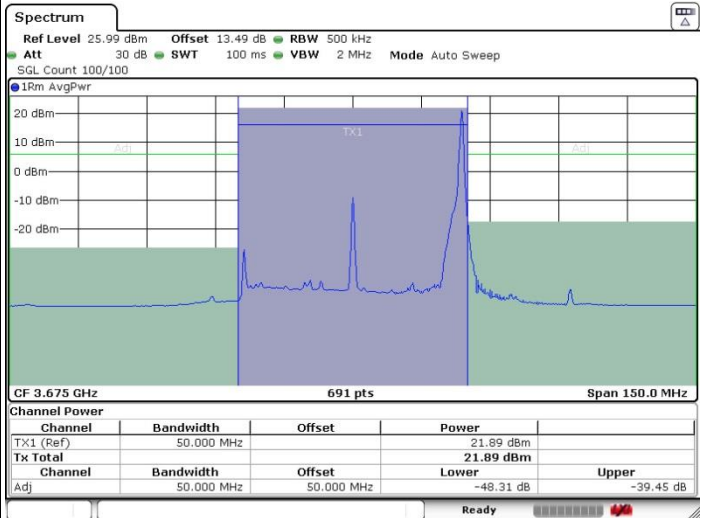
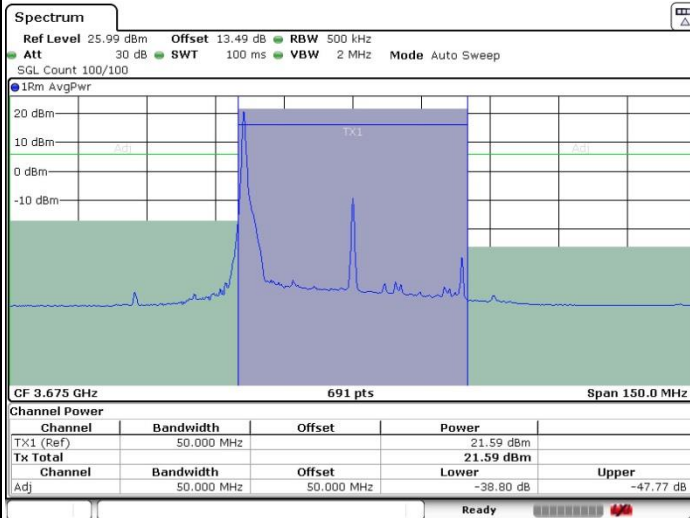


FR1 n78 / 50MHz

BPSK

Highest Channel / 1RB0

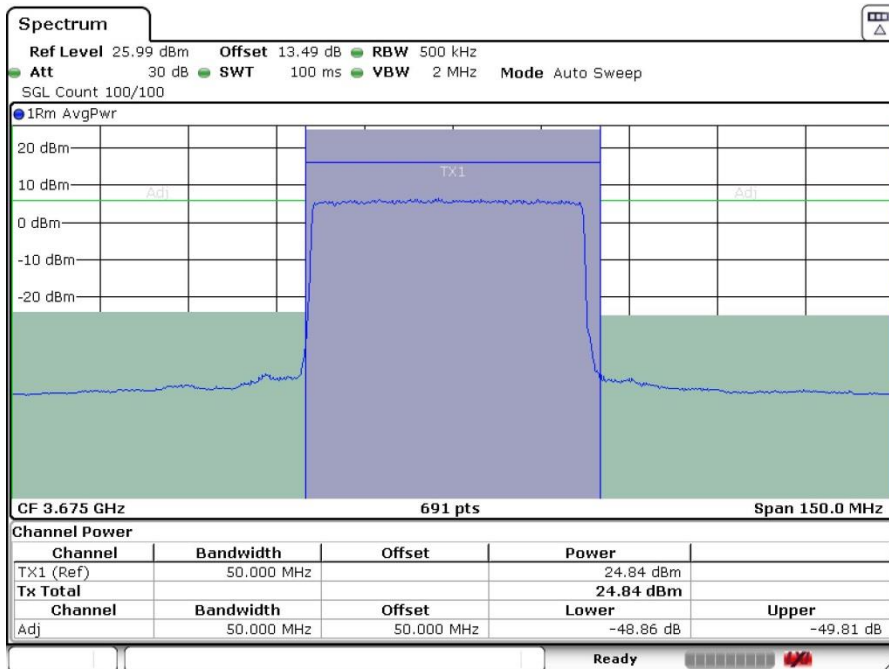
Highest Channel / 1RBmax



Date: 14.APR.2023 04:58:38

Date: 14.APR.2023 04:52:43

Highest Channel / FullRB



Date: 14.APR.2023 04:59:13

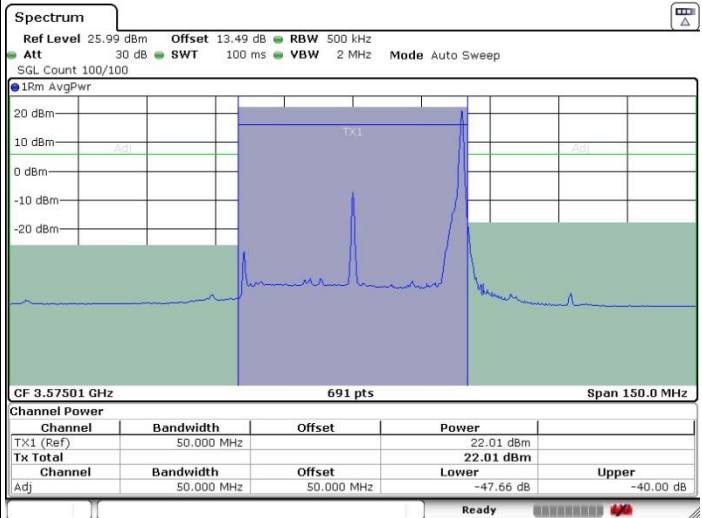
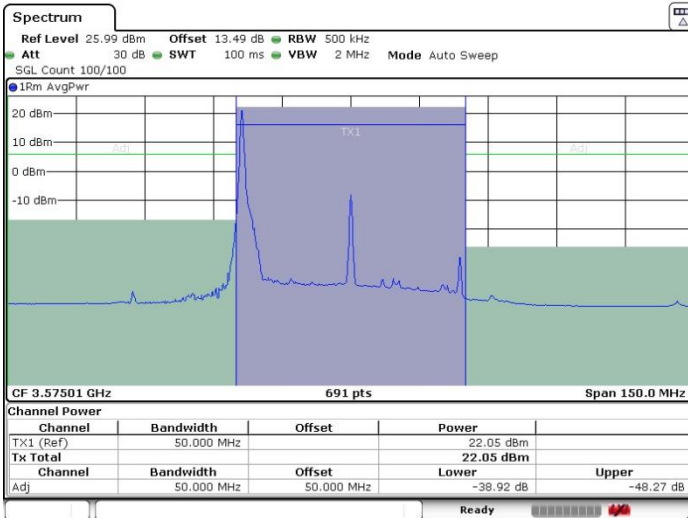


FR1 n78 / 50MHz

QPSK

Lowest Channel / 1RB0

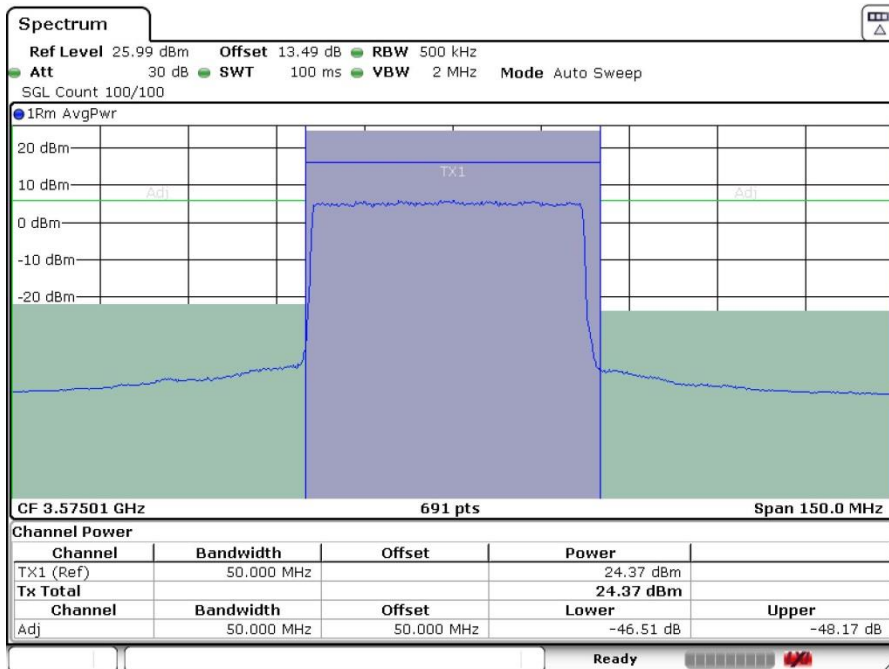
Lowest Channel / 1RBmax



Date: 14.APR.2023 04:39:19

Date: 14.APR.2023 04:44:14

Lowest Channel / FullRB



Date: 14.APR.2023 04:37:48



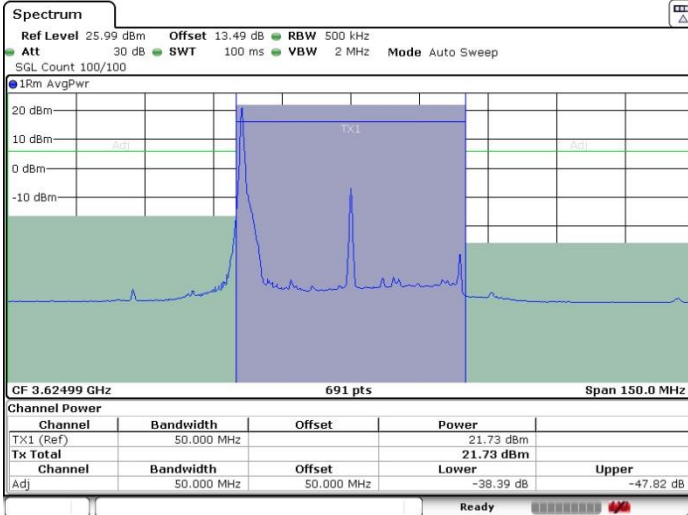


FR1 n78 / 50MHz

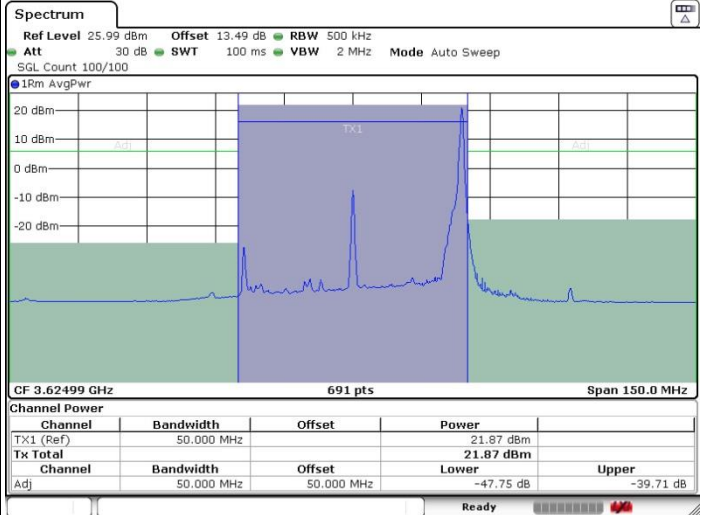
QPSK

Middle Channel / 1RB0

Middle Channel / 1RBmax

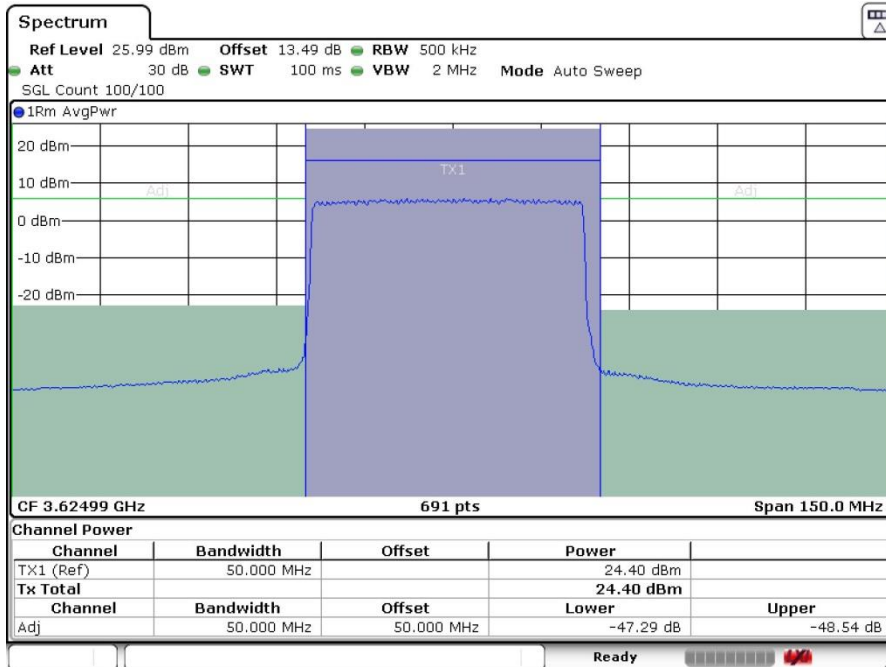


Date: 14.APR.2023 04:27:15



Date: 14.APR.2023 04:30:52

Middle Channel / FullRB



Date: 14.APR.2023 04:33:06

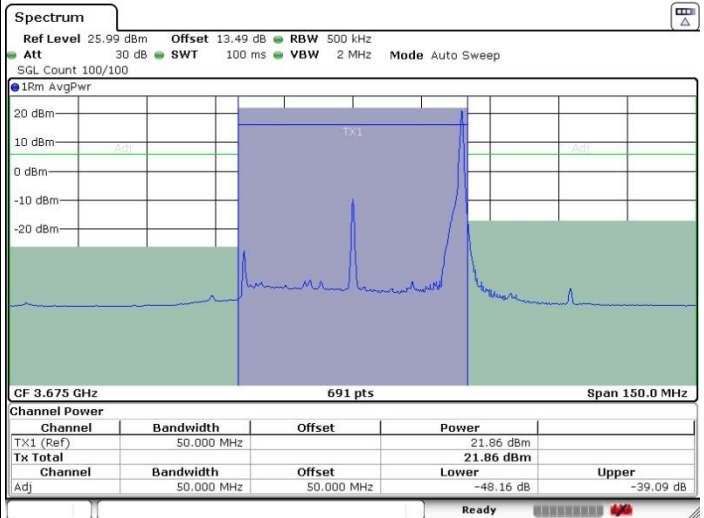
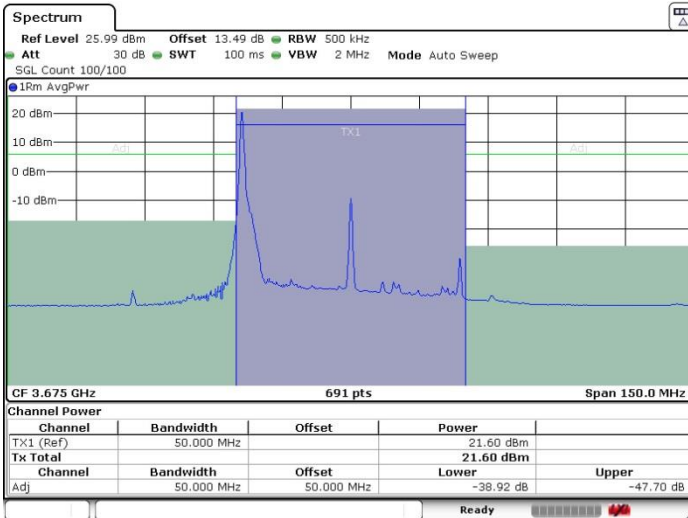


FR1 n78 / 50MHz

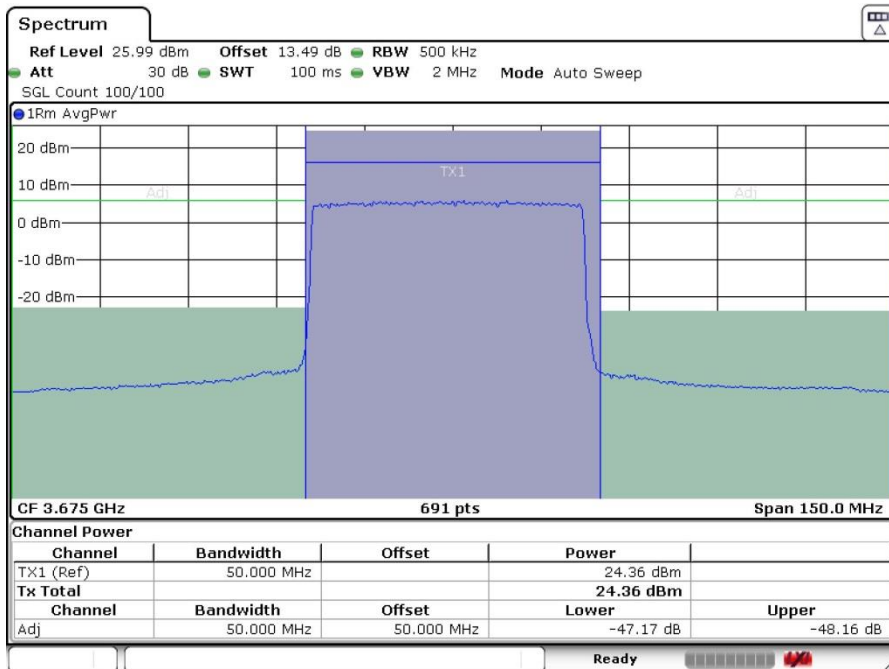
QPSK

Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB



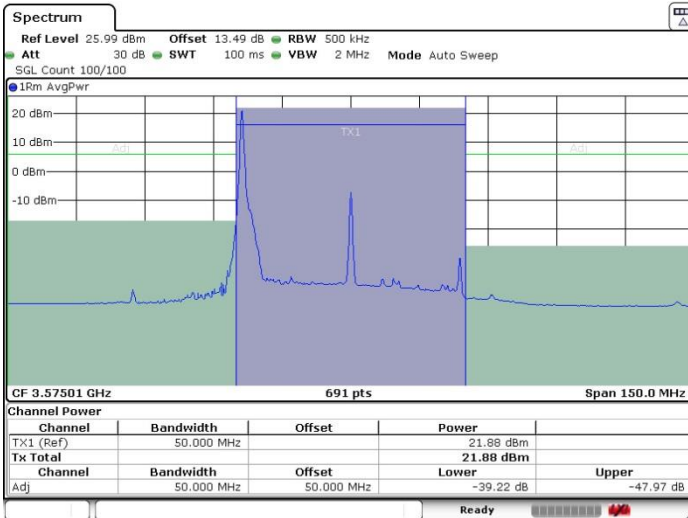


FR1 n78 / 50MHz

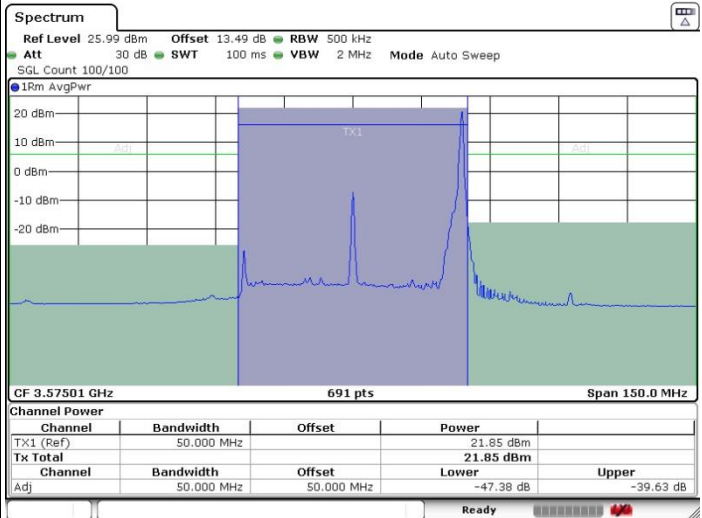
16QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

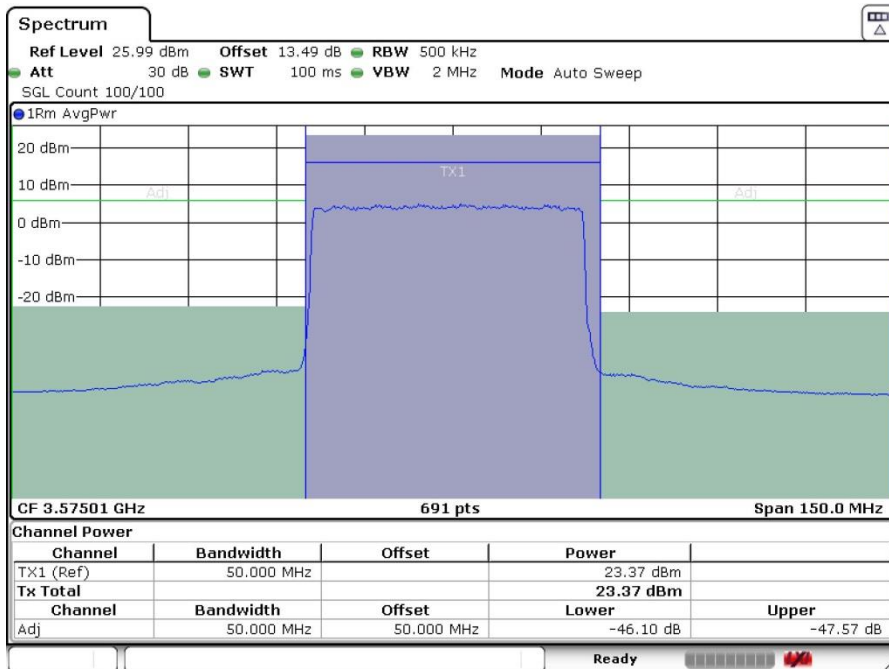


Date: 14.APR.2023 04:39:50



Date: 14.APR.2023 04:43:31

Lowest Channel / FullIRB



Date: 14.APR.2023 04:37:06

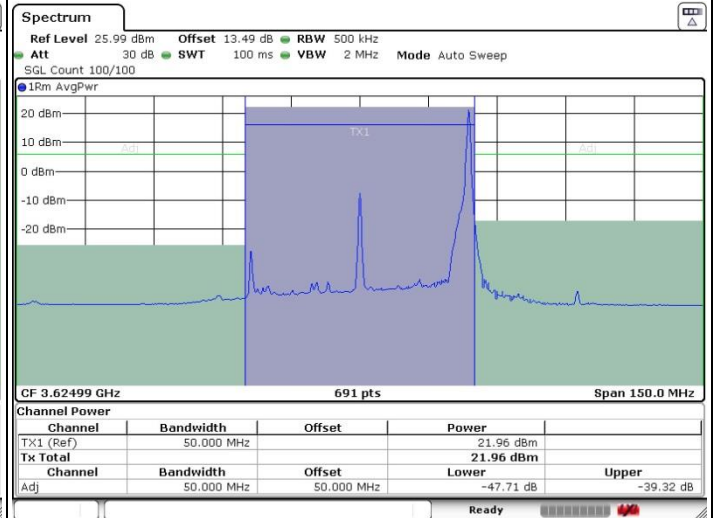
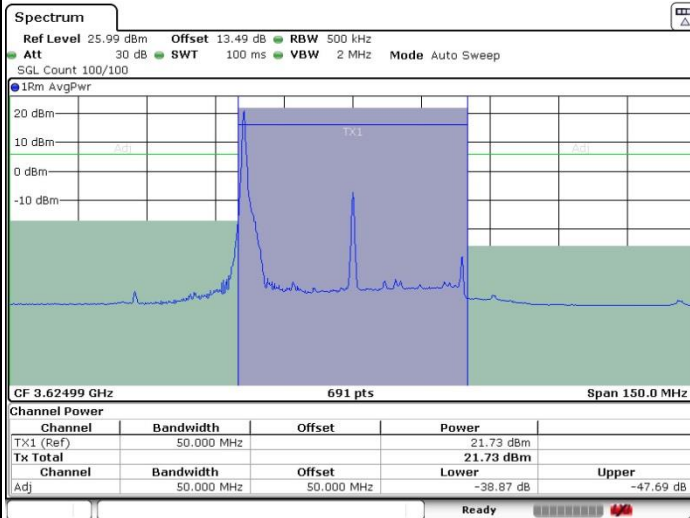


FR1 n78 / 50MHz

16QAM

Middle Channel / 1RB0

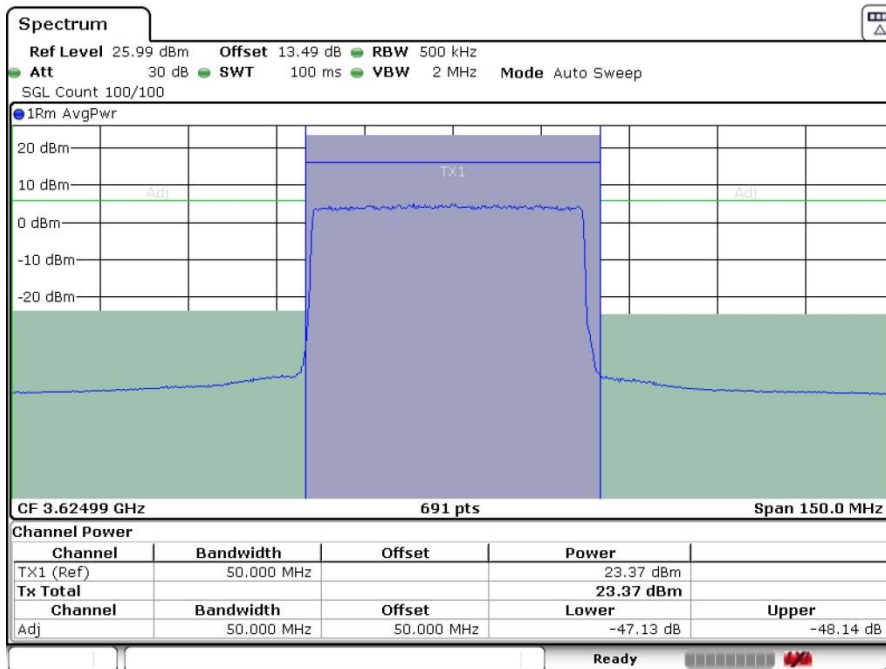
Middle Channel / 1RBmax



Date: 14.APR.2023 04:27:42

Date: 14.APR.2023 04:30:05

Middle Channel / FullRB



Date: 14.APR.2023 04:33:34

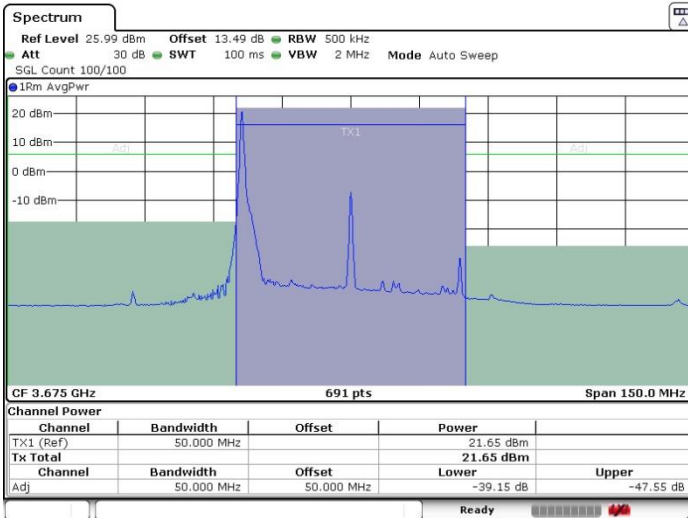


FR1 n78 / 50MHz

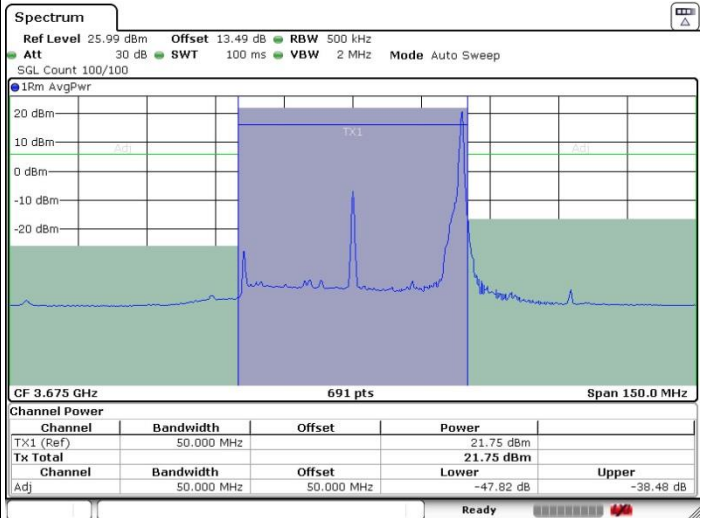
16QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

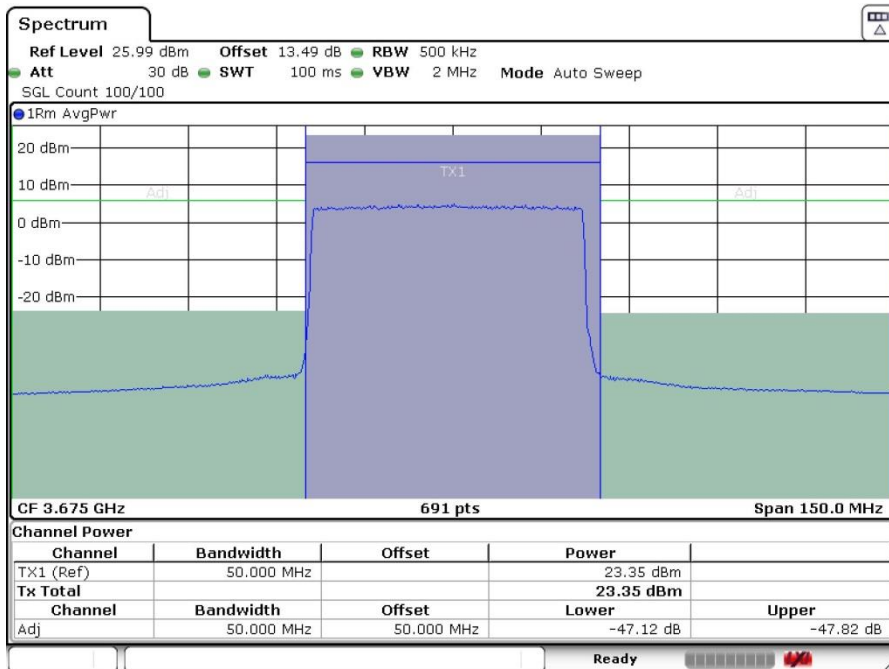


Date: 14.APR.2023 04:57:29



Date: 14.APR.2023 04:54:56

Highest Channel / FullRB



Date: 14.APR.2023 05:00:23

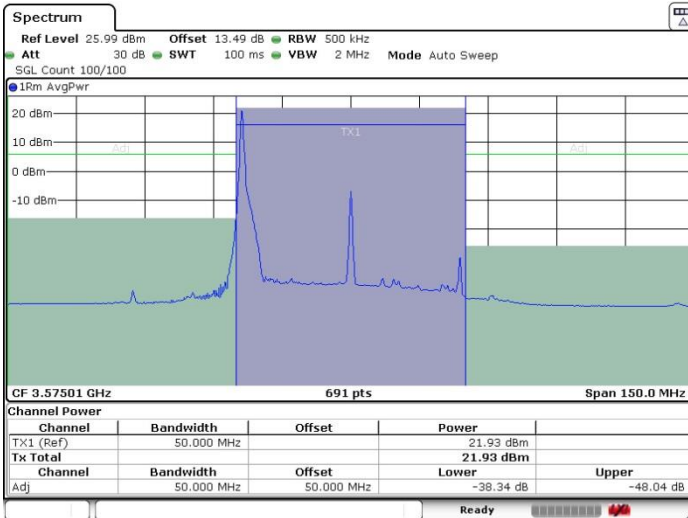


FR1 n78 / 50MHz

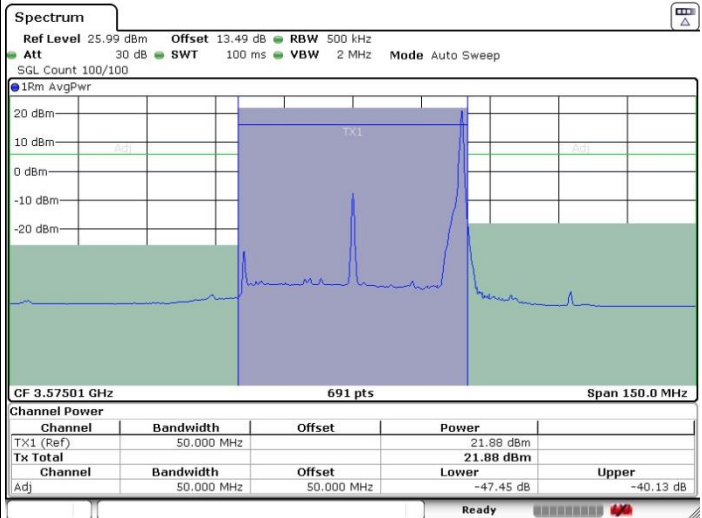
64QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

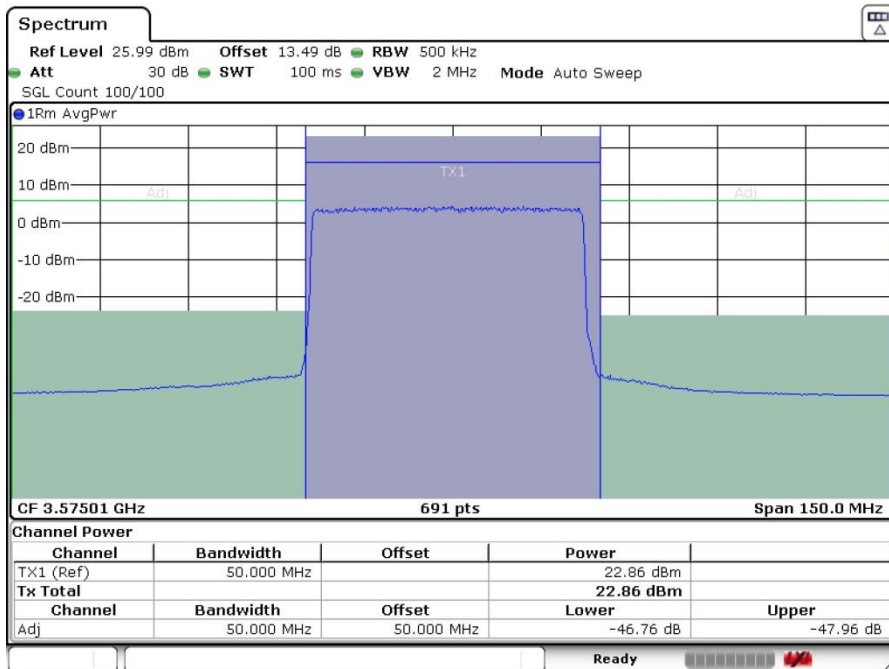


Date: 14.APR.2023 04:40:16



Date: 14.APR.2023 04:43:03

Lowest Channel / FullRB



Date: 14.APR.2023 04:36:40

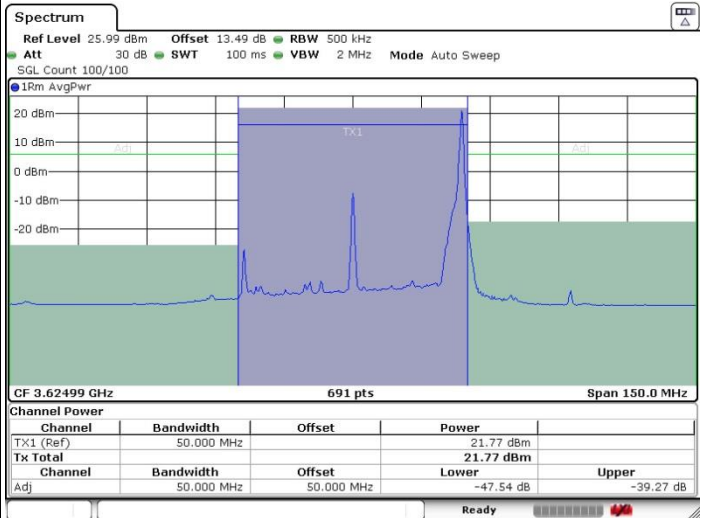
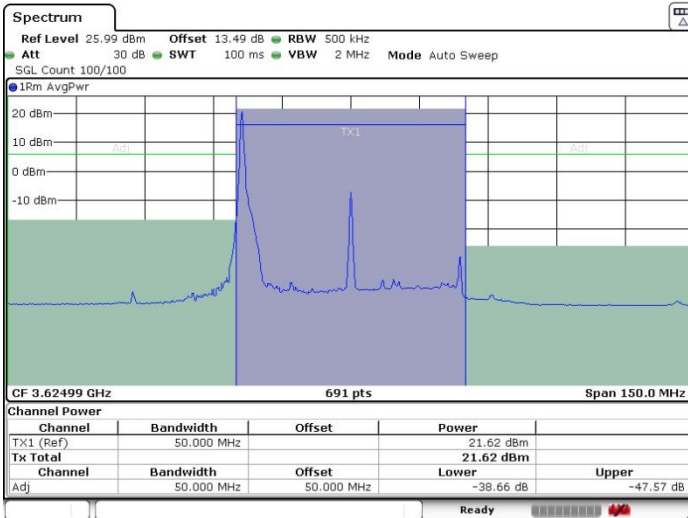


FR1 n78 / 50MHz

64QAM

Middle Channel / 1RB0

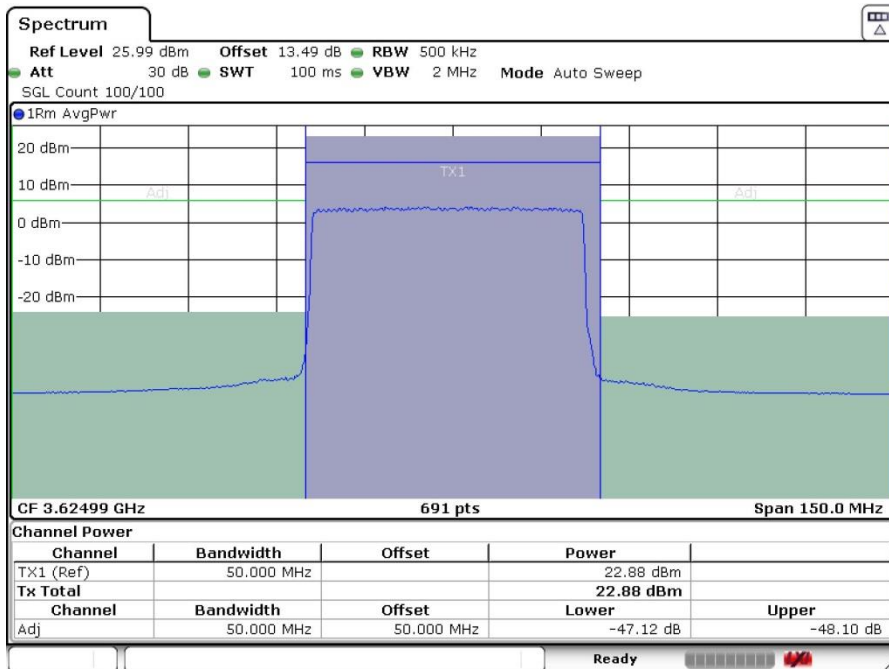
Middle Channel / 1RBmax



Date: 14.APR.2023 04:28:10

Date: 14.APR.2023 04:29:38

Middle Channel / FullRB



Date: 14.APR.2023 04:34:07

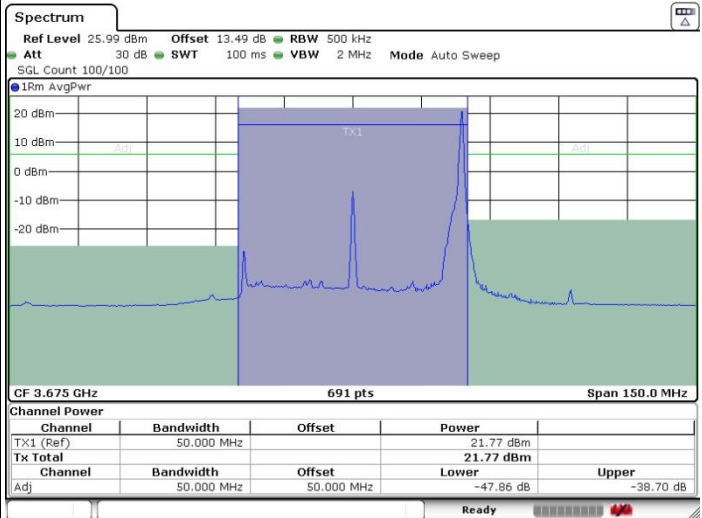
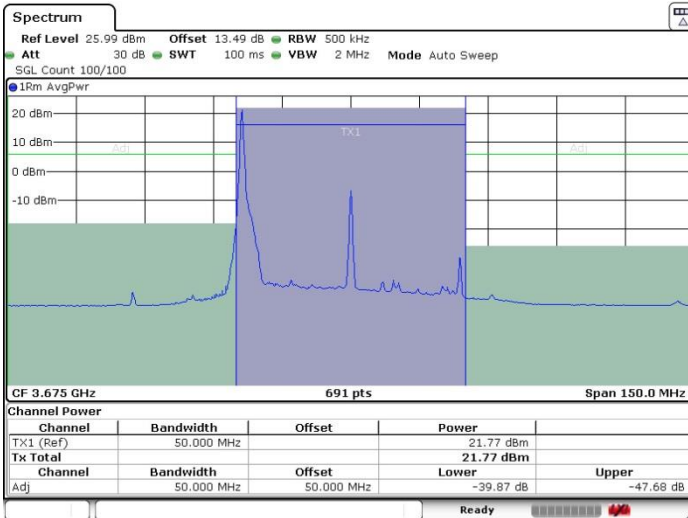


FR1 n78 / 50MHz

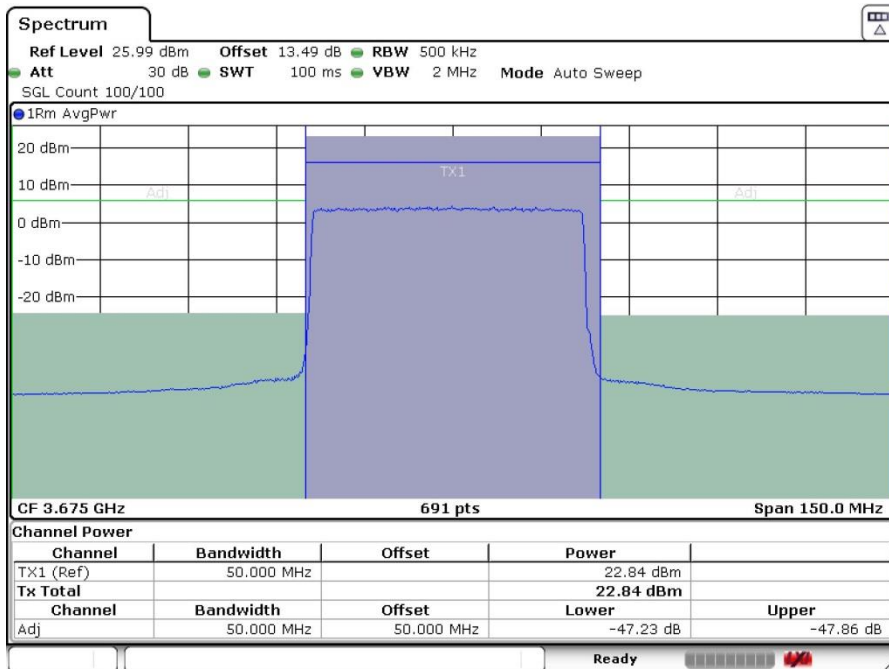
64QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB



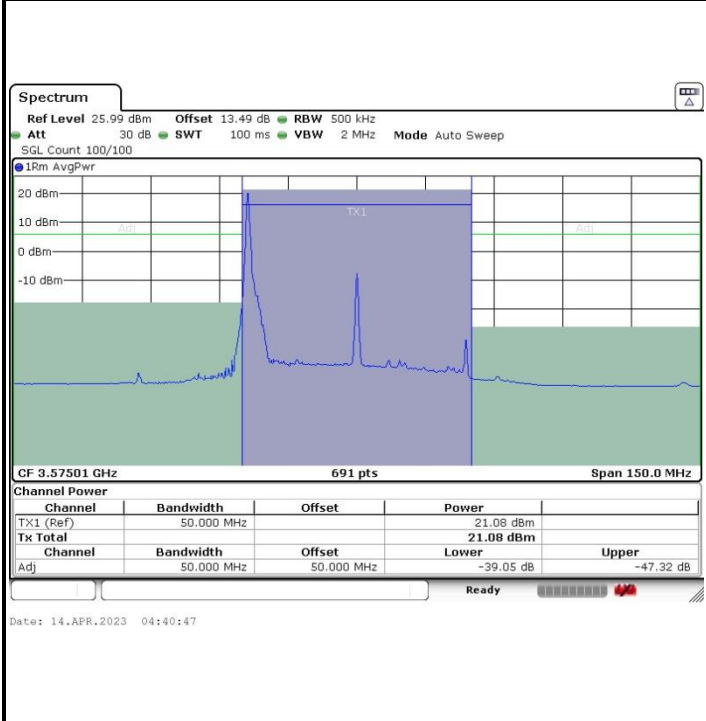




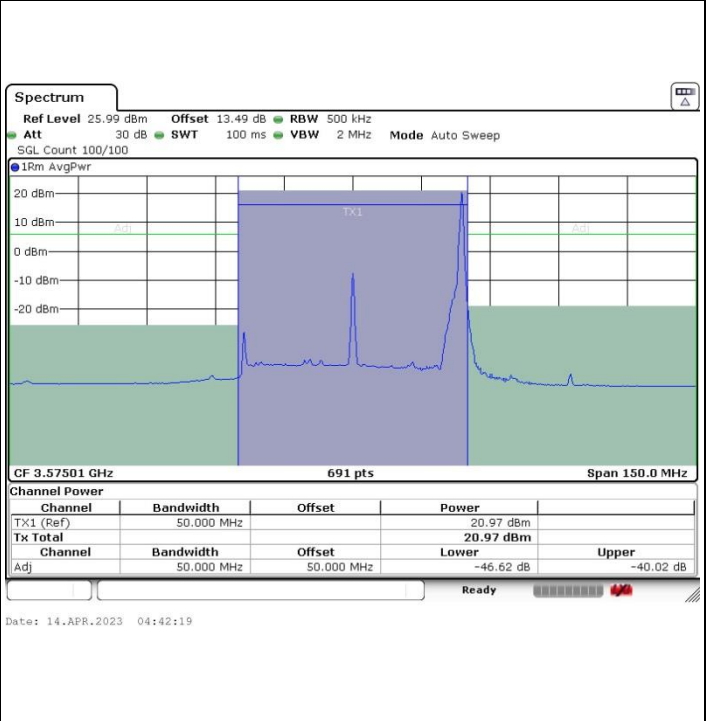
**FR1 n78 / 50MHz**

**256QAM**

**Lowest Channel / 1RB0**



**Lowest Channel / 1RBmax**



**Lowest Channel / FullRB**

