

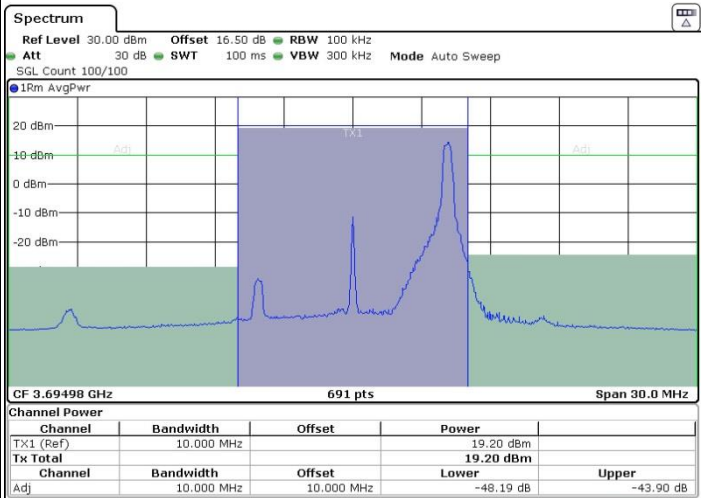
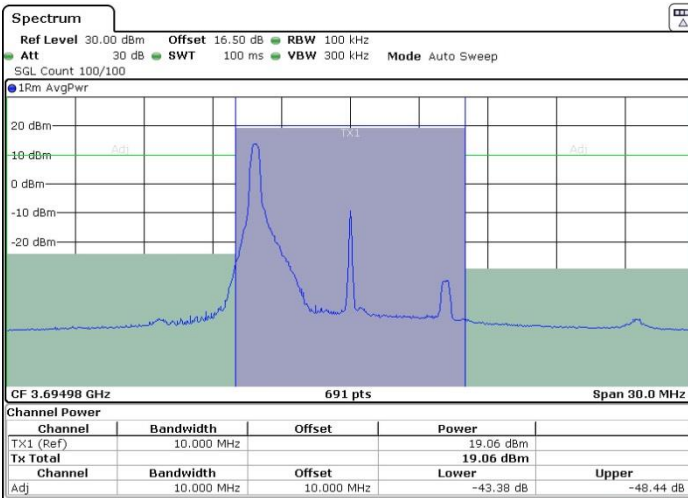


FR1 n48 / 10MHz

QPSK

Highest Channel / 1RB0

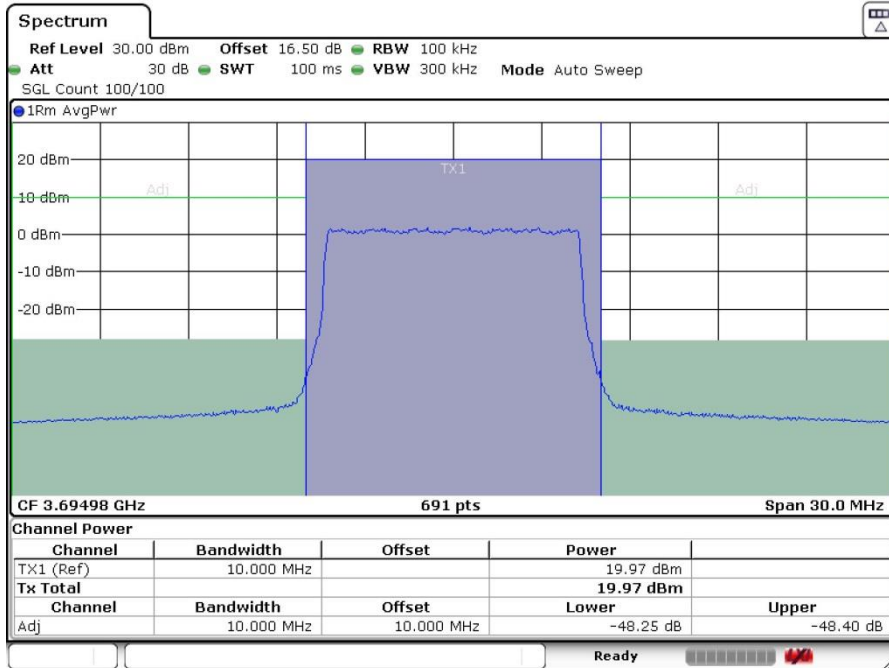
Highest Channel / 1RBmax



Date: 14.MAR.2023 04:32:23

Date: 14.MAR.2023 04:33:15

Highest Channel / FullIRB



Date: 14.MAR.2023 04:28:12

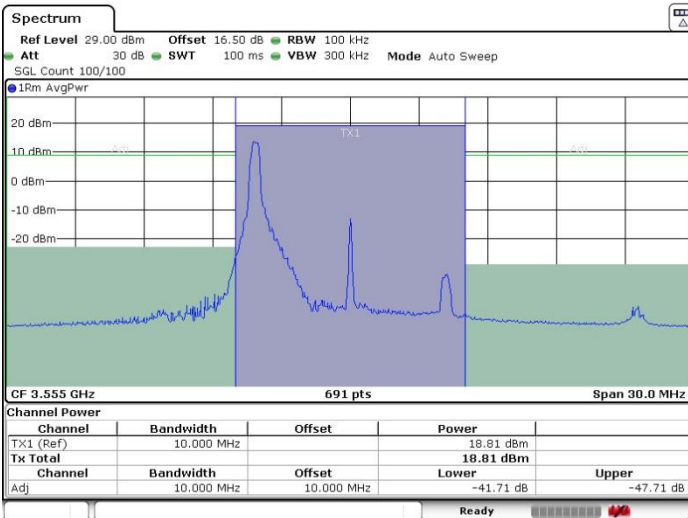


FR1 n48 / 10MHz

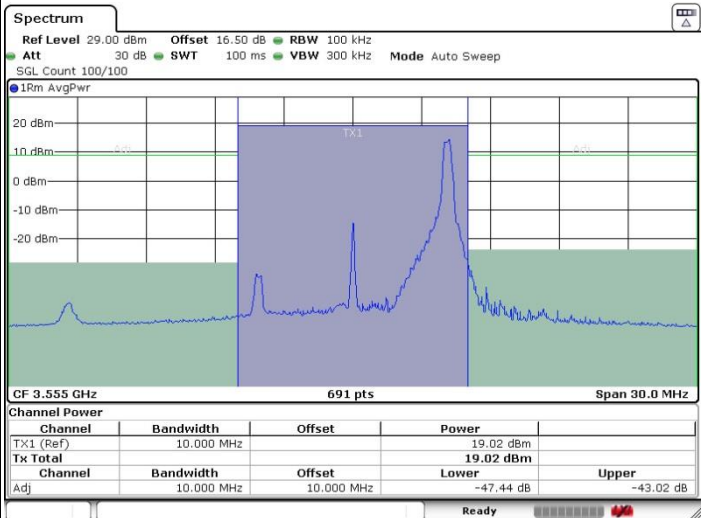
16QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

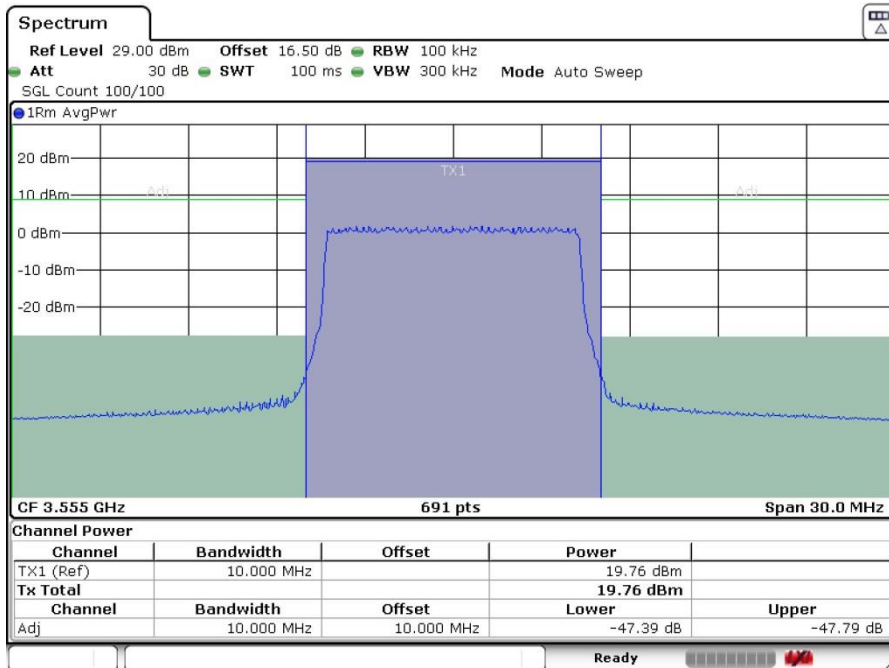


Date: 14.MAR.2023 04:16:06



Date: 14.MAR.2023 04:18:38

Lowest Channel / FullRB



Date: 14.MAR.2023 04:14:38

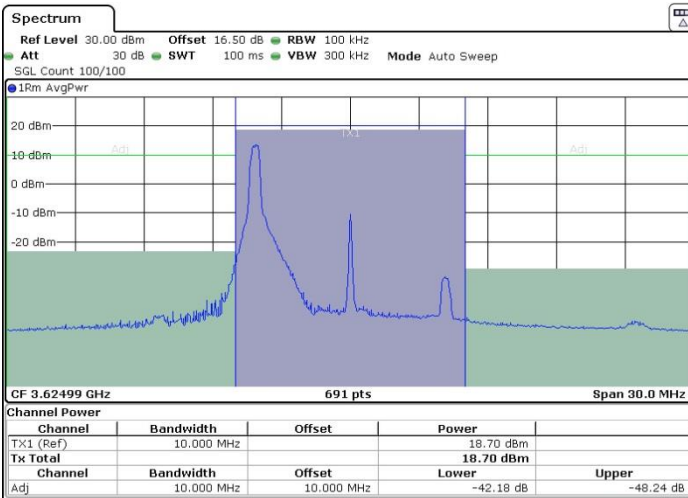


FR1 n48 / 10MHz

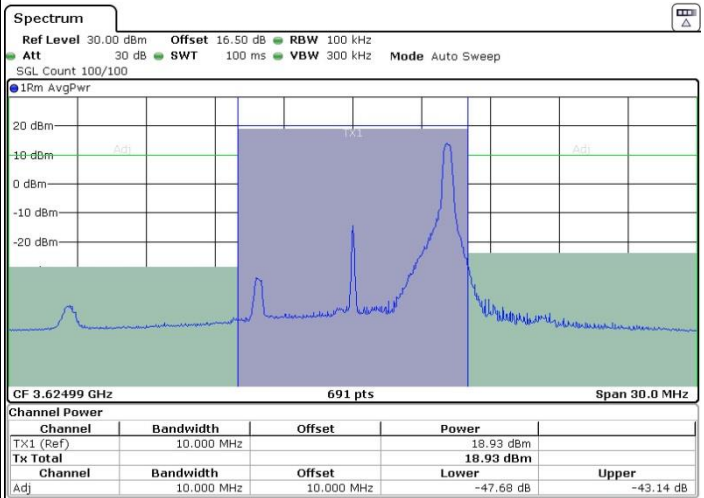
16QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

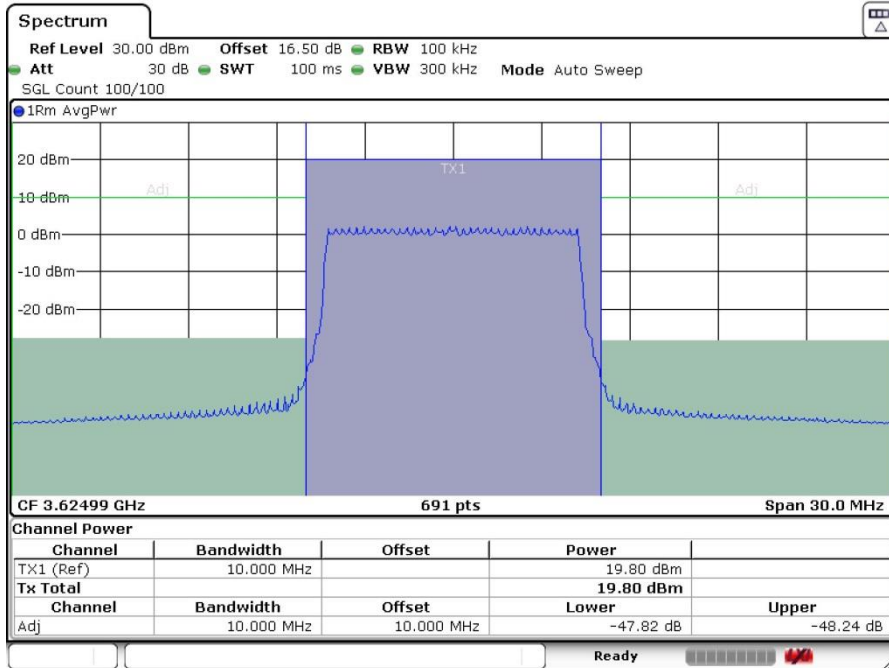


Date: 14.MAR.2023 04:24:28



Date: 14.MAR.2023 04:21:14

Middle Channel / FullIRB



Date: 14.MAR.2023 04:26:09

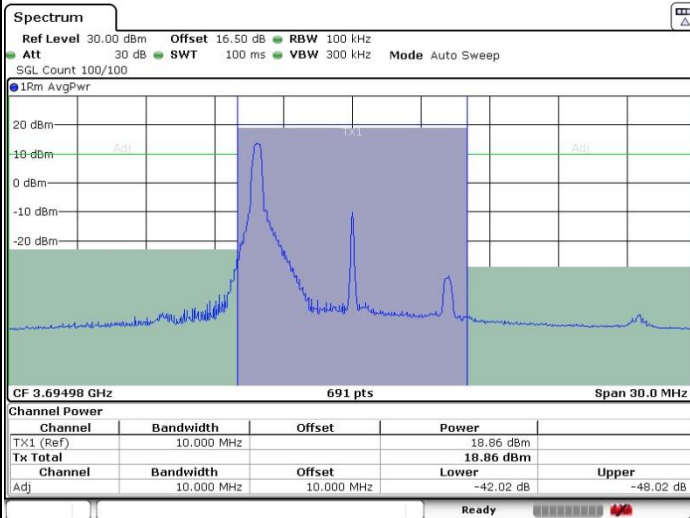


FR1 n48 / 10MHz

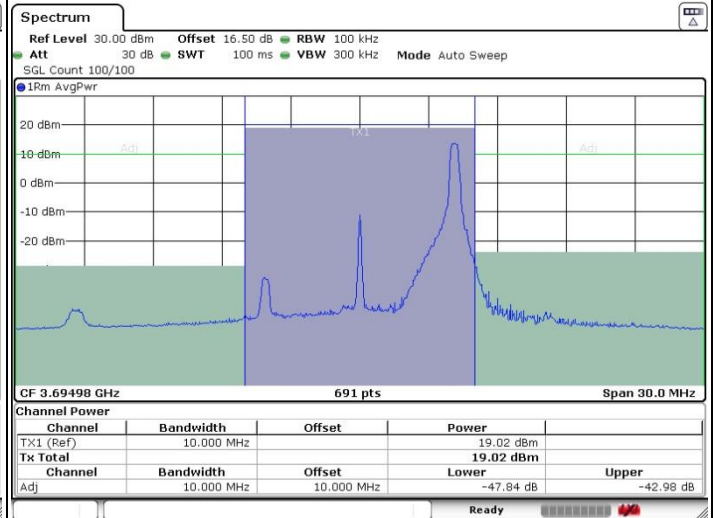
16QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

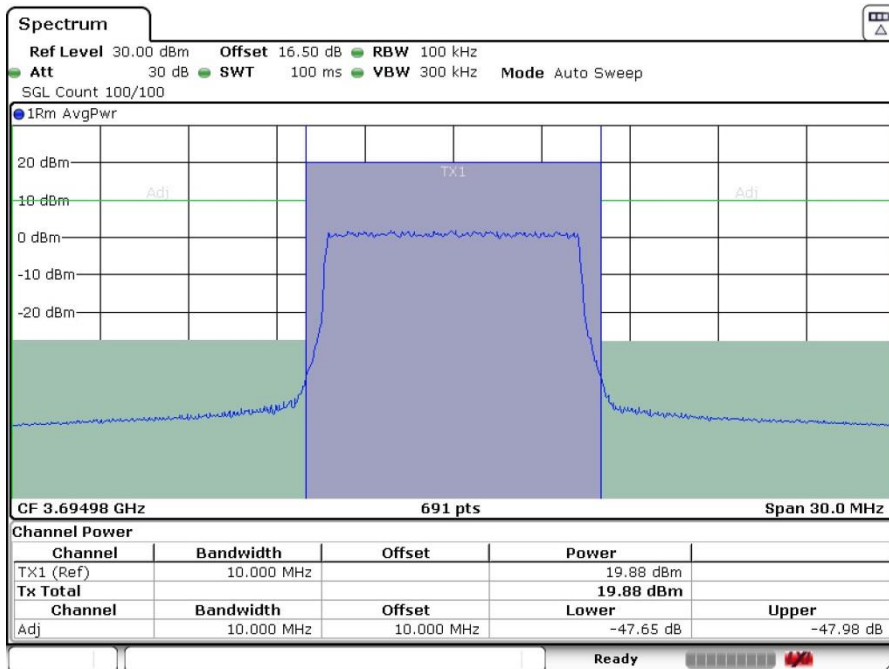


Date: 14.MAR.2023 04:31:35



Date: 14.MAR.2023 04:33:57

Highest Channel / FullRB



Date: 14.MAR.2023 04:28:47

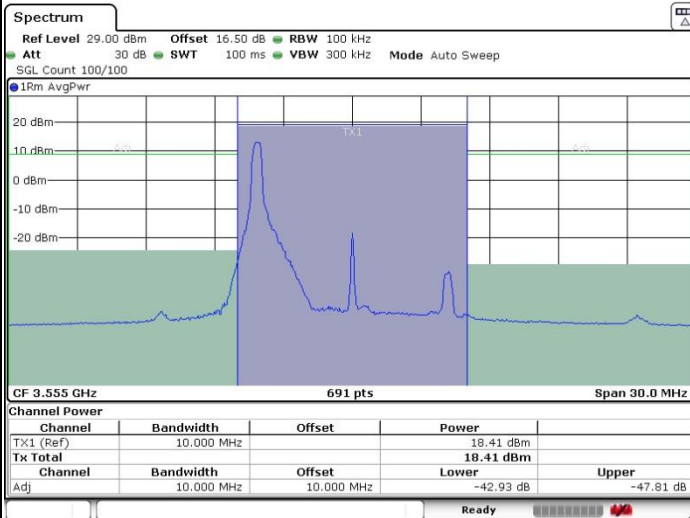


FR1 n48 / 10MHz

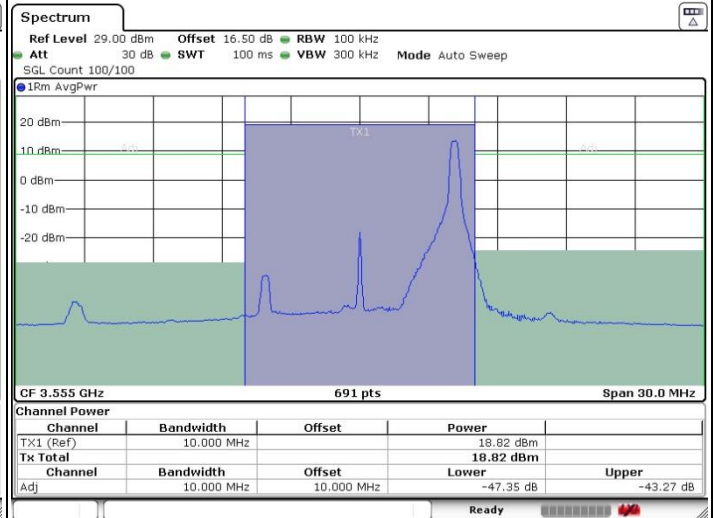
64QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

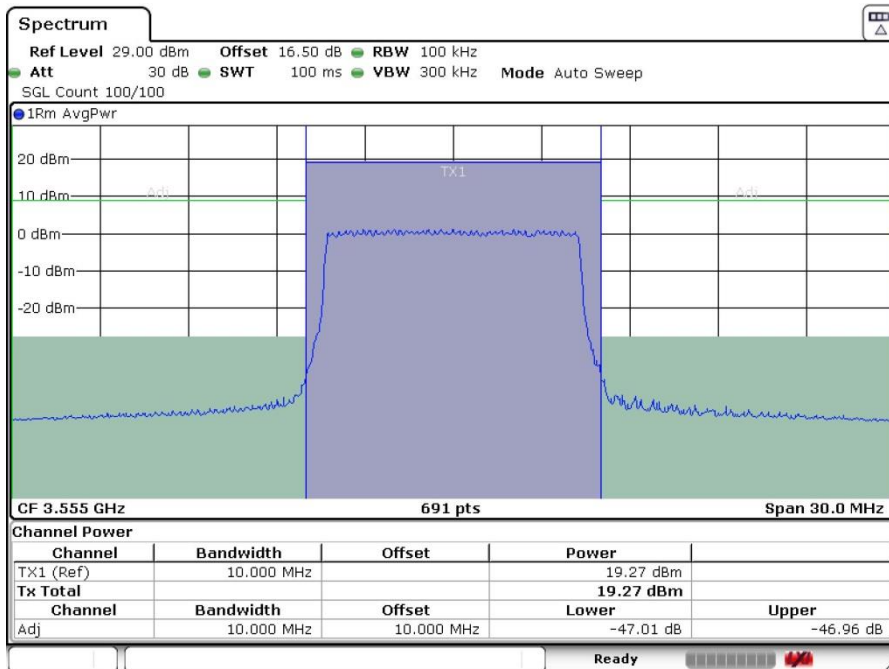


Date: 14.MAR.2023 04:16:32



Date: 14.MAR.2023 04:18:10

Lowest Channel / FullRB



Date: 14.MAR.2023 04:14:09

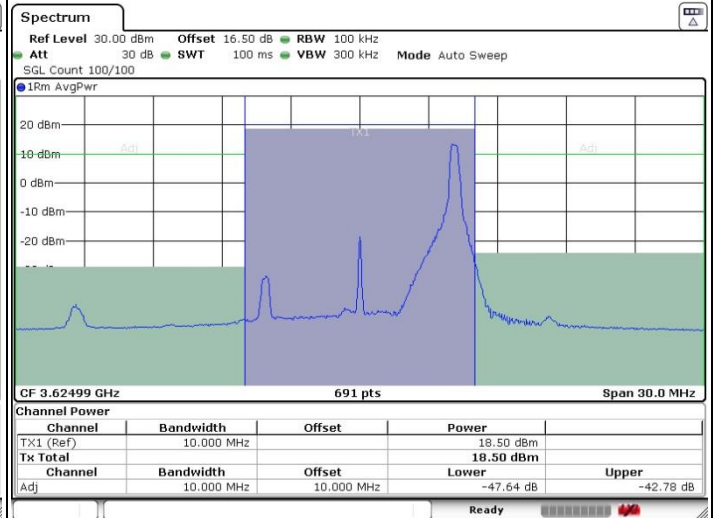
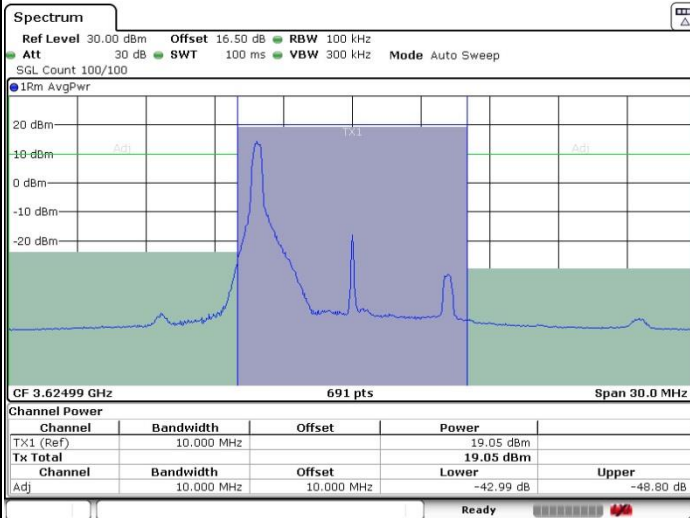


FR1 n48 / 10MHz

64QAM

Middle Channel / 1RB0

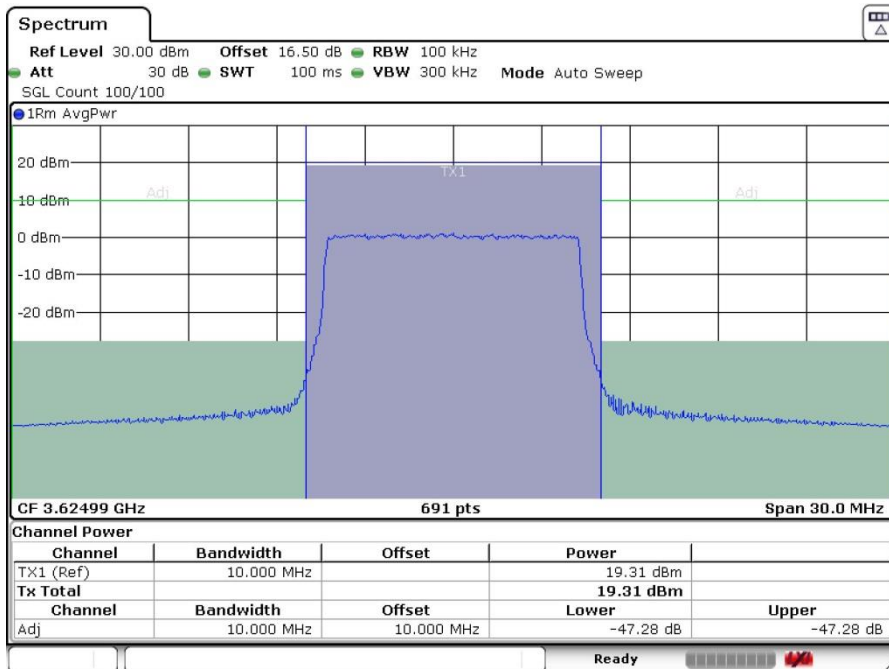
Middle Channel / 1RBmax



Date: 14.MAR.2023 04:23:58

Date: 14.MAR.2023 04:21:58

Middle Channel / FullRB



Date: 14.MAR.2023 04:26:37

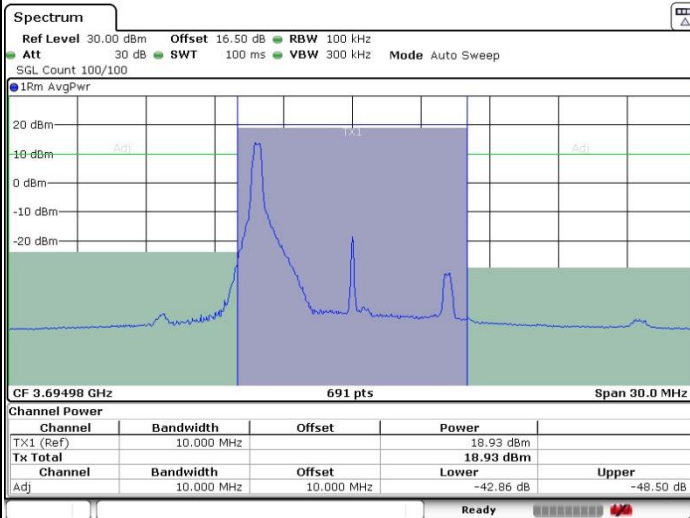


FR1 n48 / 10MHz

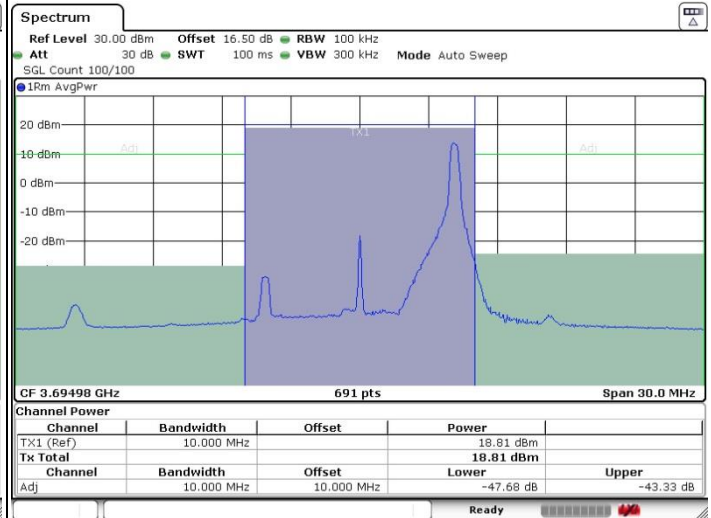
64QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

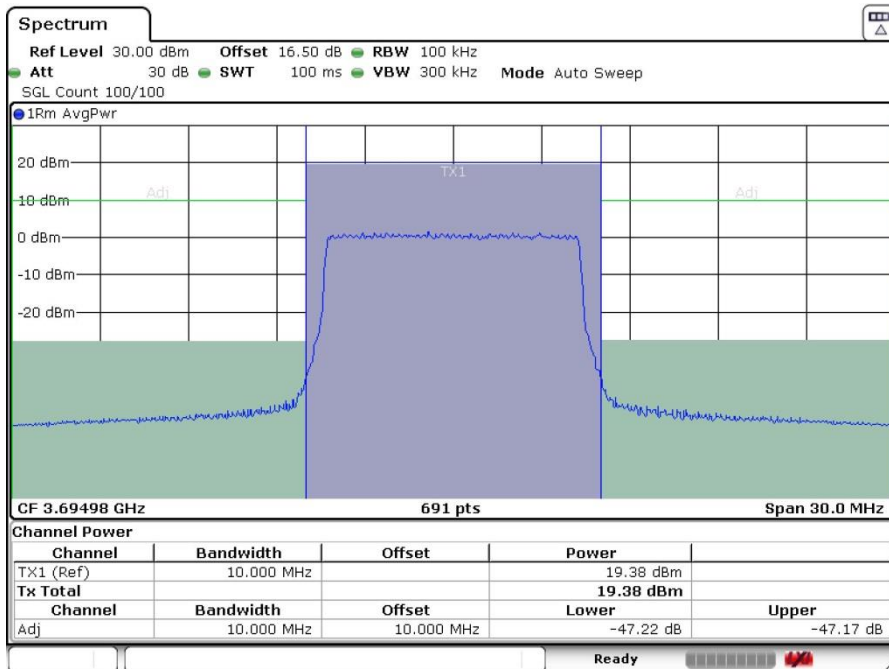


Date: 14.MAR.2023 04:31:00



Date: 14.MAR.2023 04:34:30

Highest Channel / FullRB



Date: 14.MAR.2023 04:29:21

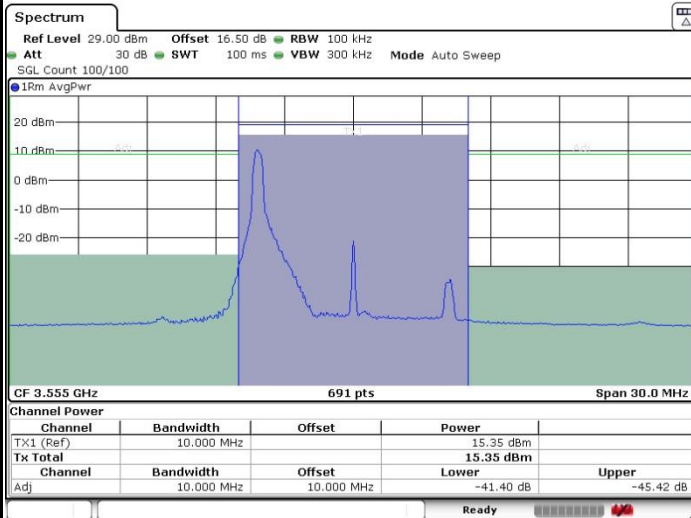


FR1 n48 / 10MHz

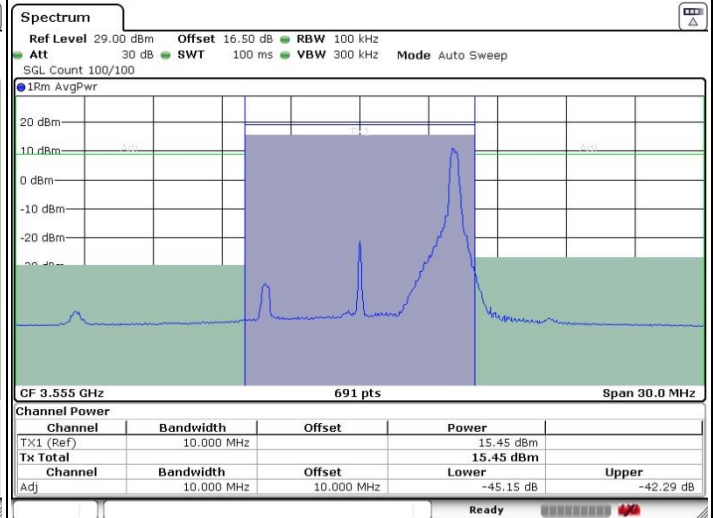
256QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

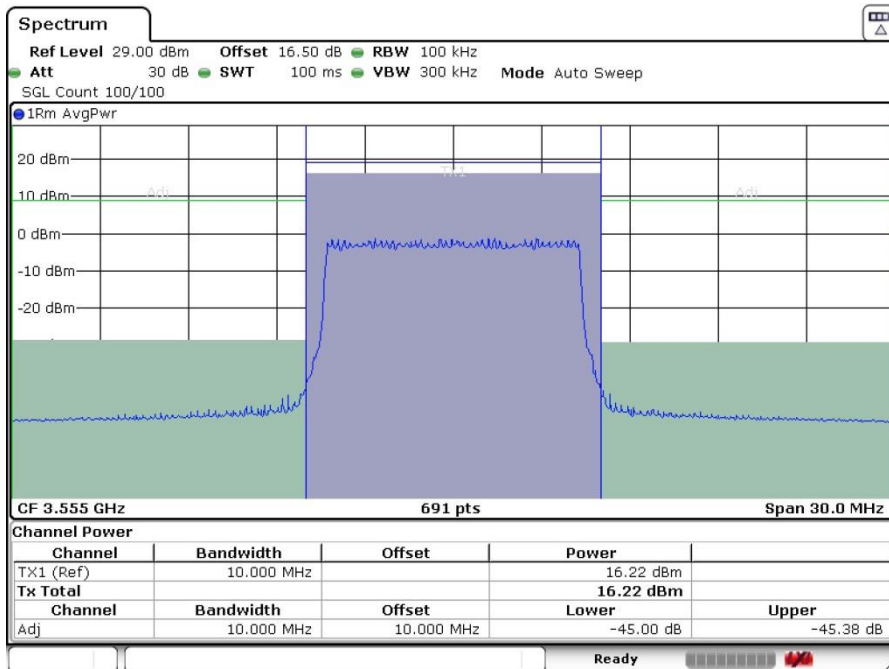


Date: 14.MAR.2023 04:17:01



Date: 14.MAR.2023 04:17:29

Lowest Channel / FullRB



Date: 14.MAR.2023 04:13:06



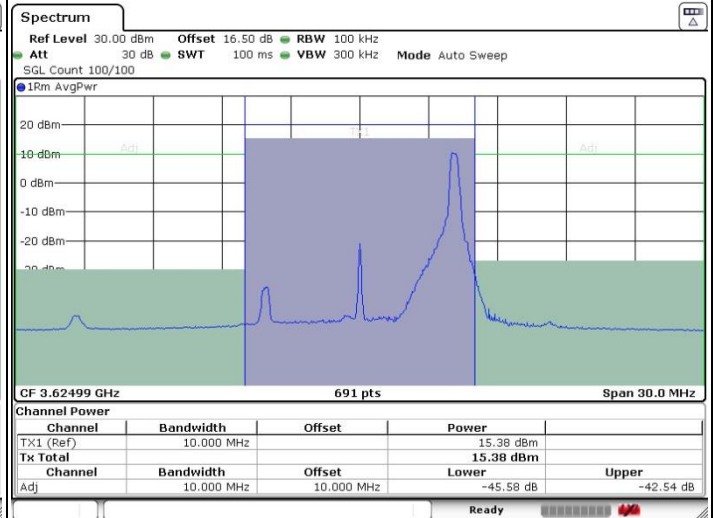
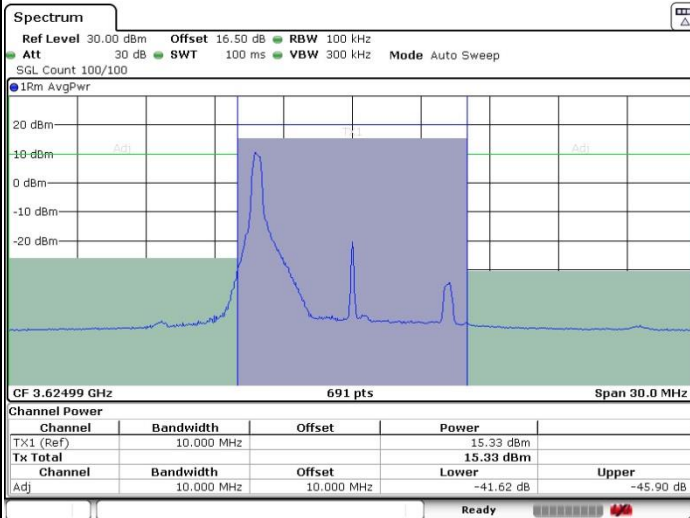


FR1 n48 / 10MHz

256QAM

Middle Channel / 1RB0

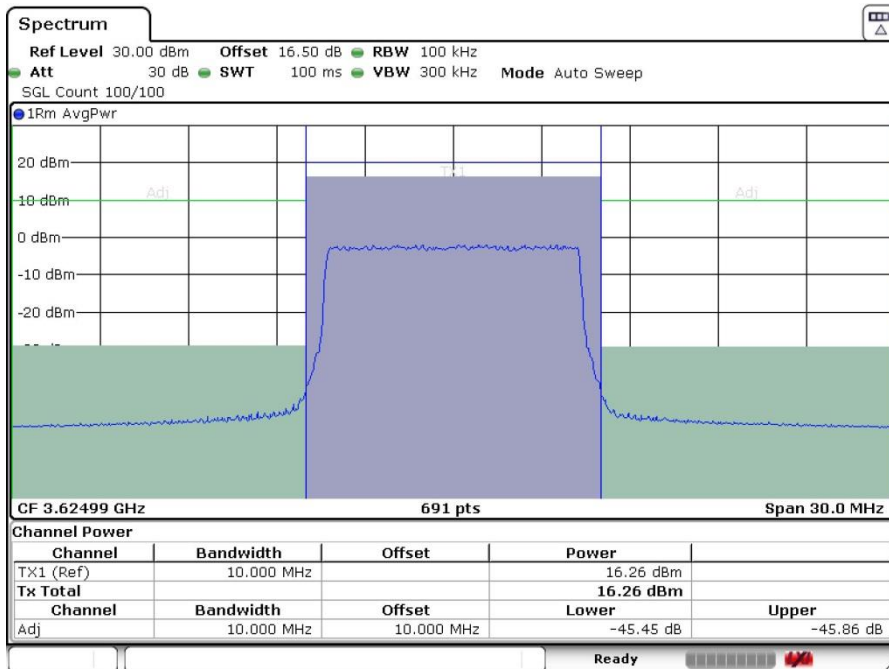
Middle Channel / 1RBmax



Date: 14.MAR.2023 04:23:26

Date: 14.MAR.2023 04:22:31

Middle Channel / FullRB



Date: 14.MAR.2023 04:27:04

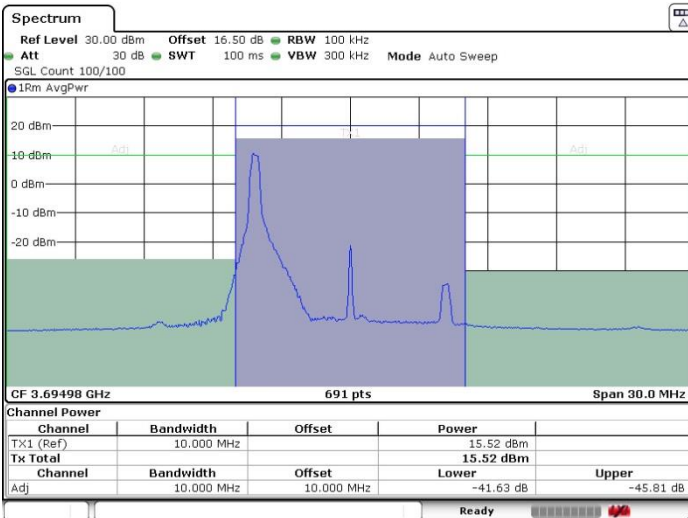


FR1 n48 / 10MHz

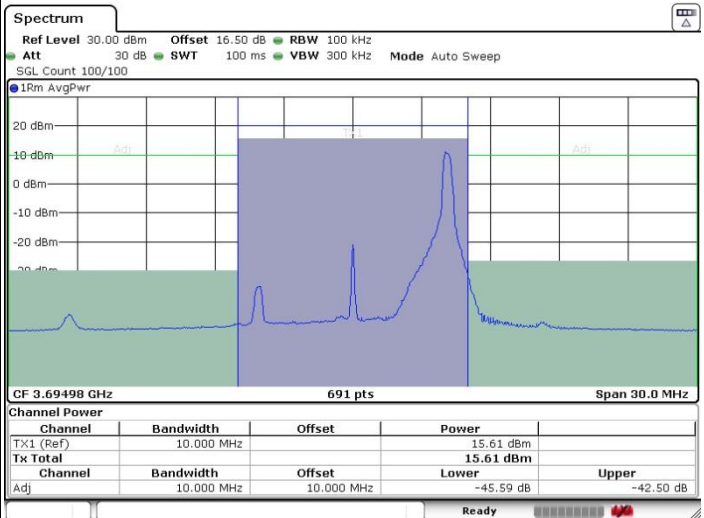
256QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

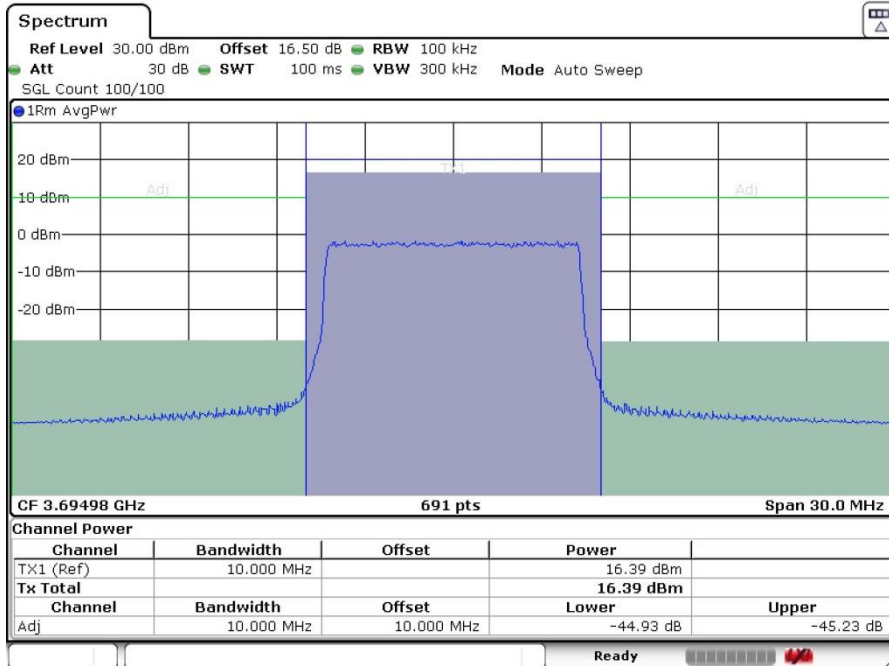


Date: 14.MAR.2023 04:30:22



Date: 14.MAR.2023 04:34:58

Highest Channel / FullRB



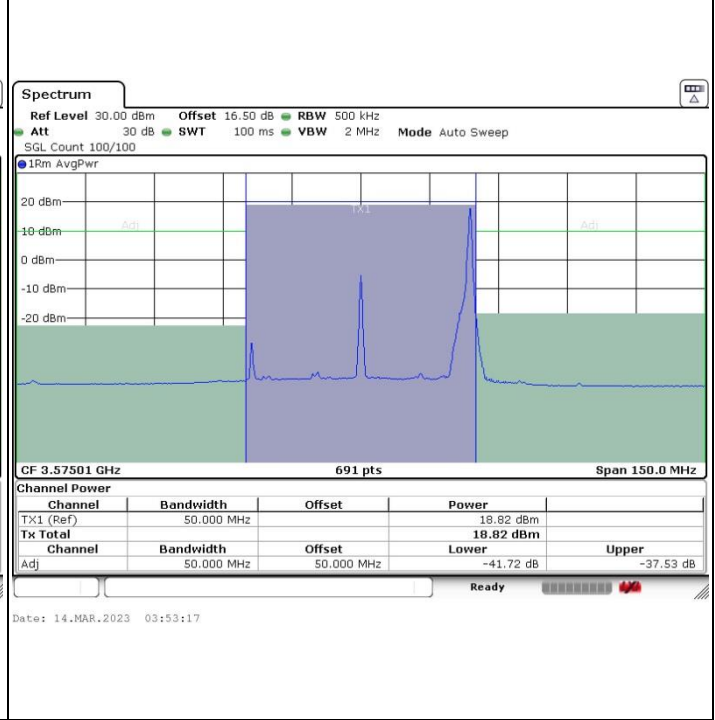
Date: 14.MAR.2023 04:29:49



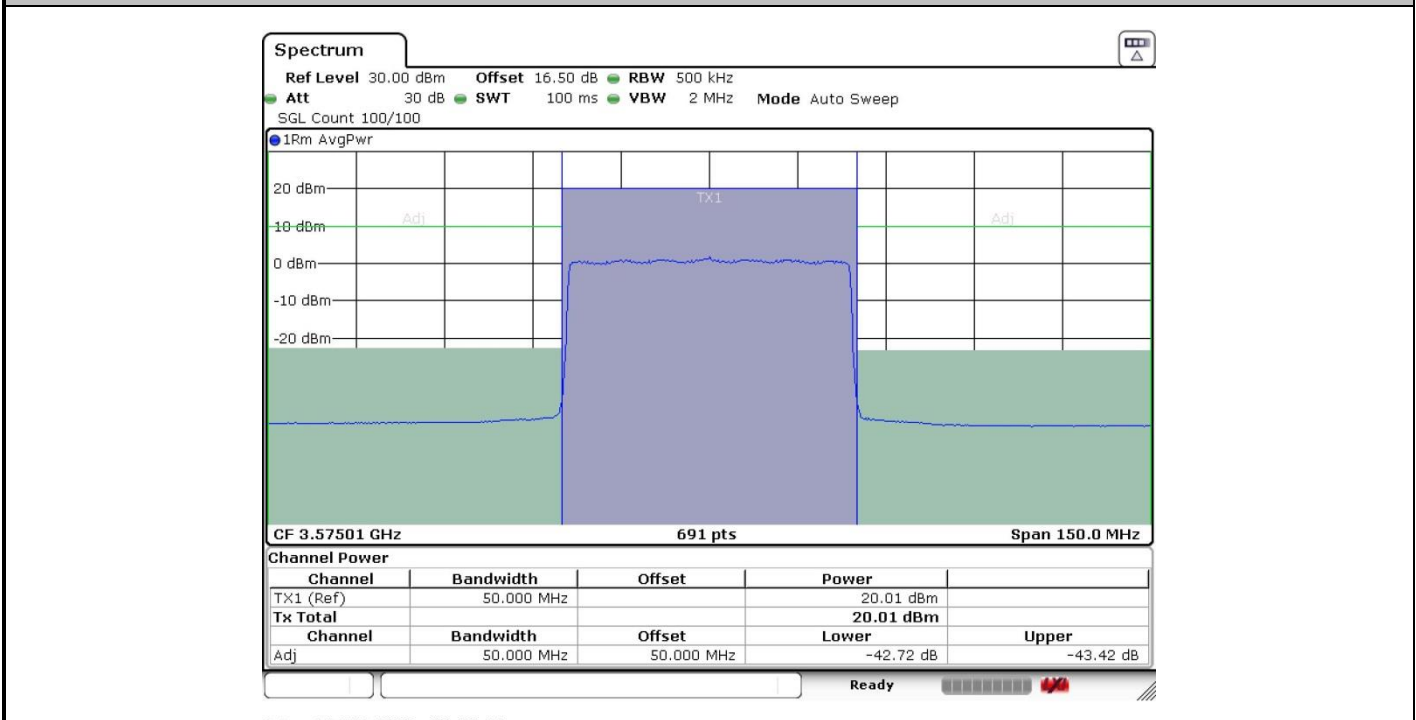
FR1 n48 / 50MHz

QPSK

Lowest Channel / 1RB0      Lowest Channel / 1RBmax



Lowest Channel / FullRB



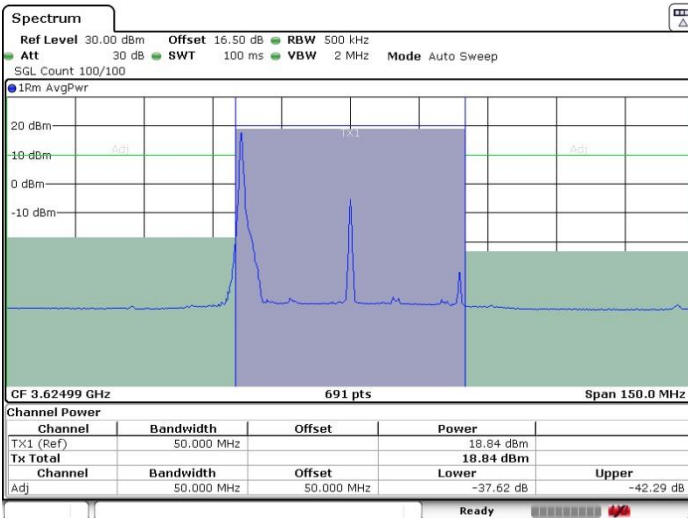


FR1 n48 / 50MHz

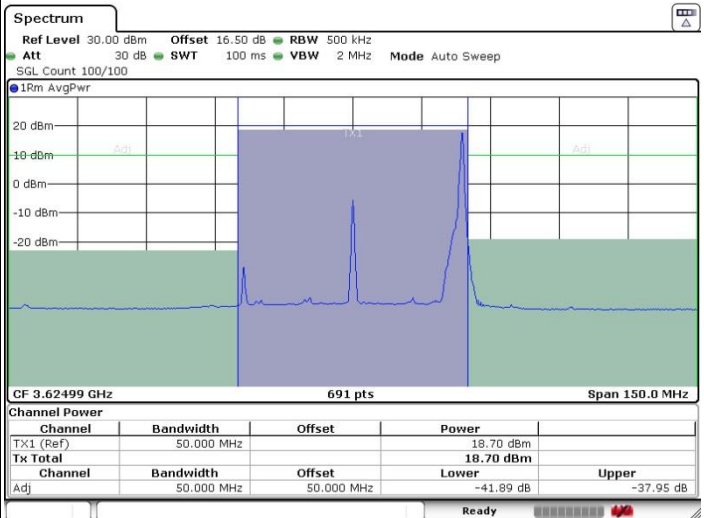
QPSK

Middle Channel / 1RB0

Middle Channel / 1RBmax

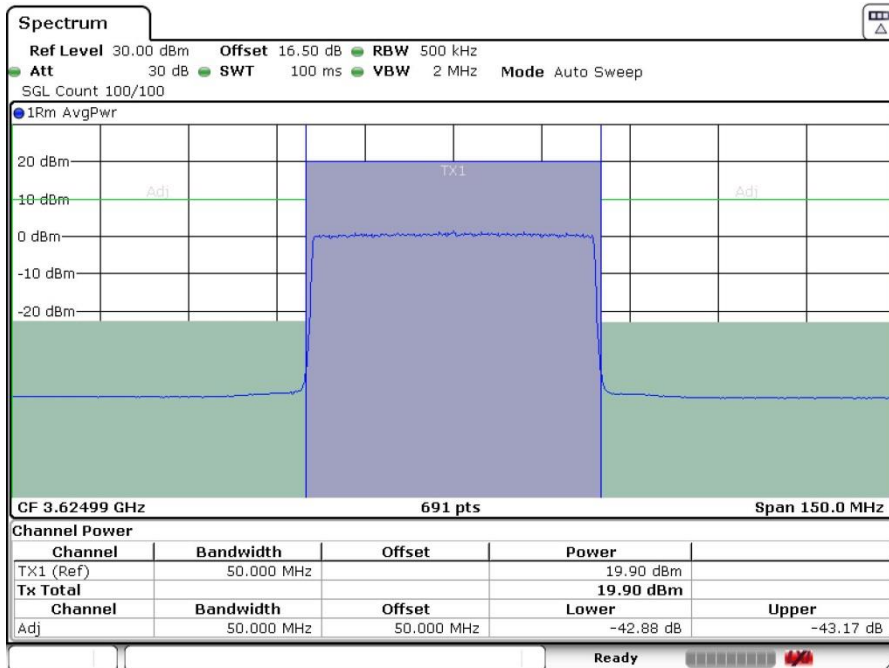


Date: 14.MAR.2023 03:58:34



Date: 14.MAR.2023 04:02:51

Middle Channel / FullIRB



Date: 14.MAR.2023 03:58:05

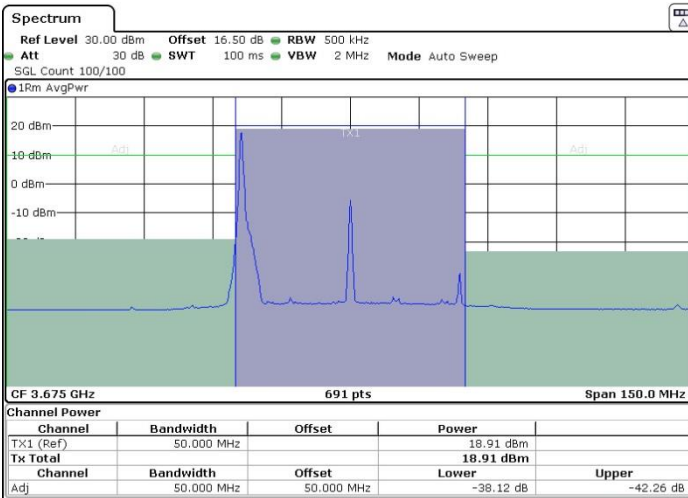


FR1 n48 / 50MHz

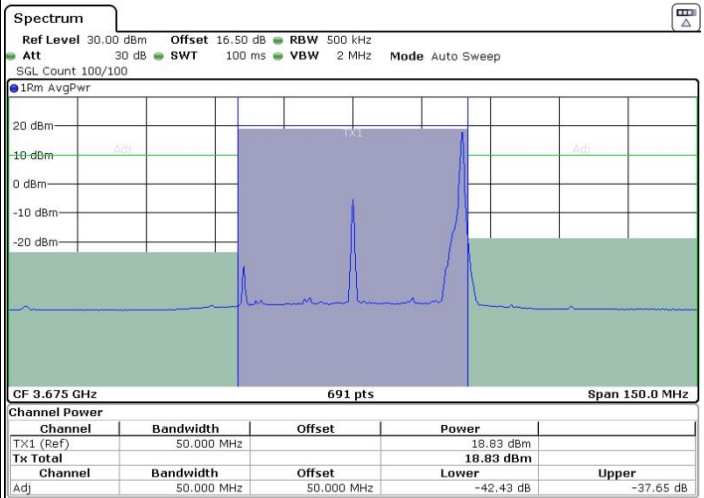
QPSK

Highest Channel / 1RB0

Highest Channel / 1RBmax

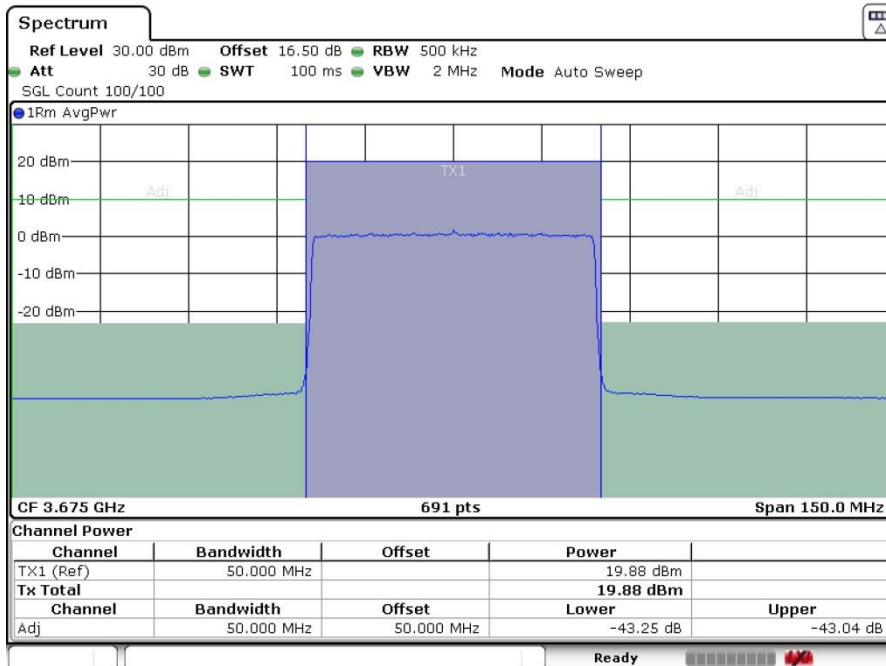


Date: 14.MAR.2023 04:03:40



Date: 14.MAR.2023 04:08:50

Highest Channel / FullRB



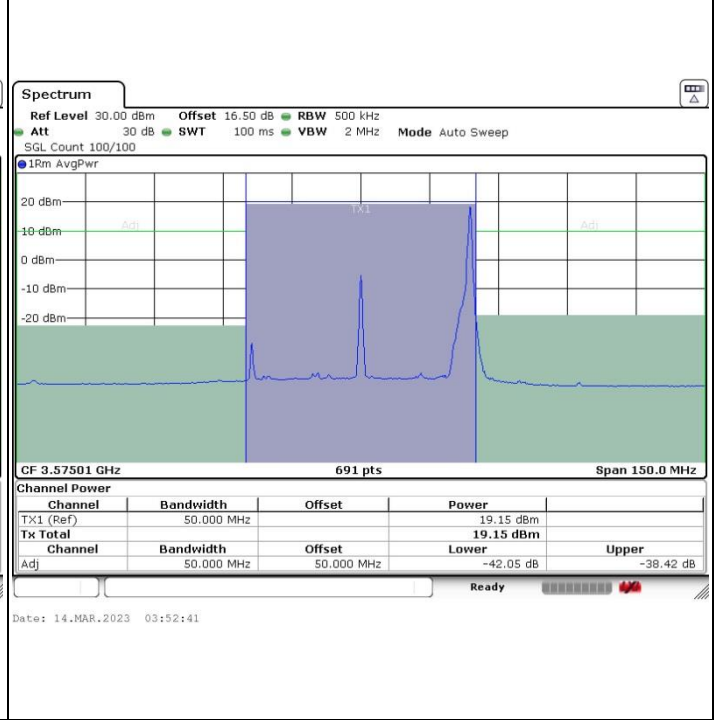
Date: 14.MAR.2023 04:09:20



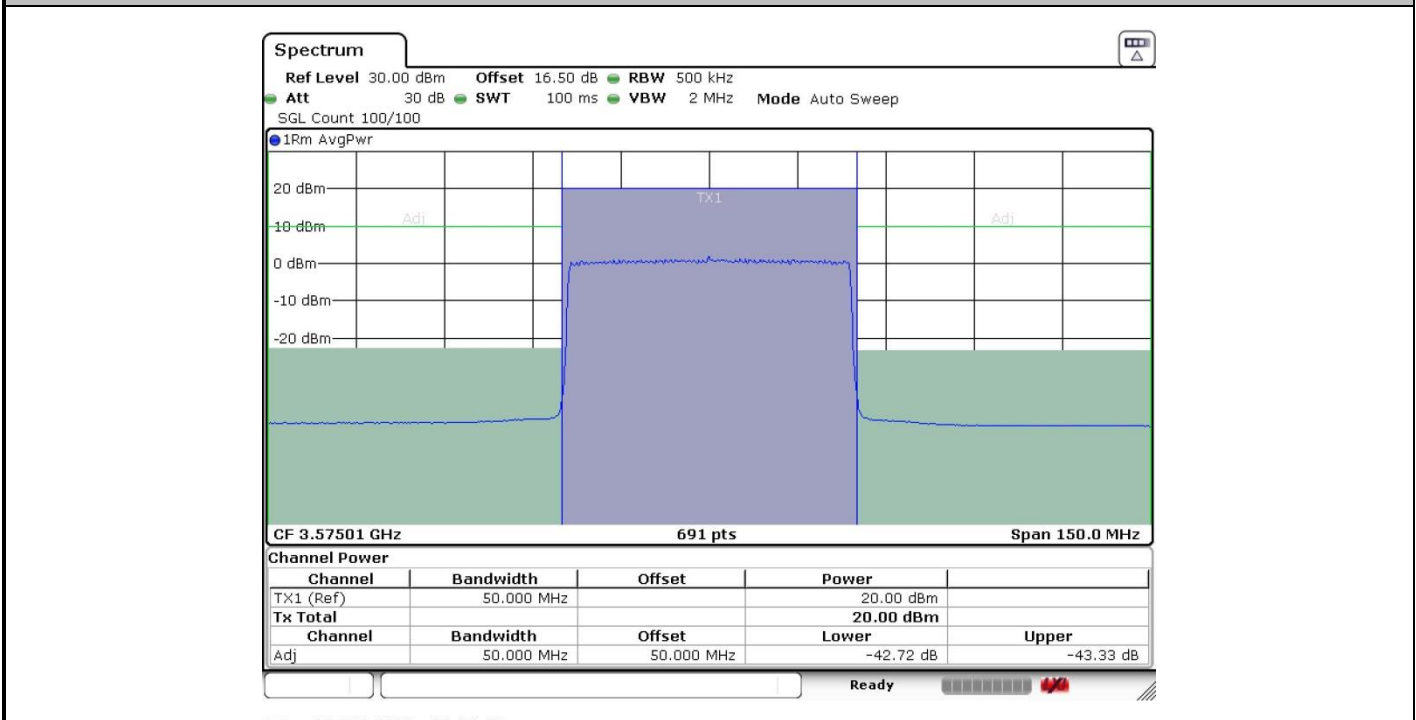
FR1 n48 / 50MHz

16QAM

Lowest Channel / 1RB0      Lowest Channel / 1RBmax



Lowest Channel / FullIRB



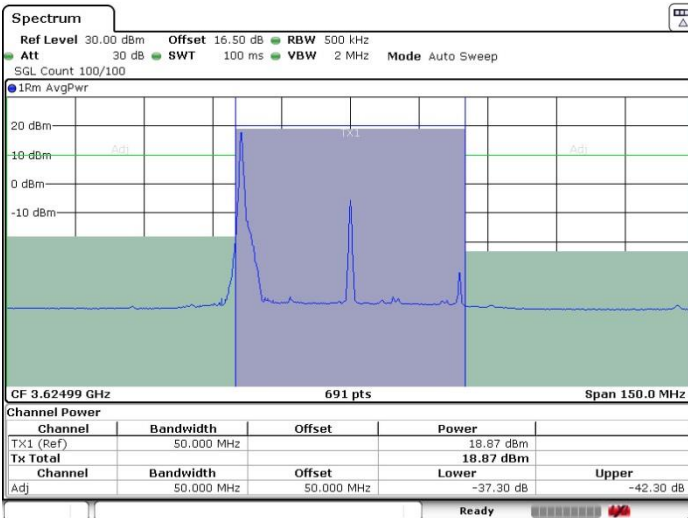


FR1 n48 / 50MHz

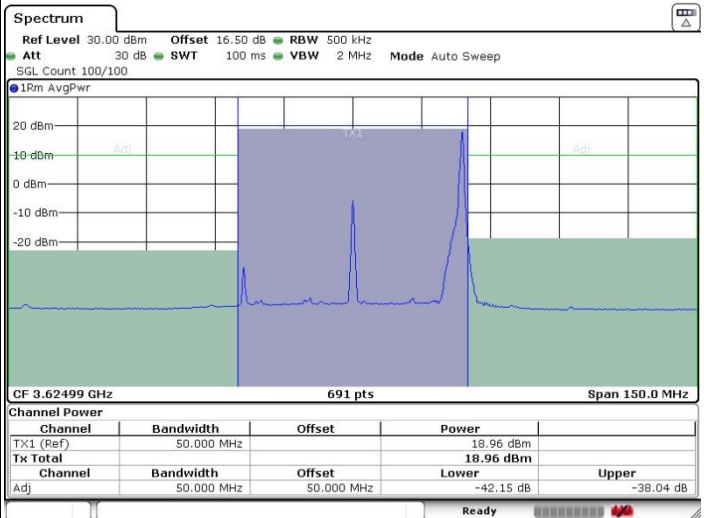
16QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

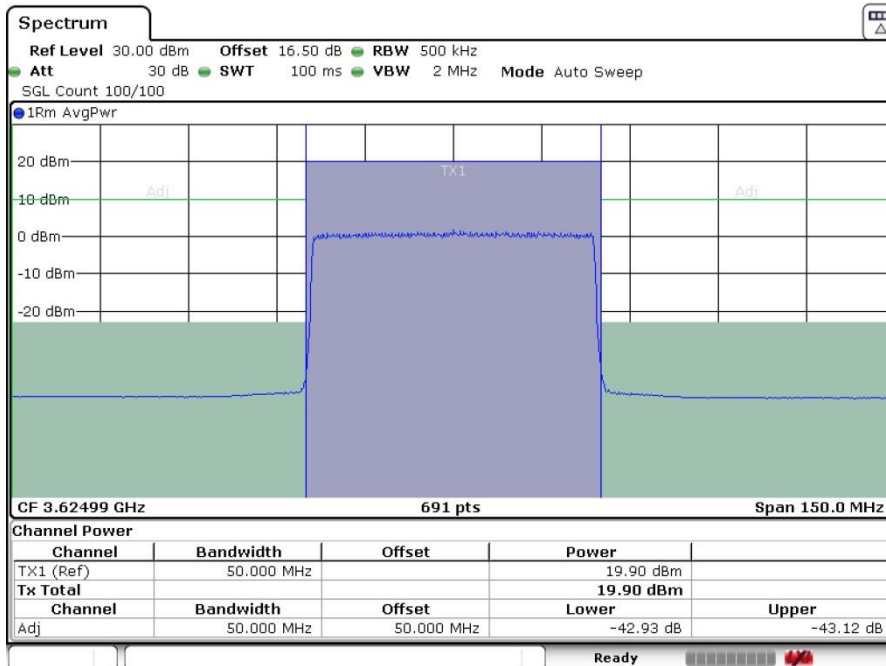


Date: 14.MAR.2023 03:59:03



Date: 14.MAR.2023 04:02:20

Middle Channel / FullIRB



Date: 14.MAR.2023 03:57:35

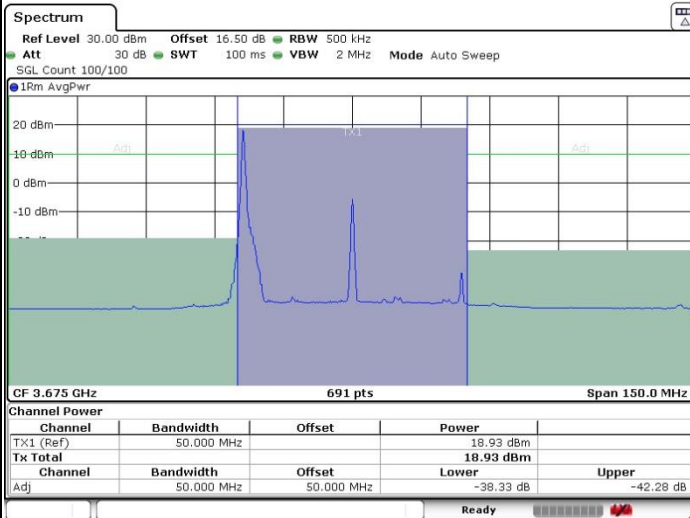


FR1 n48 / 50MHz

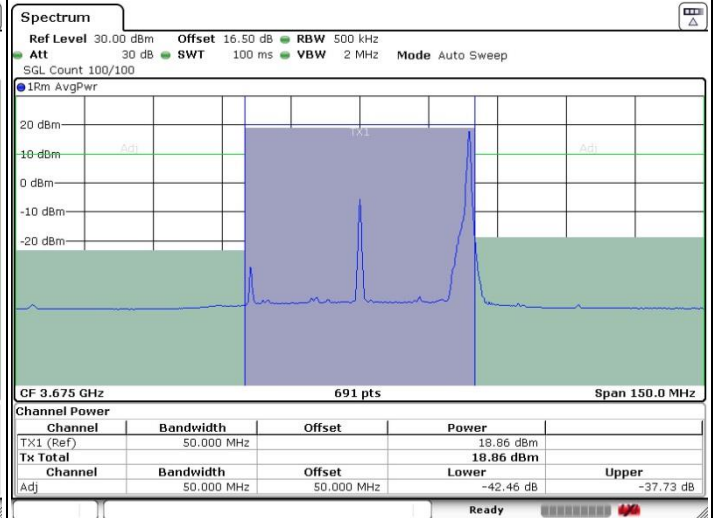
16QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

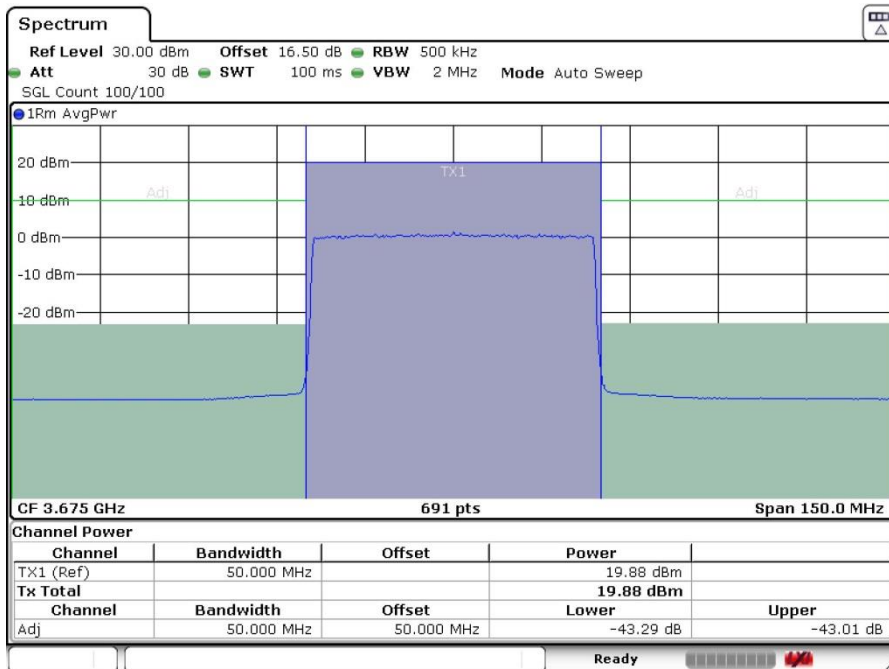


Date: 14.MAR.2023 04:04:12



Date: 14.MAR.2023 04:08:23

Highest Channel / FullRB



Date: 14.MAR.2023 04:09:56



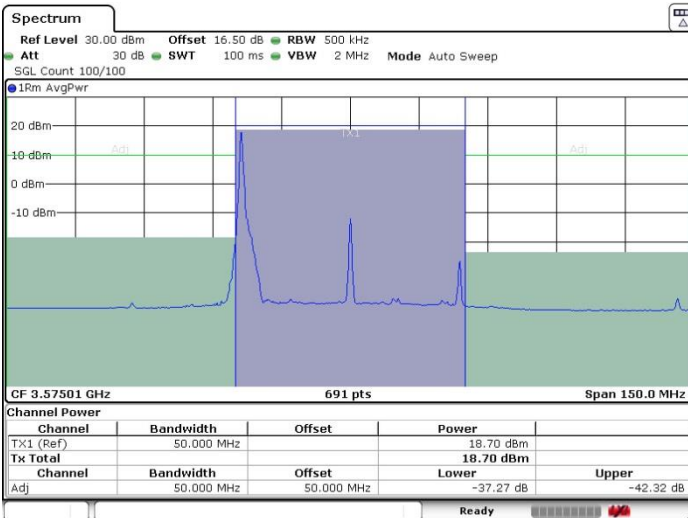


FR1 n48 / 50MHz

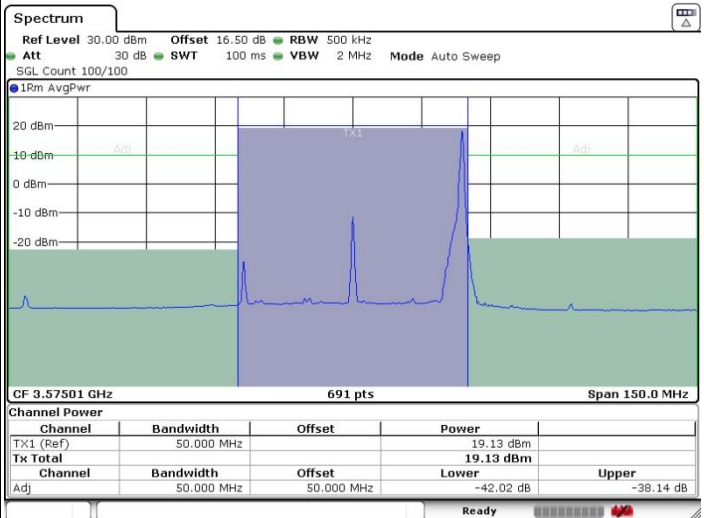
64QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

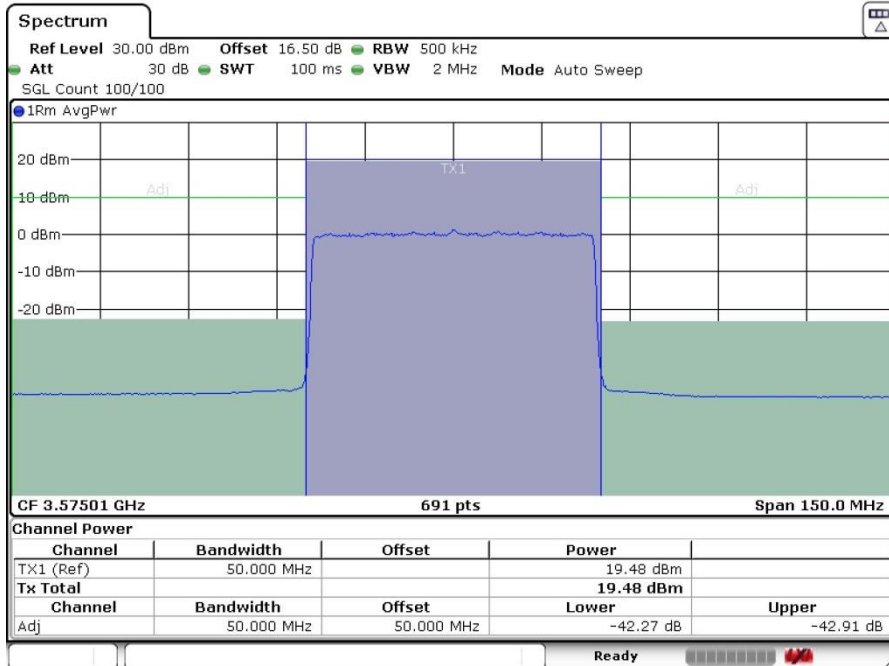


Date: 14.MAR.2023 03:41:40



Date: 14.MAR.2023 03:52:12

Lowest Channel / FullRB



Date: 14.MAR.2023 03:55:03

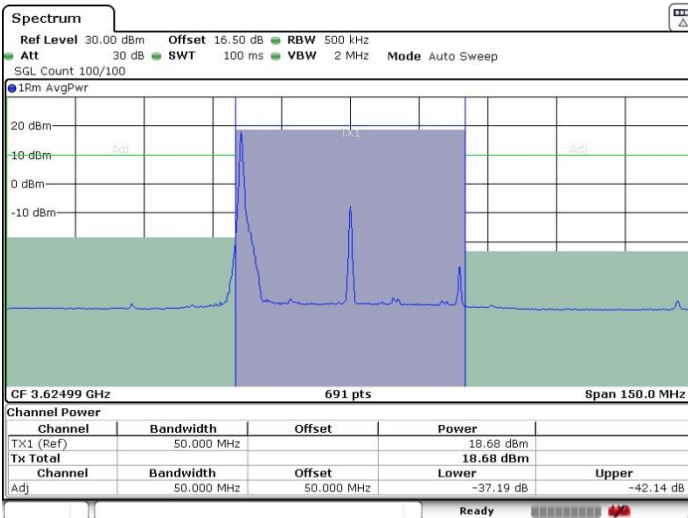


FR1 n48 / 50MHz

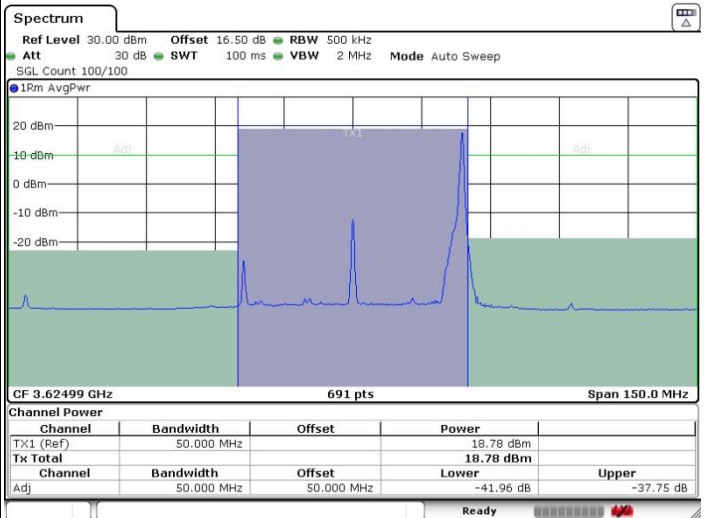
64QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

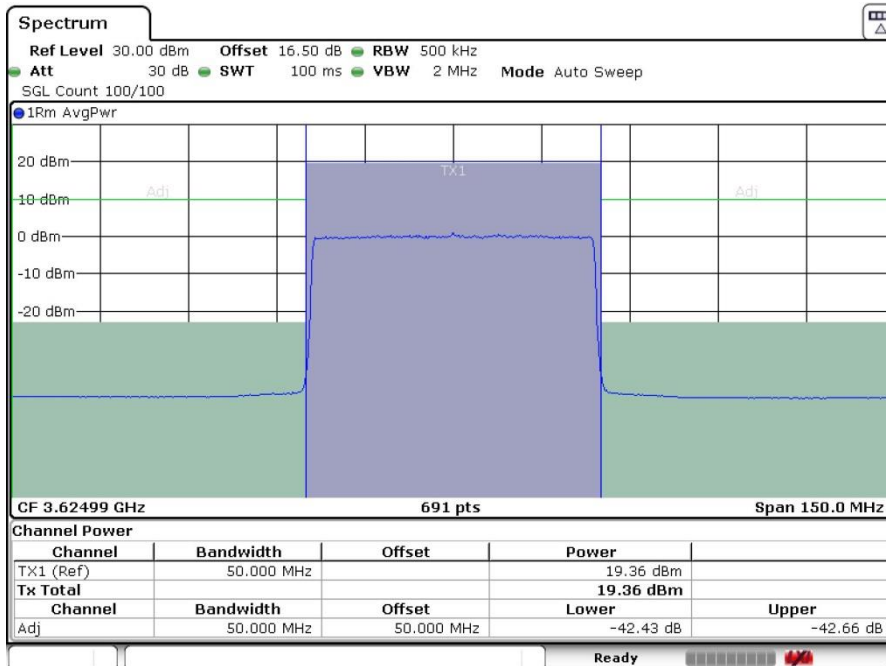


Date: 14.MAR.2023 03:59:36



Date: 14.MAR.2023 04:01:26

Middle Channel / FullIRB



Date: 14.MAR.2023 03:57:06

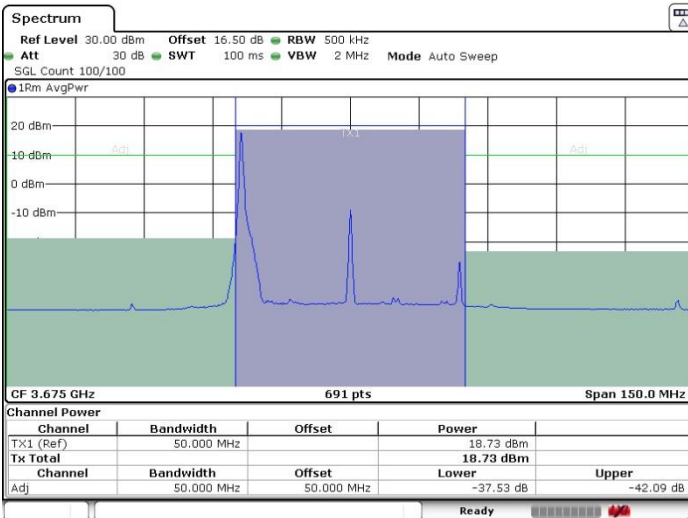


FR1 n48 / 50MHz

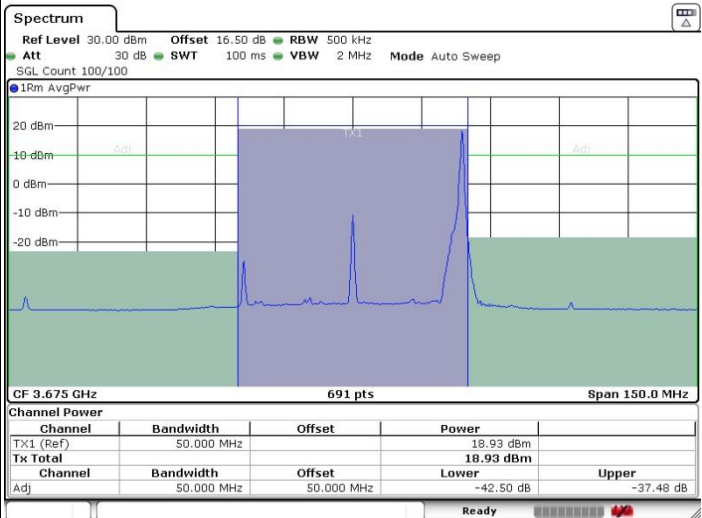
64QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

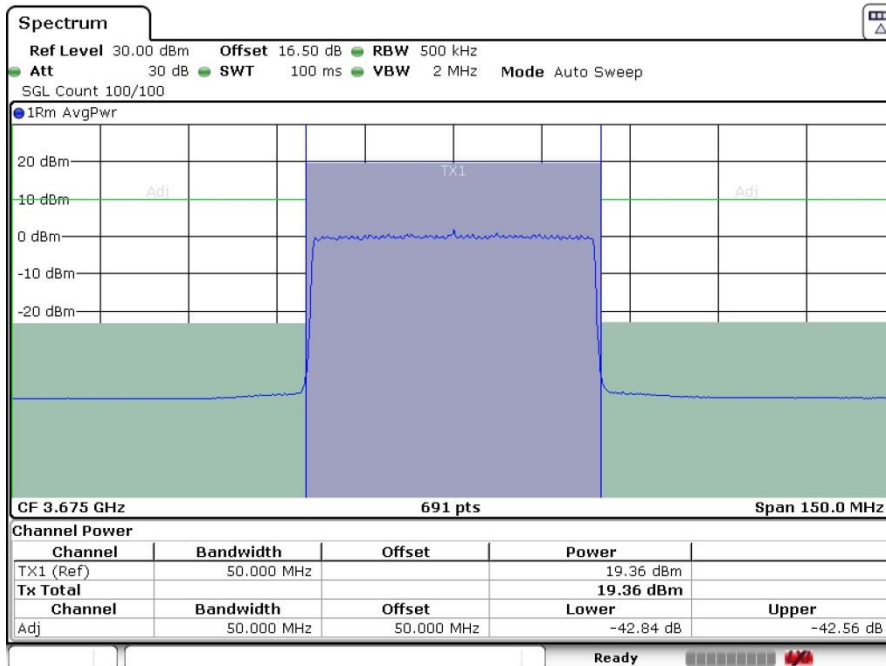


Date: 14.MAR.2023 04:06:22



Date: 14.MAR.2023 04:07:55

Highest Channel / FullRB



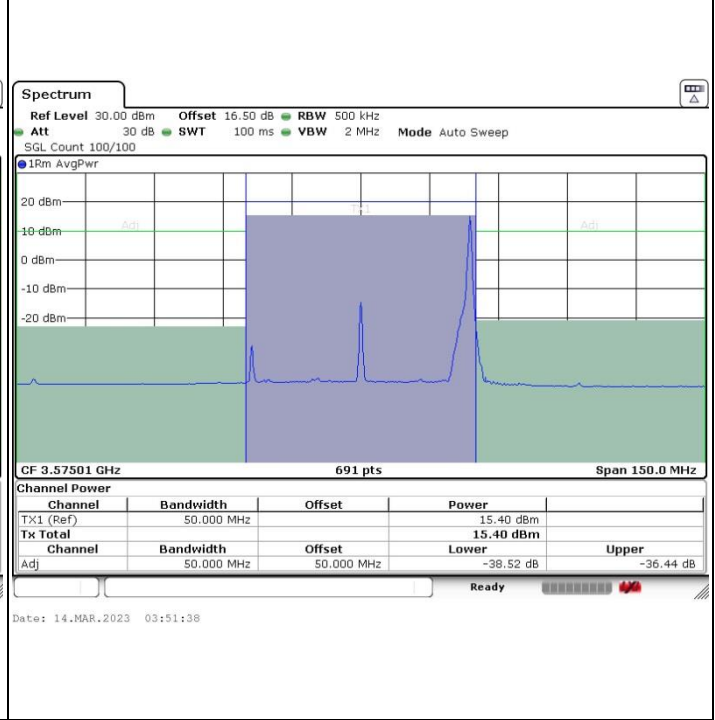
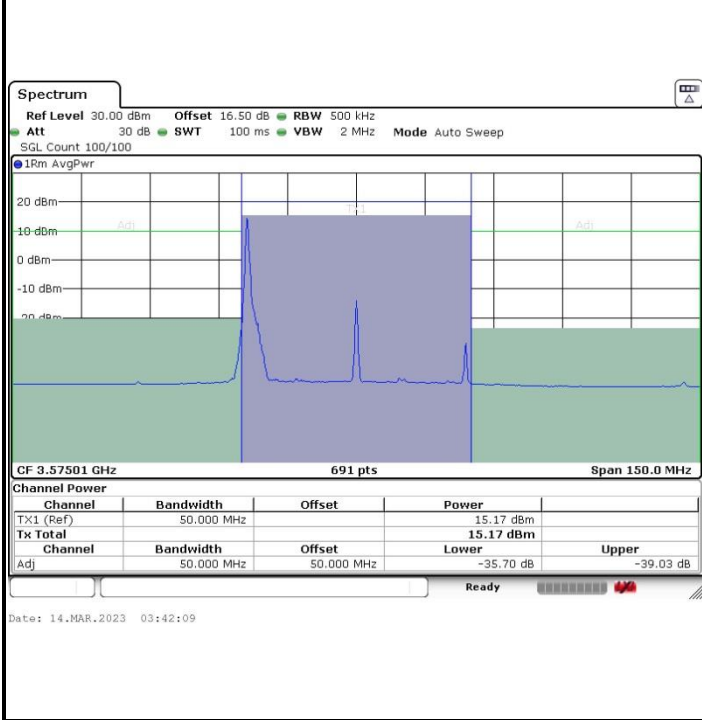
Date: 14.MAR.2023 04:10:25



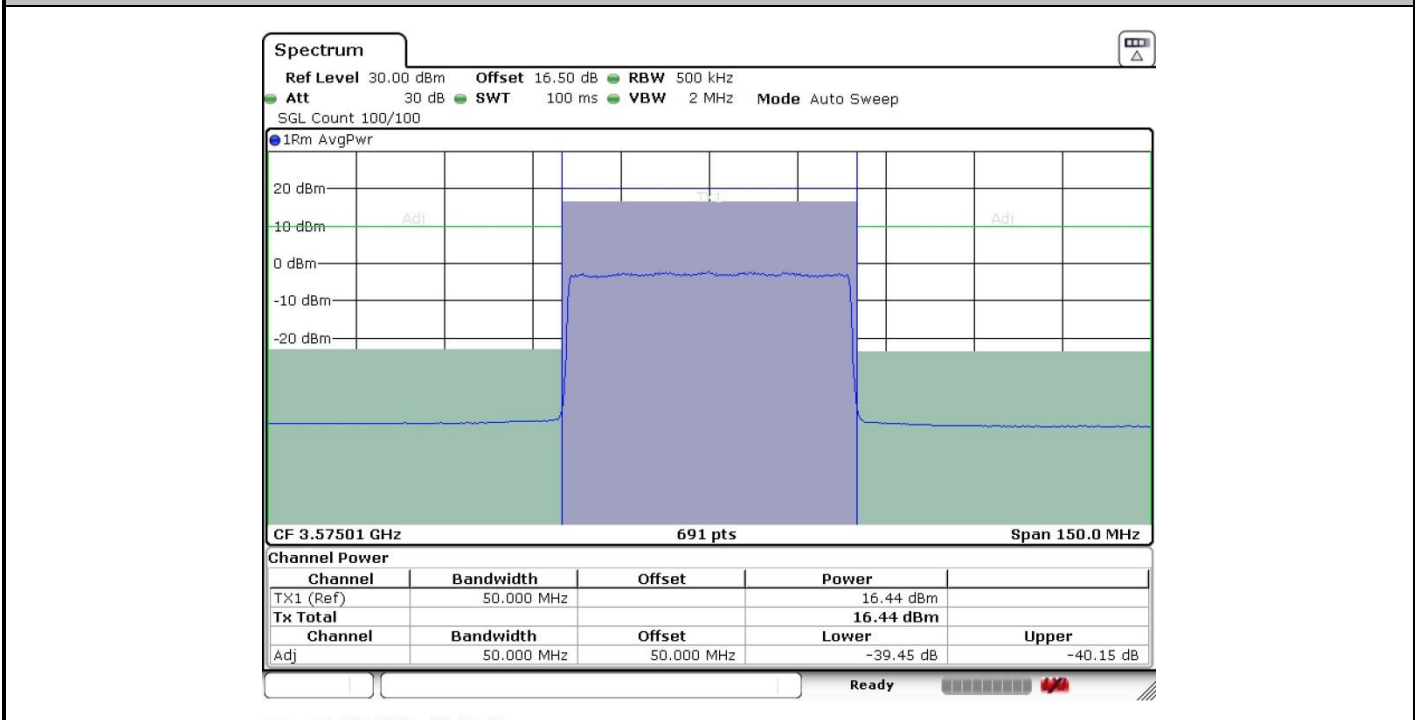
FR1 n48 / 50MHz

256QAM

Lowest Channel / 1RB0      Lowest Channel / 1RBmax



Lowest Channel / FullIRB

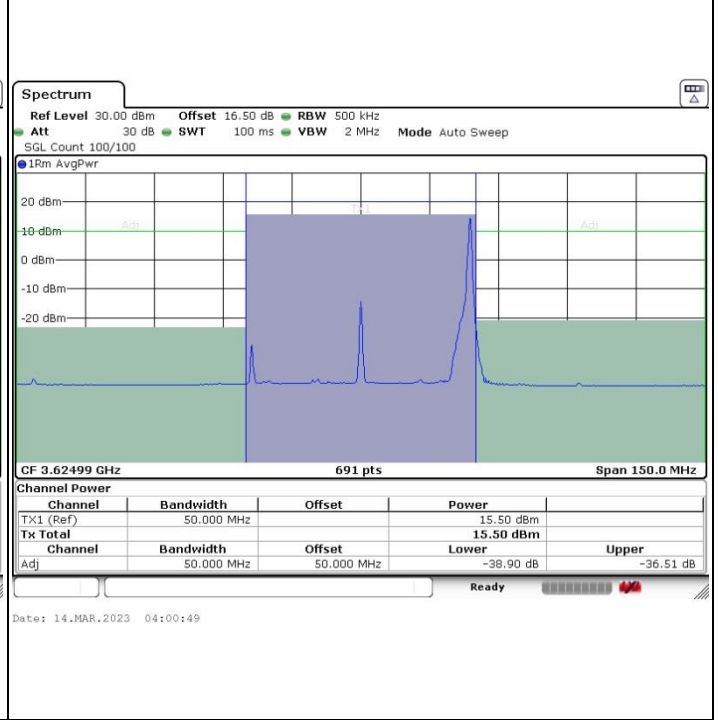
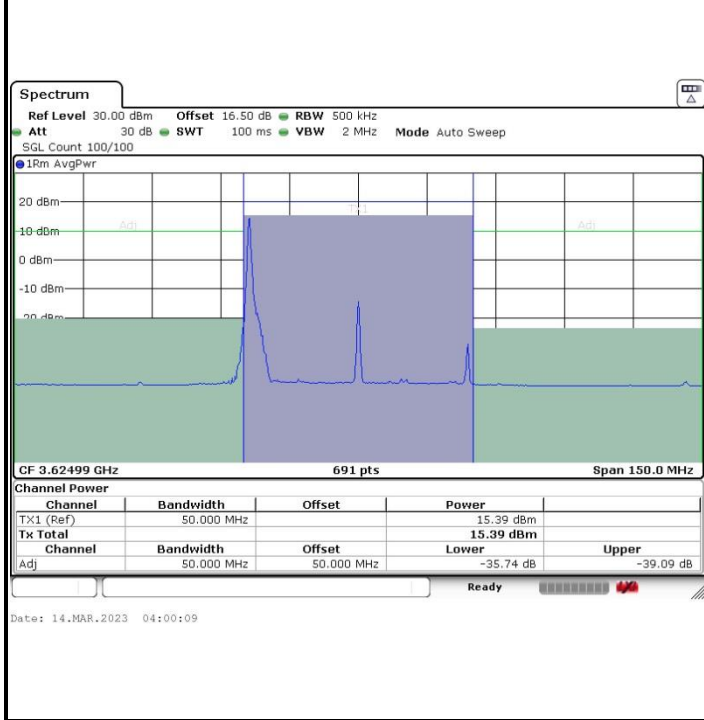




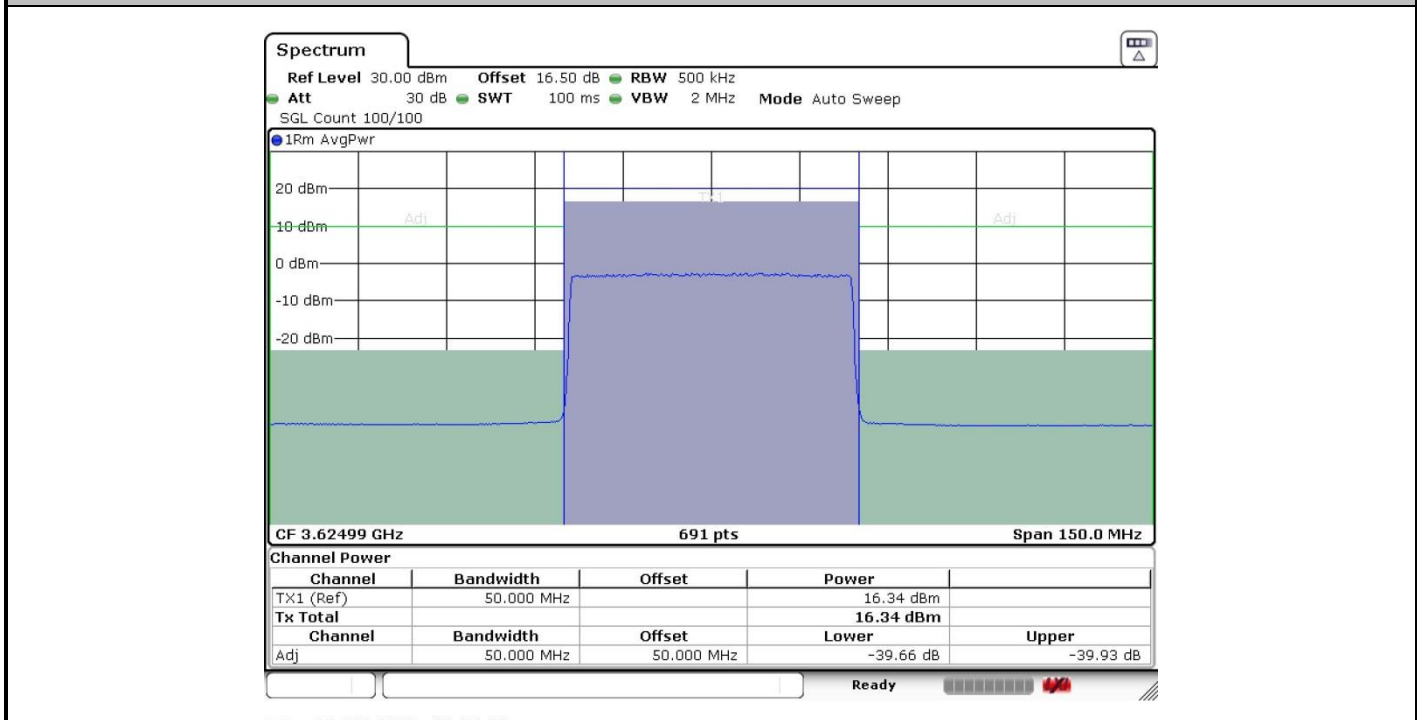
FR1 n48 / 50MHz

256QAM

Middle Channel / 1RB0      Middle Channel / 1RBmax



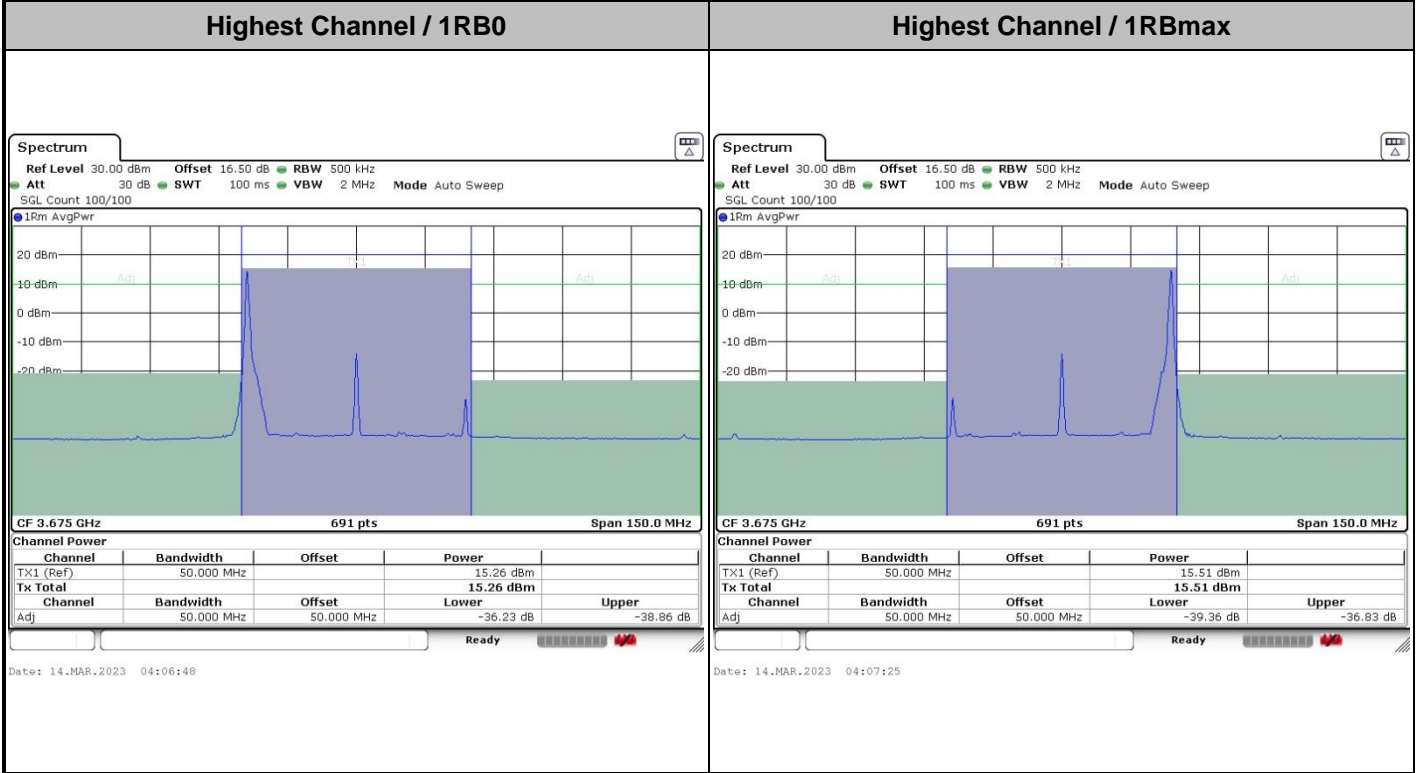
Middle Channel / FullIRB



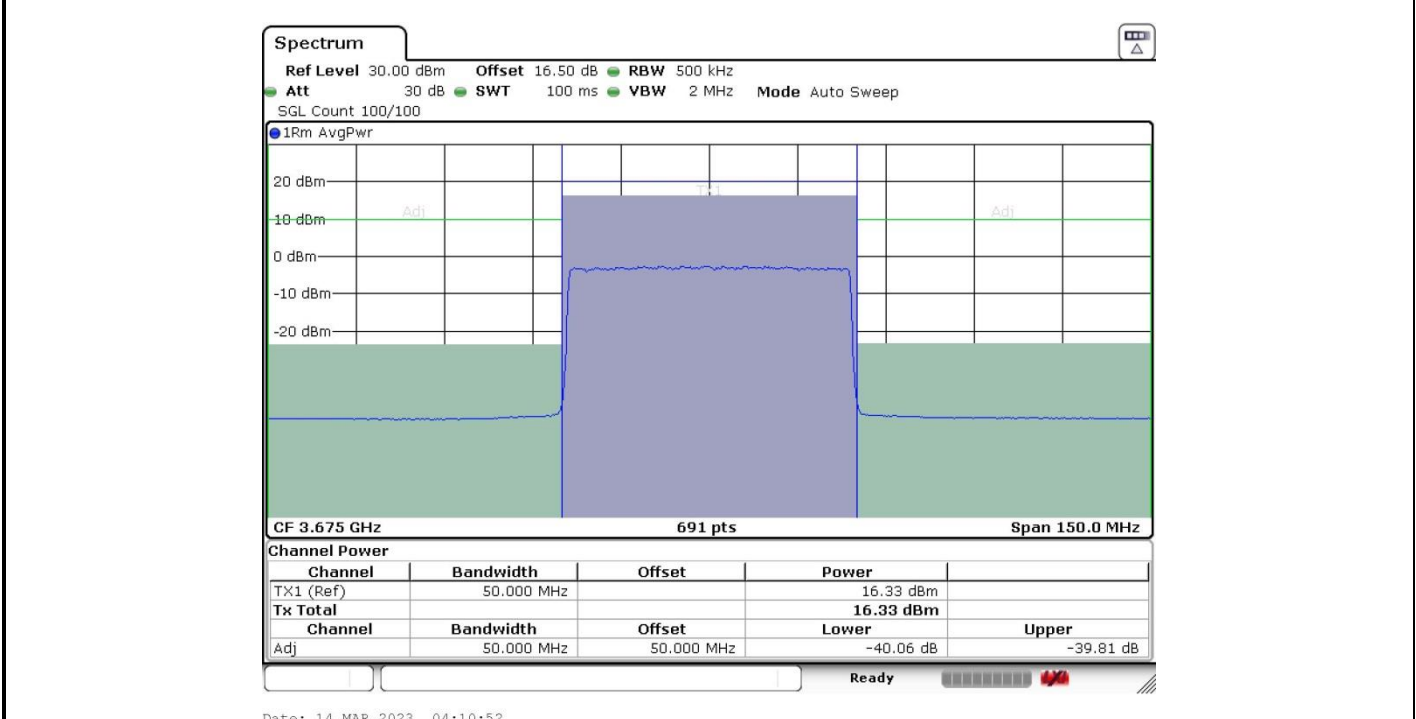


FR1 n48 / 50MHz

256QAM



**Highest Channel / FullRB**



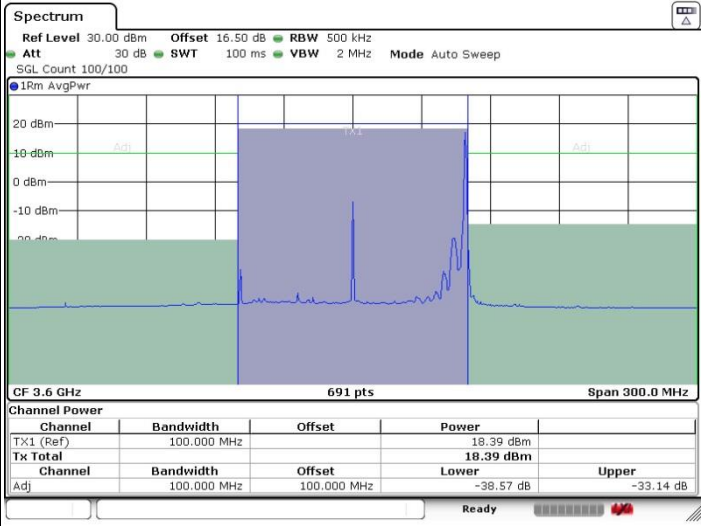
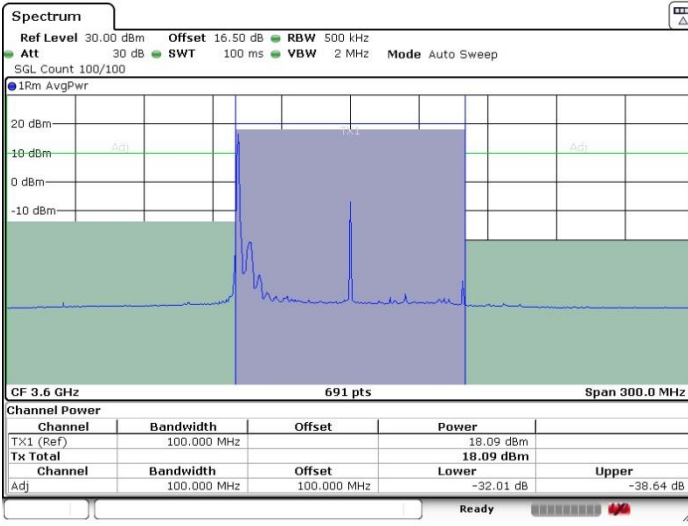


FR1 n48 / 100MHz

QPSK

Lowest Channel / 1RB0

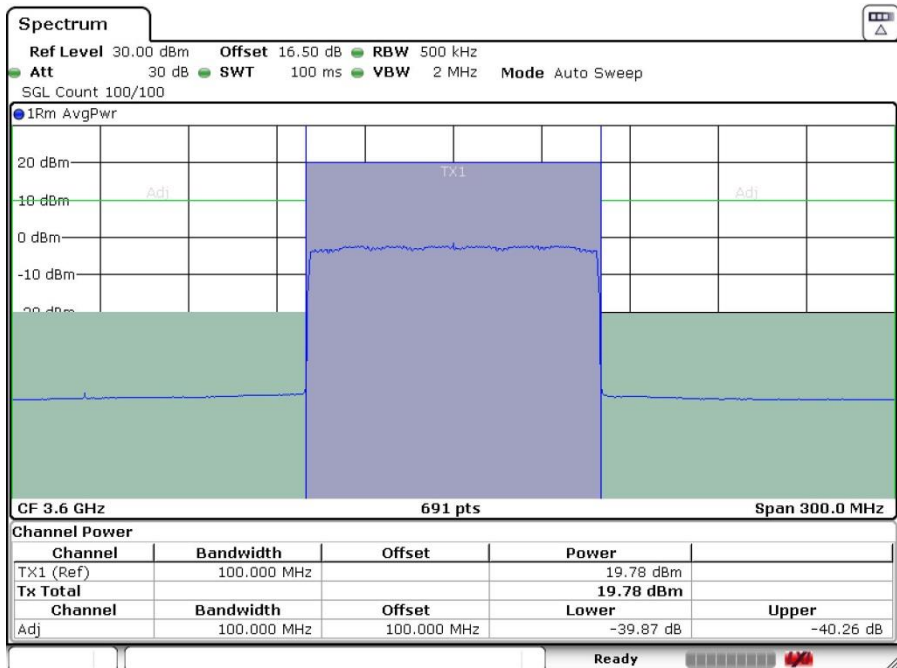
Lowest Channel / 1RBmax



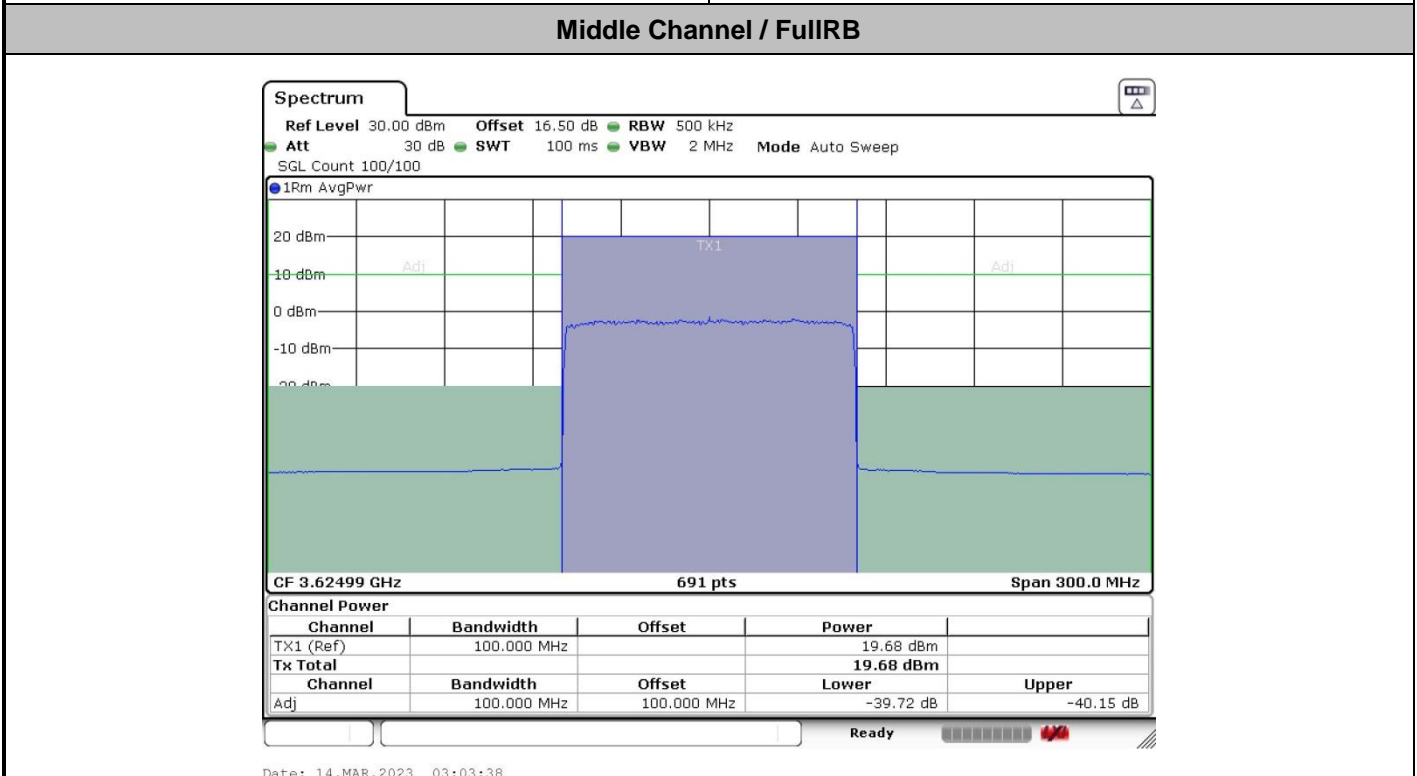
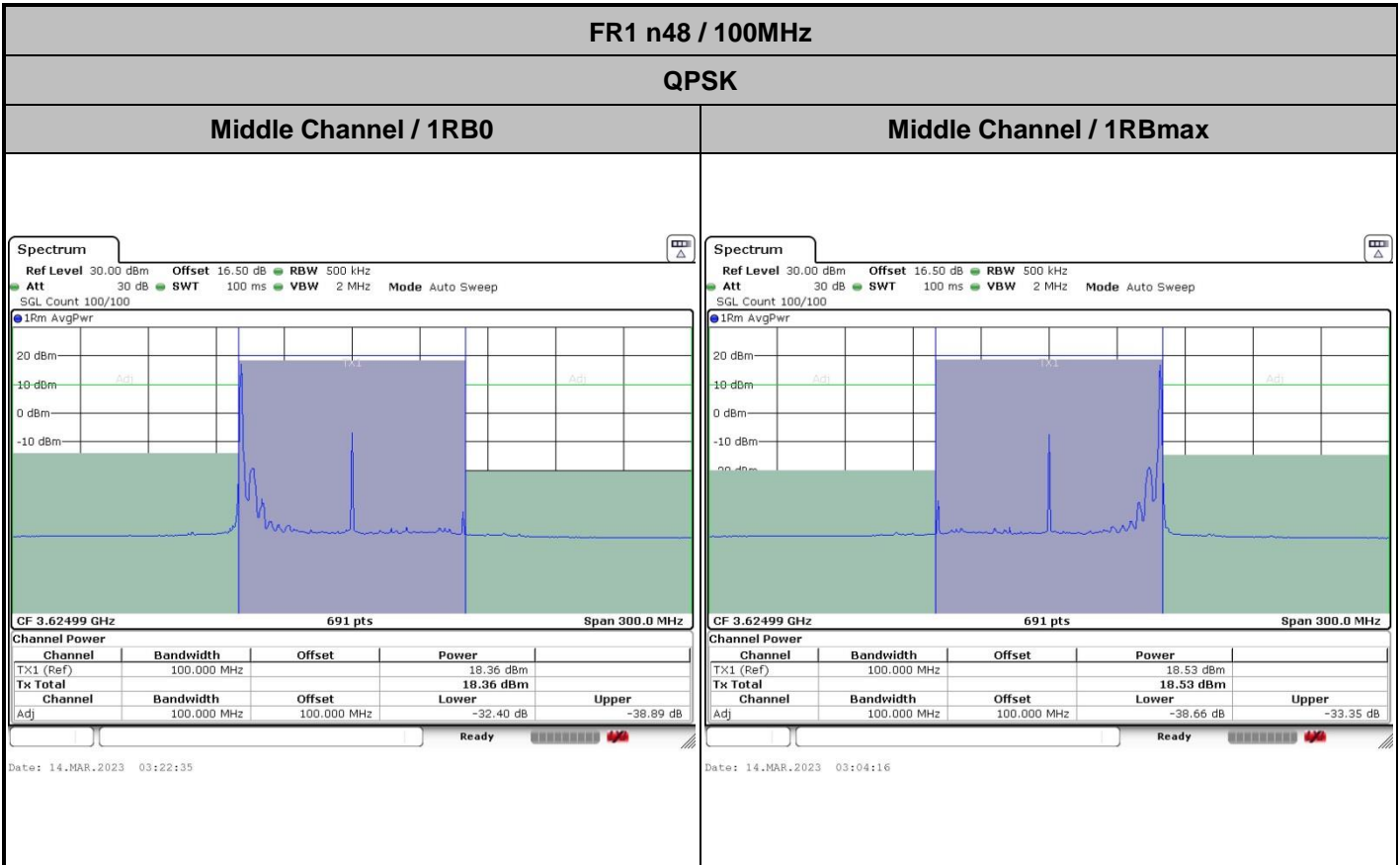
Date: 14.MAR.2023 03:23:40

Date: 14.MAR.2023 03:28:41

Lowest Channel / FullIRB



Date: 14.MAR.2023 03:29:19



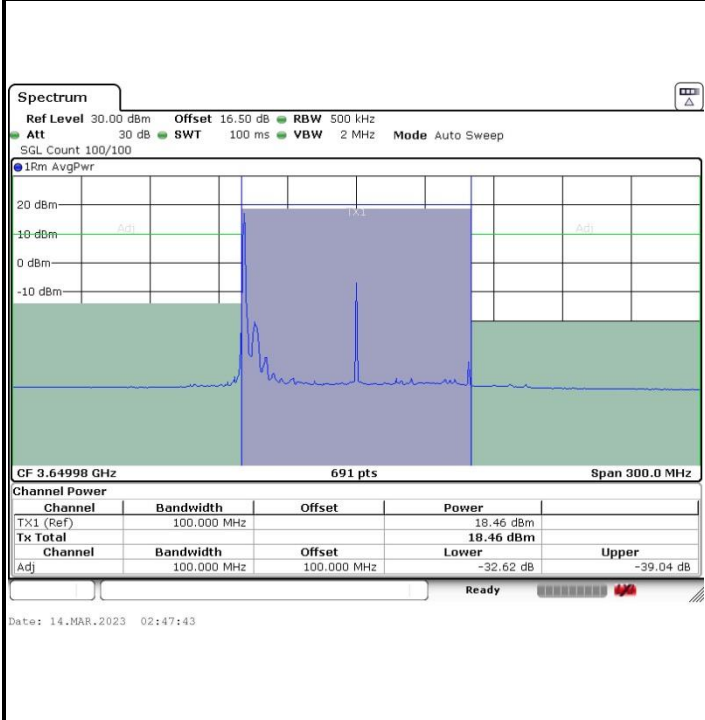




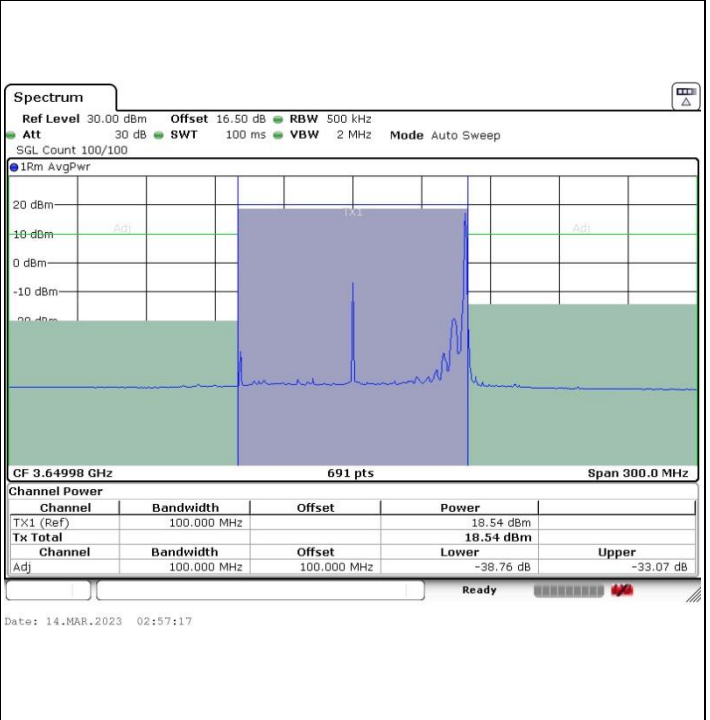
FR1 n48 / 100MHz

QPSK

Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB

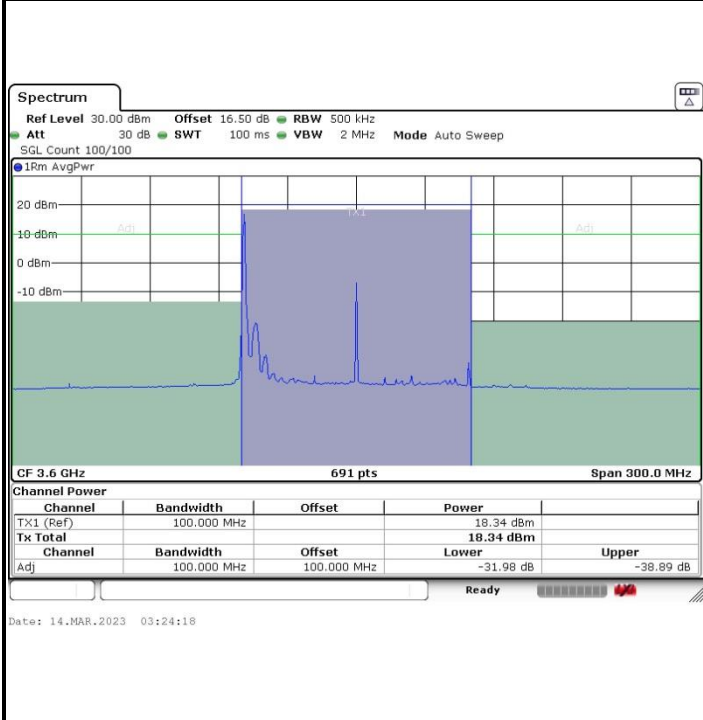




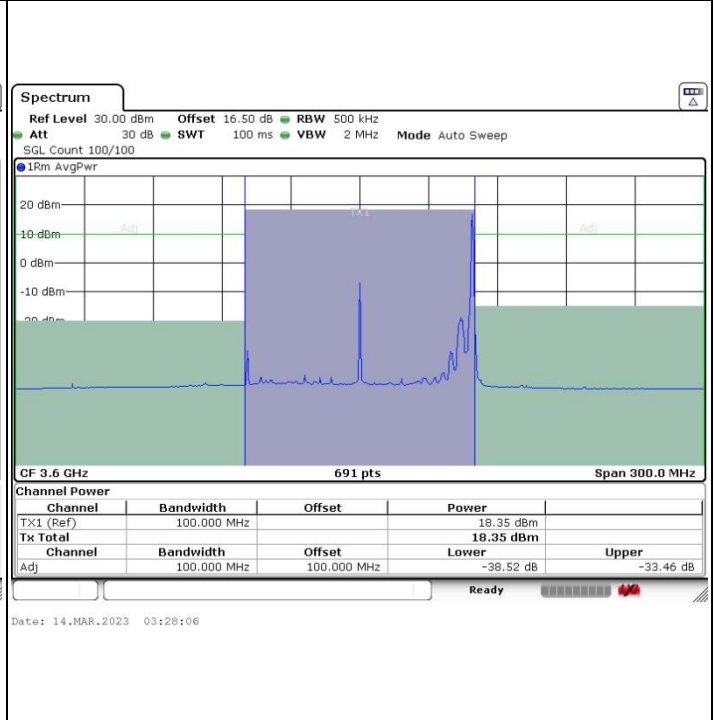
FR1 n48 / 100MHz

16QAM

Lowest Channel / 1RB0



Lowest Channel / 1RBmax



Lowest Channel / FullIRB

