



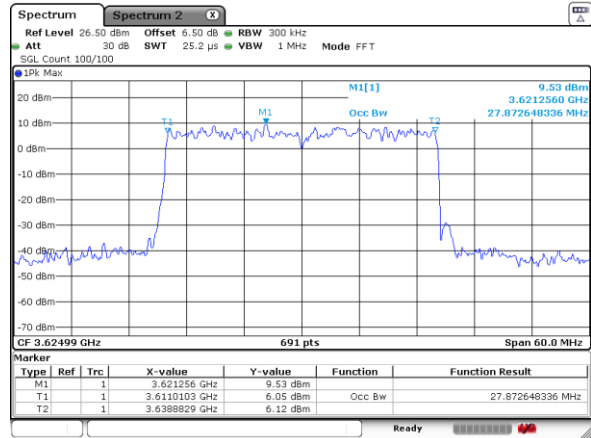
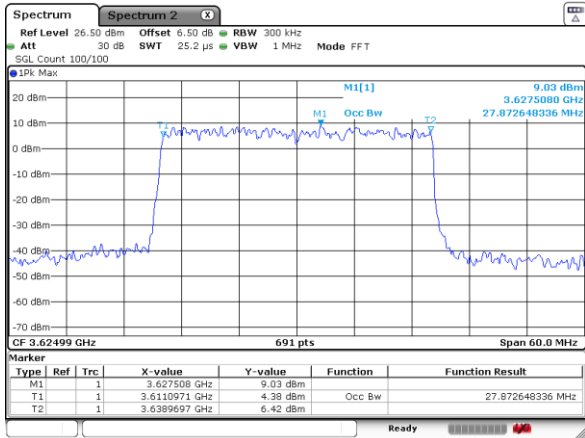
FR1 n78 / 30MHz / CP-OFDM

QPSK

16QAM

Middle Channel

Middle Channel



Date: 28.MAR.2023 23:10:44

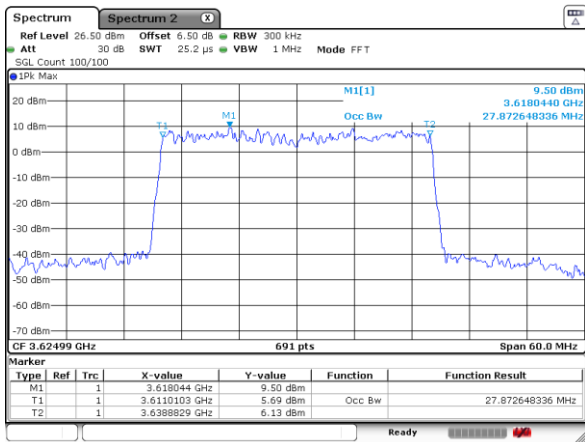
Date: 28.MAR.2023 23:11:34

64QAM

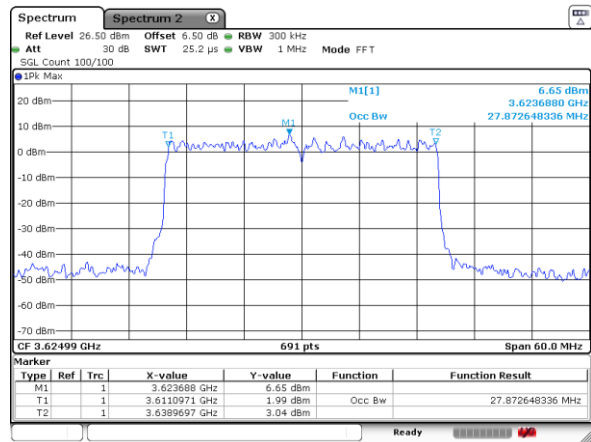
256QAM

Middle Channel

Middle Channel



Date: 28.MAR.2023 23:11:58



Date: 28.MAR.2023 23:12:52



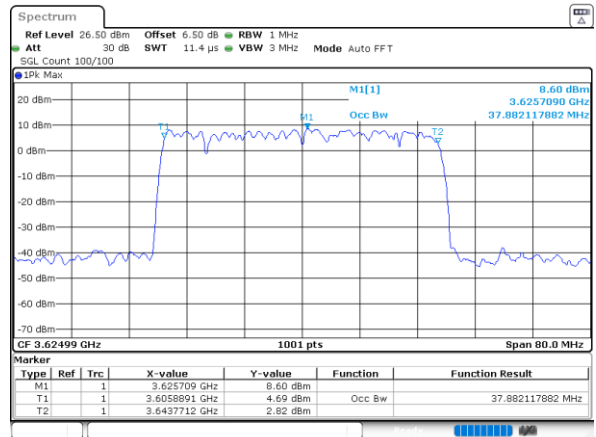
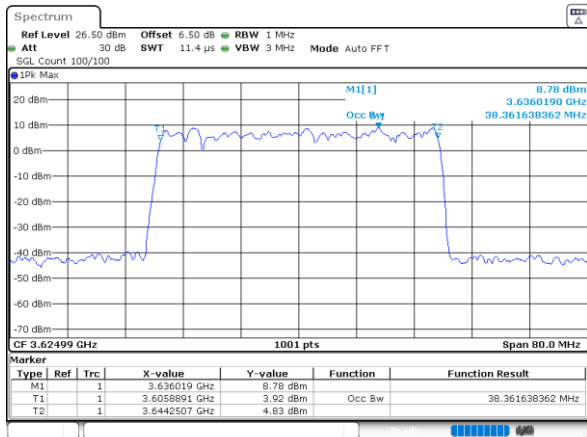
FR1 n78 / 40MHz / CP-OFDM

QPSK

16QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:28:10

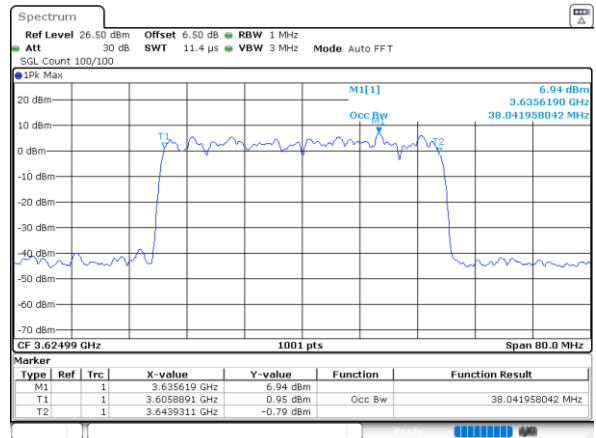
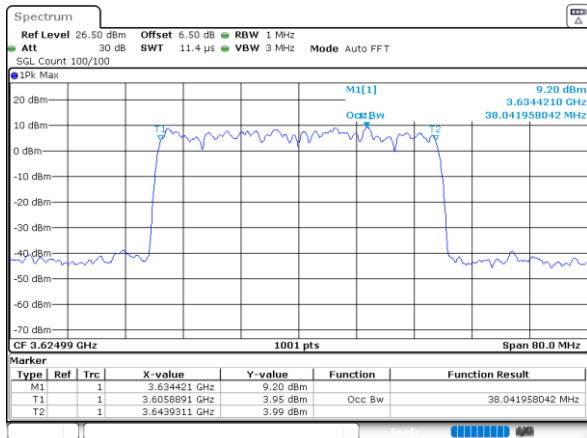
Date: 21.MAR.2023 01:26:25

64QAM

256QAM

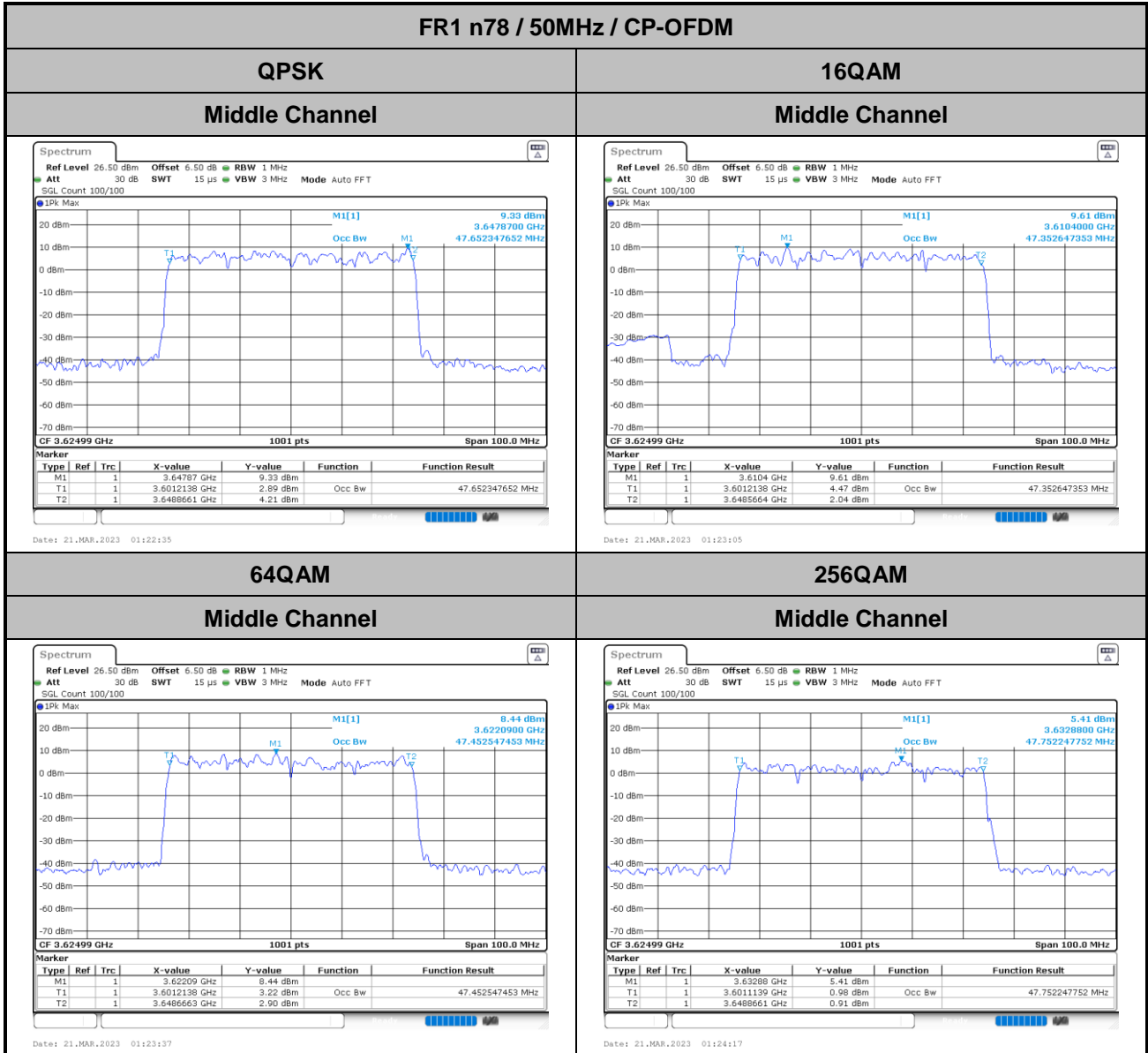
Middle Channel

Middle Channel



Date: 21.MAR.2023 01:27:09

Date: 21.MAR.2023 01:27:35





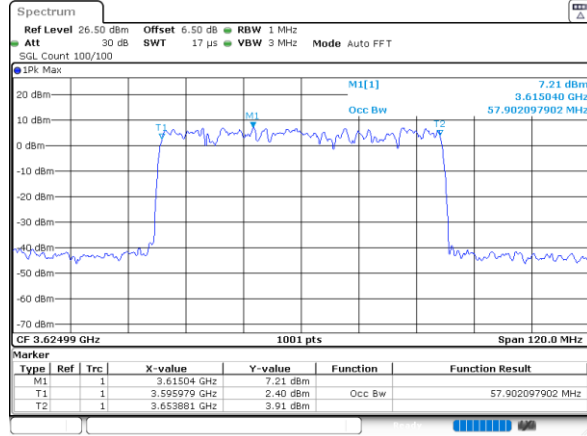
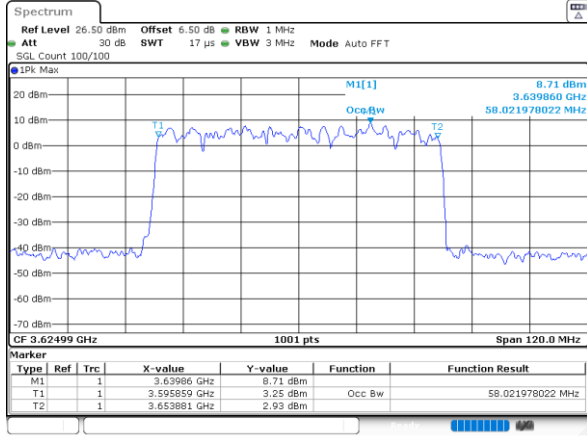
FR1 n78 / 60MHz / CP-OFDM

QPSK

16QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:21:41

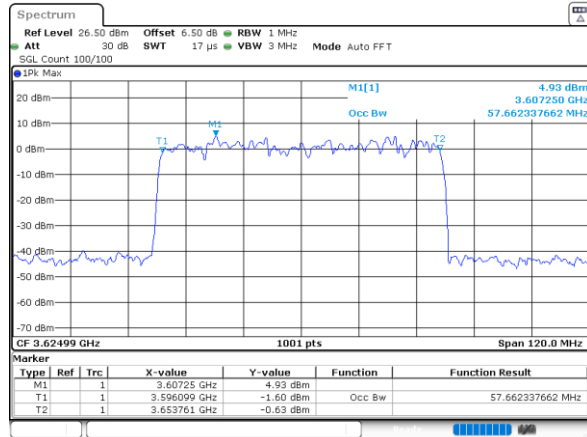
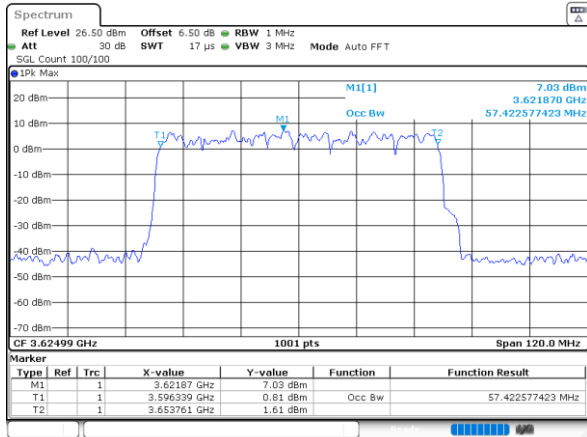
Date: 21.MAR.2023 01:20:58

64QAM

256QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:20:24

Date: 21.MAR.2023 01:20:03



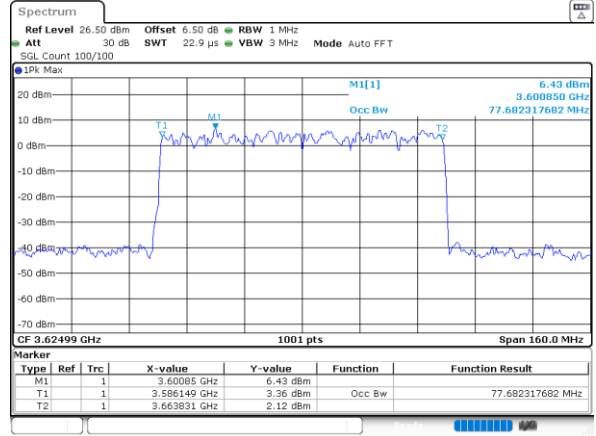
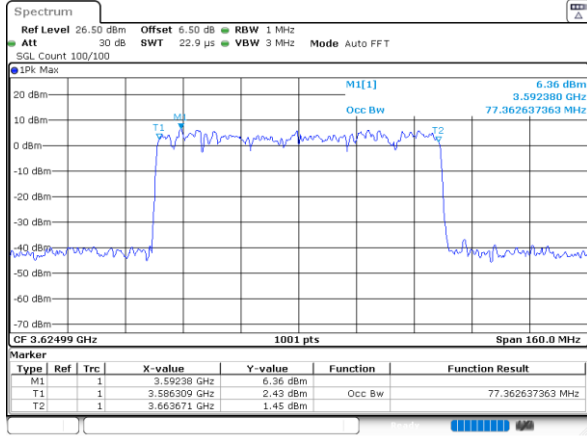
FR1 n78 / 80MHz / CP-OFDM

QPSK

16QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:16:40

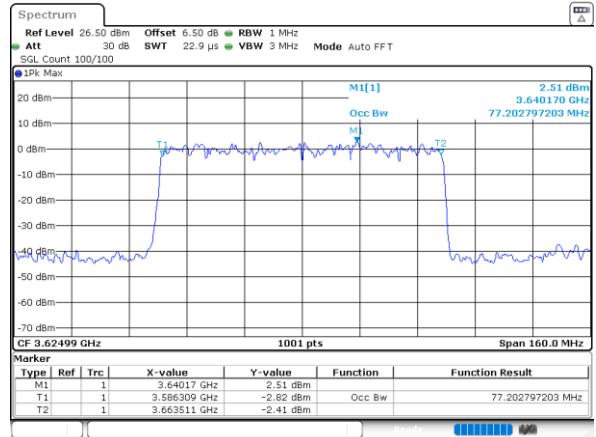
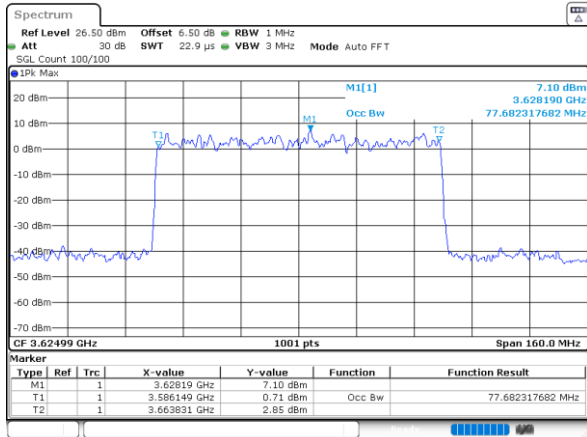
Date: 21.MAR.2023 01:17:18

64QAM

256QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:18:20

Date: 21.MAR.2023 01:19:17



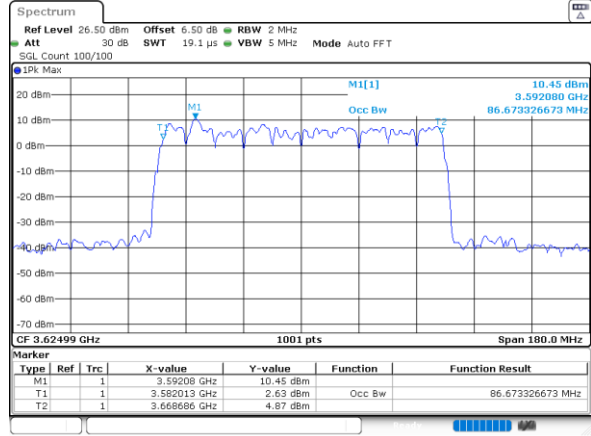
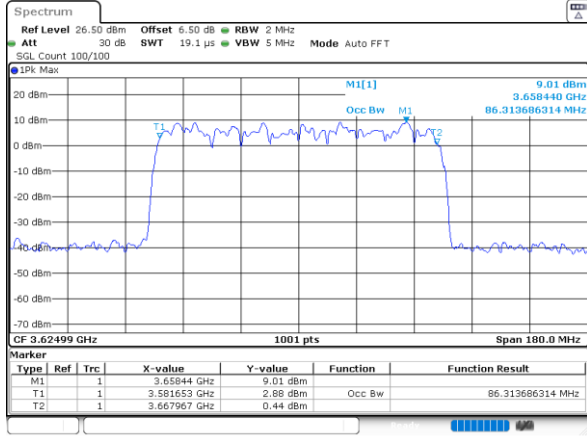
FR1 n78 / 90MHz / CP-OFDM

QPSK

16QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:15:12

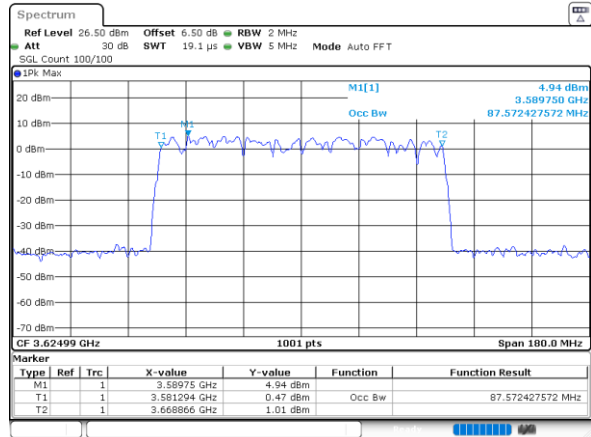
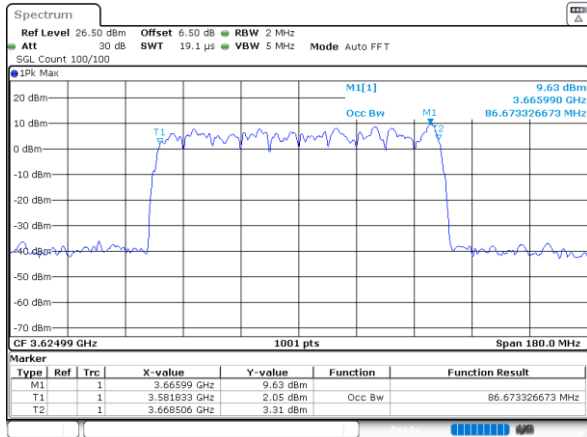
Date: 21.MAR.2023 01:14:44

64QAM

256QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:13:58

Date: 21.MAR.2023 01:13:27



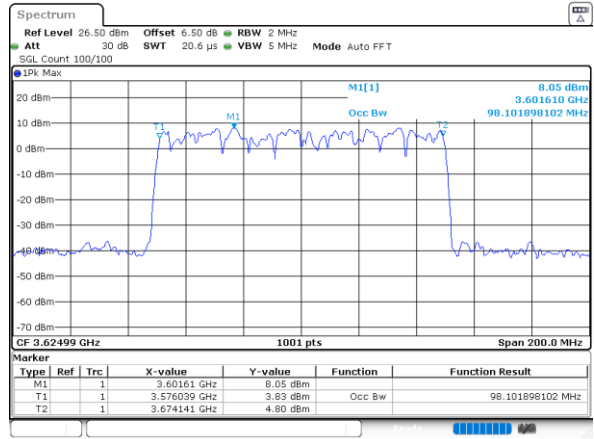
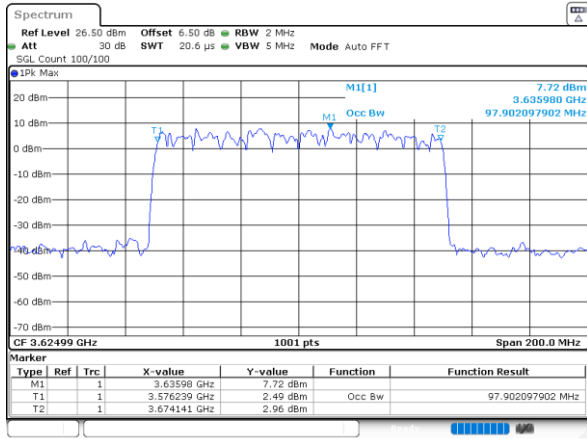
FR1 n78 / 100MHz / CP-OFDM

QPSK

16QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:09:05

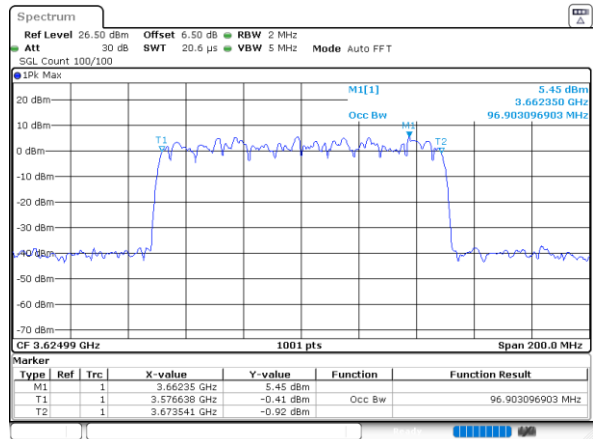
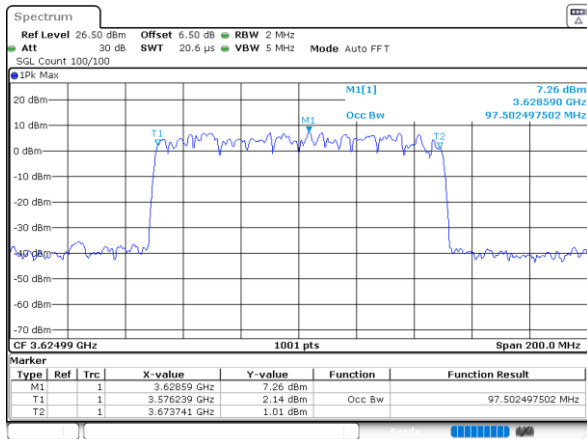
Date: 21.MAR.2023 01:10:15

64QAM

256QAM

Middle Channel

Middle Channel



Date: 21.MAR.2023 01:11:11

Date: 21.MAR.2023 01:11:57



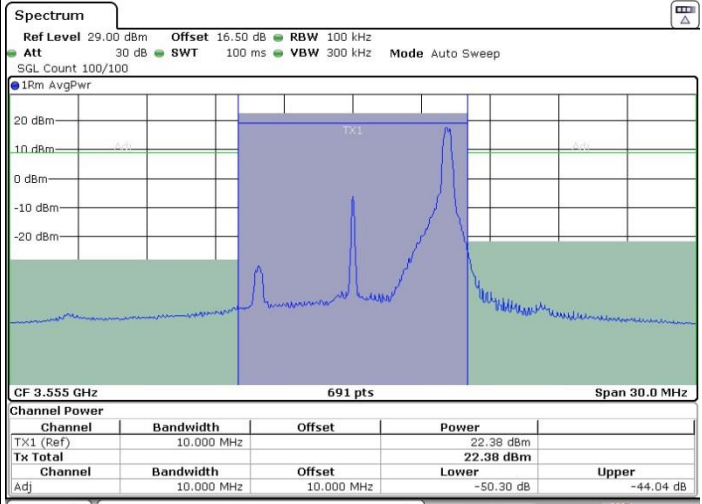
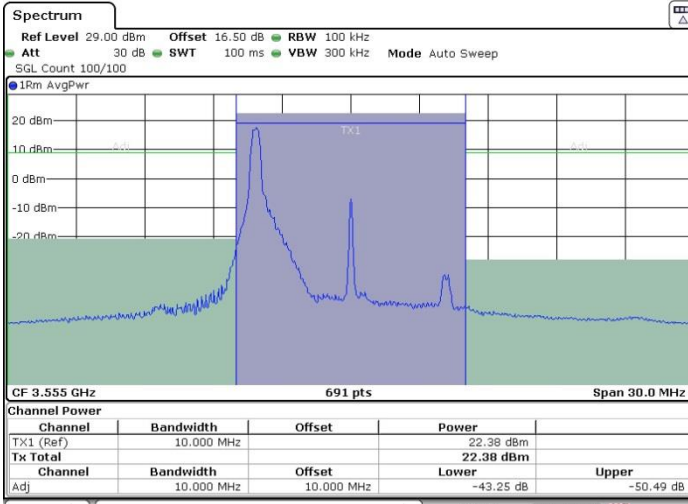
ACLR

FR1 n78 / 10MHz

QPSK

Lowest Channel / 1RB0

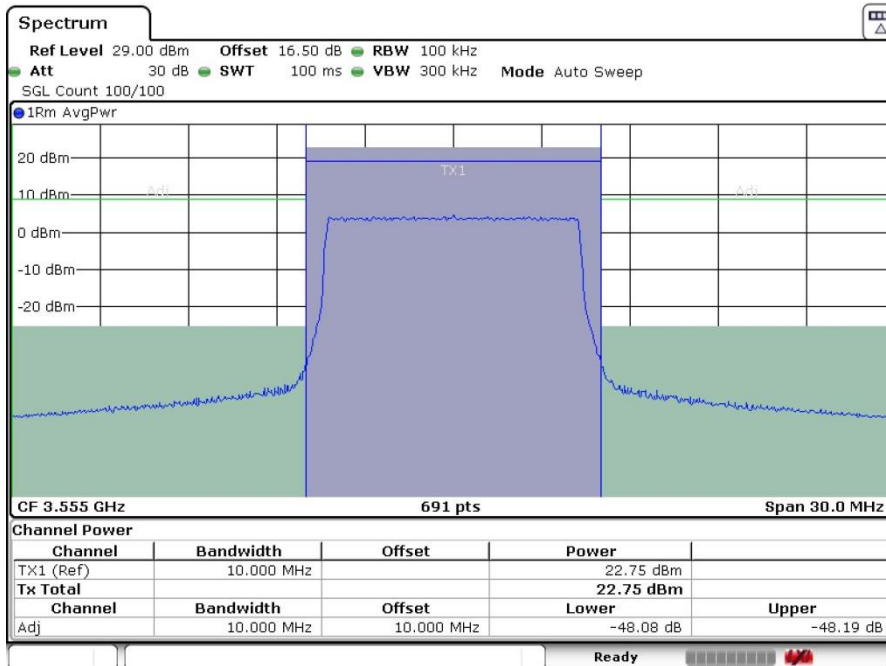
Lowest Channel / 1RBmax



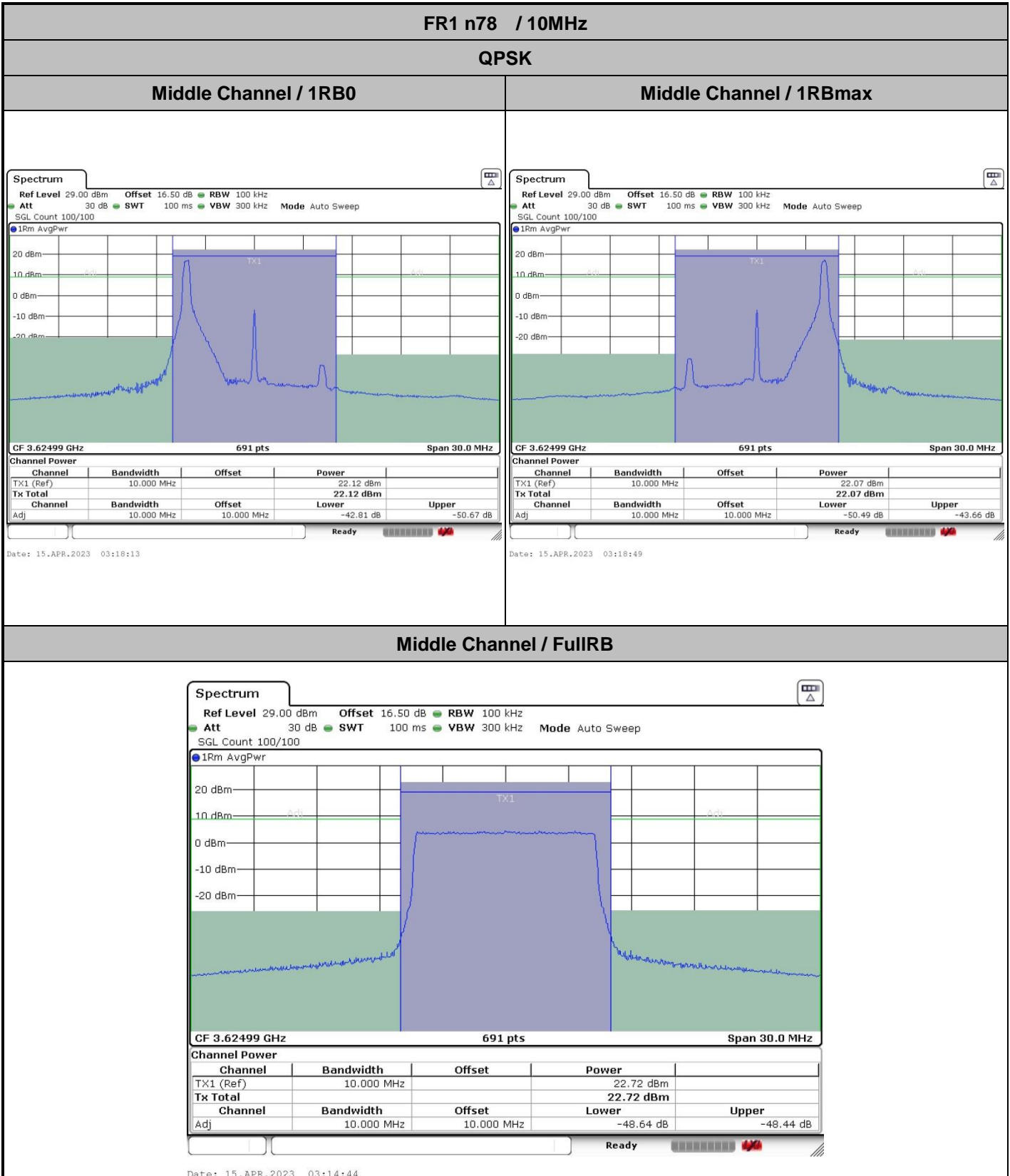
Date: 15.APR.2023 03:22:56

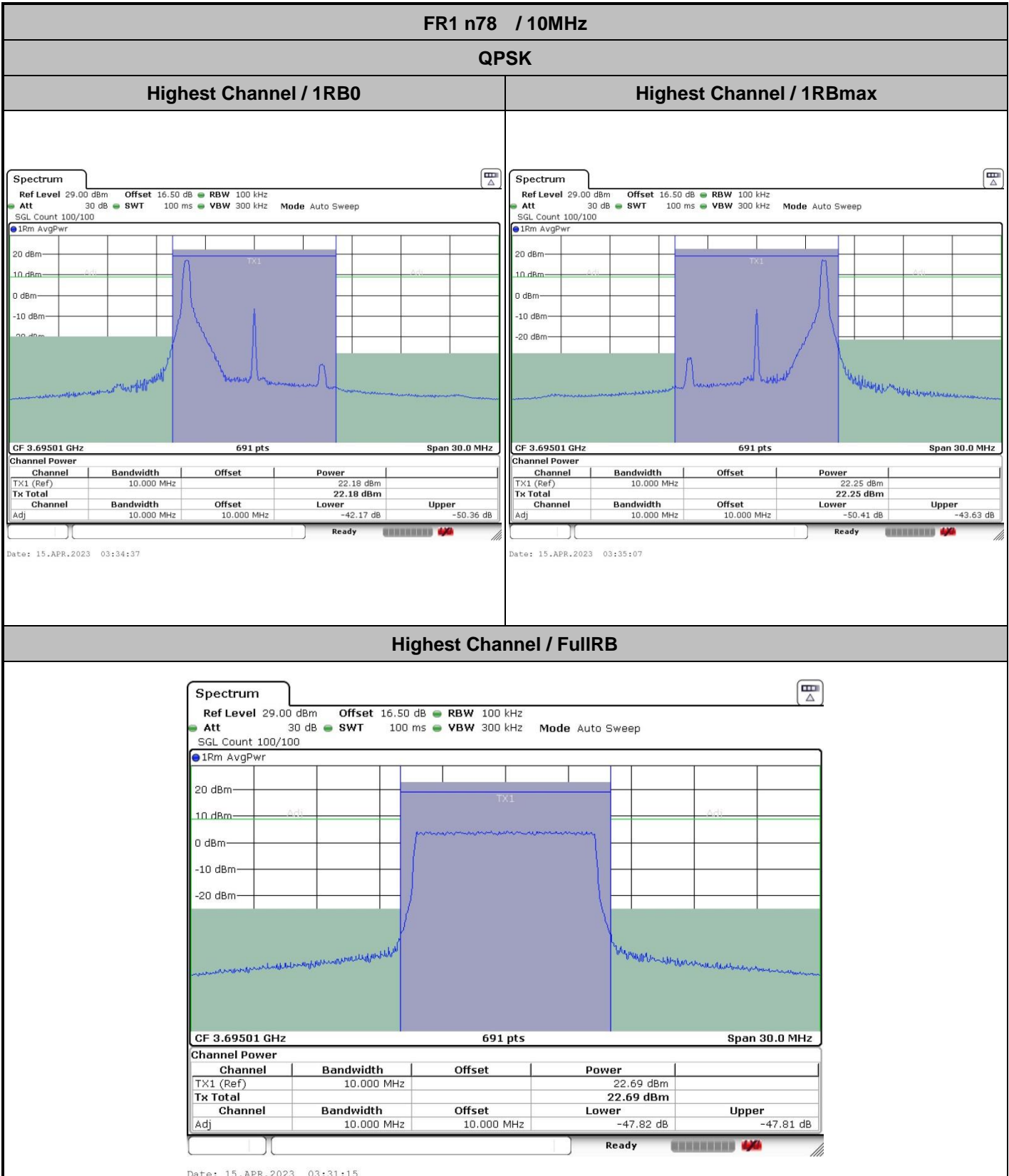
Date: 15.APR.2023 03:22:33

Lowest Channel / FullIRB



Date: 15.APR.2023 03:29:40



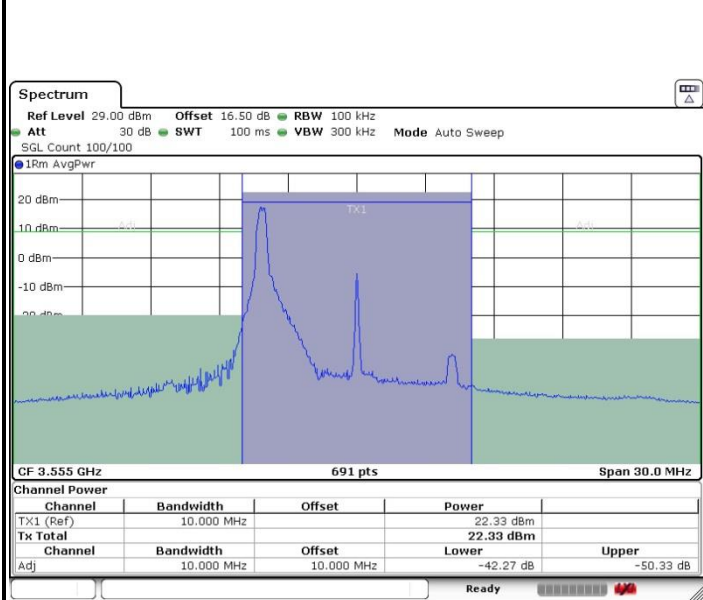




FR1 n78 / 10MHz

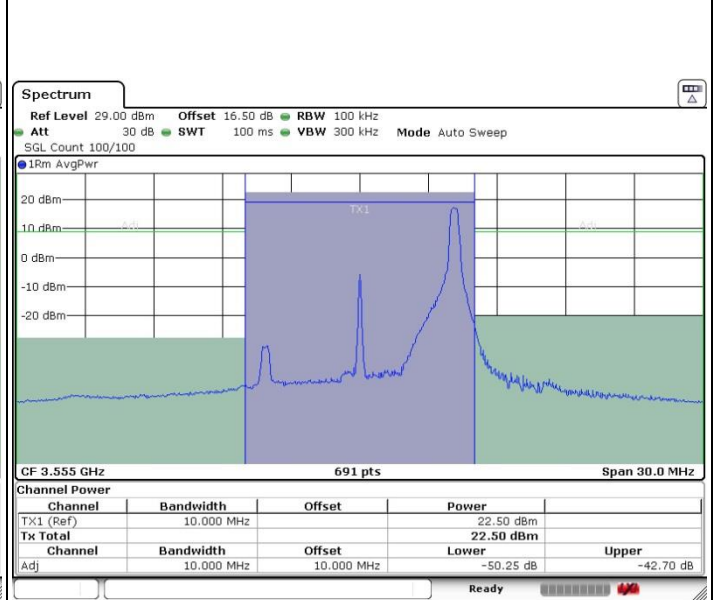
16QAM

Lowest Channel / 1RB0



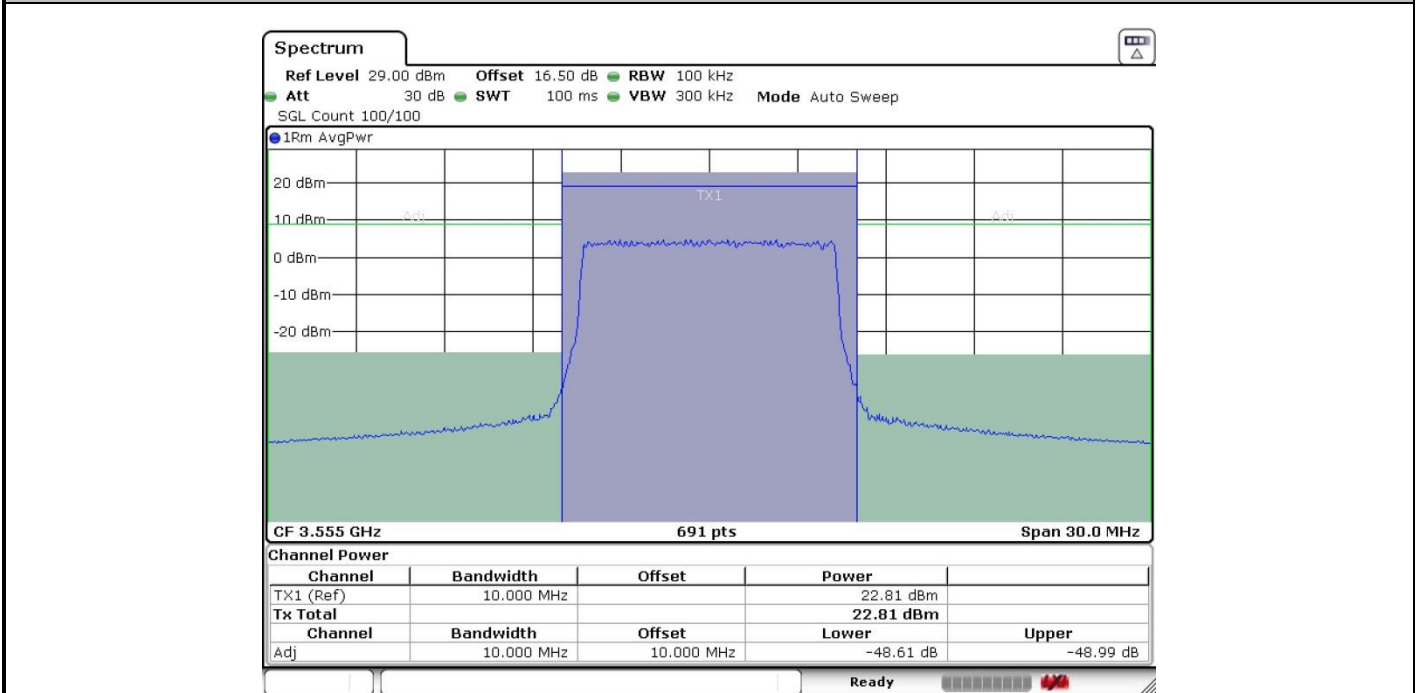
Date: 15.APR.2023 03:23:22

Lowest Channel / 1RBmax

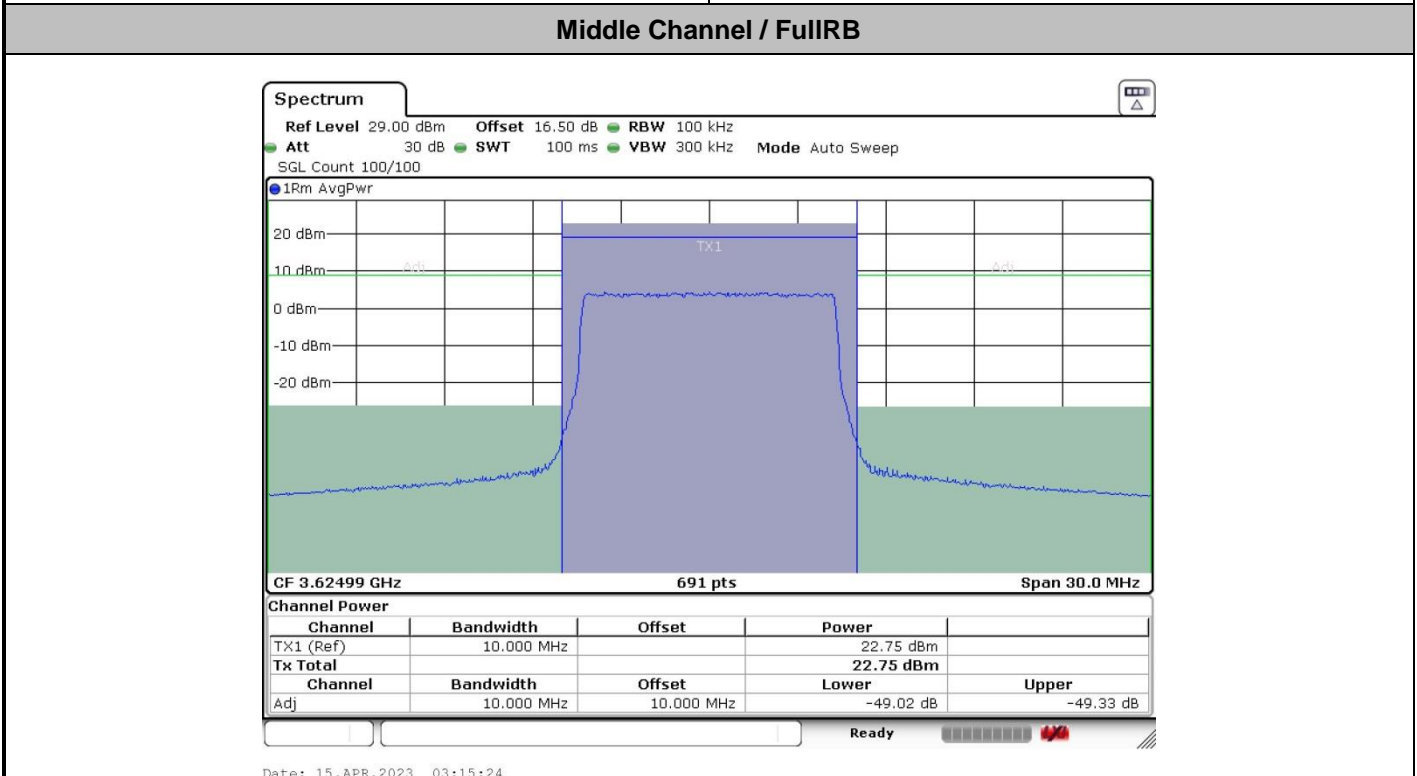
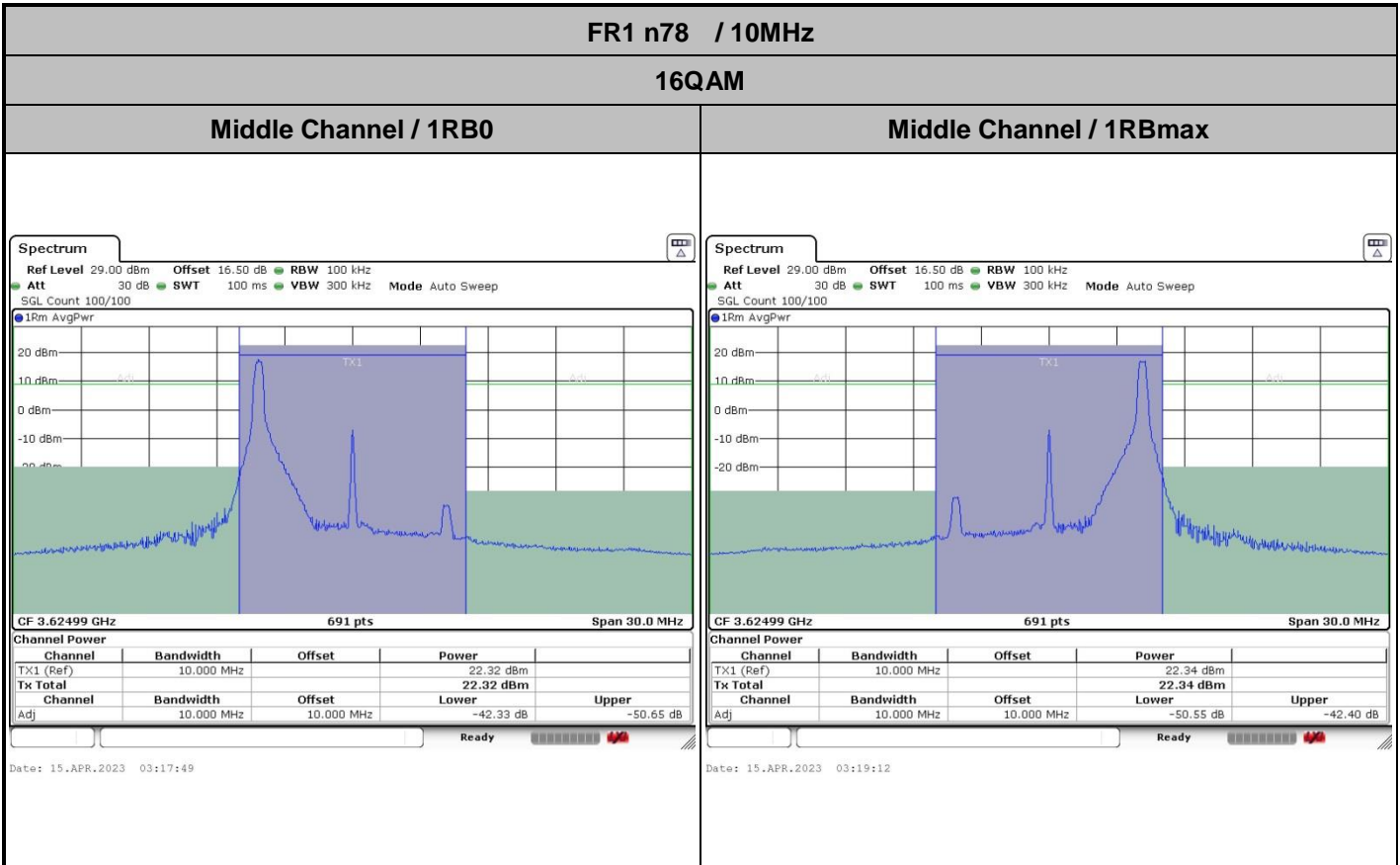


Date: 15.APR.2023 03:21:55

Lowest Channel / FullRB



Date: 15.APR.2023 03:29:13



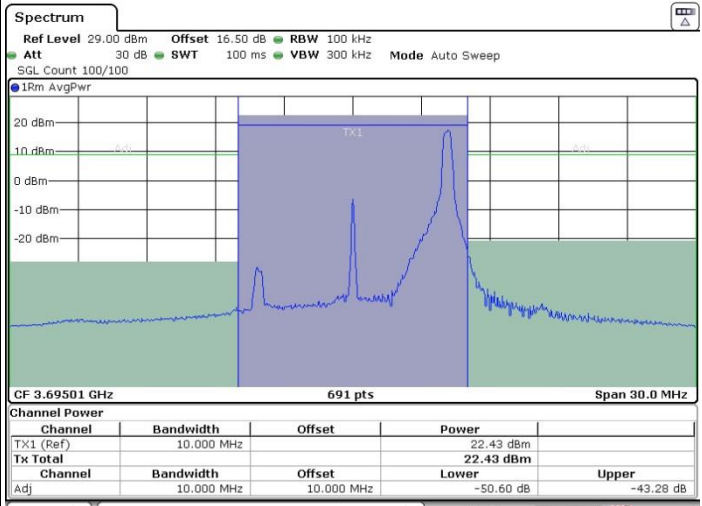
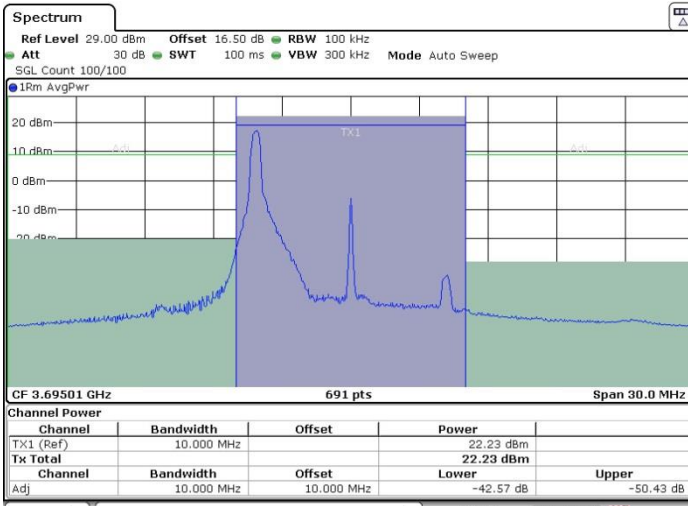


FR1 n78 / 10MHz

16QAM

Highest Channel / 1RB0

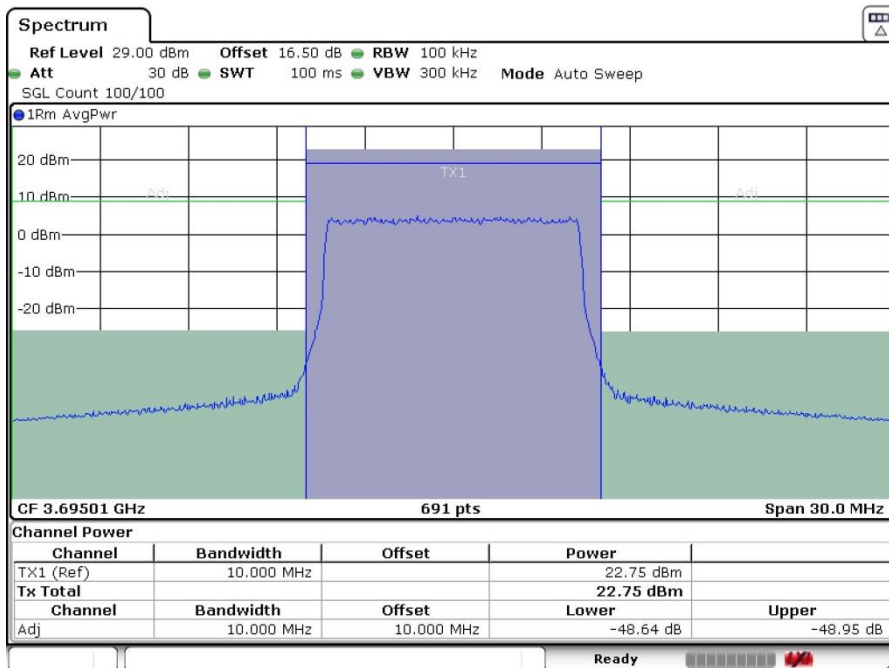
Highest Channel / 1RBmax



Date: 15.APR.2023 03:34:12

Date: 15.APR.2023 03:35:33

Highest Channel / FullRB



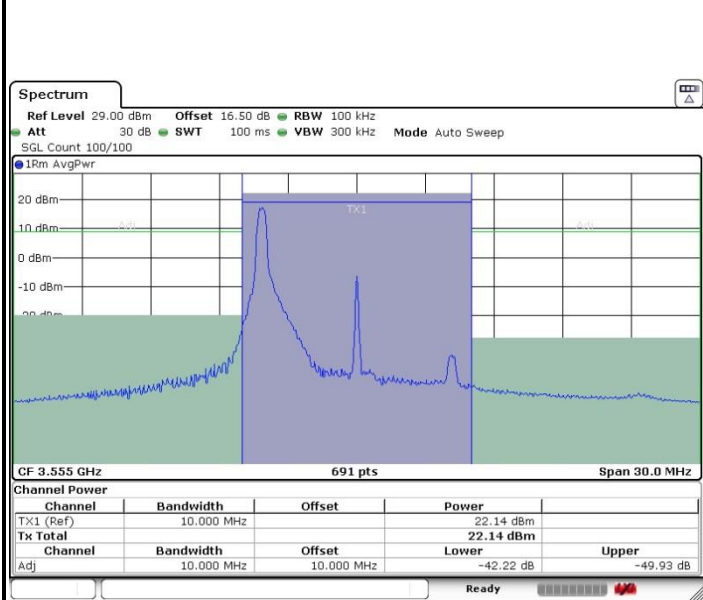
Date: 15.APR.2023 03:31:47



FR1 n78 / 10MHz

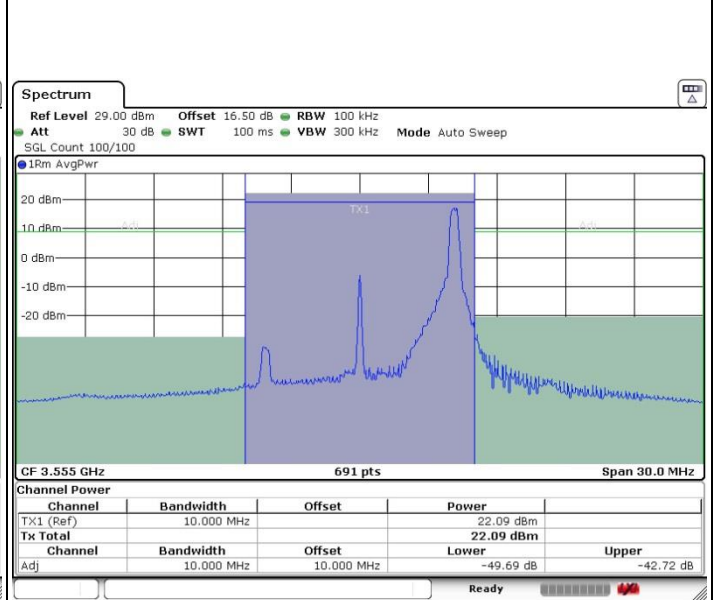
64QAM

Lowest Channel / 1RB0



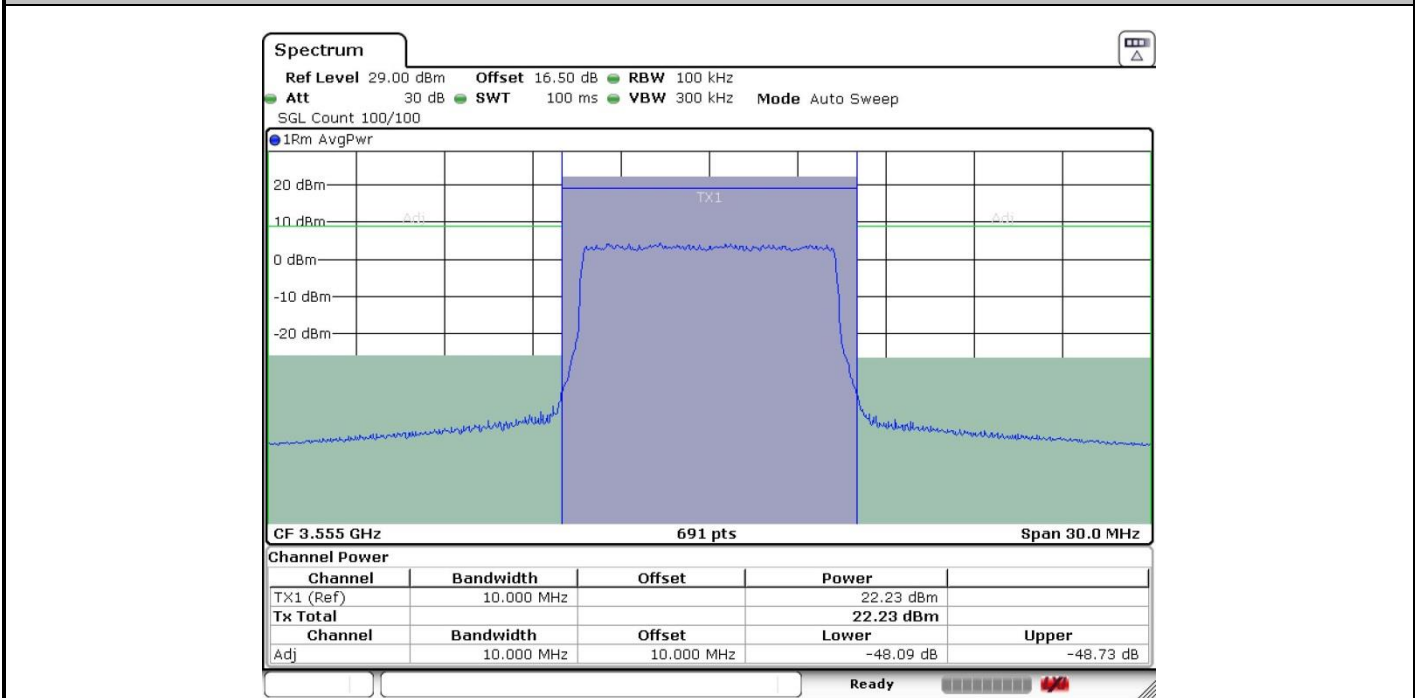
Date: 15.APR.2023 03:23:52

Lowest Channel / 1RBmax



Date: 15.APR.2023 03:21:31

Lowest Channel / FullIRB



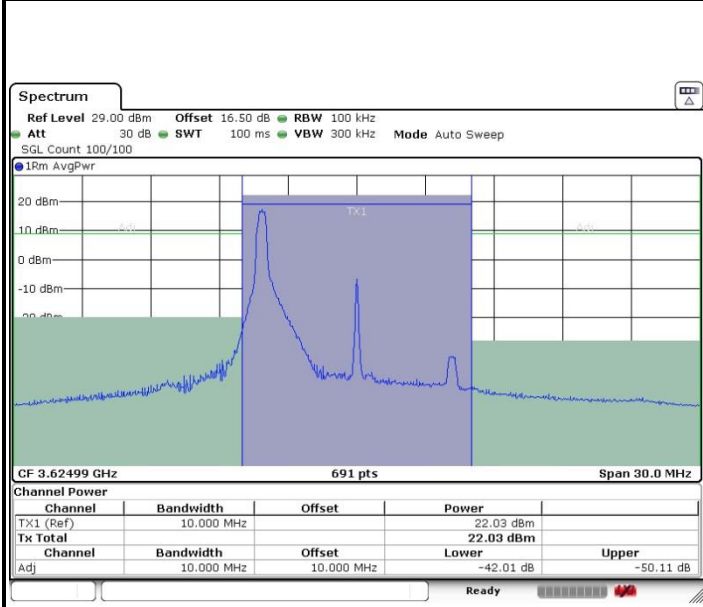
Date: 15.APR.2023 03:28:44



FR1 n78 / 10MHz

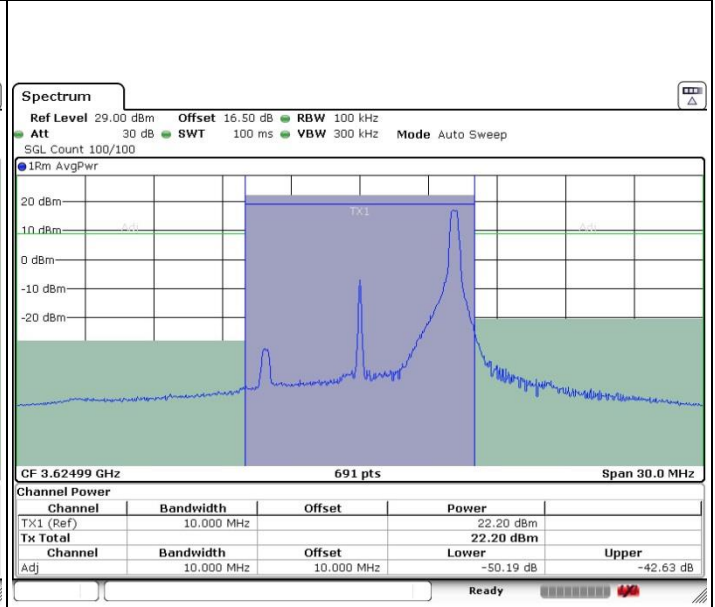
64QAM

Middle Channel / 1RB0



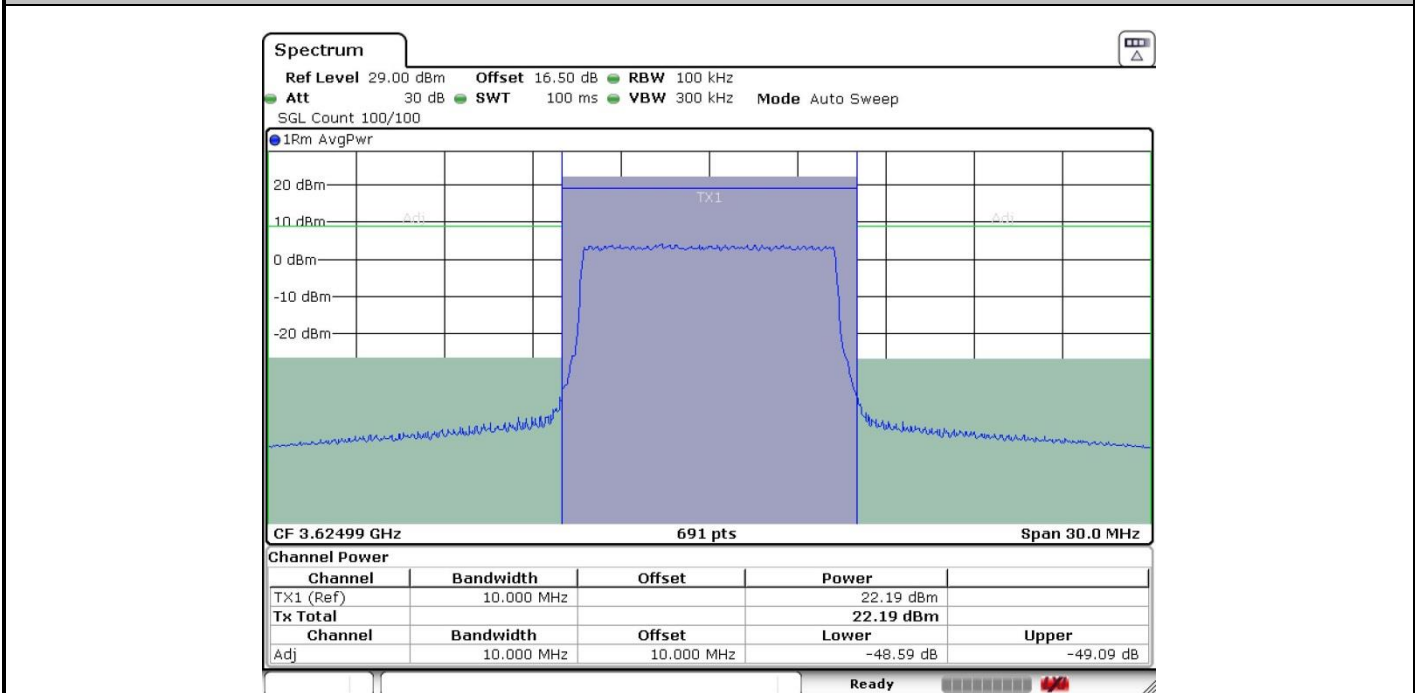
Date: 15.APR.2023 03:17:24

Middle Channel / 1RBmax



Date: 15.APR.2023 03:19:37

Middle Channel / FullIRB



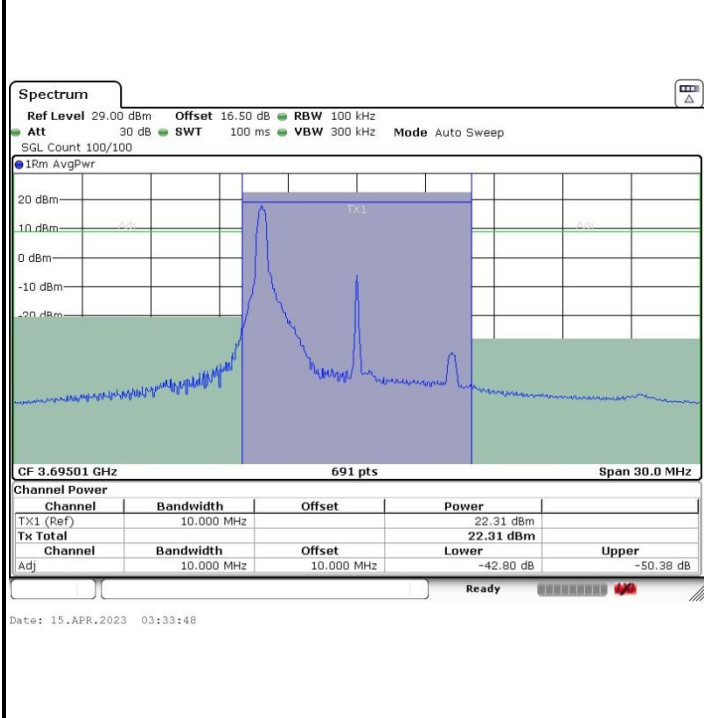
Date: 15.APR.2023 03:15:49



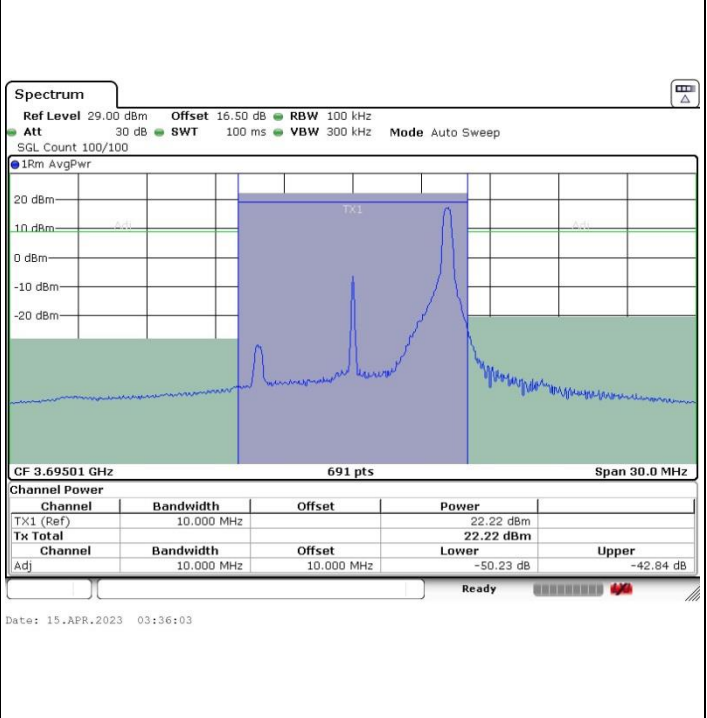
FR1 n78 / 10MHz

64QAM

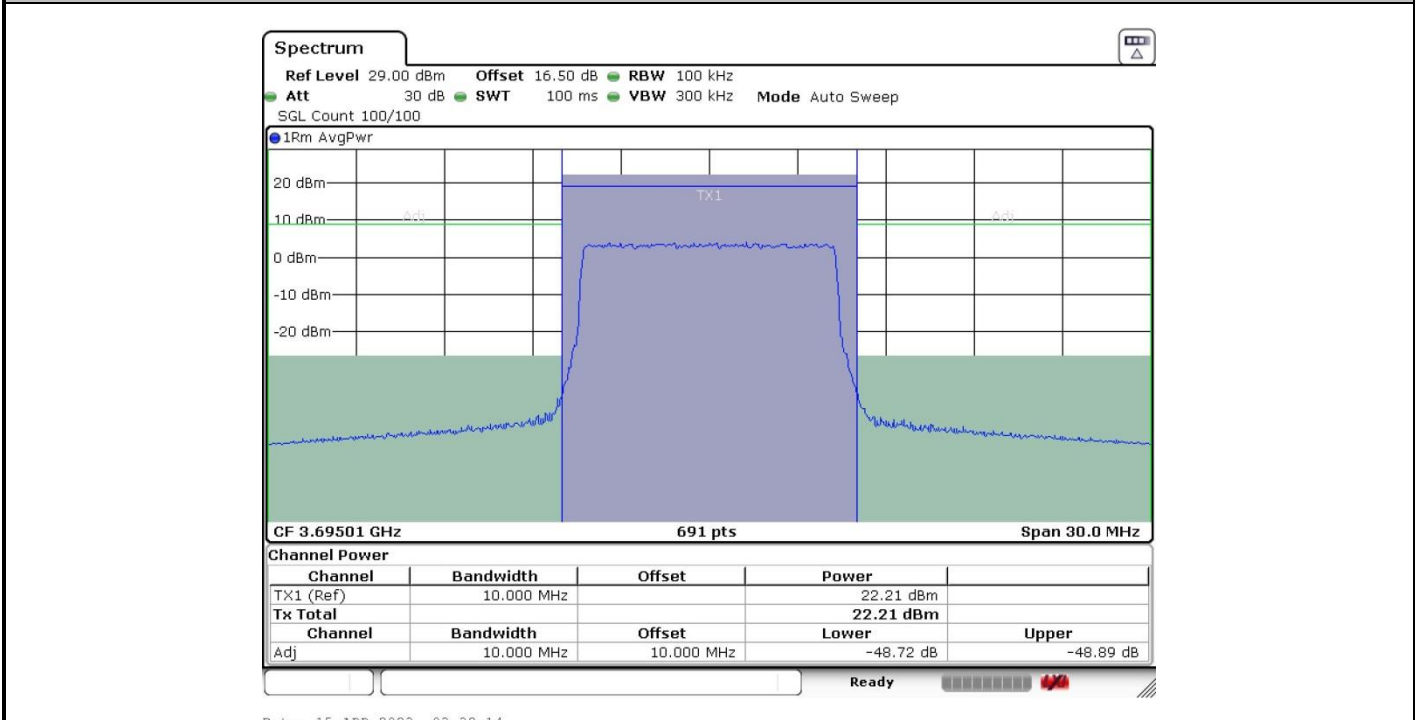
Highest Channel / 1RB0

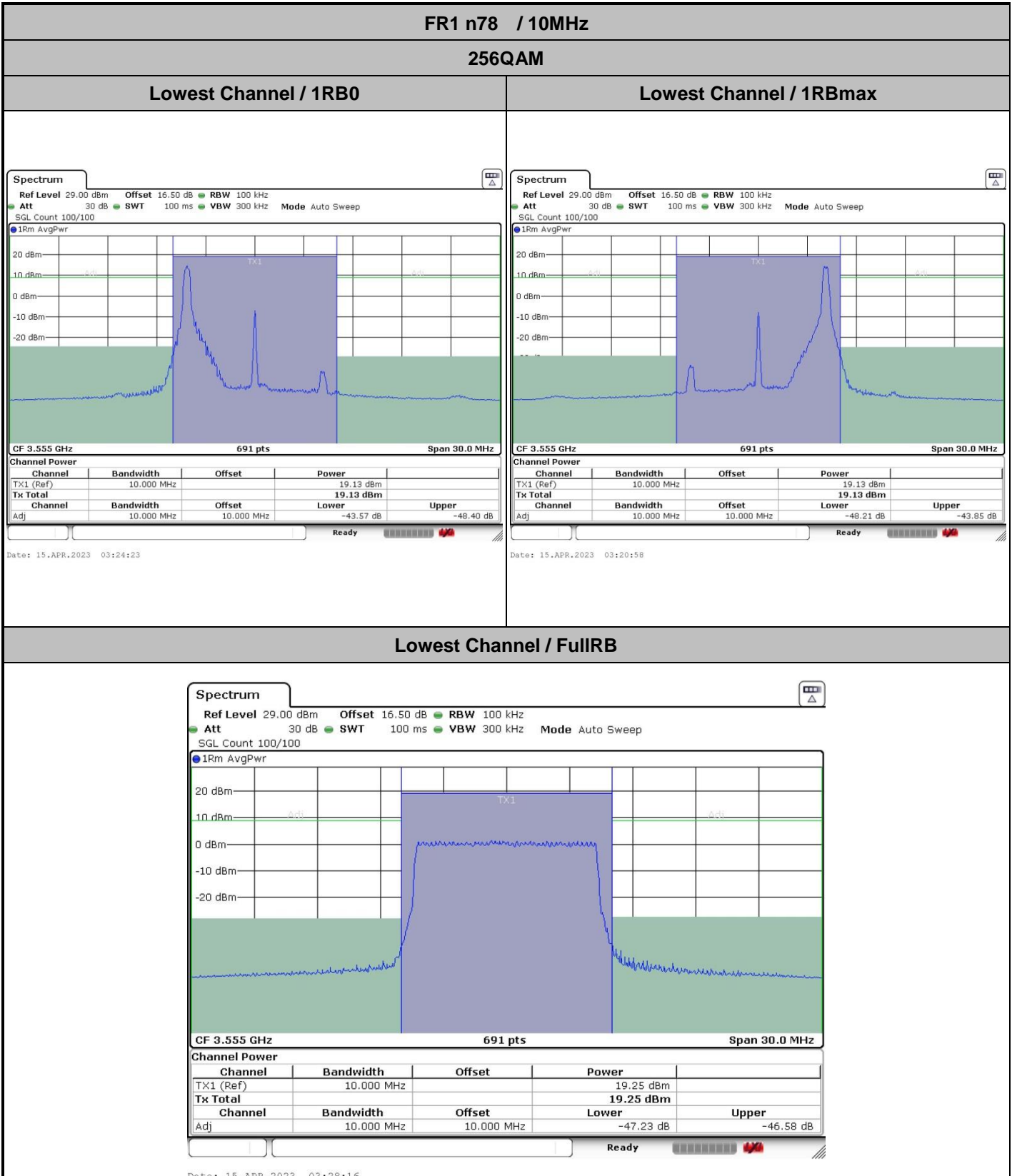


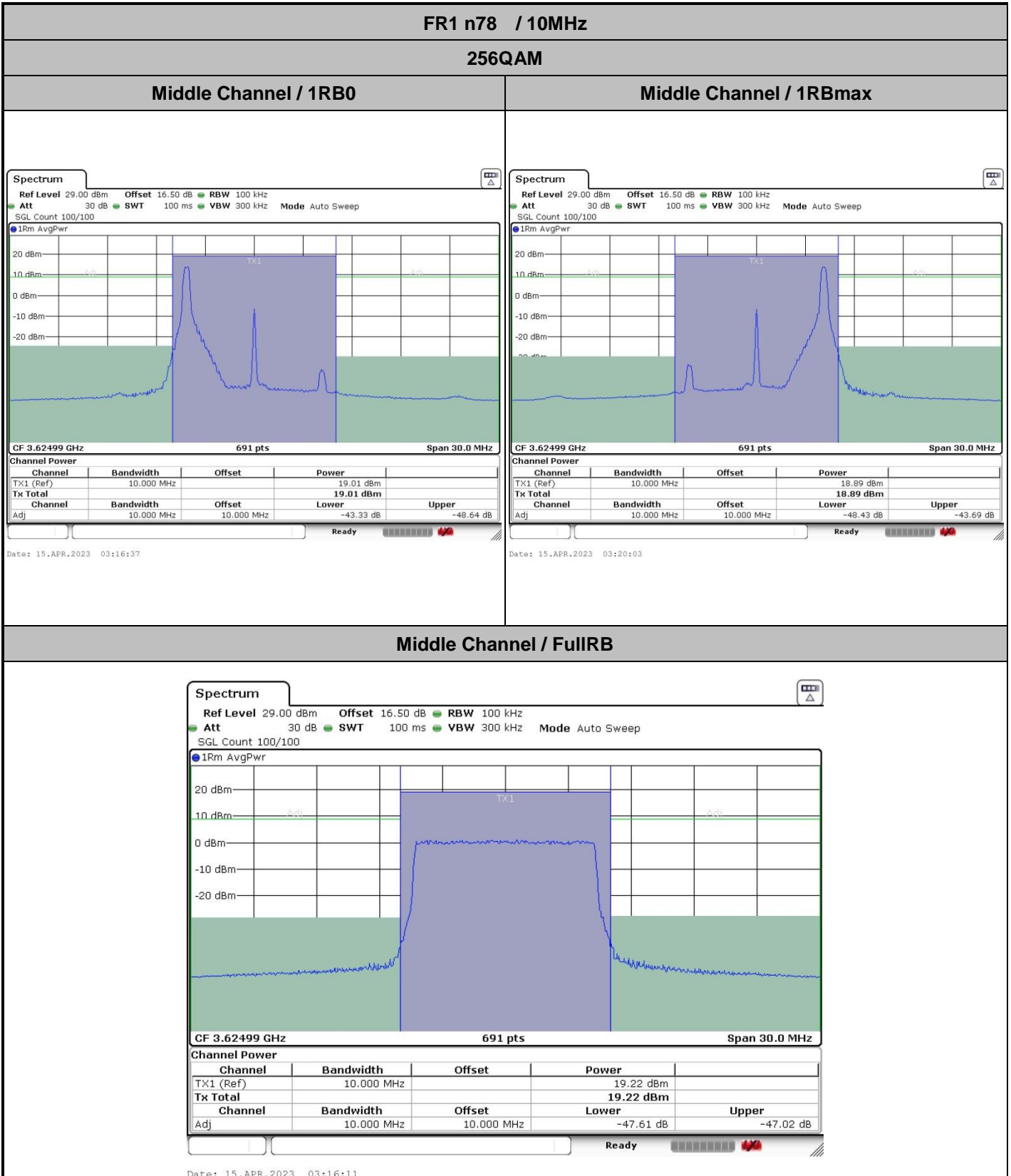
Highest Channel / 1RBmax



Highest Channel / FullRB





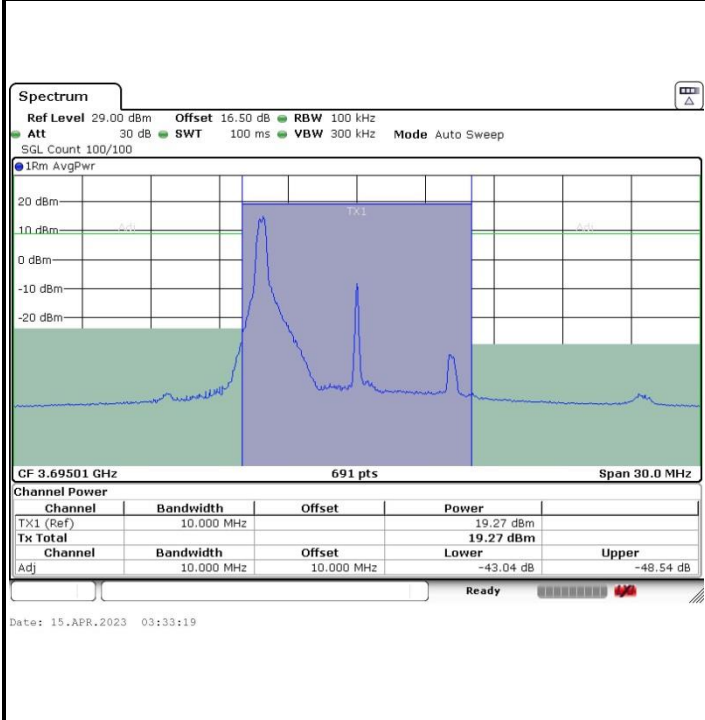




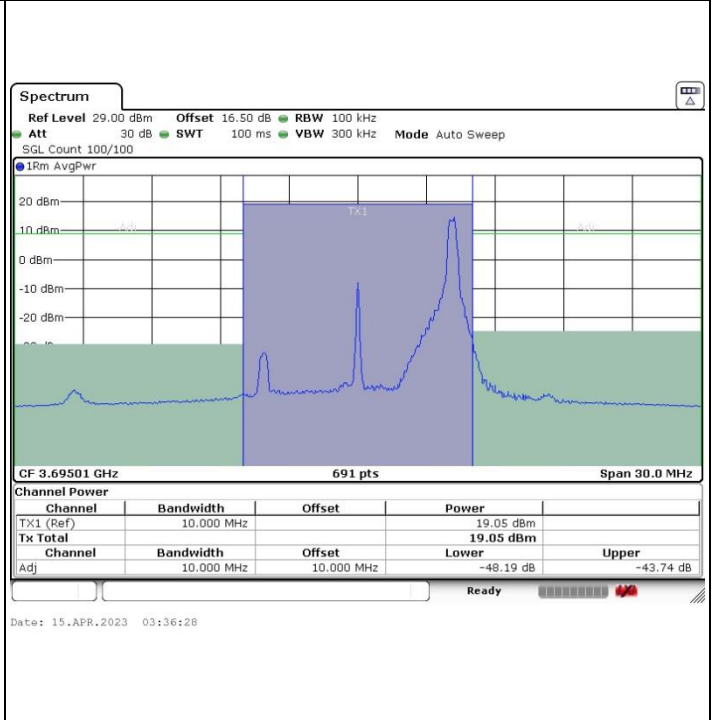
FR1 n78 / 10MHz

256QAM

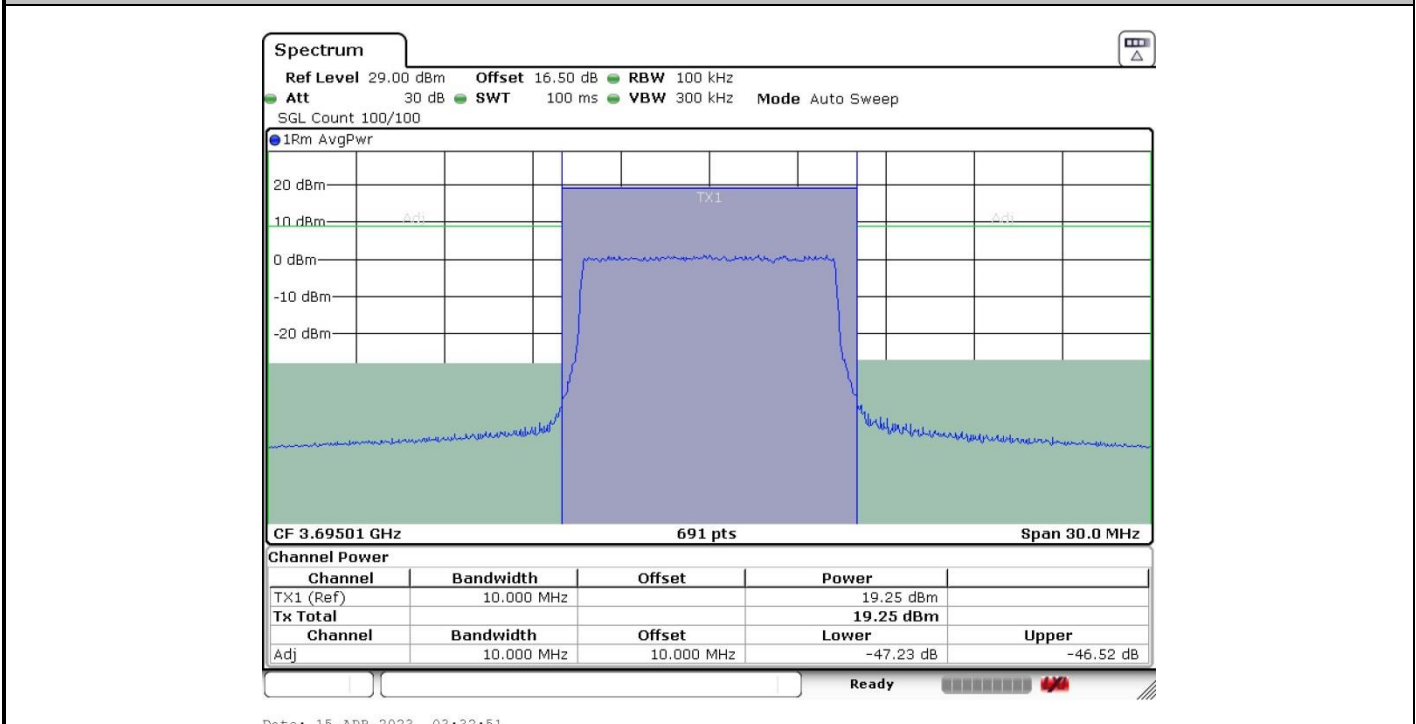
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB



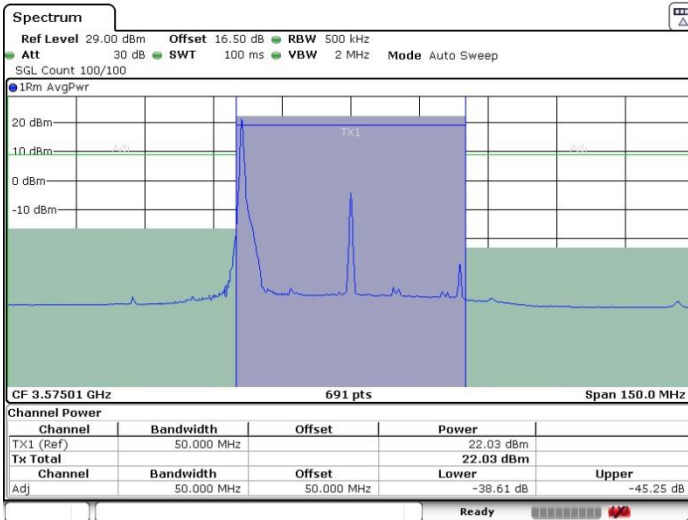


FR1 n78 / 50MHz

QPSK

Lowest Channel / 1RB0

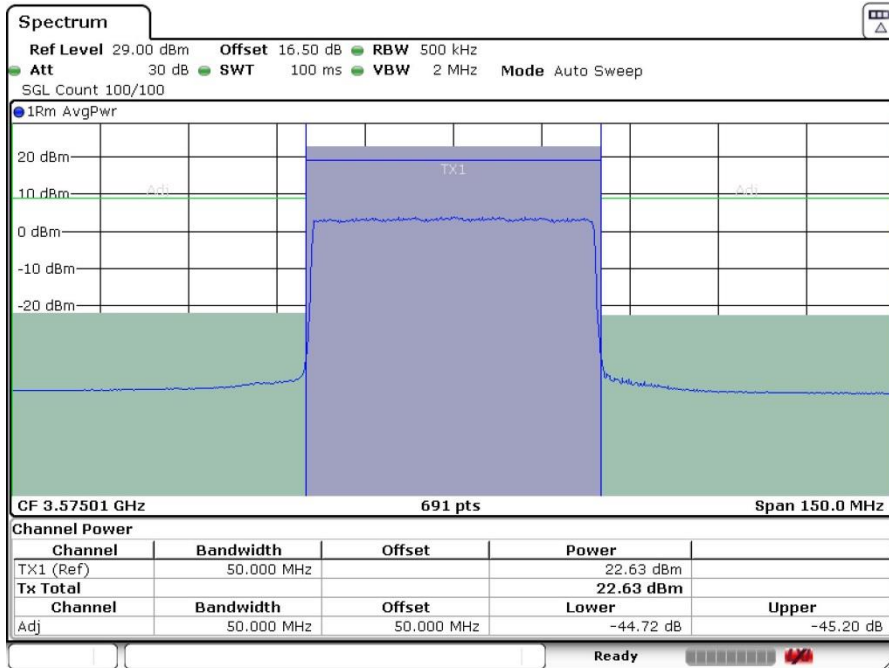
Lowest Channel / 1RBmax



Date: 15.APR.2023 03:04:36

Date: 15.APR.2023 03:00:55

Lowest Channel / FullRB



Date: 15.APR.2023 03:05:05

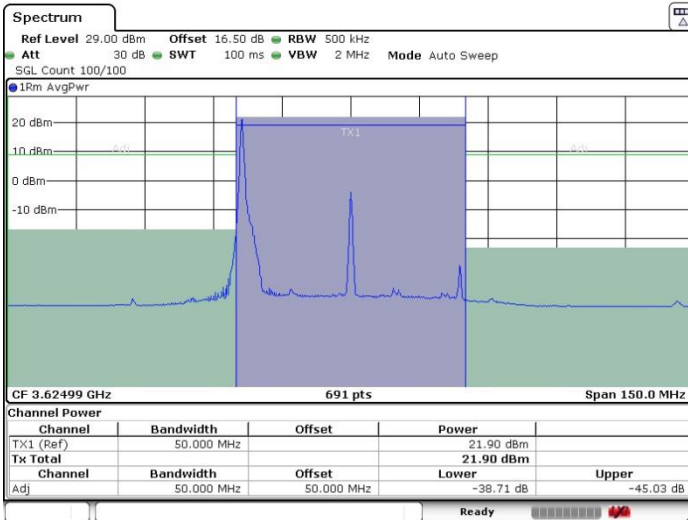


FR1 n78 / 50MHz

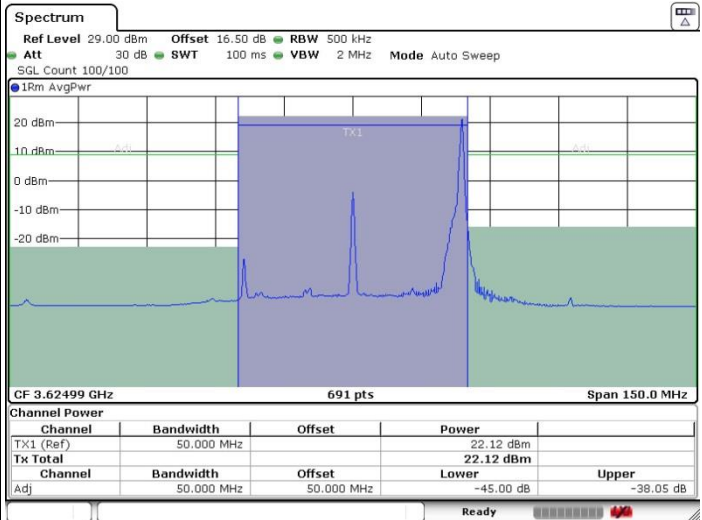
QPSK

Middle Channel / 1RB0

Middle Channel / 1RBmax

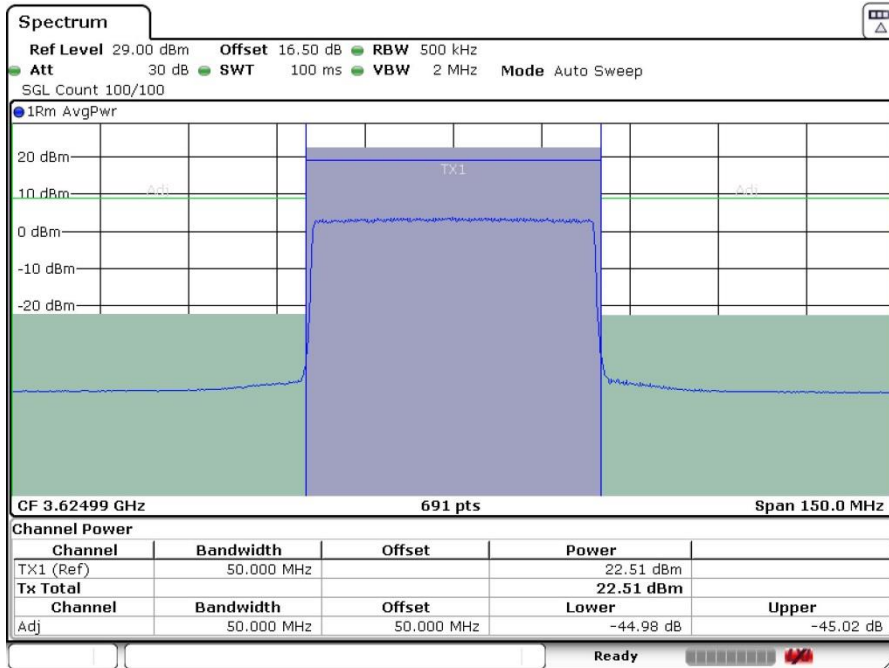


Date: 15.APR.2023 02:55:36



Date: 15.APR.2023 02:59:16

Middle Channel / FullIRB



Date: 15.APR.2023 02:55:12

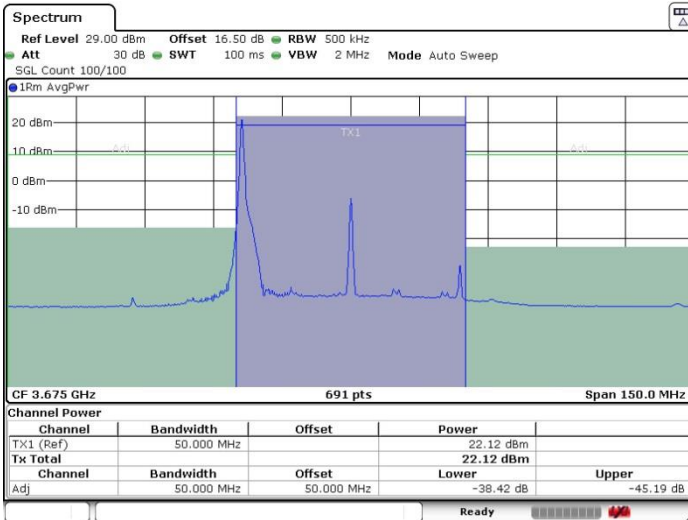


FR1 n78 / 50MHz

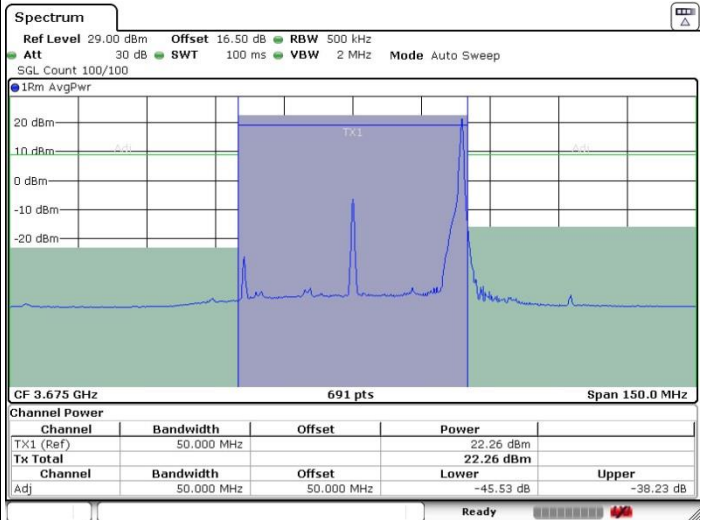
QPSK

Highest Channel / 1RB0

Highest Channel / 1RBmax

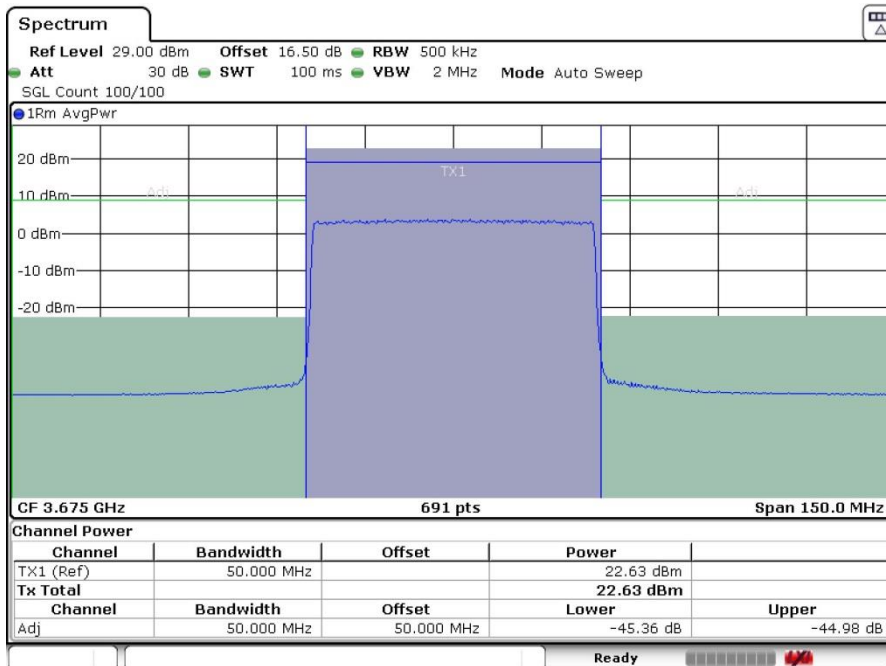


Date: 15.APR.2023 03:09:02



Date: 15.APR.2023 03:12:05

Highest Channel / FullRB



Date: 15.APR.2023 03:08:35

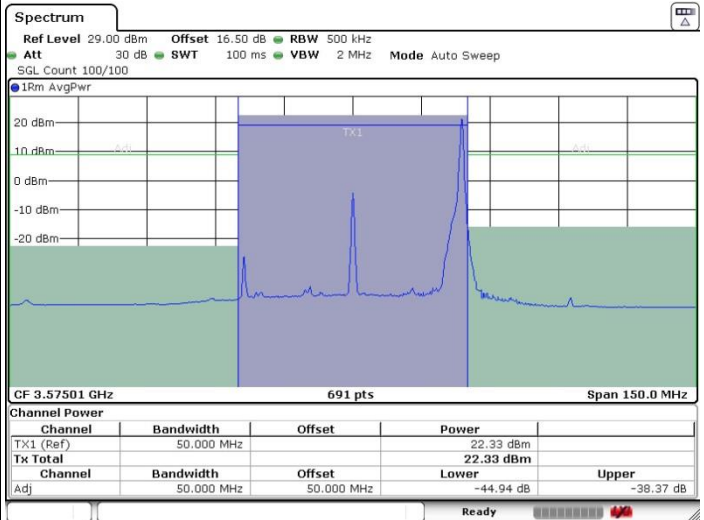
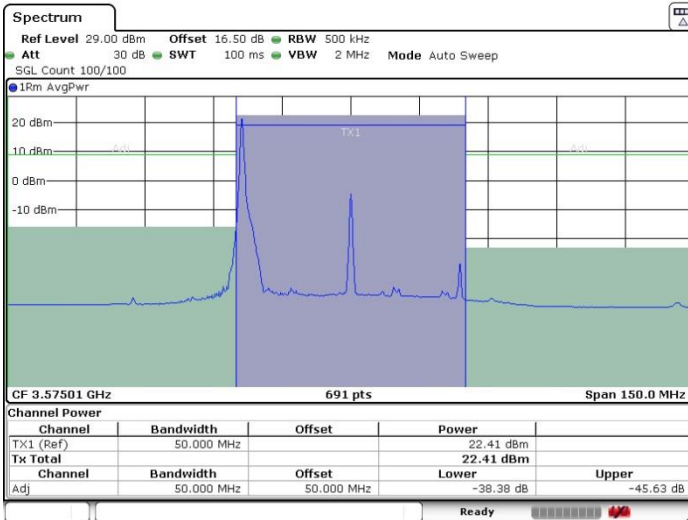


FR1 n78 / 50MHz

16QAM

Lowest Channel / 1RB0

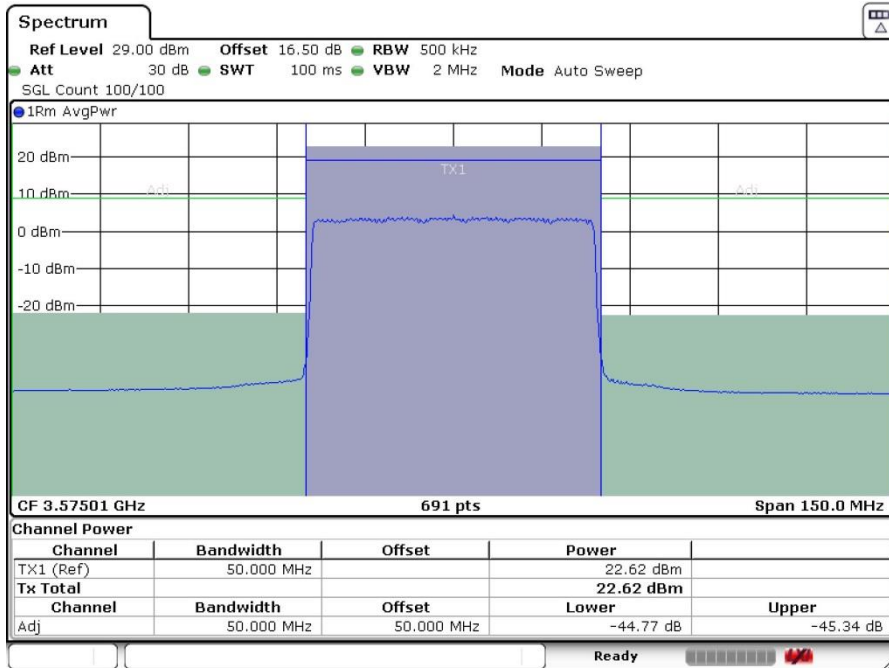
Lowest Channel / 1RBmax



Date: 15.APR.2023 03:04:09

Date: 15.APR.2023 03:02:15

Lowest Channel / FullRB



Date: 15.APR.2023 03:05:38

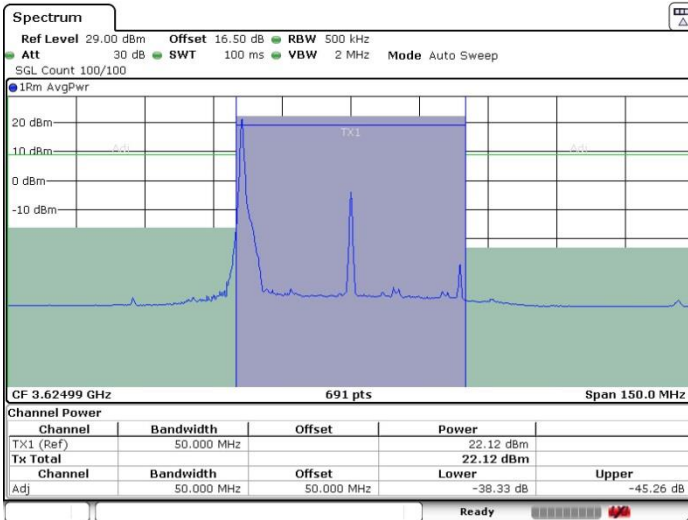


FR1 n78 / 50MHz

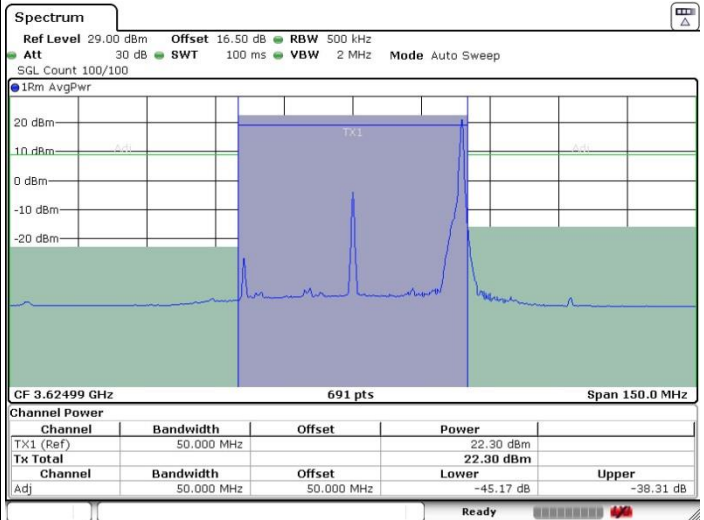
16QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

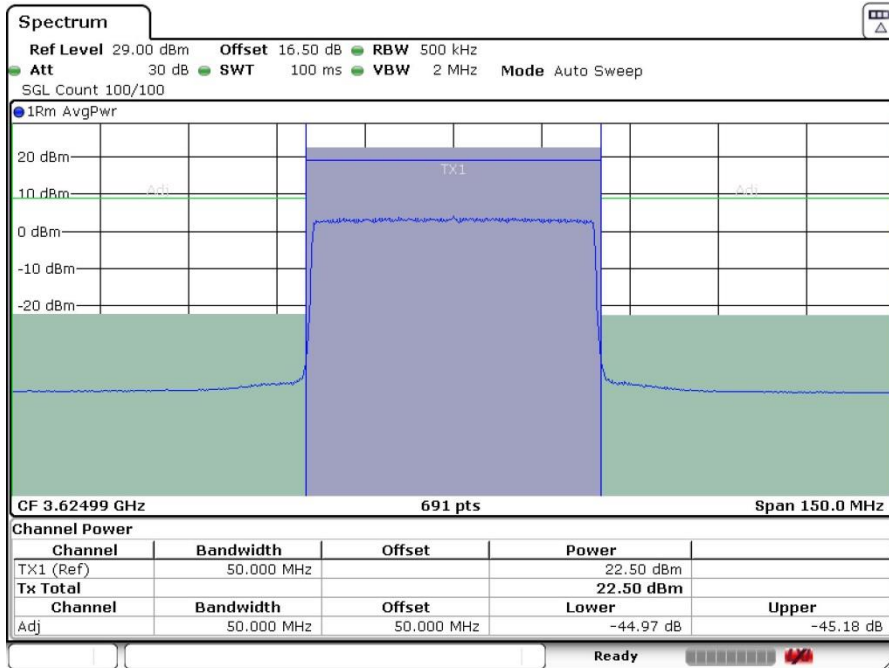


Date: 15.APR.2023 02:56:00



Date: 15.APR.2023 02:58:57

Middle Channel / FullIRB



Date: 15.APR.2023 02:54:51

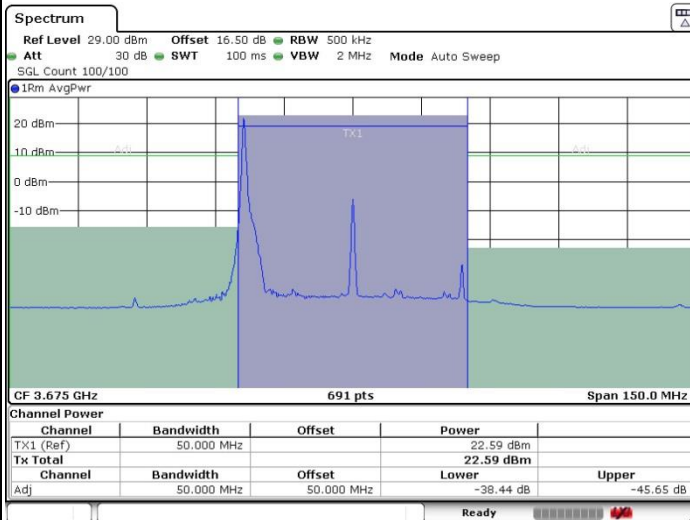


FR1 n78 / 50MHz

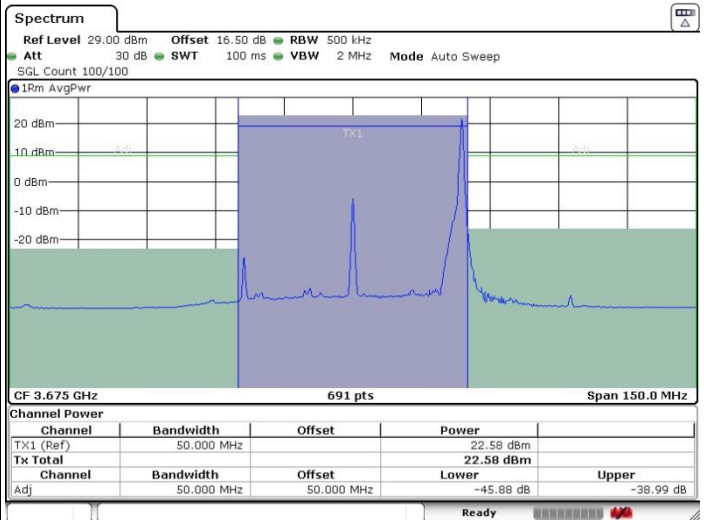
16QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

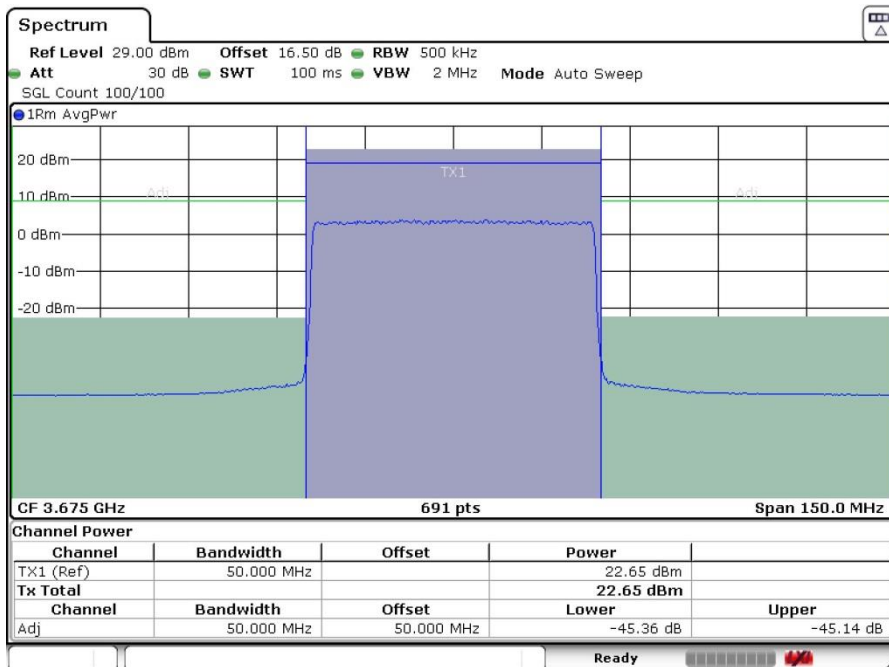


Date: 15.APR.2023 03:09:27



Date: 15.APR.2023 03:11:39

Highest Channel / FullRB



Date: 15.APR.2023 03:08:05

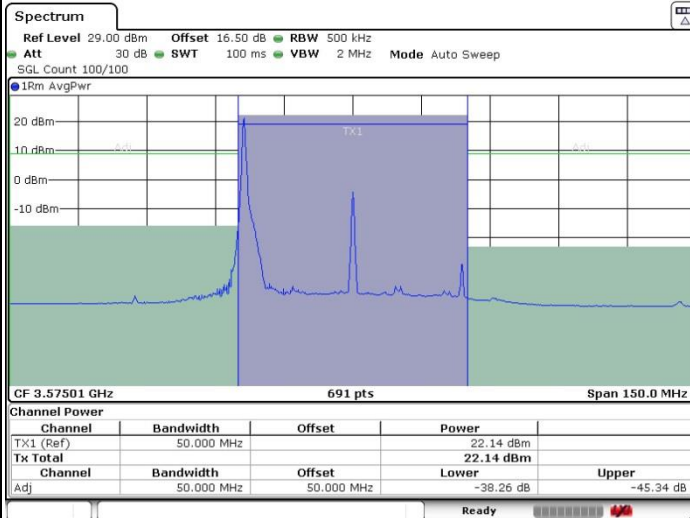


FR1 n78 / 50MHz

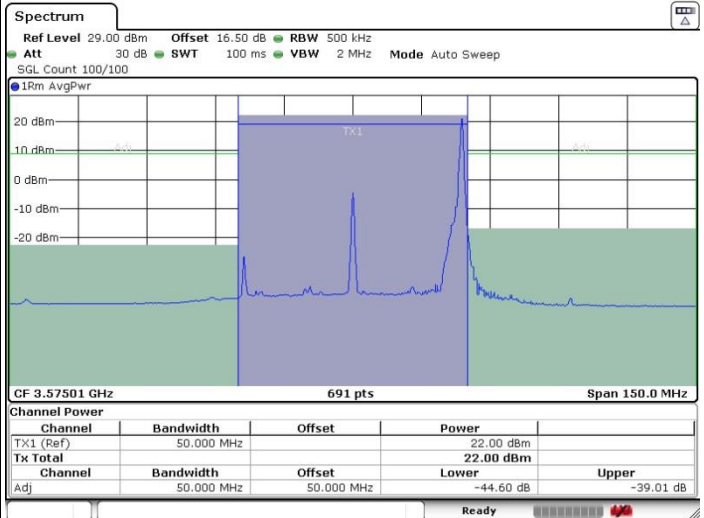
64QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

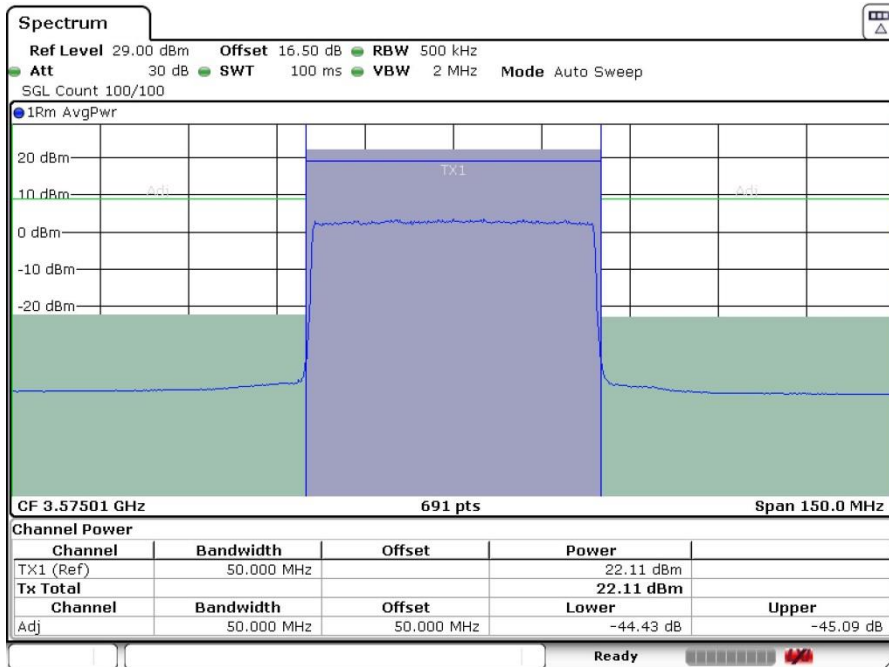


Date: 15.APR.2023 03:03:46



Date: 15.APR.2023 03:01:44

Lowest Channel / FullRB



Date: 15.APR.2023 03:06:03

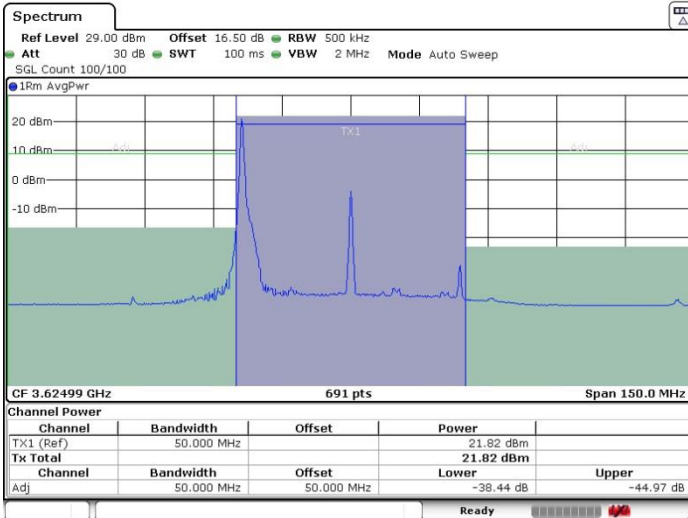


FR1 n78 / 50MHz

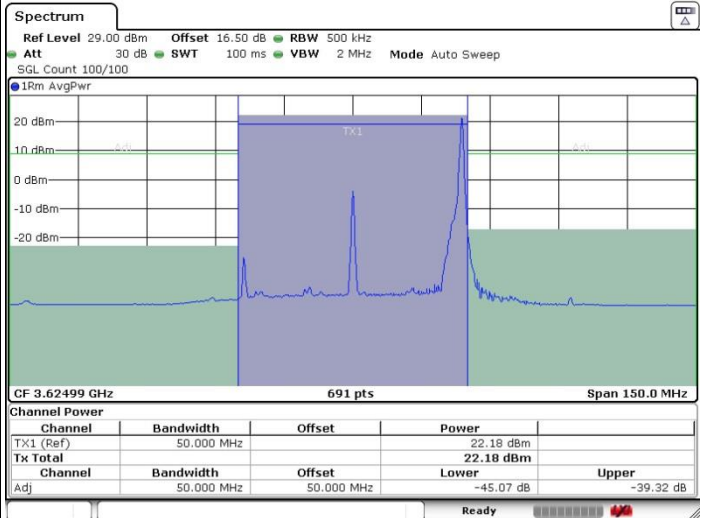
64QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

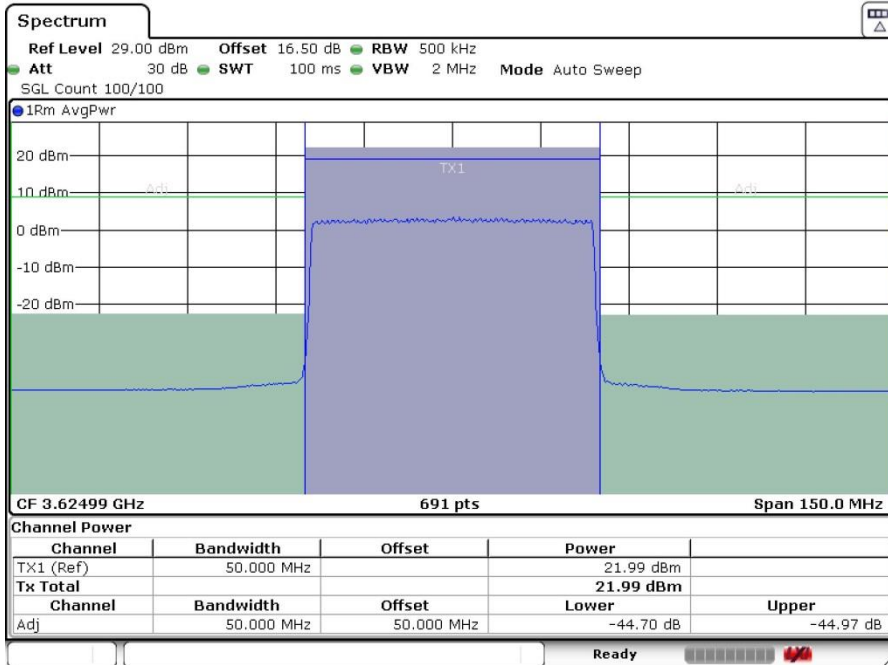


Date: 15.APR.2023 02:56:21



Date: 15.APR.2023 02:57:23

Middle Channel / FullIRB



Date: 15.APR.2023 02:54:31