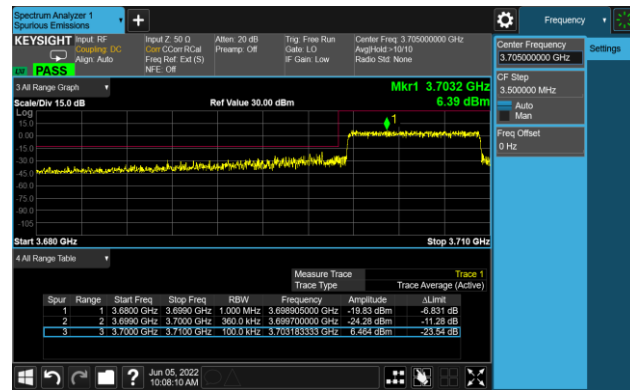
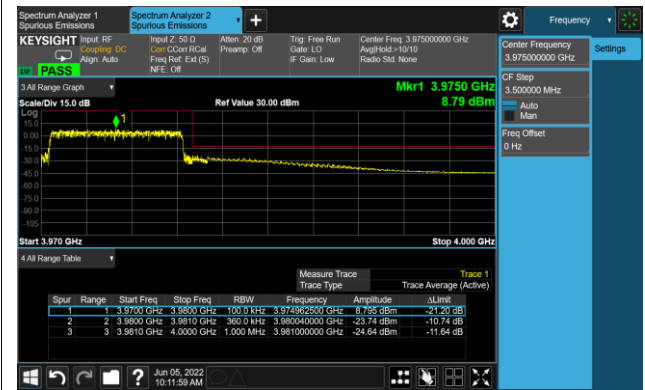


### 10MHz Channel Bandwidth - Full RB

#### Lower Band Edge

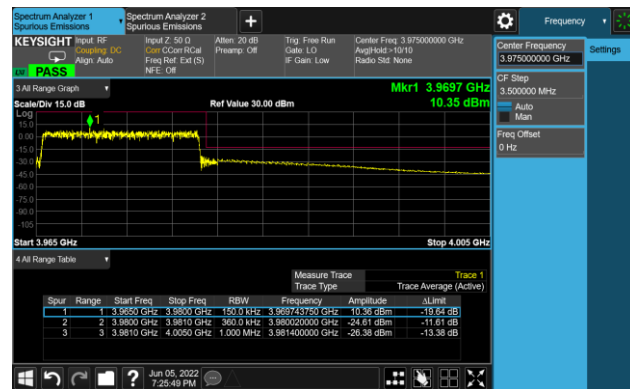


#### Upper Band Edge

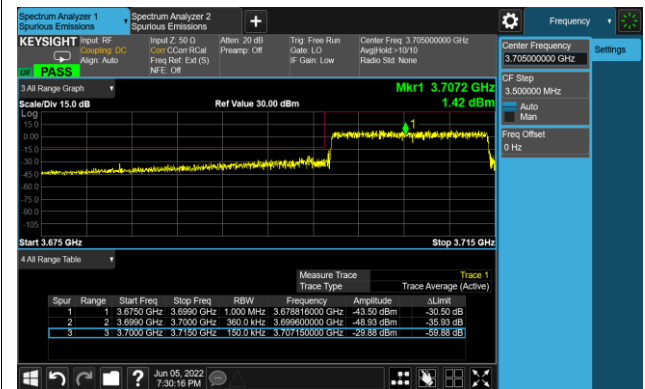


### 15MHz Channel Bandwidth - Full RB

#### Lower Band Edge

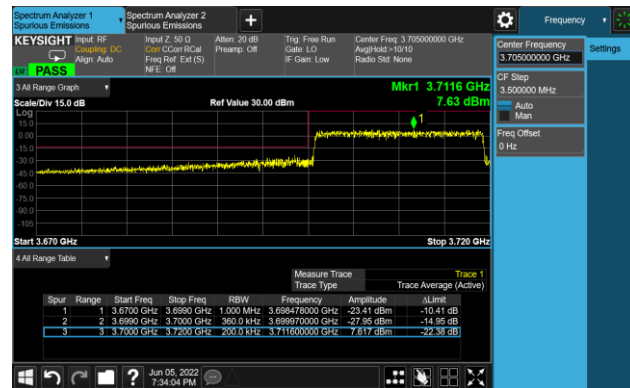


#### Upper Band Edge

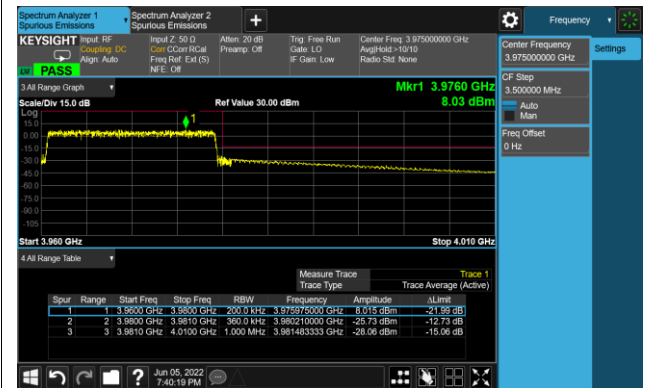


### 20MHz Channel Bandwidth - Full RB

#### Lower Band Edge

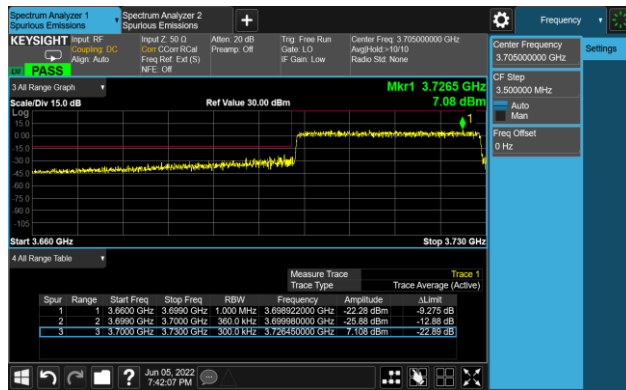


#### Upper Band Edge

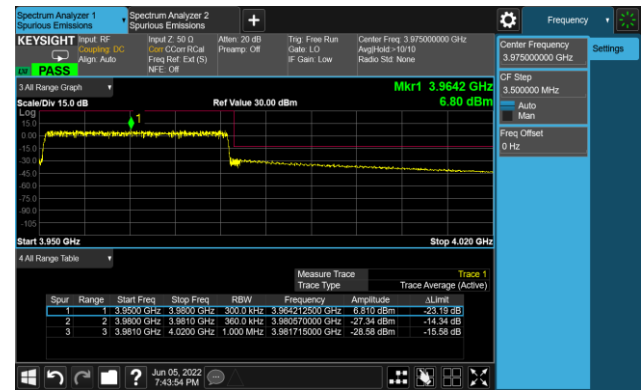


### 30MHz Channel Bandwidth - Full RB

#### Lower Band Edge

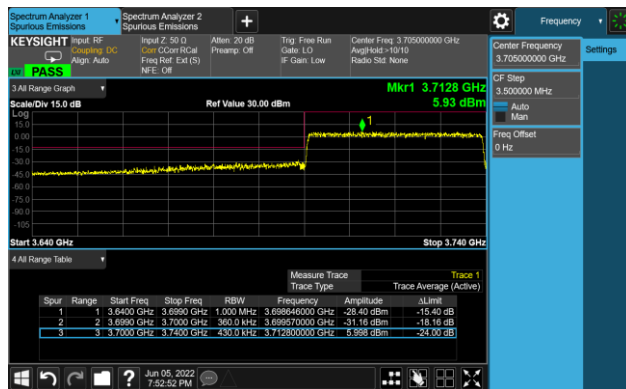


#### Upper Band Edge

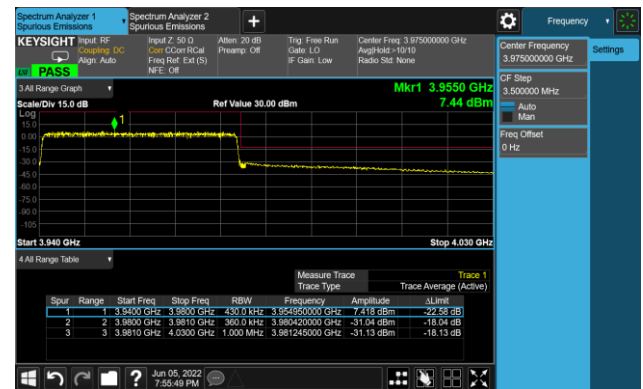


### 40MHz Channel Bandwidth - Full RB

#### Lower Band Edge

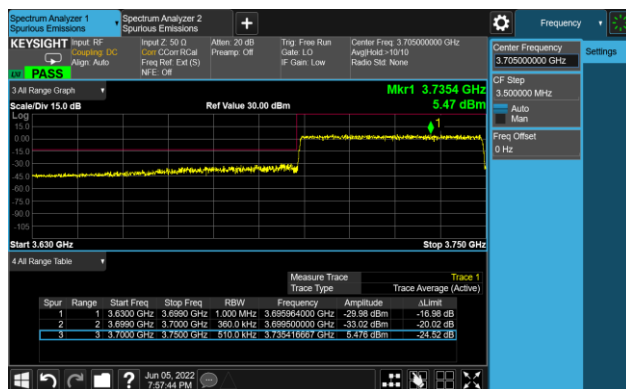


#### Upper Band Edge

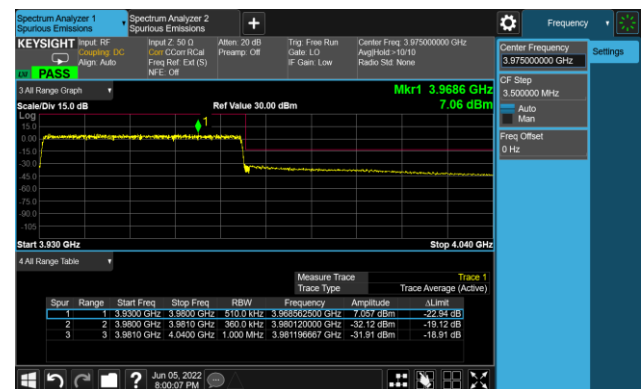


### 50MHz Channel Bandwidth - Full RB

#### Lower Band Edge

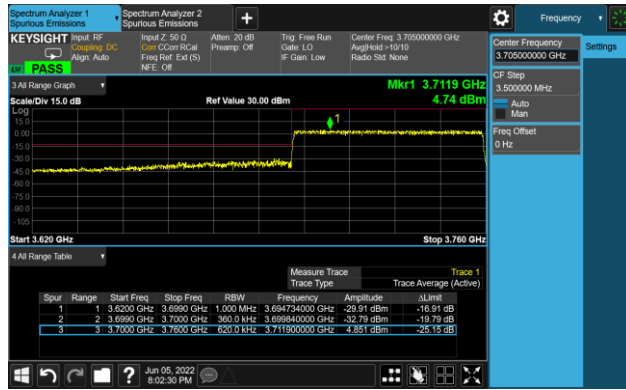


#### Upper Band Edge

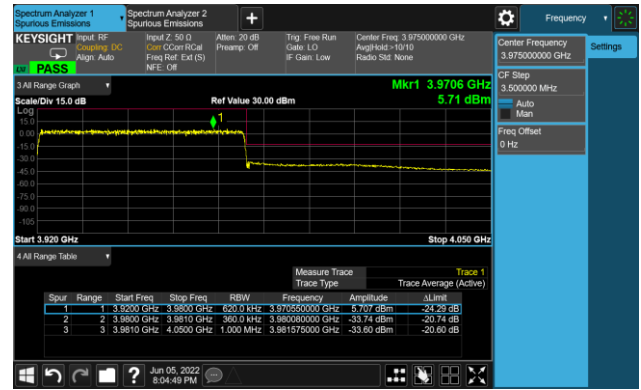


### 60MHz Channel Bandwidth - Full RB

#### Lower Band Edge

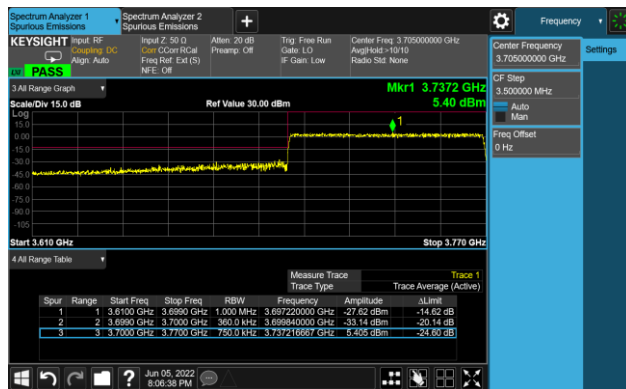


#### Upper Band Edge

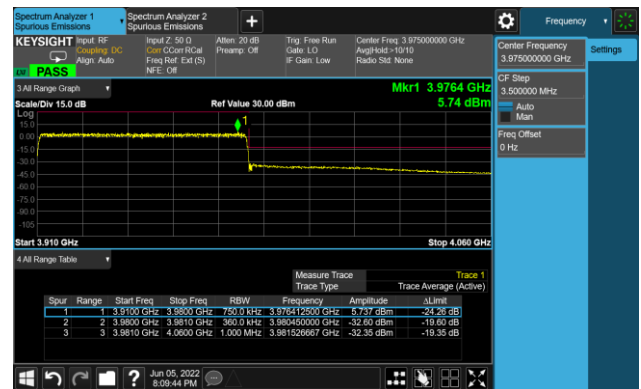


### 70MHz Channel Bandwidth - Full RB

#### Lower Band Edge

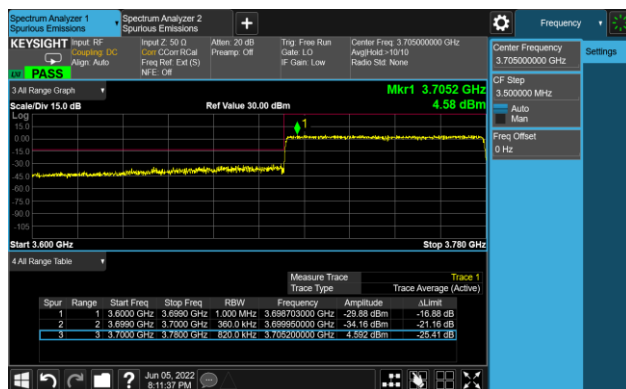


#### Upper Band Edge

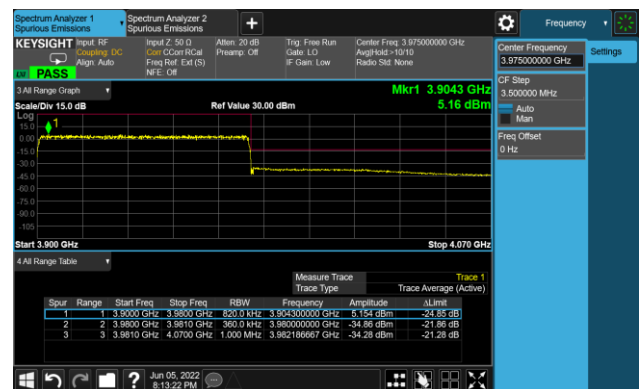


### 80MHz Channel Bandwidth - Full RB

#### Lower Band Edge



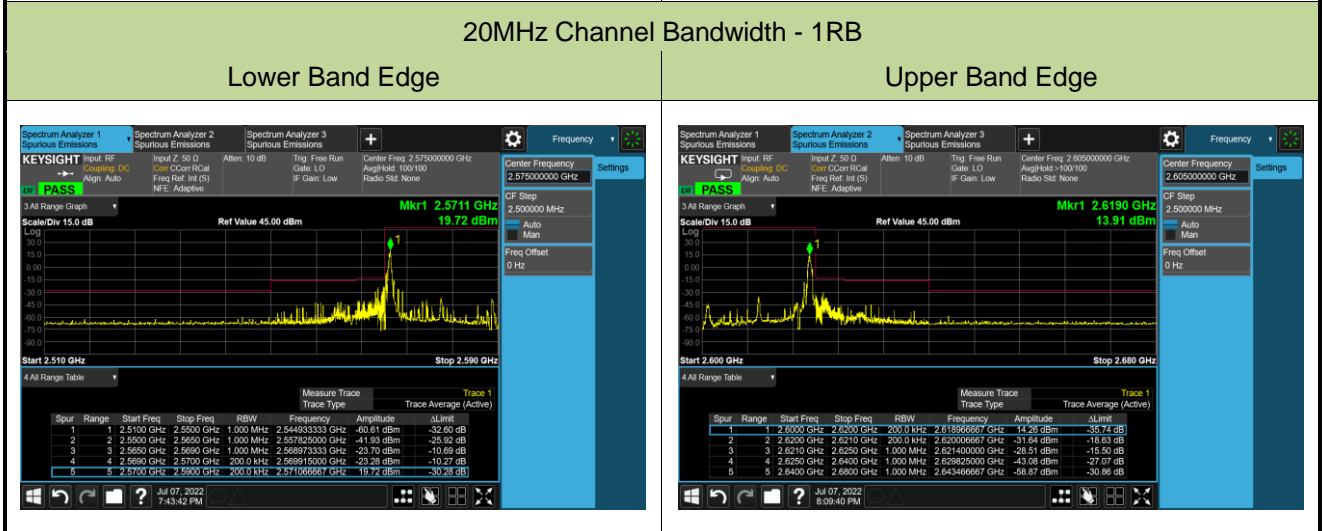
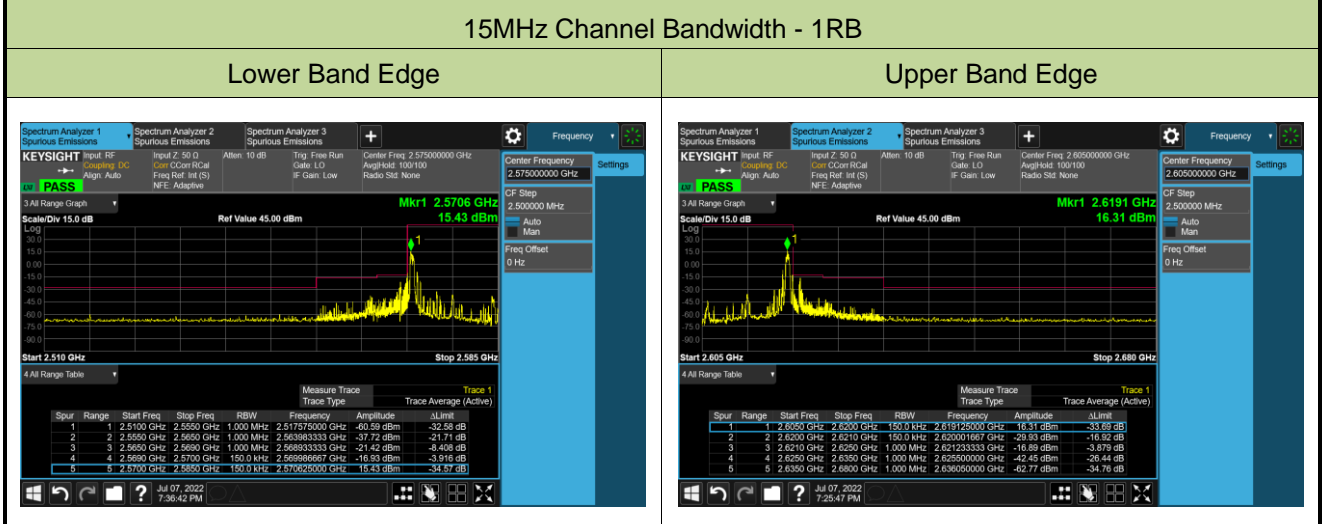
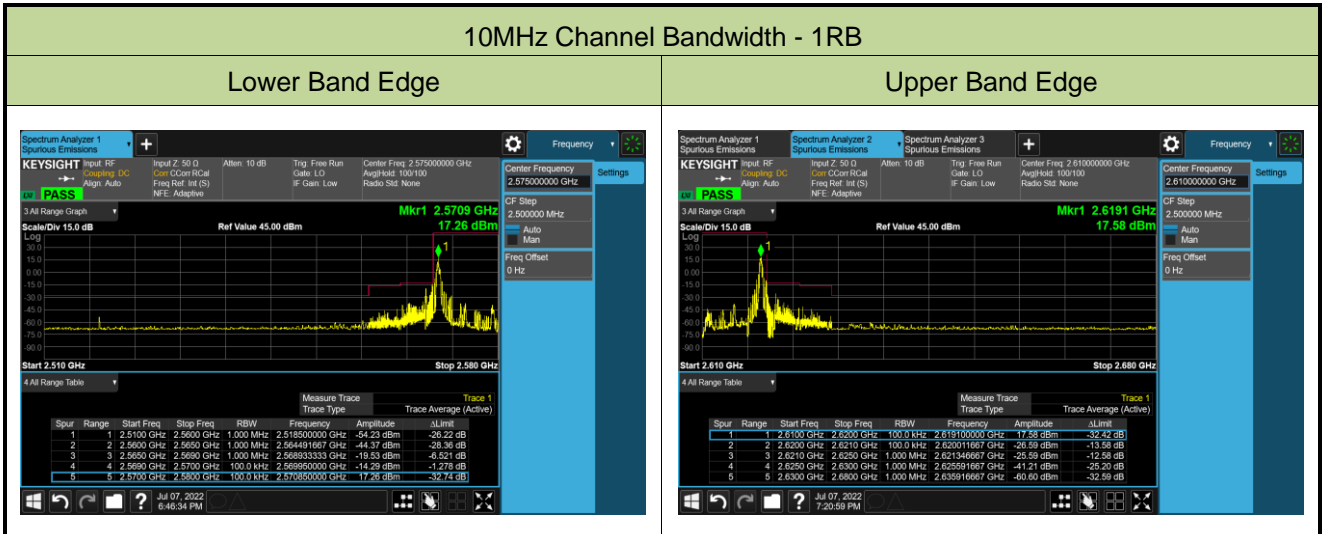
#### Upper Band Edge





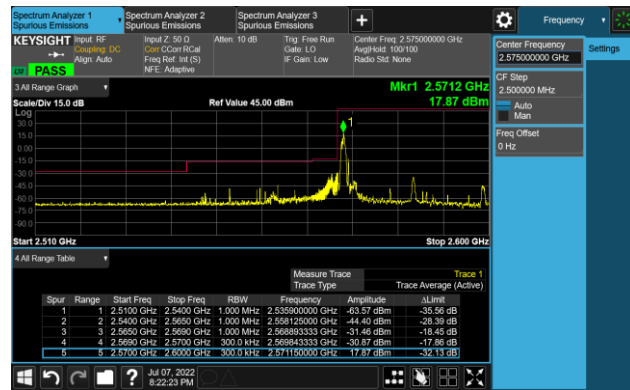
Note: “\*” means that the fail frequency has been verified by the plot of “Channel Power < 13dBm Pass”

Test Site	SIP-SR1	Test Engineer	Candy Luo
Test Date	2022/07/07	Test Band	n38_SA_HPUE (Port 1)

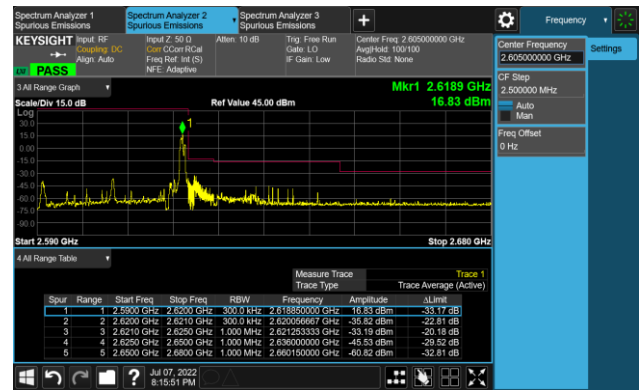


### 30MHz Channel Bandwidth - 1RB

#### Lower Band Edge

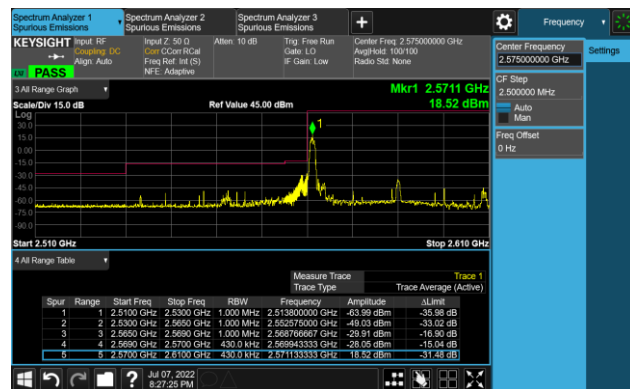


#### Upper Band Edge

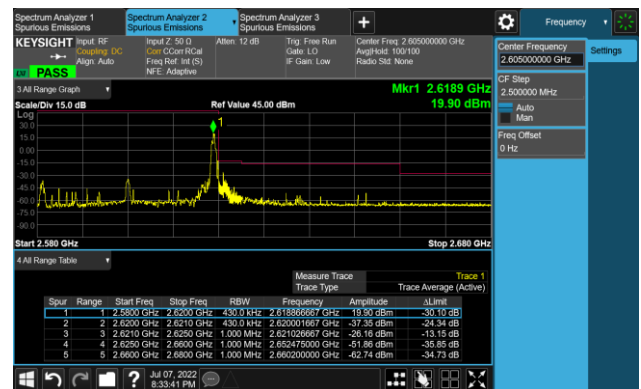


### 40MHz Channel Bandwidth - 1RB

#### Lower Band Edge

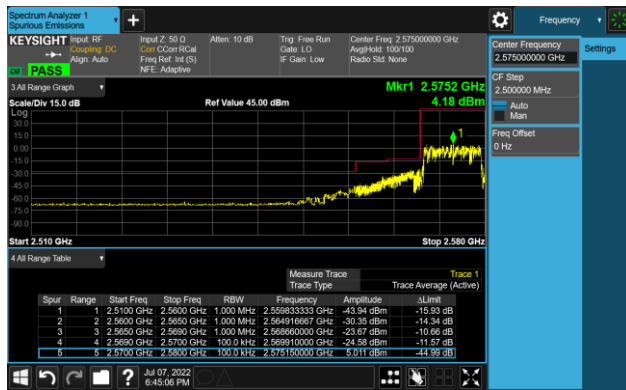


#### Upper Band Edge

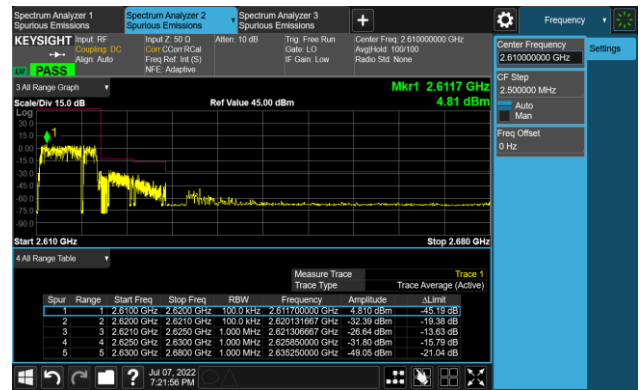


### 10MHz Channel Bandwidth - Full RB

#### Lower Band Edge

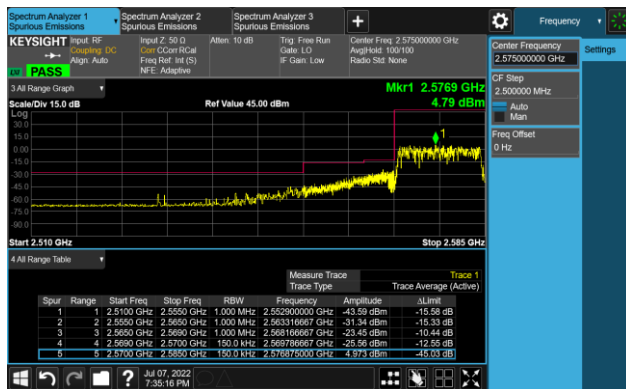


#### Upper Band Edge

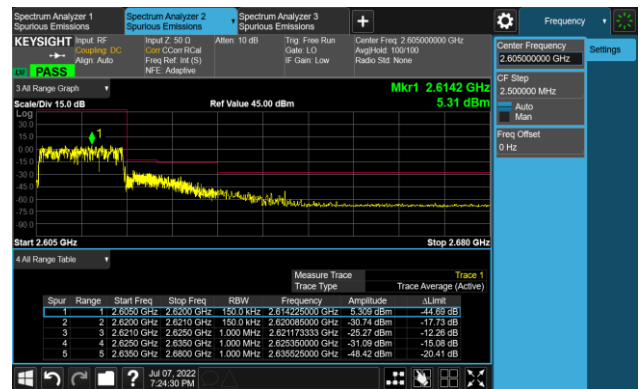


### 15MHz Channel Bandwidth - Full RB

#### Lower Band Edge

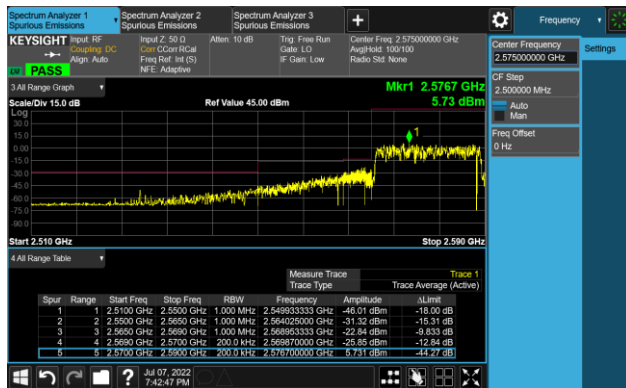


#### Upper Band Edge

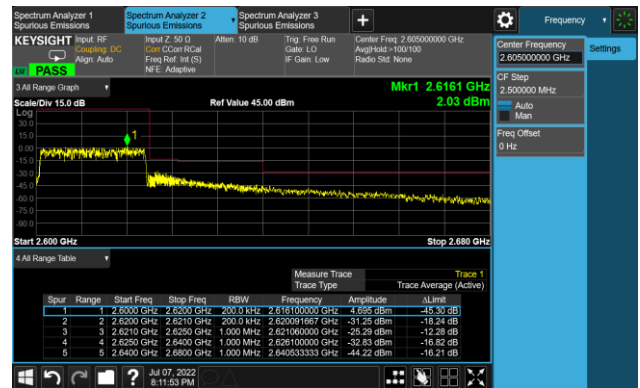


### 20MHz Channel Bandwidth - Full RB

#### Lower Band Edge

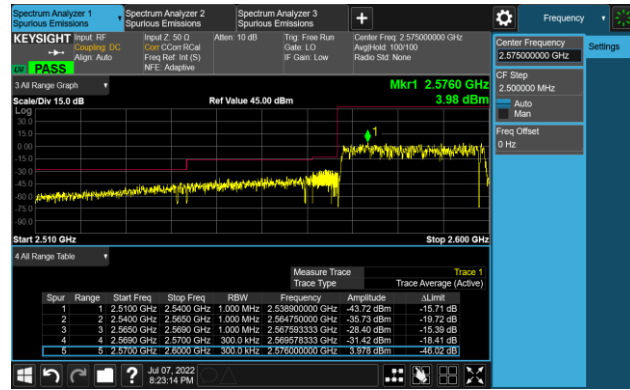


#### Upper Band Edge

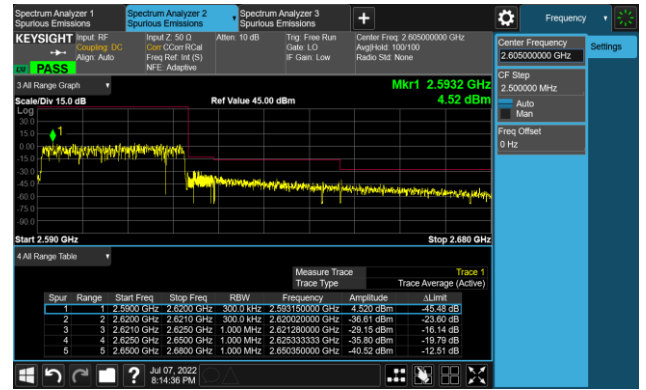


### 30MHz Channel Bandwidth - Full RB

#### Lower Band Edge

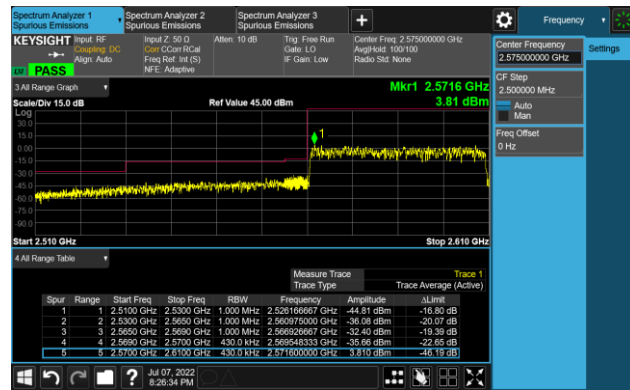


#### Upper Band Edge

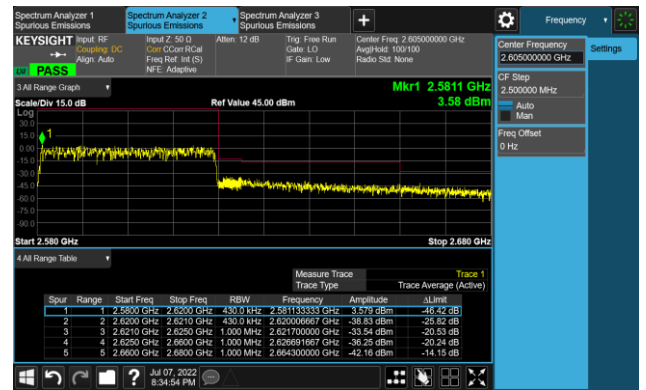


### 40MHz Channel Bandwidth - Full RB

#### Lower Band Edge



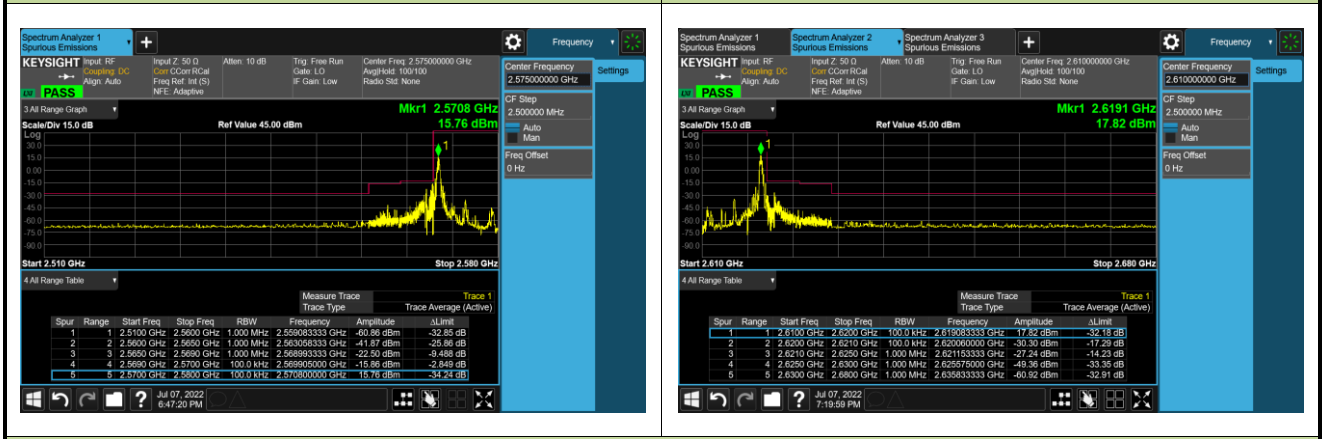
#### Upper Band Edge



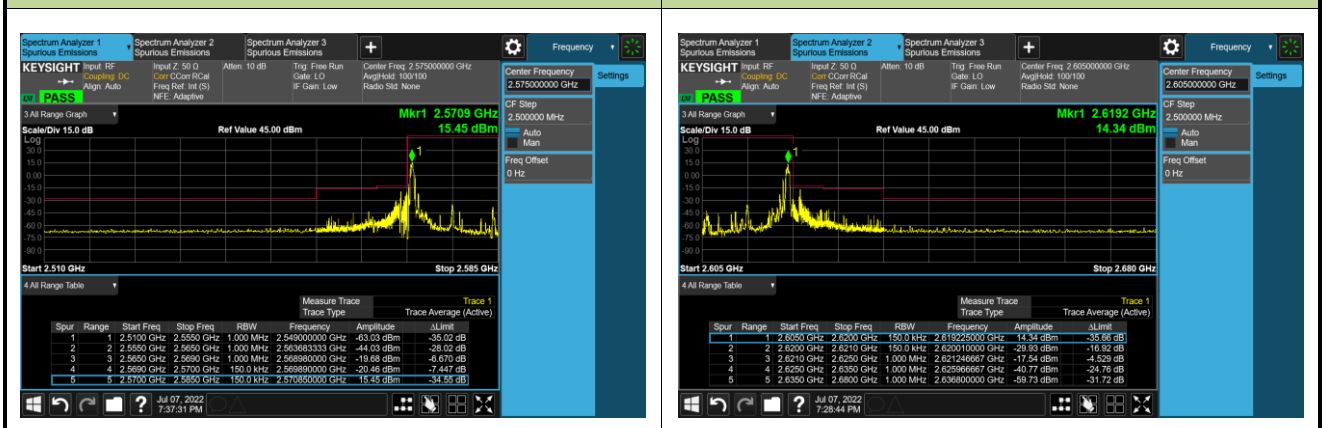


Test Site	SIP-SR1	Test Engineer	Candy Luo
Test Date	2022/07/07	Test Band	n38_SA_HPUE (Port 2)

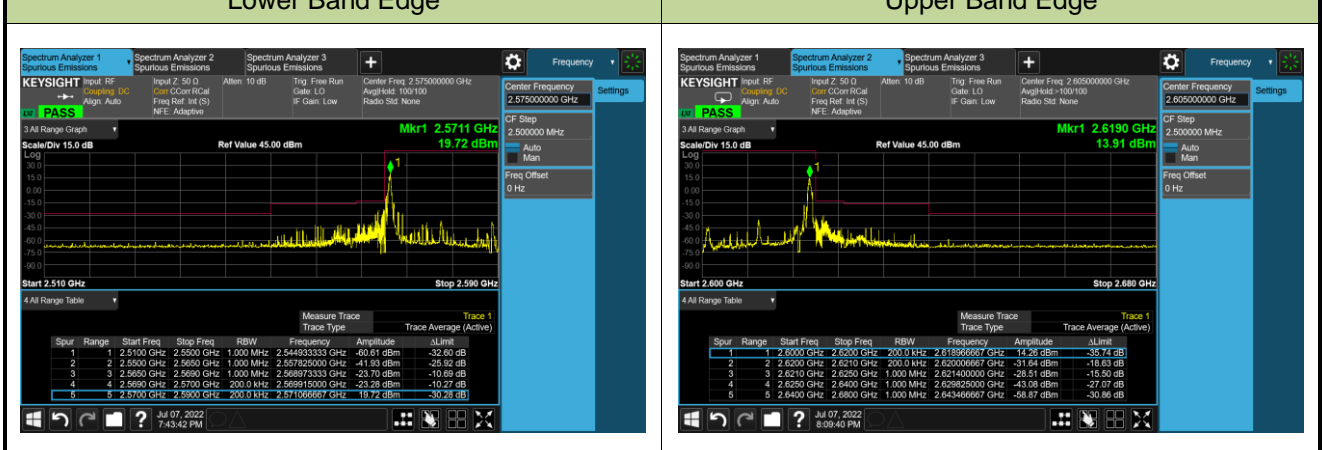
**10MHz Channel Bandwidth - 1RB**



**15MHz Channel Bandwidth - 1RB**

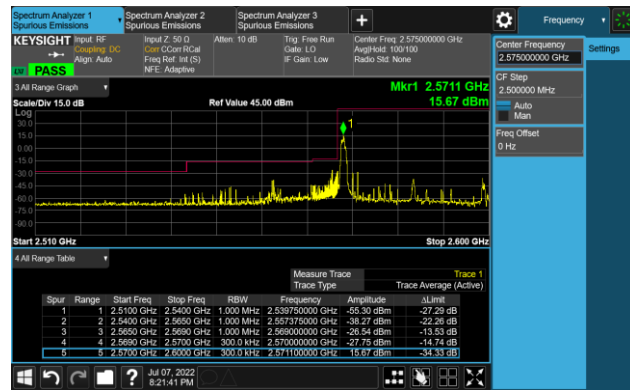


**20MHz Channel Bandwidth - 1RB**

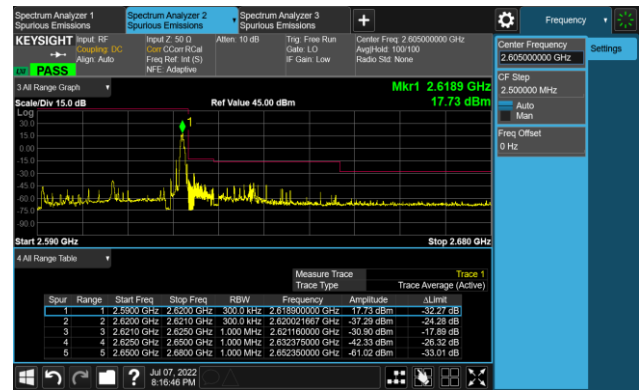


### 30MHz Channel Bandwidth - 1RB

#### Lower Band Edge

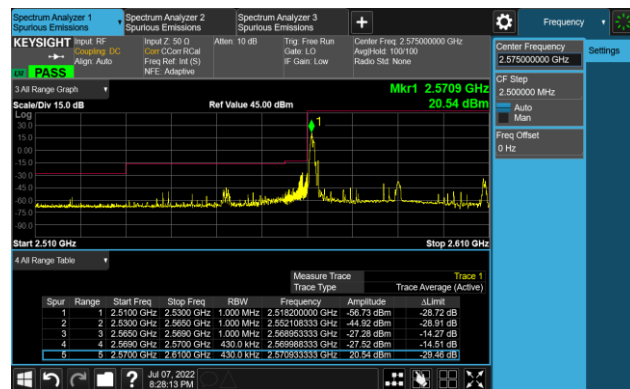


#### Upper Band Edge

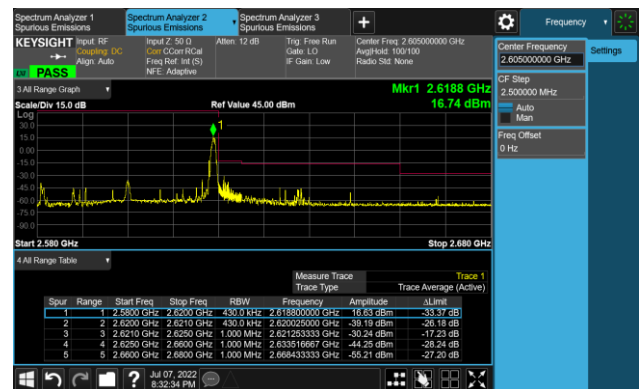


### 40MHz Channel Bandwidth - 1RB

#### Lower Band Edge

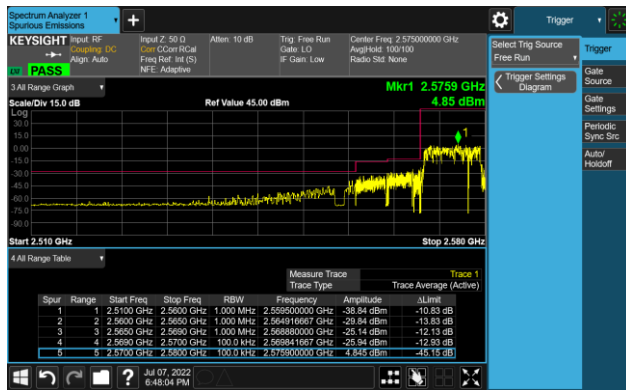


#### Upper Band Edge

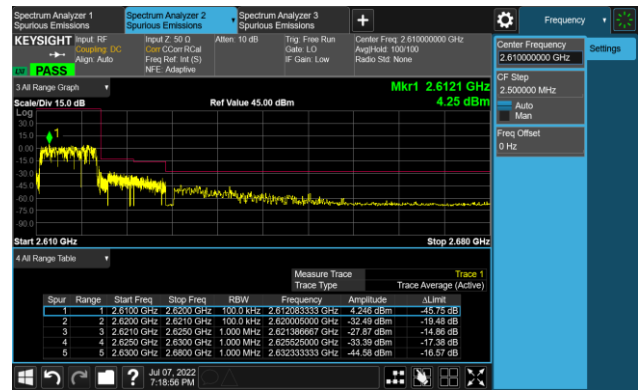


### 10MHz Channel Bandwidth - Full RB

#### Lower Band Edge

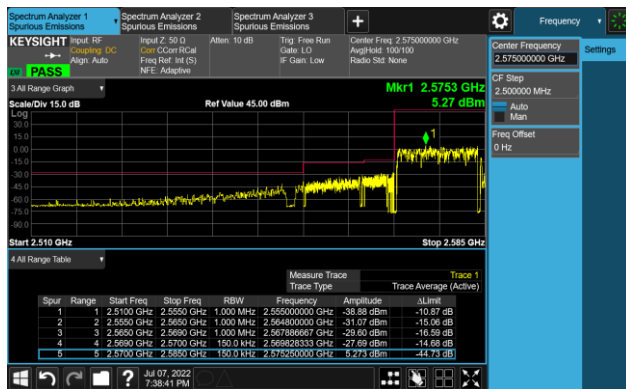


#### Upper Band Edge

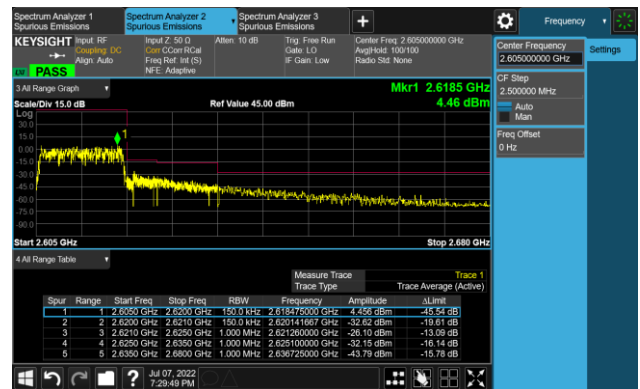


### 15MHz Channel Bandwidth - Full RB

#### Lower Band Edge

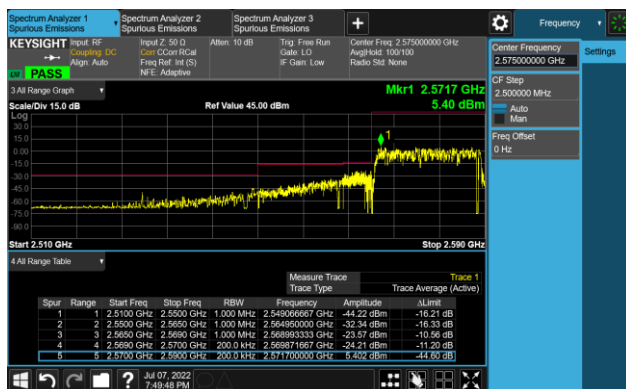


#### Upper Band Edge

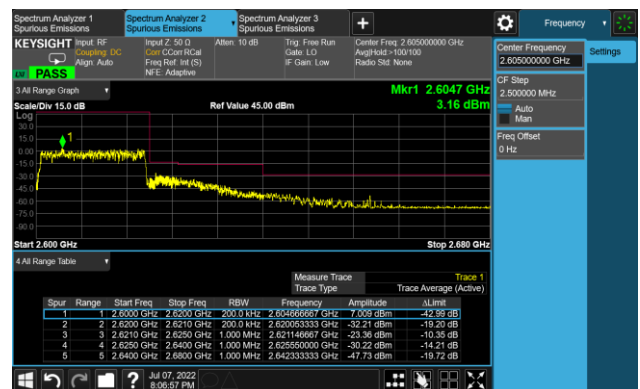


### 20MHz Channel Bandwidth - Full RB

#### Lower Band Edge

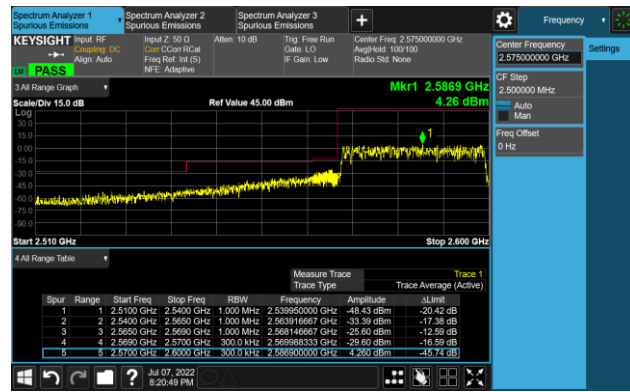


#### Upper Band Edge

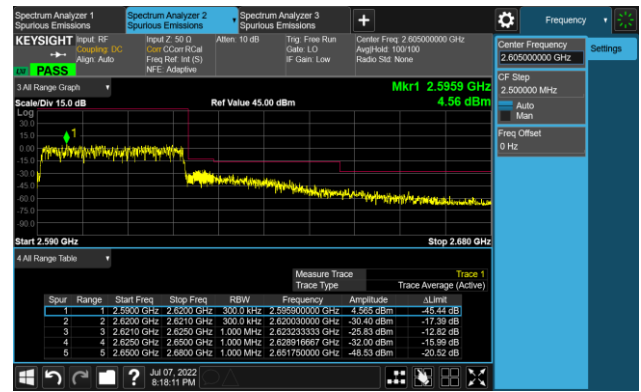


### 30MHz Channel Bandwidth - Full RB

#### Lower Band Edge

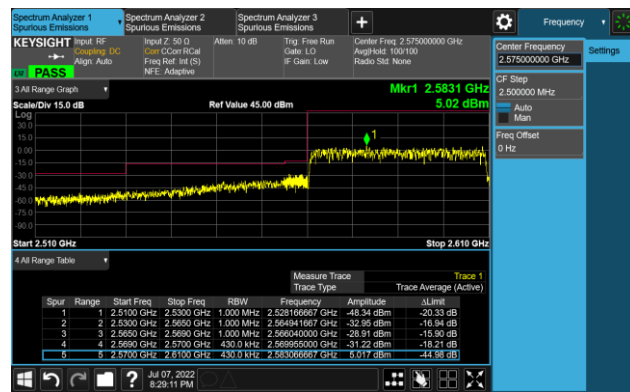


#### Upper Band Edge

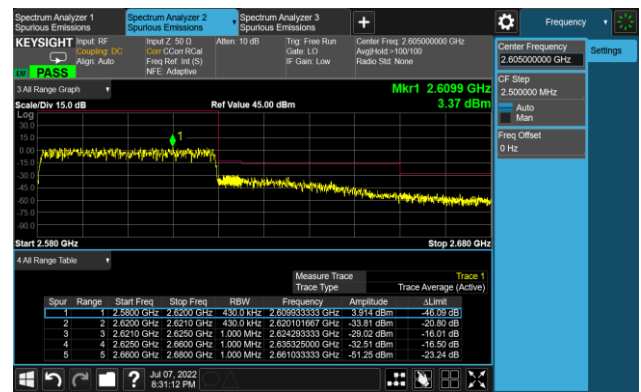


### 40MHz Channel Bandwidth - Full RB

#### Lower Band Edge

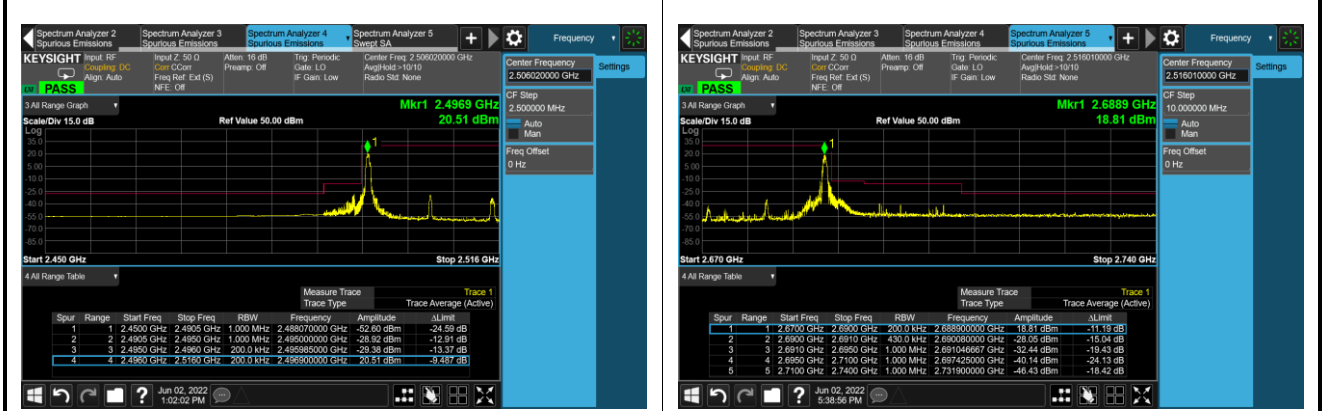


#### Upper Band Edge

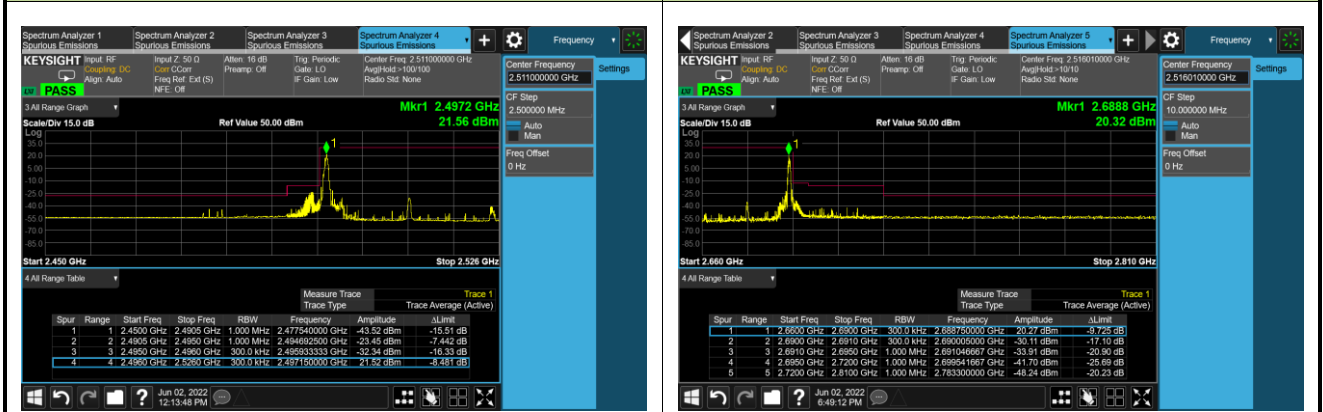


Test Site	WZ-SR6	Test Engineer	Cloud Guo
Test Date	2022/06/02	Test Band	n41_MIMO_HPUE (Port 1)

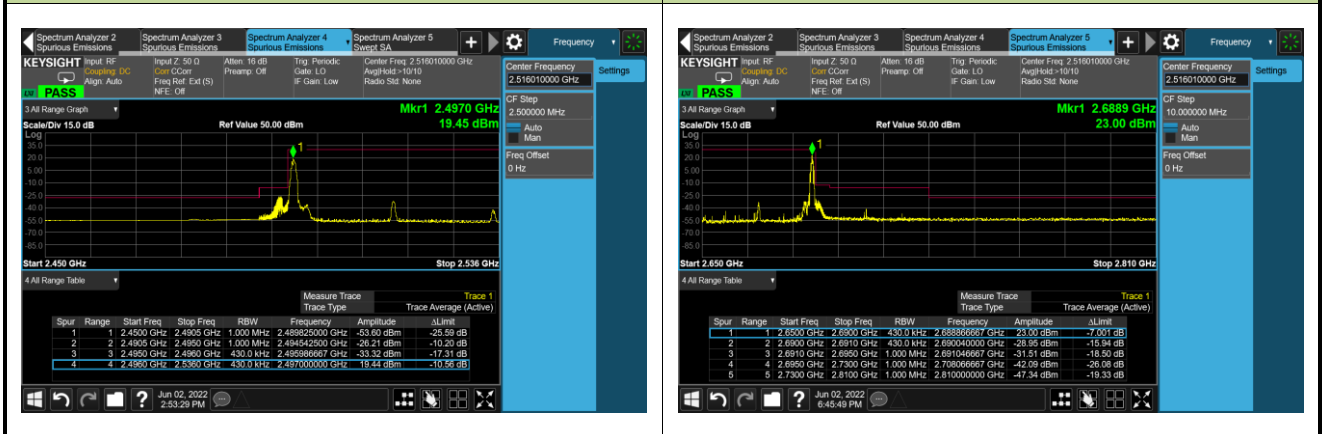
### 20MHz Channel Bandwidth - 1RB



### 30MHz Channel Bandwidth - 1RB

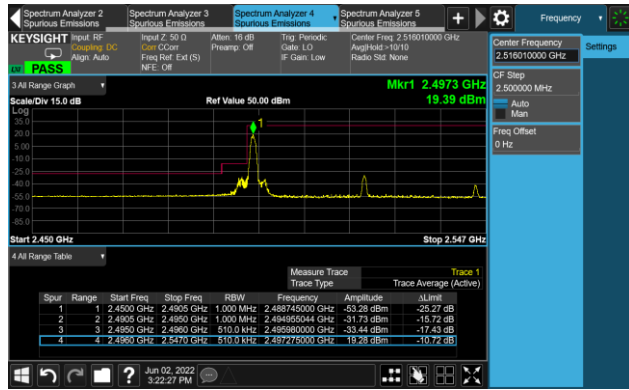


### 40MHz Channel Bandwidth - 1RB

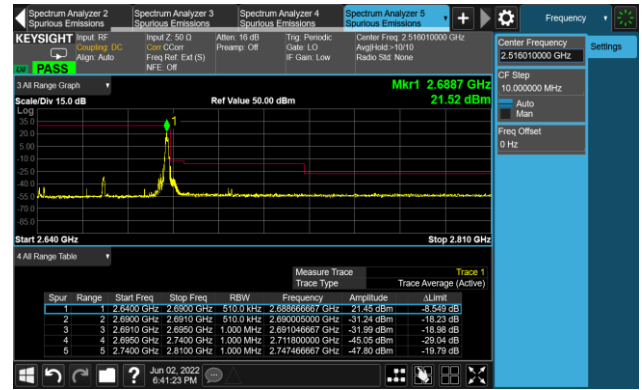


### 50MHz Channel Bandwidth - 1RB

#### Lower Band Edge

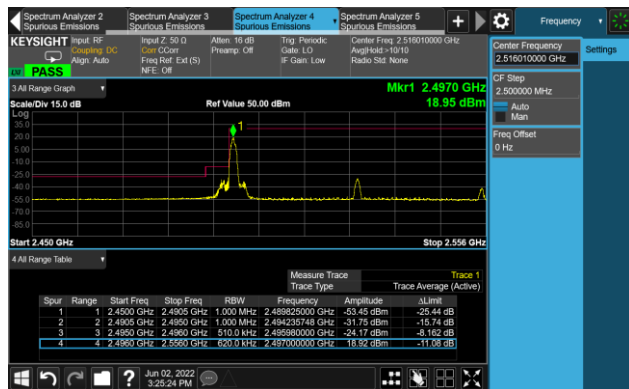


#### Upper Band Edge

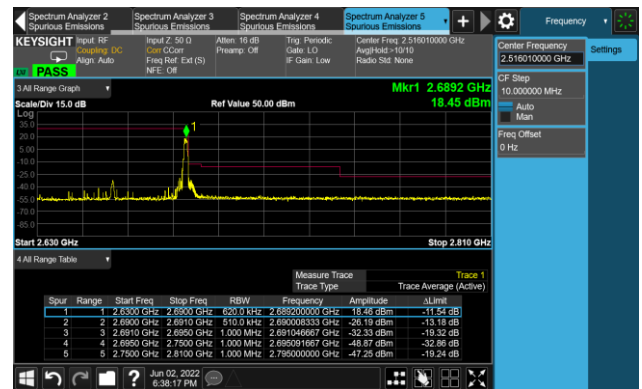


### 60MHz Channel Bandwidth - 1RB

#### Lower Band Edge

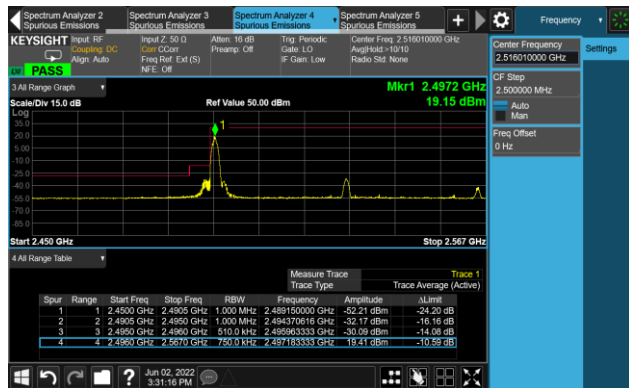


#### Upper Band Edge



### 70MHz Channel Bandwidth - 1RB

#### Lower Band Edge



#### Upper Band Edge

