

## 2.4G-01T & 2.4G-03R System Description

1. 2.4G frequency is oscillated on 26MHz and generated by frequency multiplier, the phase-locking technology is adopted.

2. This transmitter include 2.4G-01T-IC2 & 2.4G-03R-IC1, Receiver include 2.4G-03R-IC2 & 2.4G-03R-IC1.

3. The TX system generates precise packet timing at approx. every 20ms. Each data packet includes 16-bit preamble bits, and 16-bit sync word and 8-bit length and 8-bit address and 8\*8 bits data and 16 bits CRC.

4. If there is signals interrupt halfway, the receiver would search for the transmitter signals on the assigned three points.

5. The address would be stored in the system after automatic code pairing, it will work normally after power on or reset.