

- 01 Cover sheet
- 02 BLOCK_DIAGRAM
- 03 SMBUS_&_IRQ_ROUTING
- 04 POWER_ON_SEQUENCE
- 05 POWER_Block
- 06 POWER_BUDGET
- 07 POWER_SEQUENCE
- 08 CLOCK_GEN
- 09 CPU1
- 10 CPU2
- 11 NB1
- 12 NB2
- 13 NB3
- 14 NB4
- 15 NB5
- 16 NB6
- 17 DDR2_SODIMMO
- 18 DDR2_TEMINATION
- 19 LCD_CON
- 20 CRT
- 21 ICH7M1
- 22 ICH7N2
- 23 ICH7M3
- 24 ICH7M4
- 25 USB_PORT
- 26 HDD
- 27 MINICARD
- 28 MODEM CON
- 29 LAN
- 30 RICHO 1394
- 31 RICHO CARDBUS
- 32 ALC260 CODEC
- 33 AUDIO PA@JACK
- 34 RTC POWER
- 35 KBC_1
- 36 CPU FAN
- 37 ACIN
- 38 PWR_Charger
- 39 PWR_System
- 40 PWR_DDR2
- 41 PWR_VCCP&VCC_GMCH
- 42 PWR_VCore
- 43 S3_S4_CNTR&Discharge
- 44 System_PWRGD
- 45 ME
- 46 PCMCIA
- 47 KBC_CON
- 48 DAUGHTER BOARD
- 49 BAT CON
- 50 TPM
- 51 BULETOOTH

INTEL LCBN Platform S101


Version :2.0

Drawing by :Matto huang

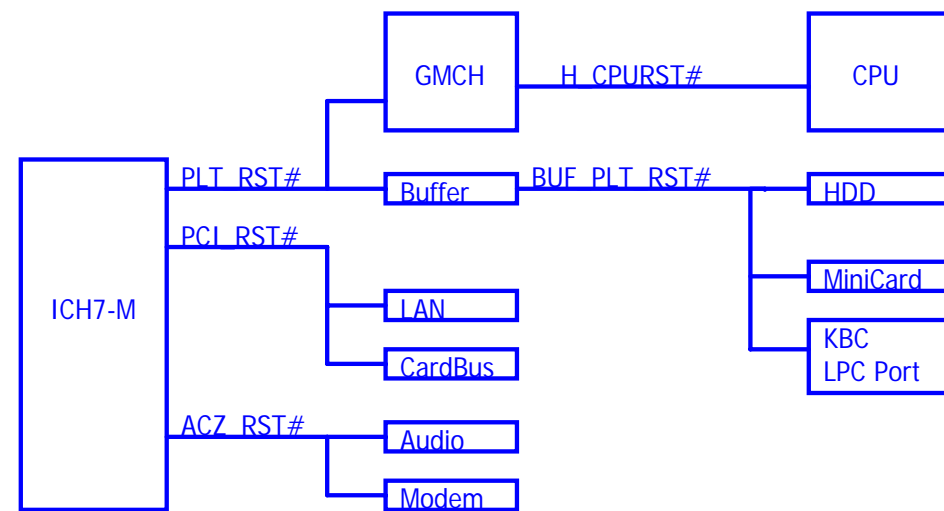
Notes:

Part Value Prefix : "POIP=NA" means nopop
 Net Value postfix : "#" means Low Active

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	BYD COMPANY LIMITED
<small>Page Name</small>	<small>Mobile Name</small>
Cover sheet	S101
<small>Date: Wednesday, March 11, 2009</small>	<small>Rev V2.0</small>
<small>Sheet 01 of 43</small>	

RESET TOPOLOGY



PCI Device:

REQ0: X
 REQ1: PCMCIA
 REQ2: LAN
 REQ3: X

PIRQA#:RTL8100CL/8110SBL
 PIRQB#: NC
 PIRQC#: MINI PCI
 PIRQD#: MINI PCI (Function0)
 PIRQE#: R5C841 (Function0)
 PIRQF#: R5C841
 PIRQG#: R5C841
 PIRQH#: Internal USB

AD18:LAN
 AD22:MINIPCI
 AD25:R5C841

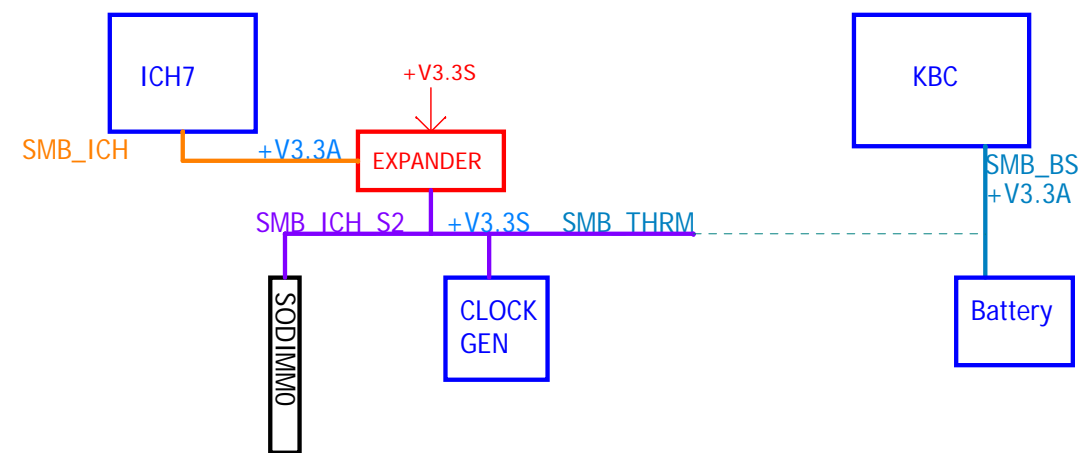
SMBUS ADDRESS:

Device:	Address	Hex	BUS
Clock Gen	1101001x	D2/D3	SMB_ICH_S2
SODIMM0	1010000x	A0	SMB_ICH_S2
SODIMM1	1010010x	A4	SMB_ICH_S2
Smart Battery			SMB_BS

Notes:
 First address is for a write command and second is for a read command

Buses labeled SMB_ICH_xx come out of ICH, Via an I2C expander.
 The rest come out of EC

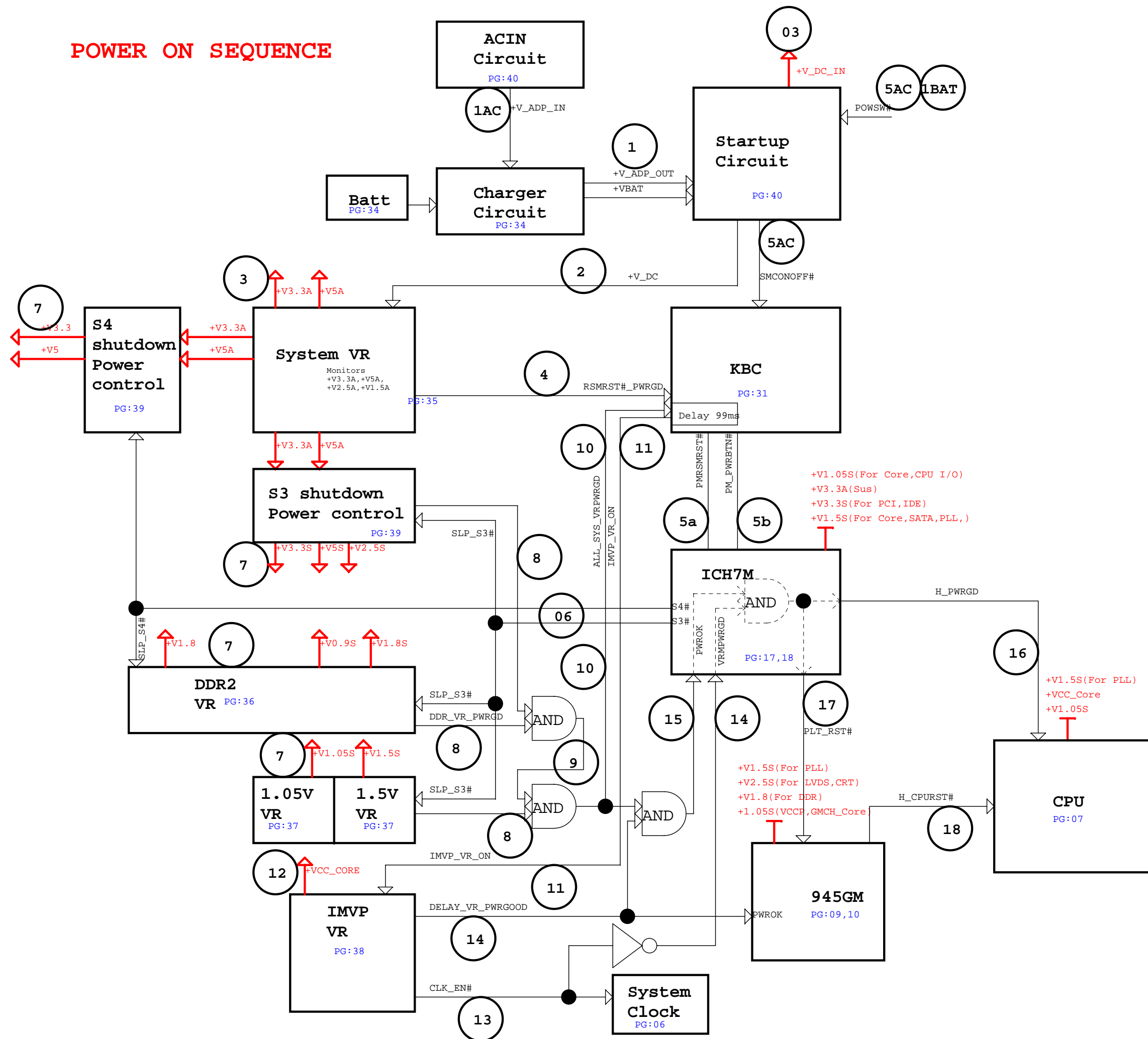
SMBUS TOPOLOGY



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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	IRQ ROUTING & SMBUS	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 03 of 43	

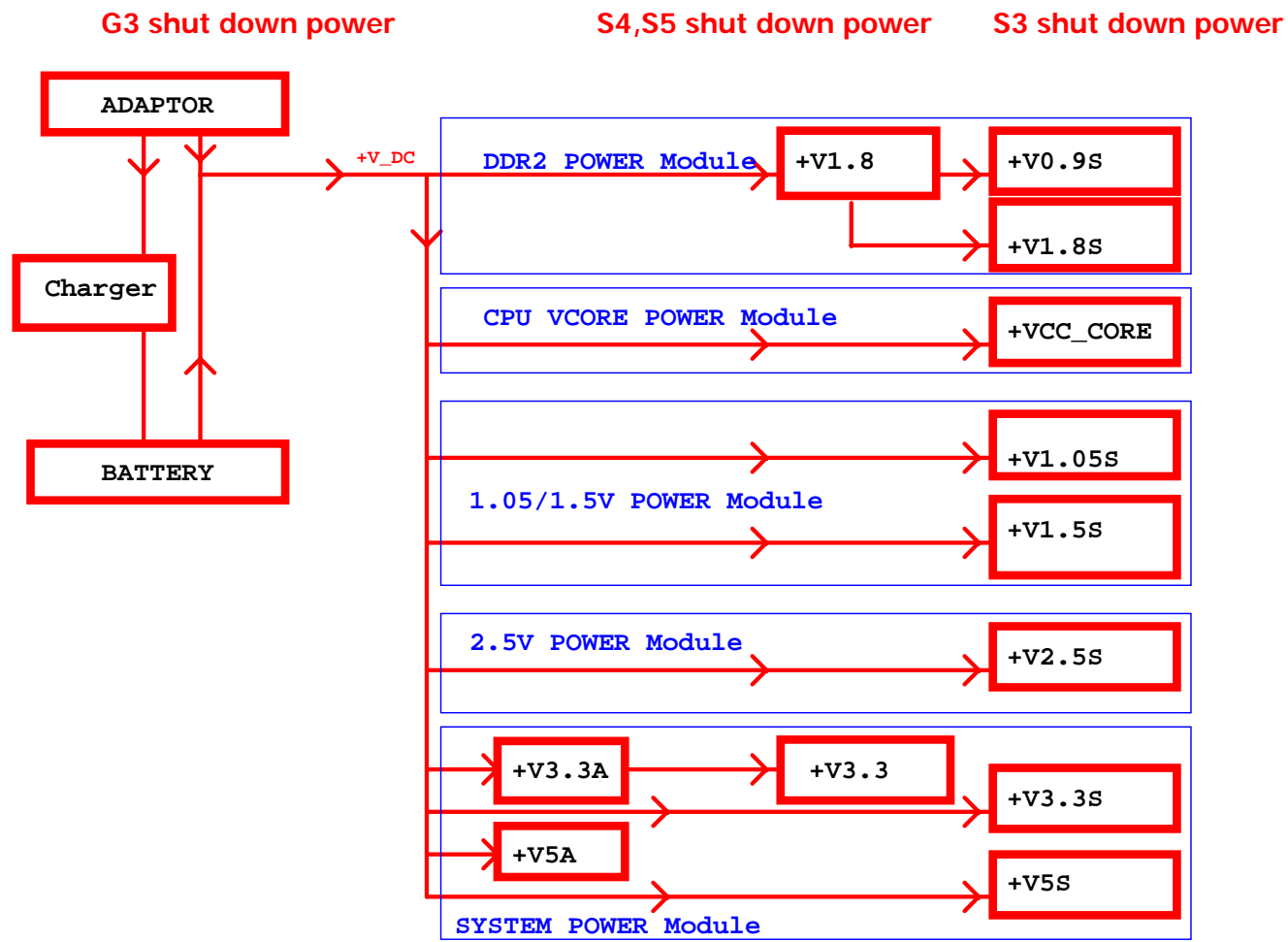
POWER ON SEQUENCE



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	Mobile Name	S101
Page Name	Power on Sequence	Rev V1.1
Date: Wednesday, March 11, 2009	Sheet 04 of 43	

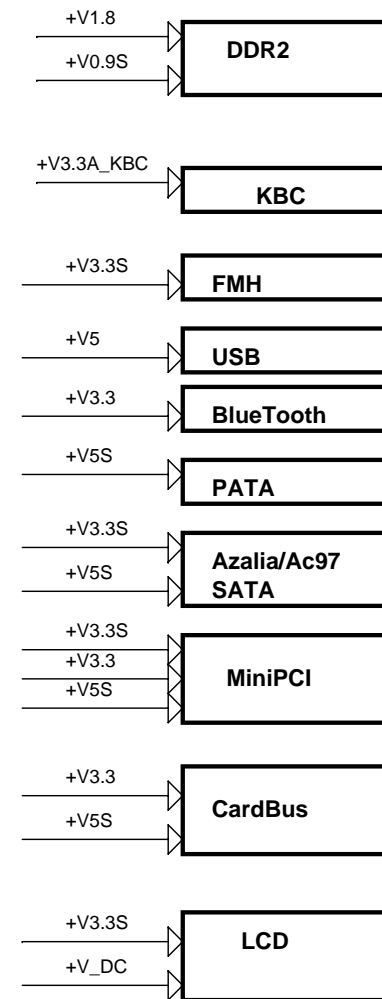
POWER Delivery Architectural Block Diagram



Dothan-LV	
+Vcore	Vcore:0.726-1.116V /?A /S0
+VCCP	VCCP:1.05V /3.0A /S0
+VCC_PROC	VPLL:1.50V /0.3A /S0

GMCH	
+VCCP	VCCP:1.05V /S0
+VCC_GMCH	Vcore:1.05-1.50V /S0
+V1.5S	VPLLs:1.50V /S0
	VccPCIE:1.5V /S0
	VccDLVDS:1.5V /S0
+V1.8	VccSM:1.8V /S0,S3
+V2.5S	VccALVDS:2.5V /S0
+V3.3S	VCRTDAC:2.5V /S0
	VTVDAC:3.3V /S0


ICH6-M	
+VCCP	VccpCPU:1.05V /S0
+V1.5S	Vcore:1.50V /S0
	VPLLs:1.50V /S0
	VccPCIE:1.5V /S0
+V2.5S	V2.5REF:2.5V /S0
+V3.3S	VccPCI:3.3V /S0
	VccIDE:3.3V /S0
	Vccp:3.3V /S0
	VccpAUX(LAN):3.3V /S0
+V3.3A	VccSUS:3.3V /S0,S3
+V5S	V5REF:5V /S0
+V5A	V5REFSUS:5V /S0,S3



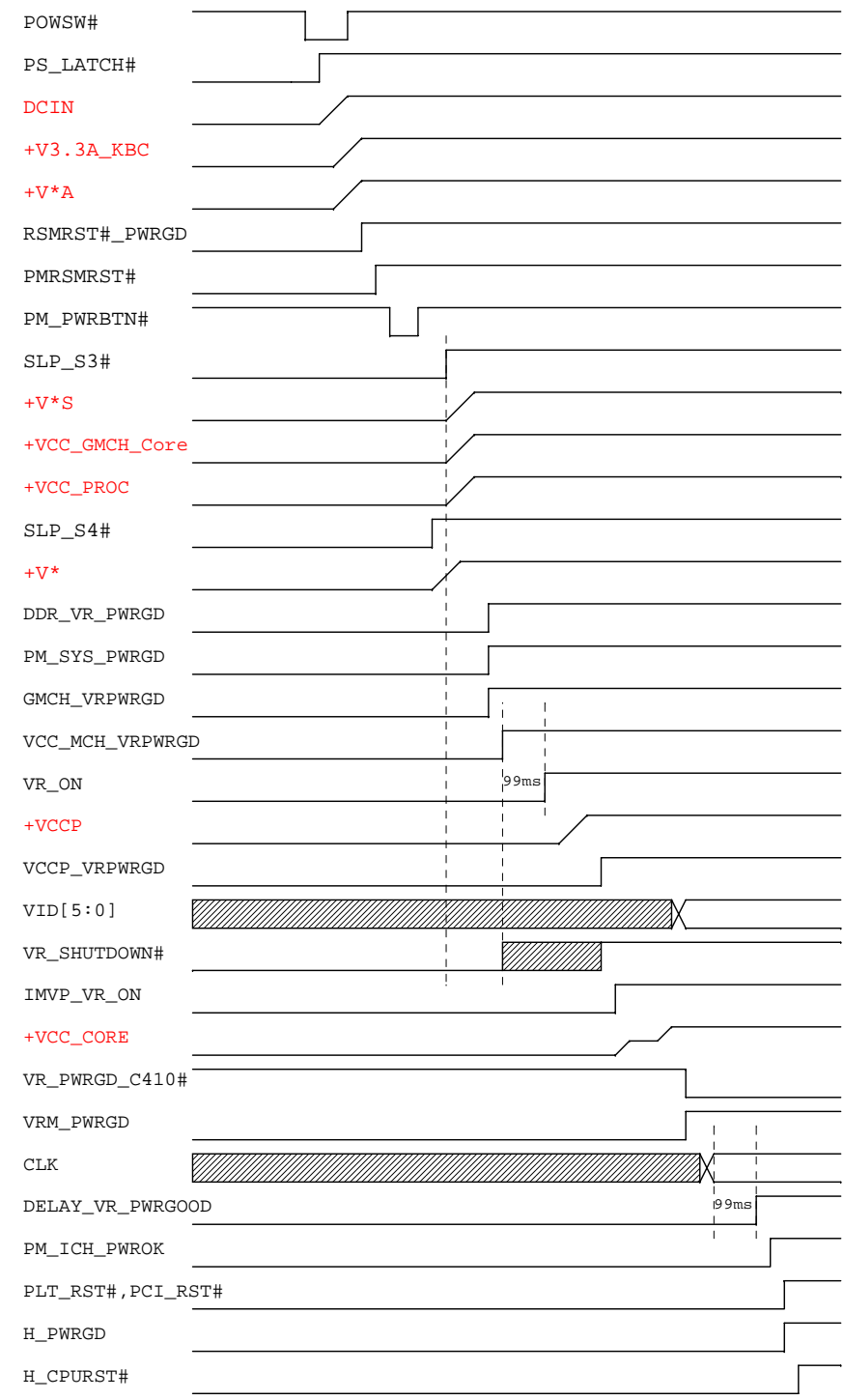
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POWER RAIL	DESTINATION	VOLTAGE	S0 CURRENT
VCORE_CPU	Banias	0.7-1.708V	32A
VCCP	Banias	0.9-1.105V	2.5A
	ICH4M MontaraGM		0.72A
1.8VDDM	Banias (PLL)	1.8V	0.3A
	MontaraGM(PLL)		0.099A
1.2VDDM	(CORE, HUB, DDRDDL)	1.2V	1.89A
			(1.4A, 0.09A, 0.4A)
1.5VDDM	MontaraGM	1.5V	0.23A
	(LVDS, DAC, DVO)		(0.07A, 0.07A, 0.09A)
	ICH4M (CORE)		0.5A
2.5VDDS	MontaraGM	2.5V	2.12A
	(DDR, LVDSIO)		(2.07A, 0.05A)
	DDR RAM		1.046A (Idle) 1.692A (Run)
	R5C551		0.13A
1.8VDDS	82541EI		0.22A
1.25VDDM	DDR RAM	1.25V	0.0769A
1.5VDDS	ICH4M (LAN)	1.5V	0.0155A
1.2VDDS	82541EI	1.2V	0.47A
1.5VDDA	ICH4M (SUS)	1.5V	0.0675A
3VDDM	ICH4M (IO)	3.3V	0.528A
	R5C551		0.13A
	MiniPCI		
	FWH BIOS		
	LPC KBC		0.0308A (Idle)
	AC97 CODEC		0.0461A (Idle)
	CLK GEN		0.36A
	LVDS		0.246A
3VDDS	ICH4M (LAN)	3.3V	0.0092A
	R5C551		
	MiniPCI		
	82540EM		0.15A
	PCMCIA VCCA		
3VDDA	ICH4M (SUS)	3.3V	0.165A
5VDDM	AMP2020		0.0615A (Idle) 0.338A (Run)
	CDROM		0.0461A (Idle) 0.677~0.8A (Run)
	HDD		0.0461A (Idle) 0.492A (Run)
	INT KB/ INT MS		
	INVERTER		0.0615A (Idle) 0.569A (Run)
5VDDS	PCMCIA VCCA		
5VDDA	ICH4M		10UA
	USB		
PMU3V	PMU08		0.0615A
PMU5V	ASIC_B0		0.0615A

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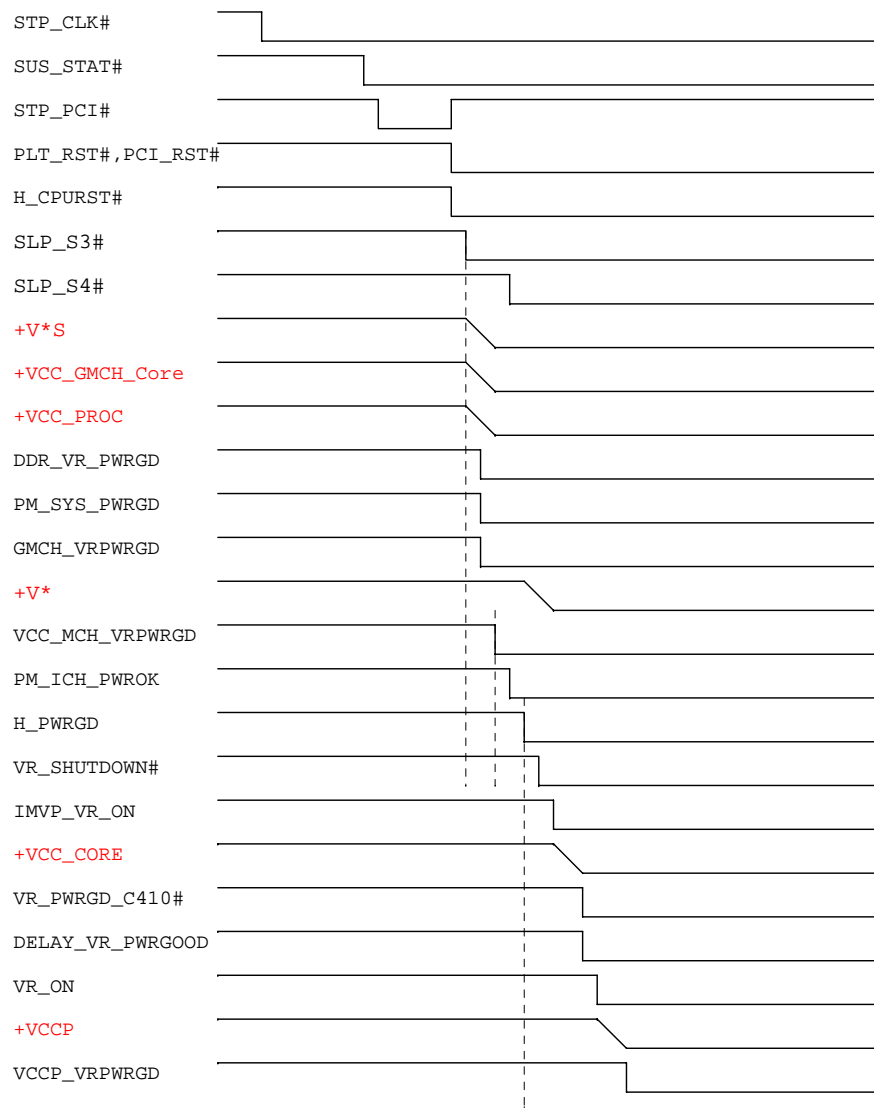
	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	PWR_Budget	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 06 of 43	

Power On Sequence



Need confirm the power sequency
PM_PWRBTN# and PMRSMRST#

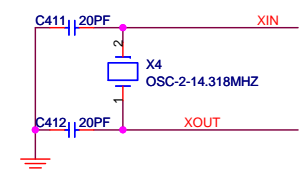
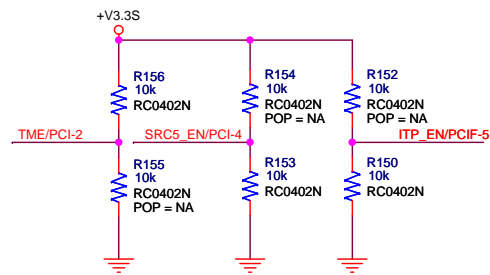
Power Down Sequence



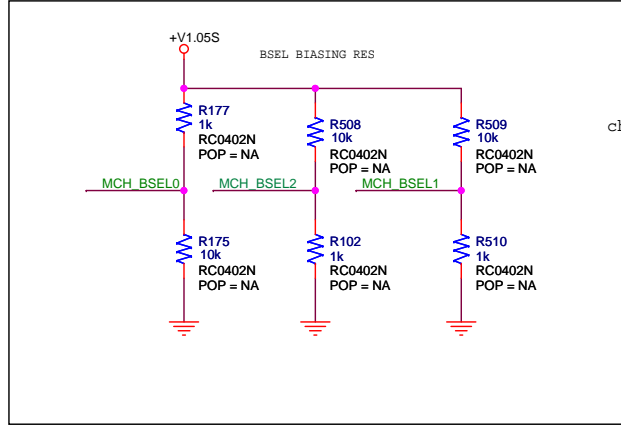
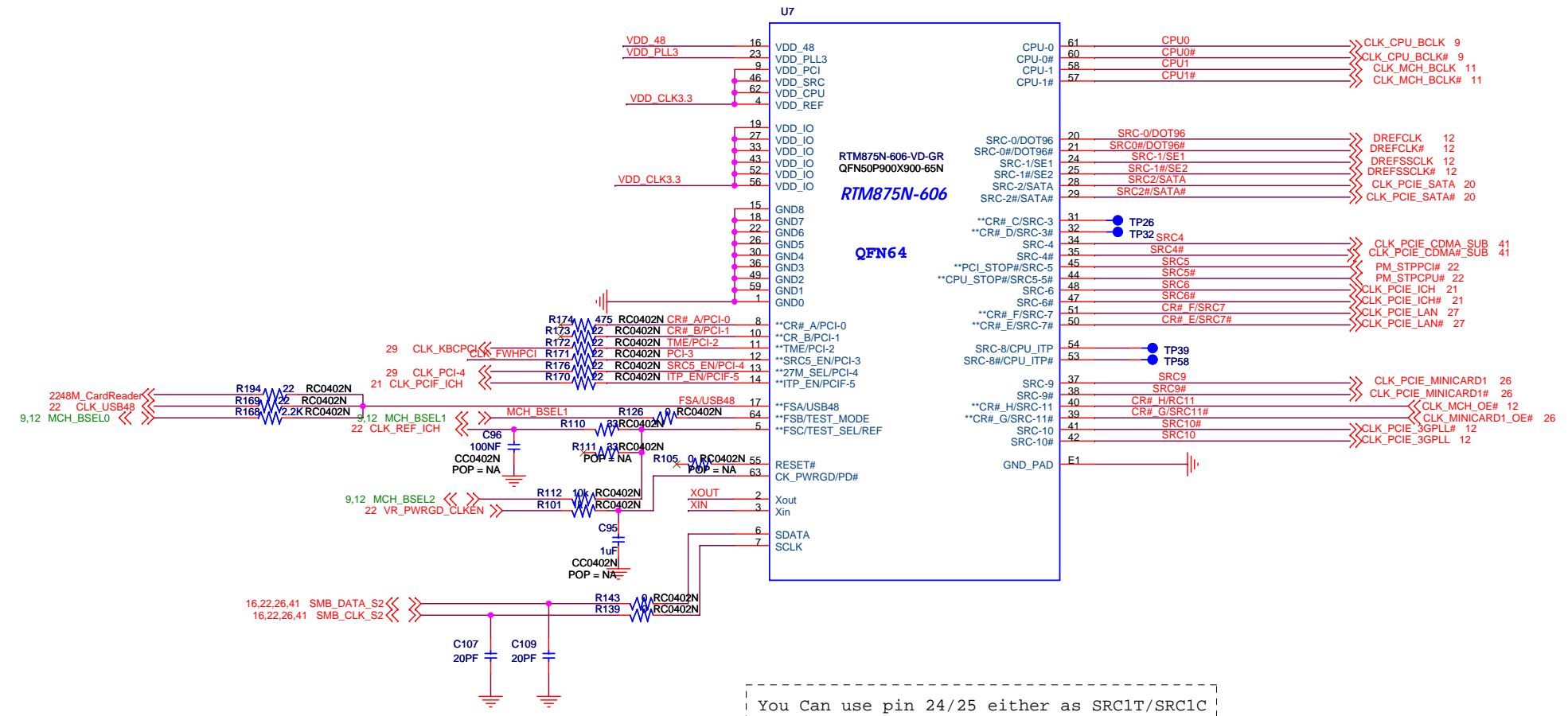
CPU Power Down Sequence Intel request:

Intel request: CPU Power on Sequence

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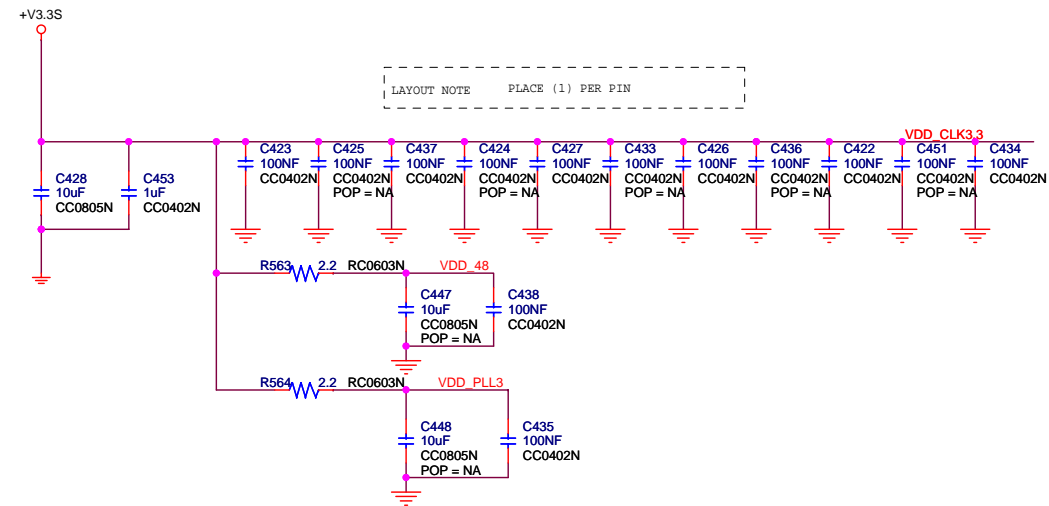
12-15 更换CLOCK芯片, 将RTM875T-606 换为RTM875N-606-VD-GR



CLK_PCIF_ICH	C114	10pF	CC0402N	POP = NA
CLK_KBCPCI	C116	10pF	CC0402N	POP = NA
CLK_FWHPCI	C115	10pF	CC0402N	POP = NA
CLK_USB48	C113	10pF	CC0402N	POP = NA
CLK_REF_ICH	C406	10pF	CC0402N	POP = NA

CLOCK EMI CAPS: DEFAULT EMPTY

You can use pin 24/25 either as SRC1T/SRC1C or as SE1/SE2. Details please reference the datasheet

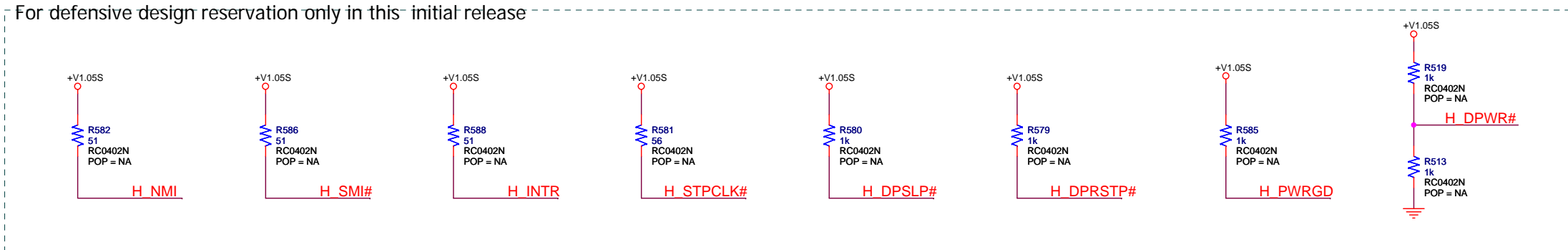
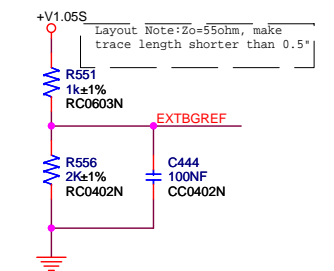
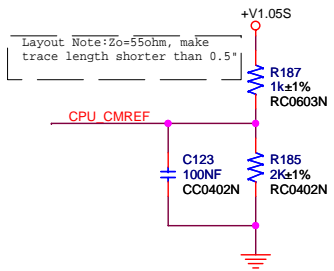
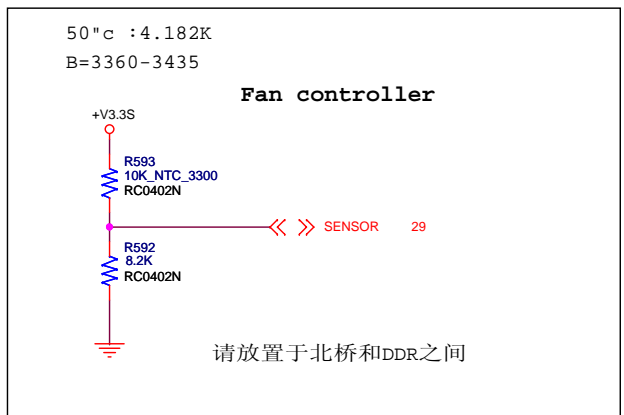
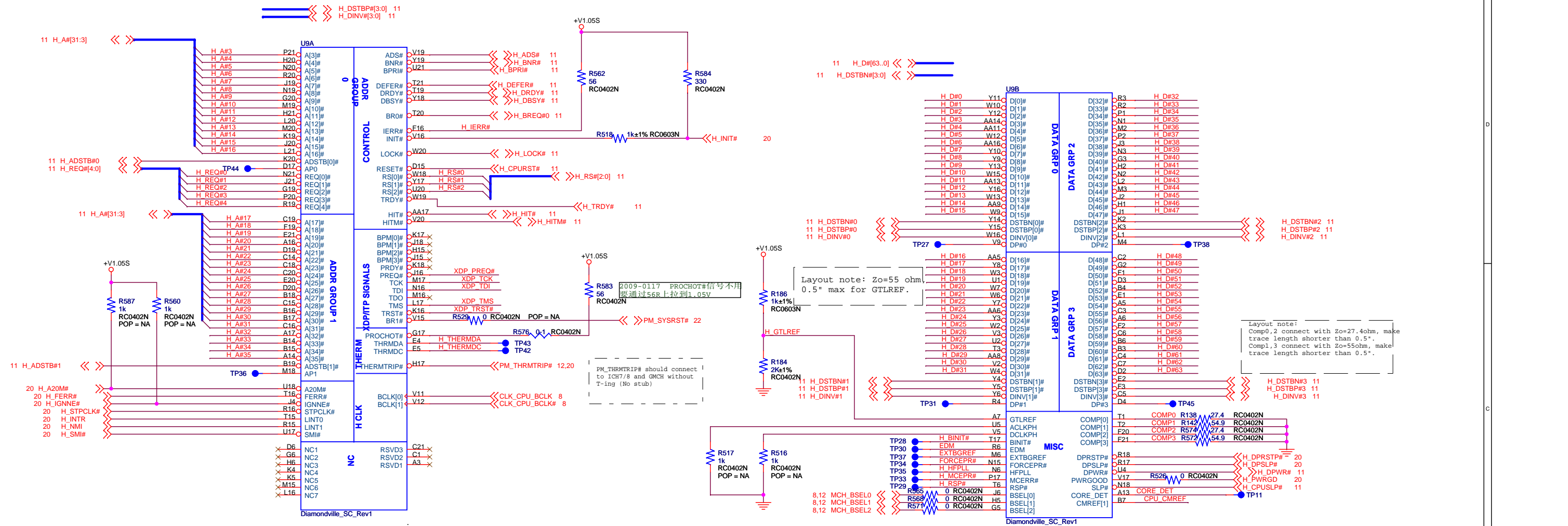


FS _L C	FS _L B	FS _L A	CPU	SRC	PCI	DOT	REF	USB
Bit2	Bit1	Bit0	(MHz)	(MHz)	(MHz)	(MHz)	(MHz)	(MHz)
0	0	0	266.66	100.00	33.33	96.00	14.318	48.00
0	0	1	133.33	100.00	33.33	96.00	14.318	48.00
0	1	0	200.00	100.00	33.33	96.00	14.318	48.00
0	1	1	166.66	100.00	33.33	96.00	14.318	48.00
1	0	0	333.33	100.00	33.33	96.00	14.318	48.00
1	0	1	100.00	100.00	33.33	96.00	14.318	48.00
1	1	0	400.00	100.00	33.33	96.00	14.318	48.00
1	1	1	200.00	100.00	33.33	96.00	14.318	48.00

STRAP	MODE	STUFF	UNSTUFF
TME	NO OVERCLOCKING	R2 PULL UP	R4 PULLDOWN
	NORMAL	R4 PULLDOWN	R2 PULL UP
SRC5_EN	SRC5 ENABLED	R1 PULL UP	R5 PULLDOWN
	SRC5 DISABLED	R5 PULLDOWN	R1 PULL UP
ITP_EN	ITP ENABLED	R3 PULL UP	R6 PULLDOWN
	SRC8 ENABLED	R6 PULLDOWN	R3 PULL UP

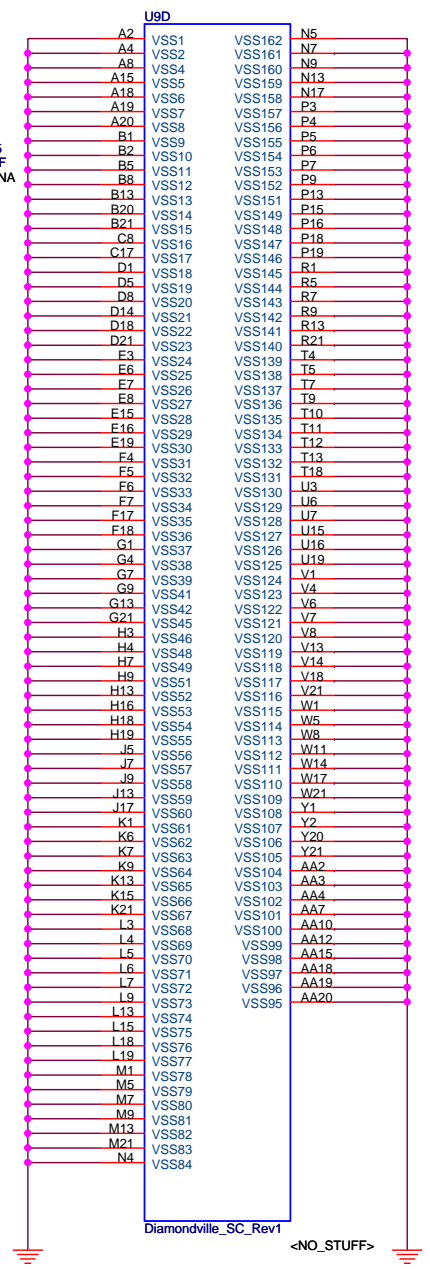
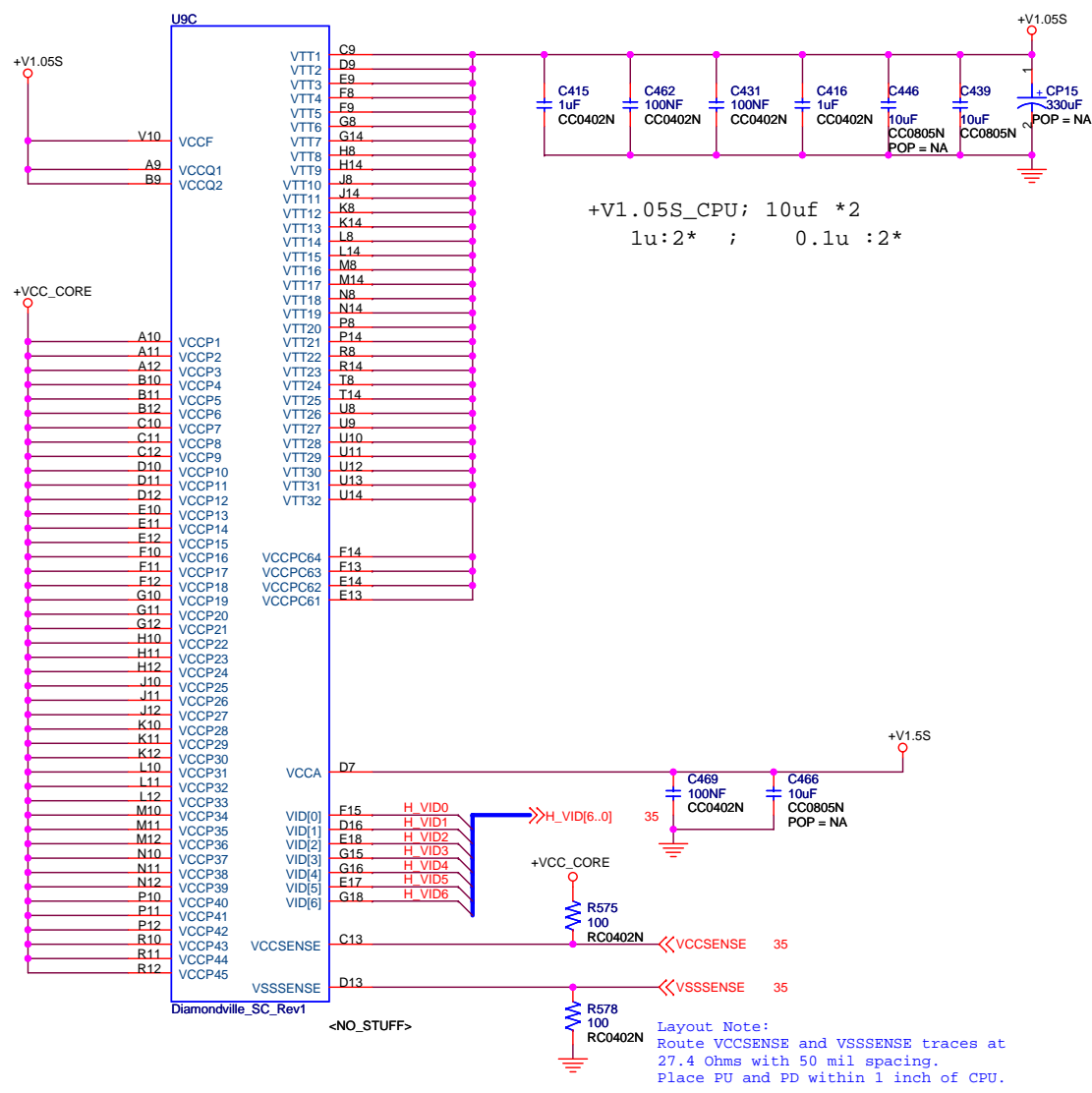
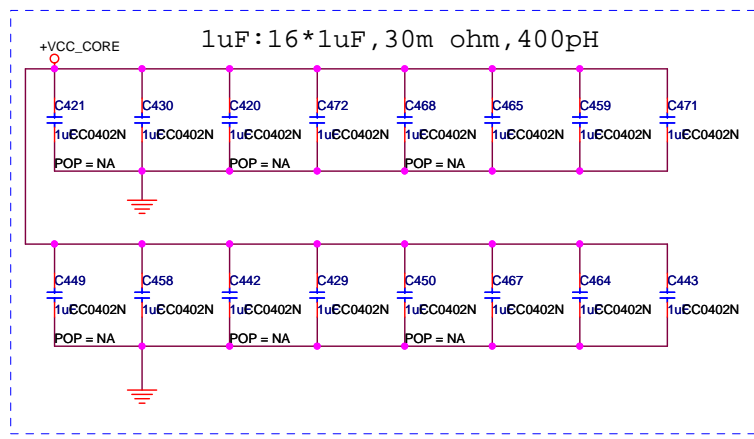
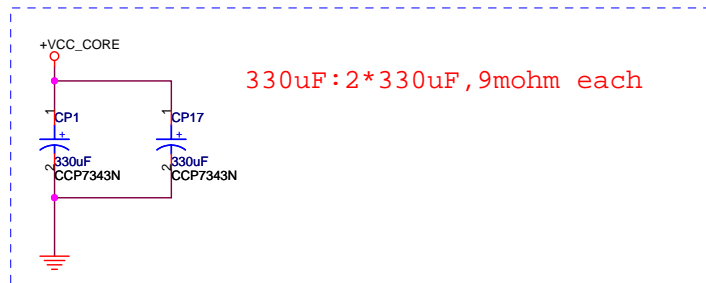
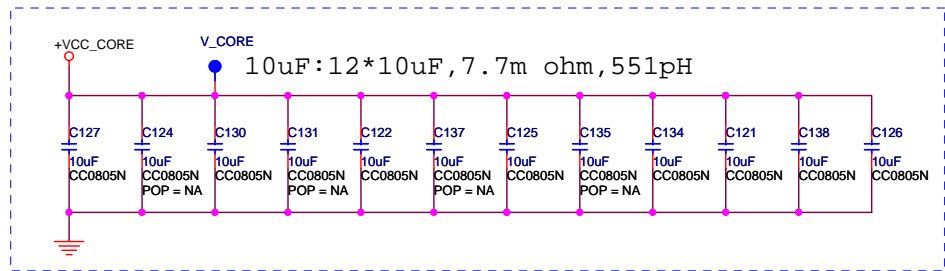
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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	Clock GEN	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 08 of 43	

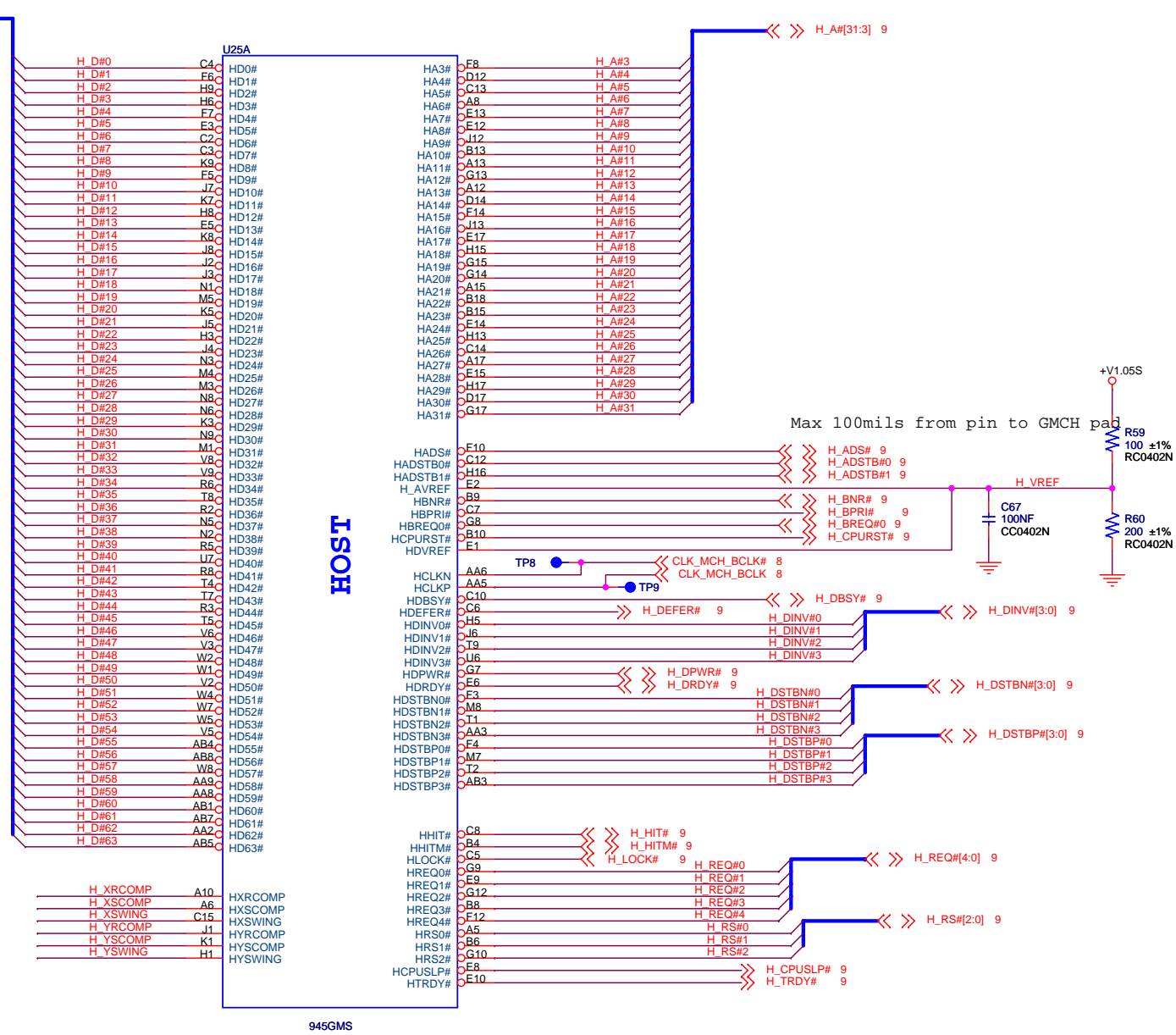
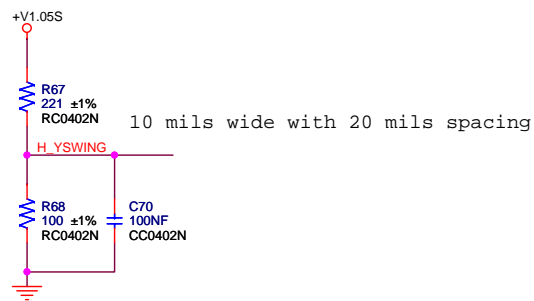
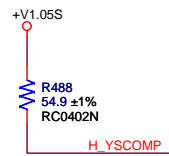
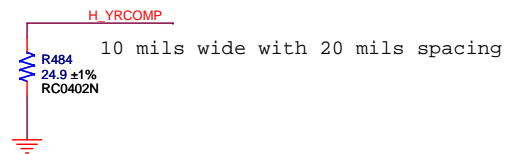
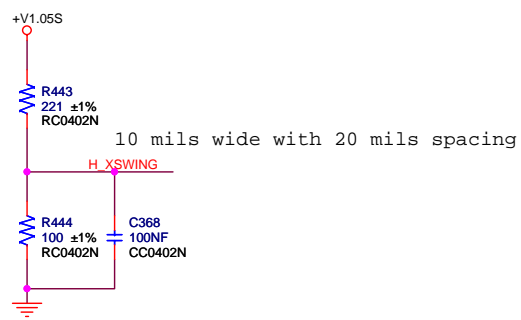
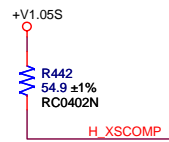
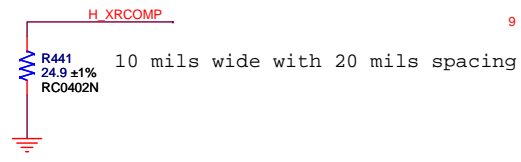


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	Mobile Name	S101
Page Name	CPU1	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 09 of 43	

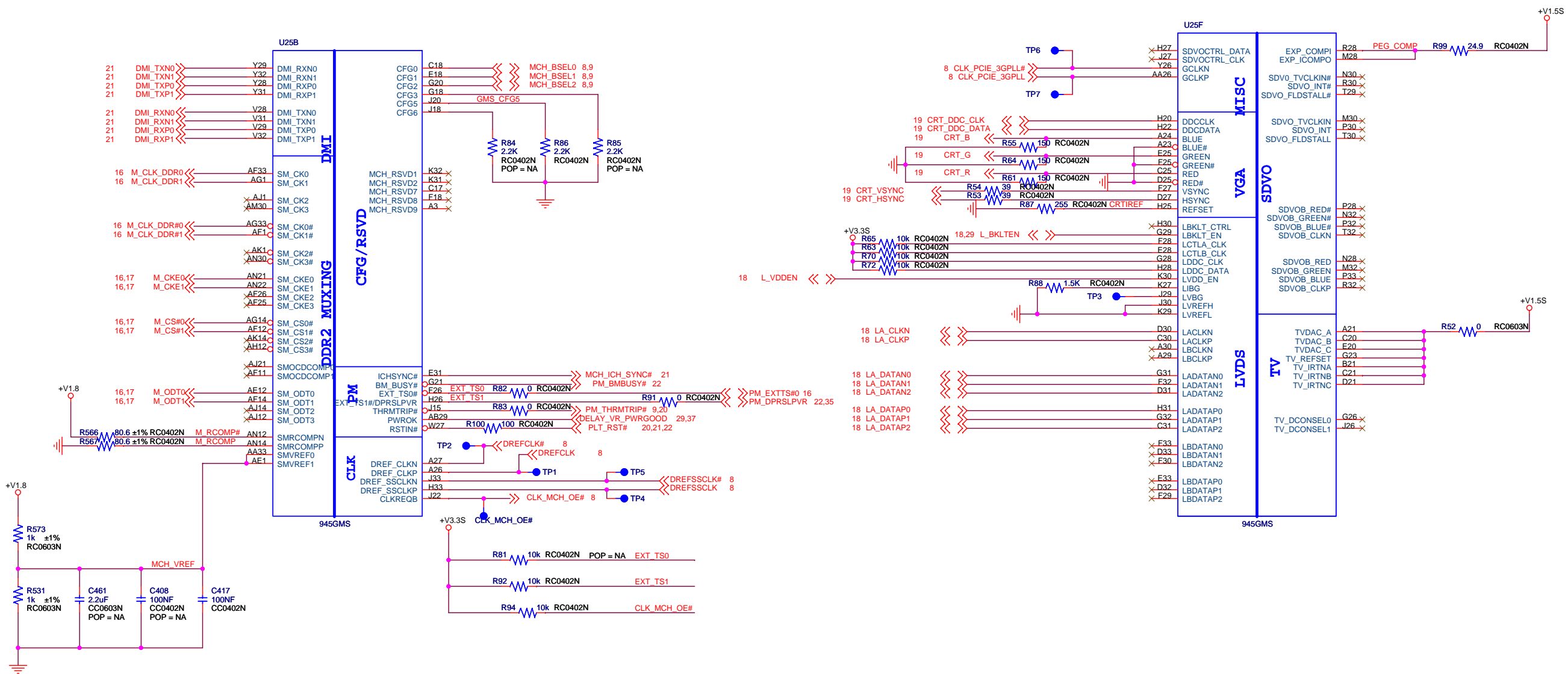


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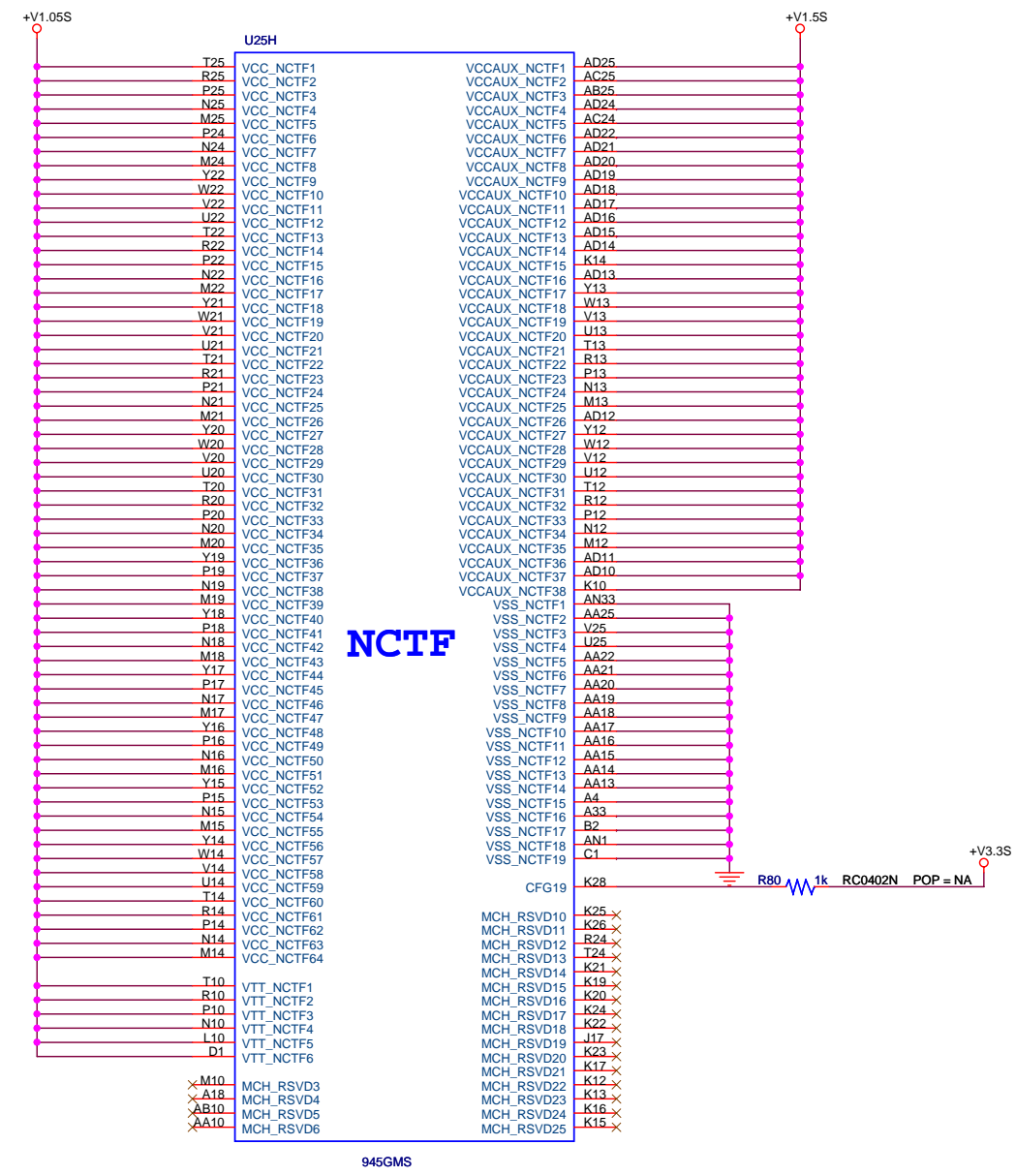
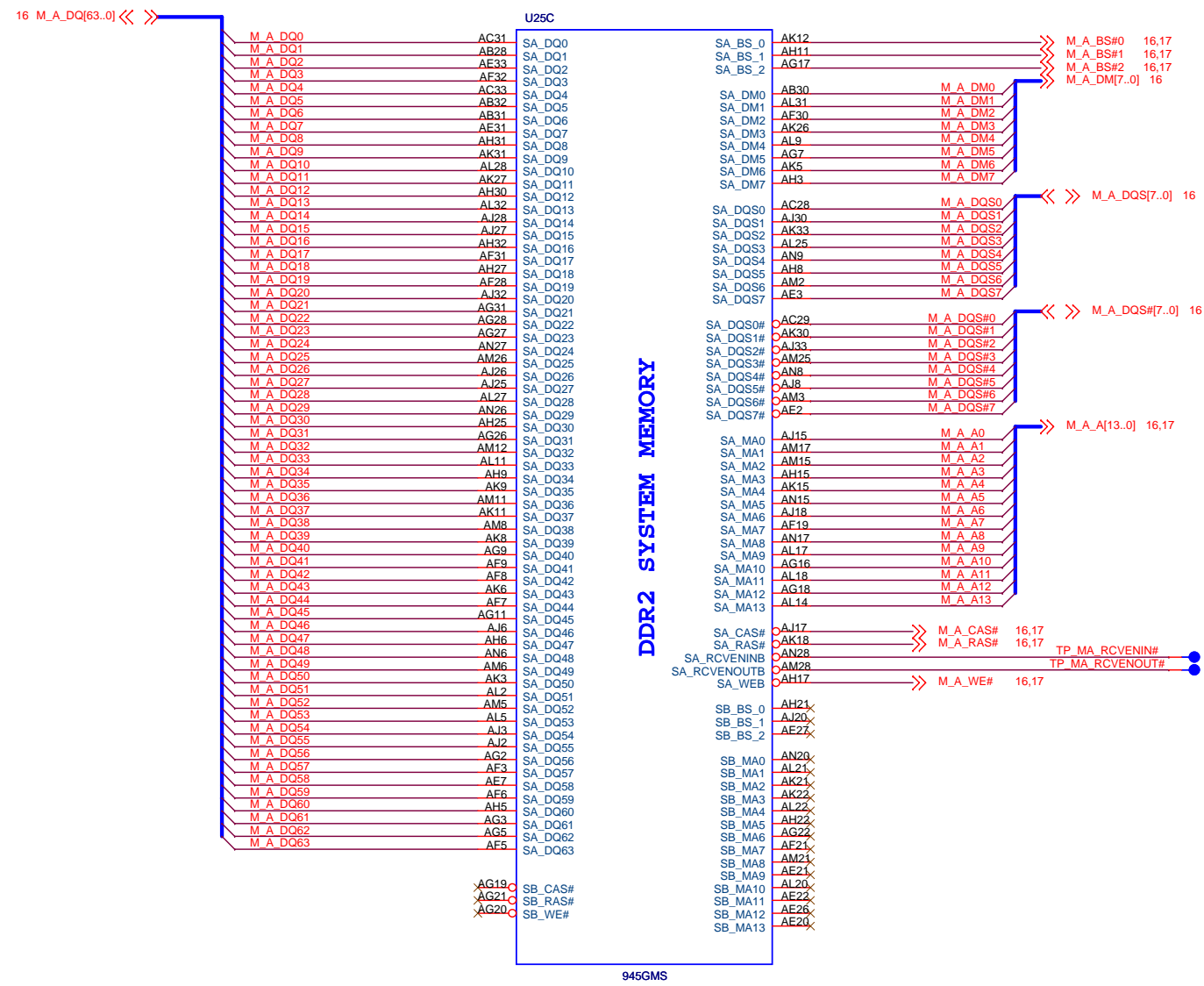
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	Mobile Name	S101
Page Name	NB1	Rev V2.0
Date:	Wednesday, March 11, 2009	Sheet 11 of 43



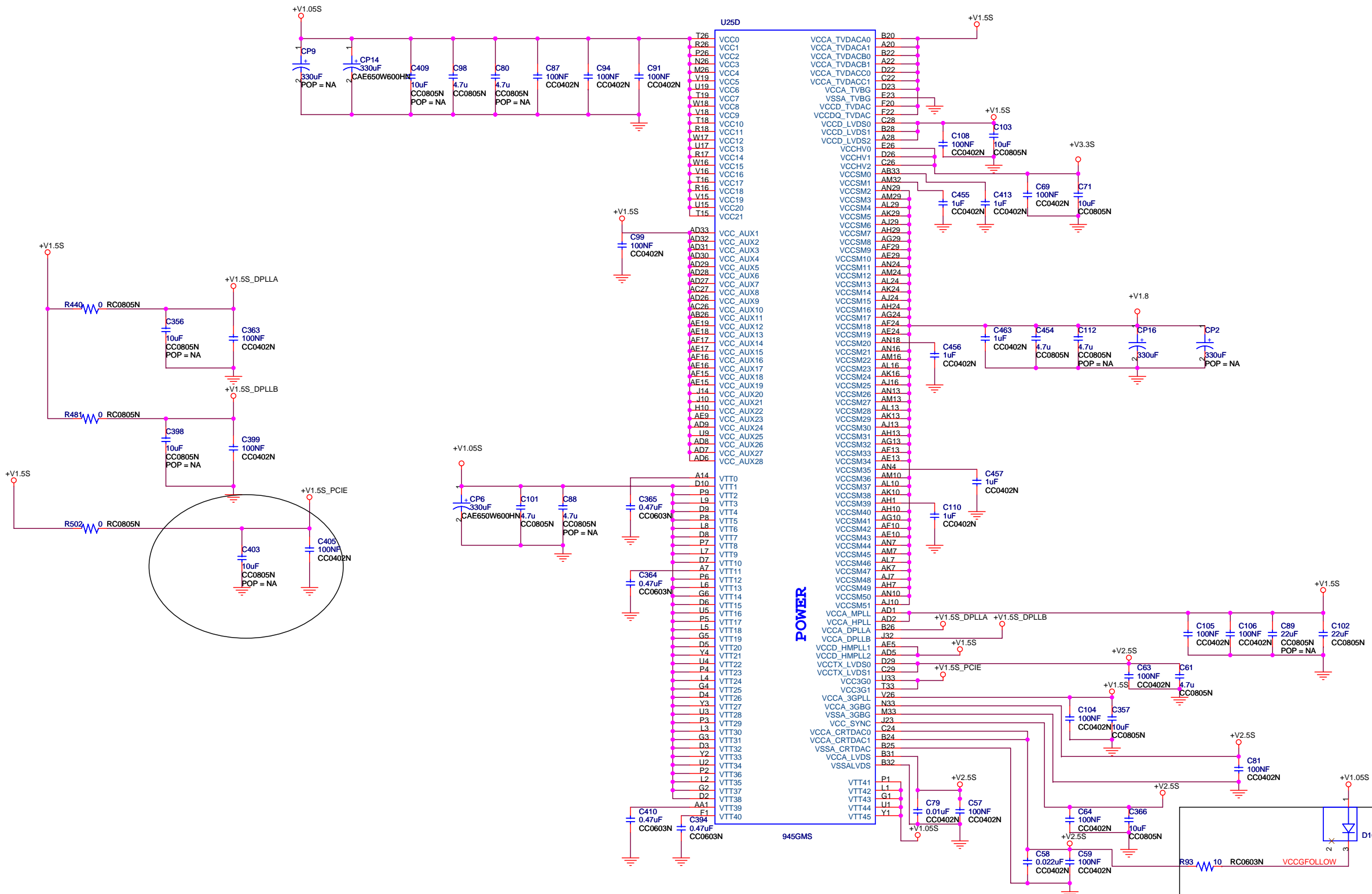
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	Mobile Name S101	
Page Name NB2	Rev V2.0	
Date: Wednesday, March 11, 2009	Sheet	12 of 43




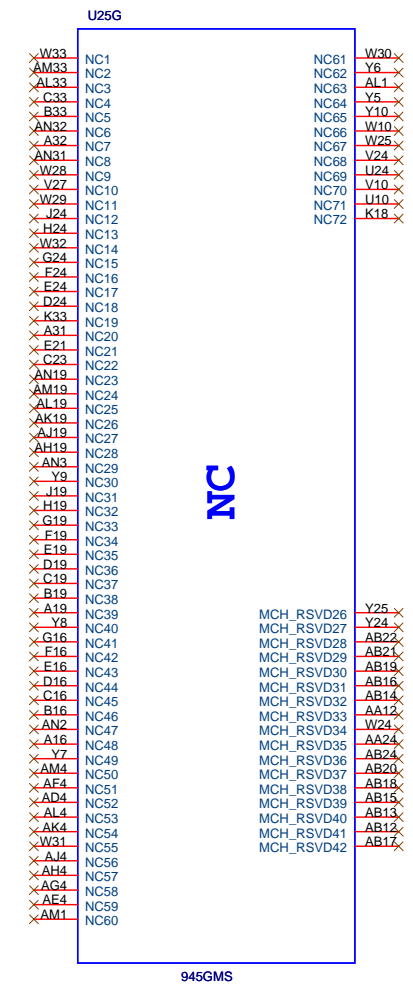
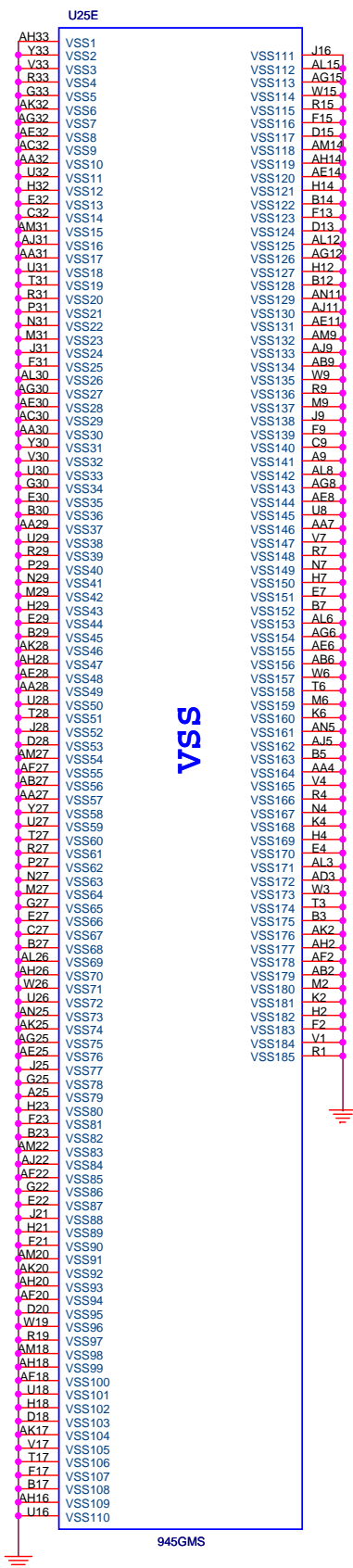
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		Mobile Name S101	
Page Name NB3	Rev V2.0		
Date: Wednesday, March 11, 2009	Sheet	13 of 43	



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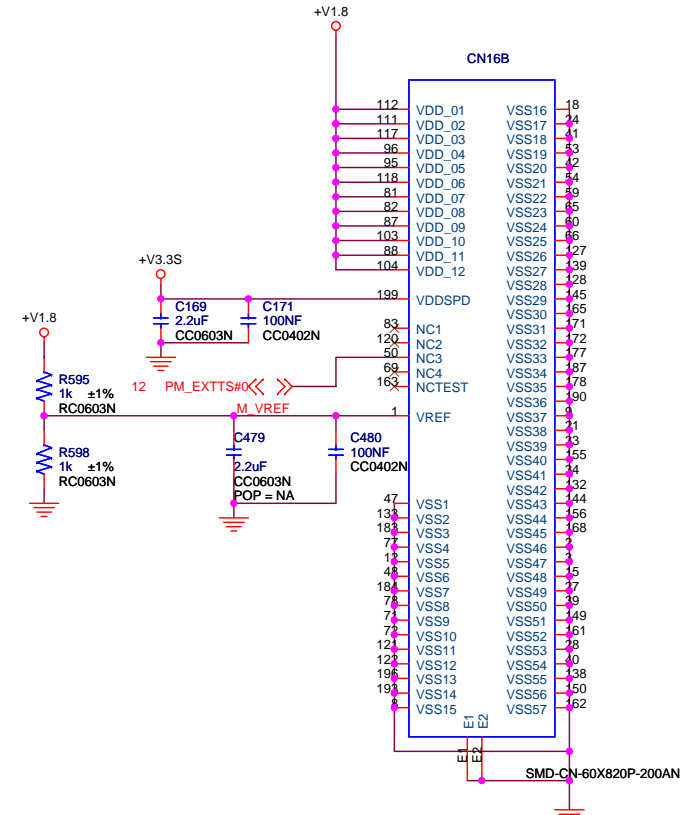
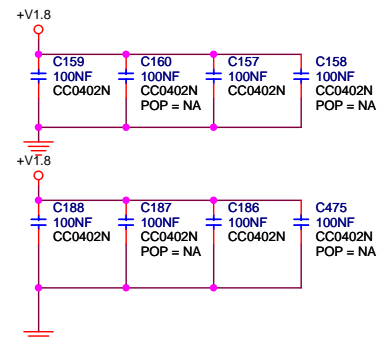
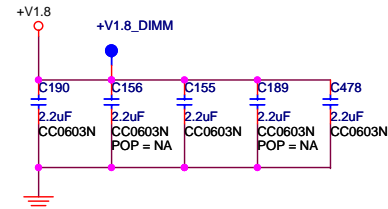
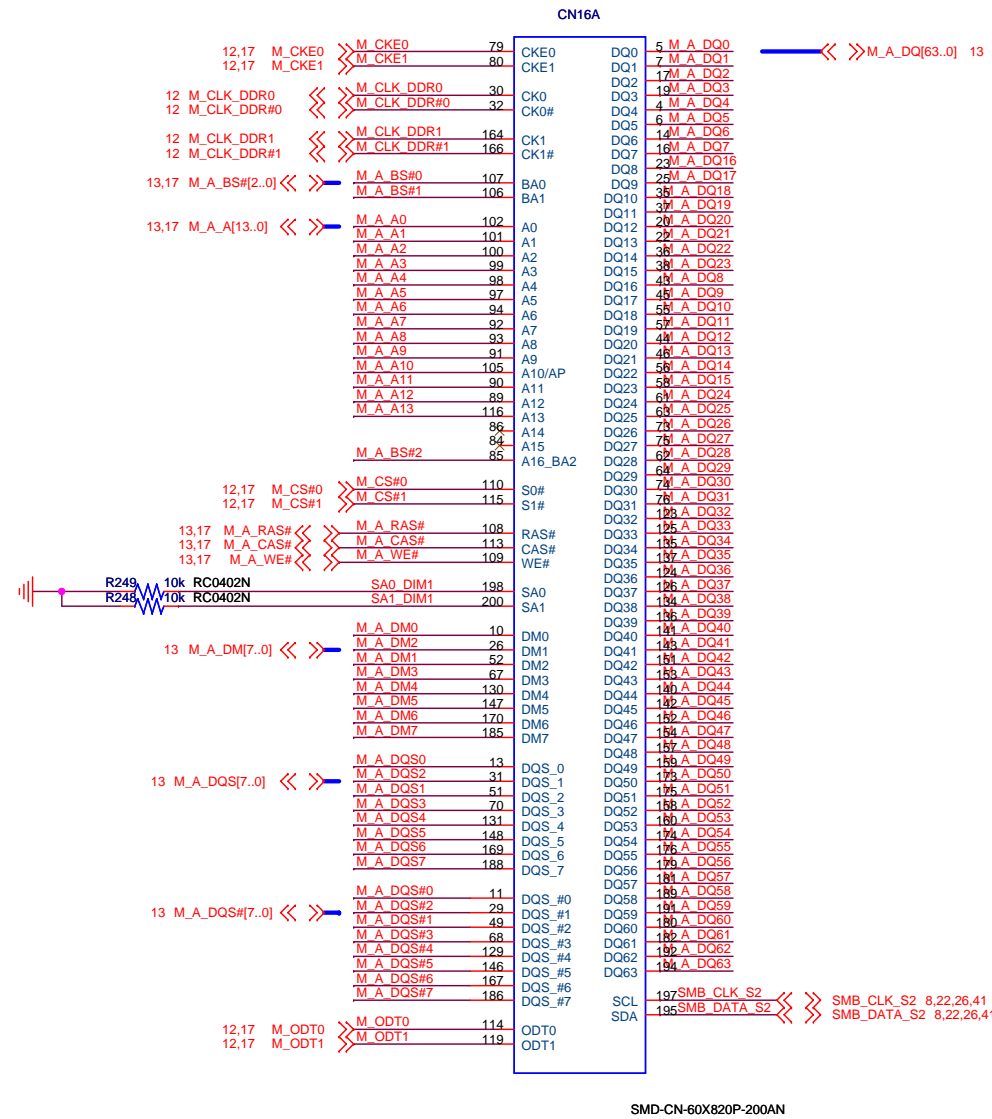
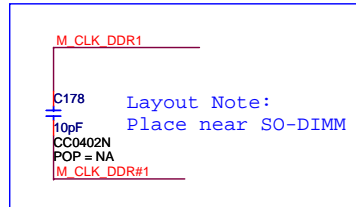
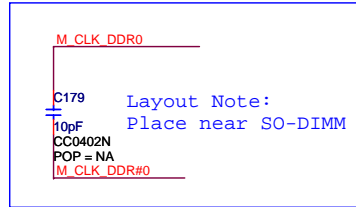
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	Mobile Name	S101
Page Name	NB4	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 14 of 43	



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	Mobile Name S101	
Page Name NB5	Rev V2.0	
Date: Wednesday, March 11, 2009	Sheet	15 of 43

DDR2 SODIMM0

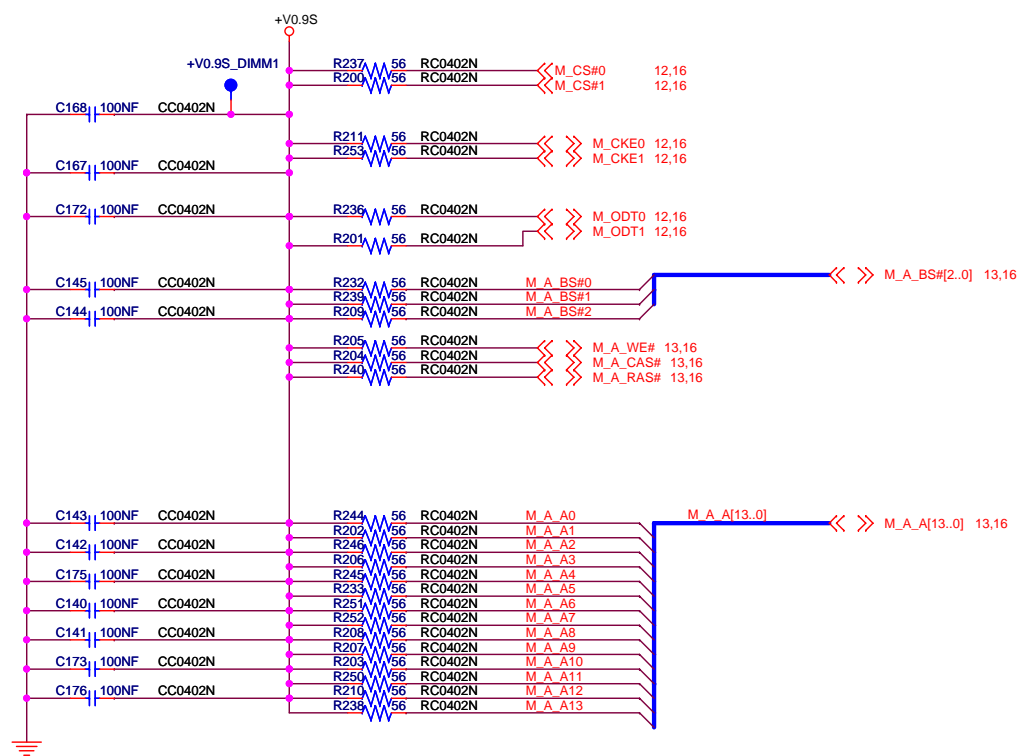


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	Mobile Name	S101
Page Name	DDR2_SODIMM0	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 16	of 43

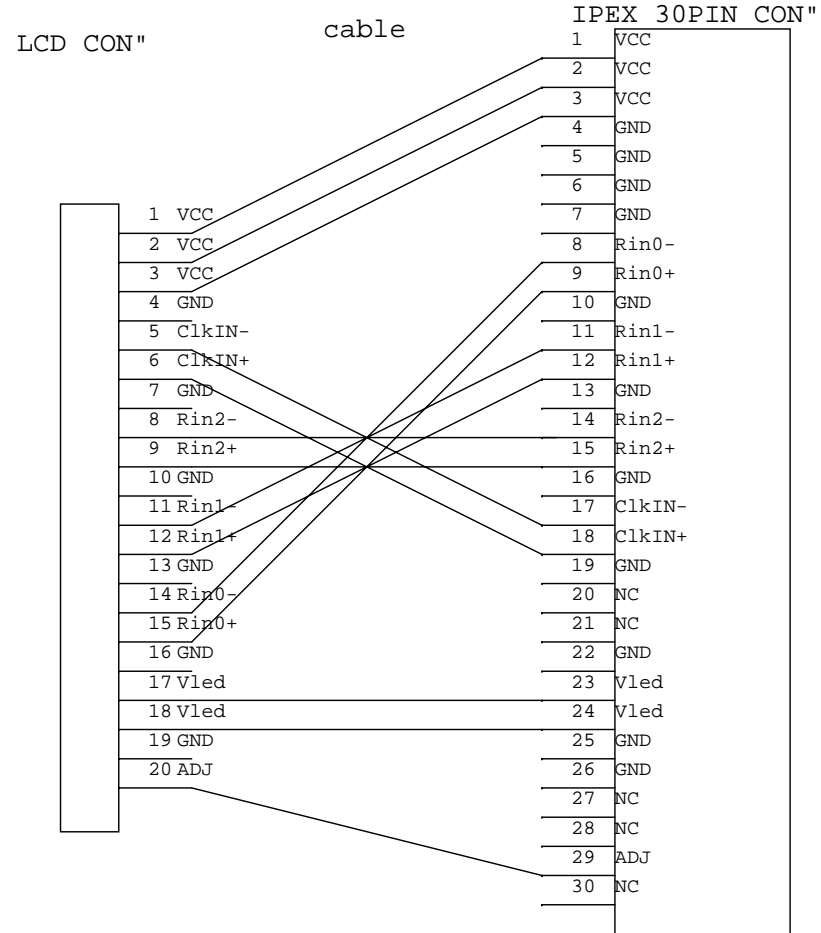
DDR2 Termination

Layout note:
Place one cap
close to every 2
pullup resistors
terminated to +0.9S

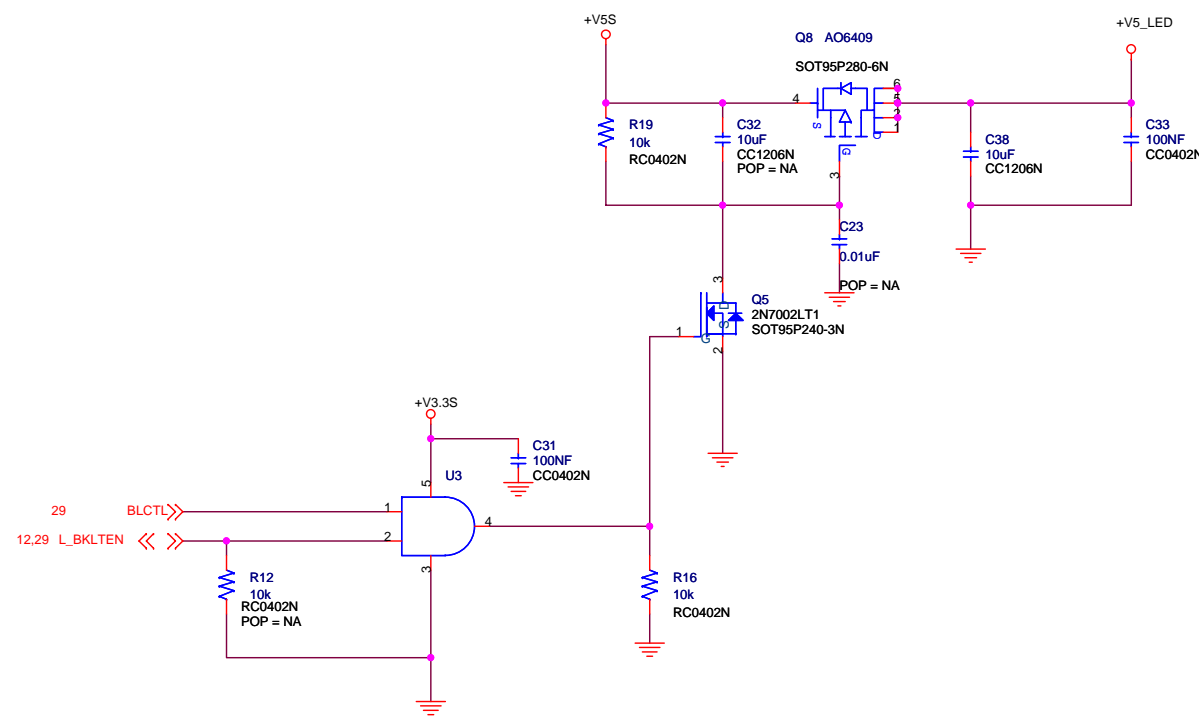
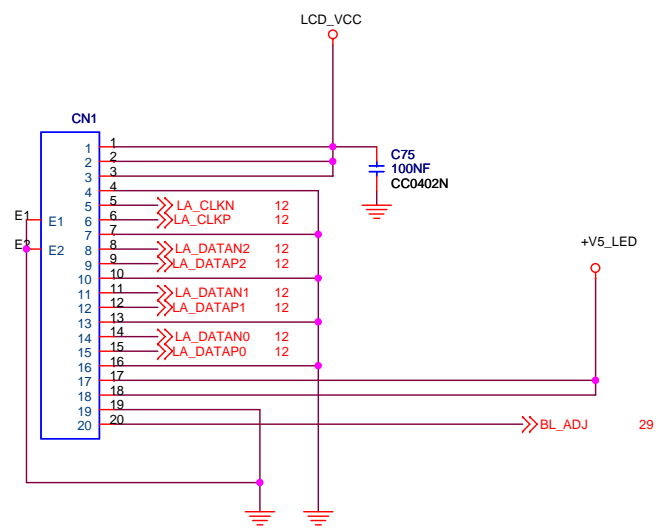
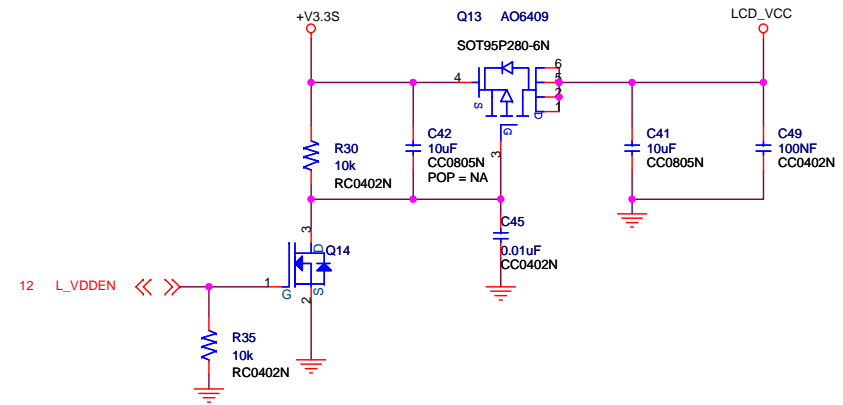


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	Mobile Name	S101
Page Name	DDR2_Termination	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 17 of 43	

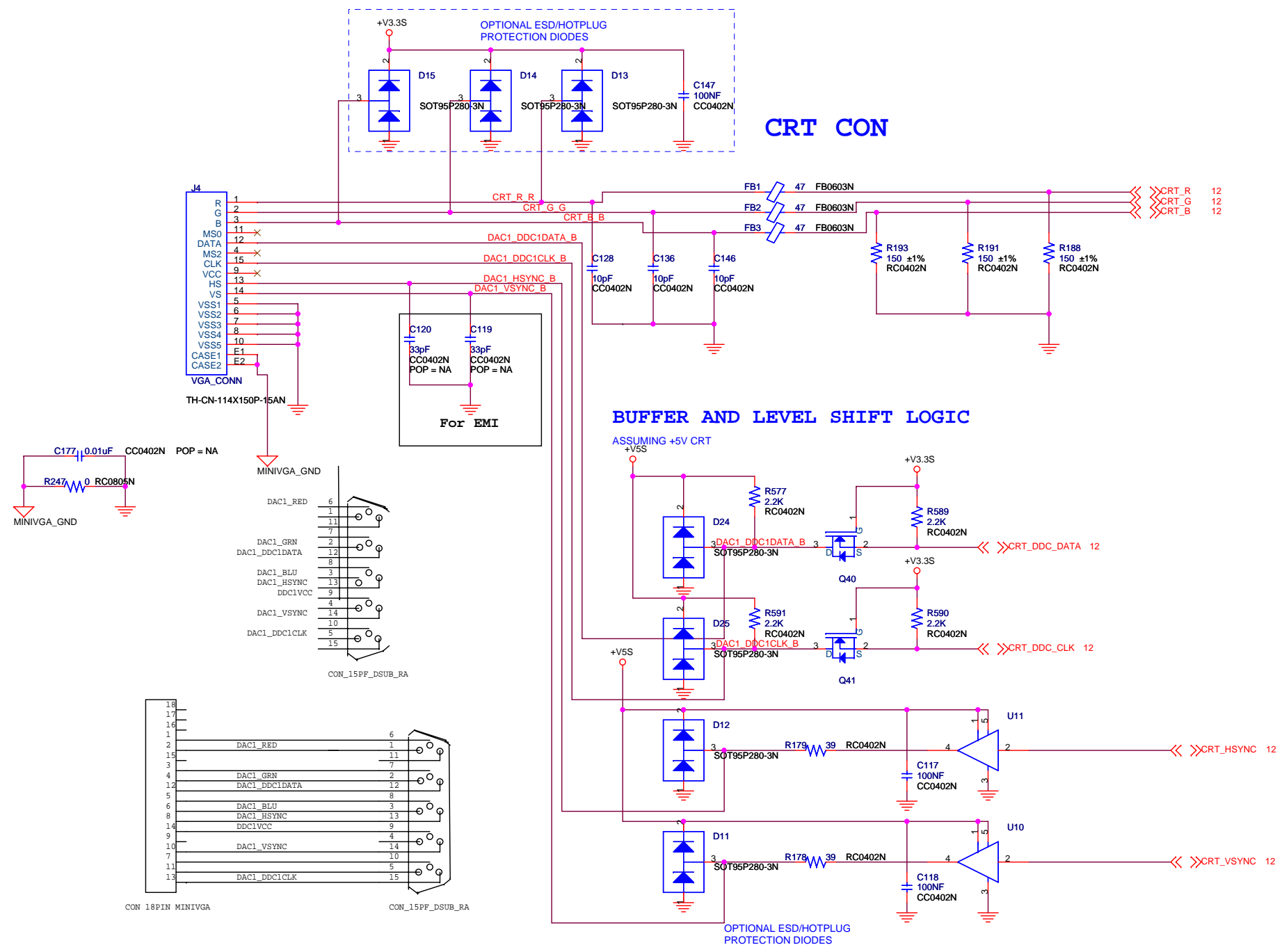


LVDS Connector




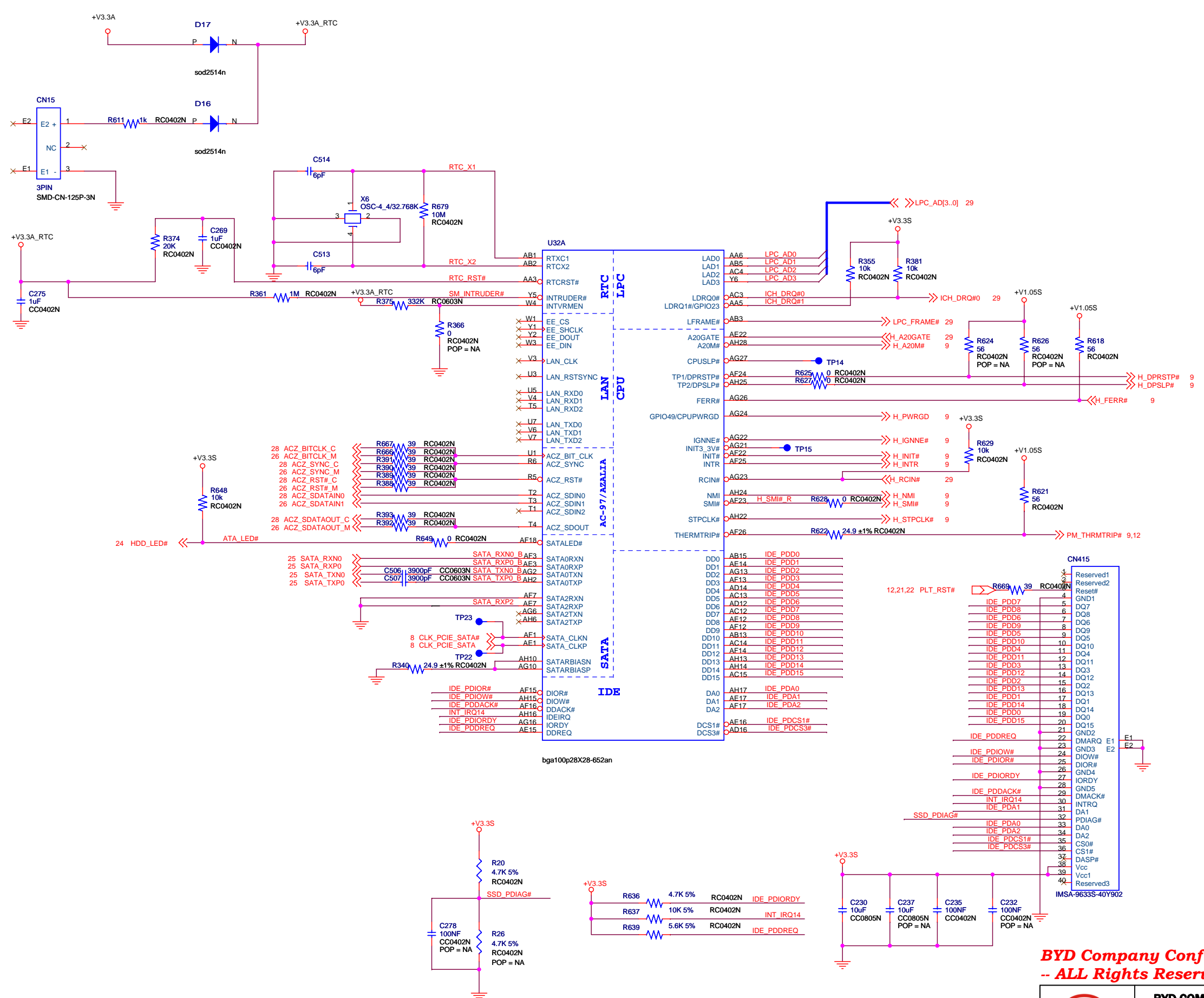
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	Mobile Name	S101
Page Name	LCD Connector	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 18	of 43



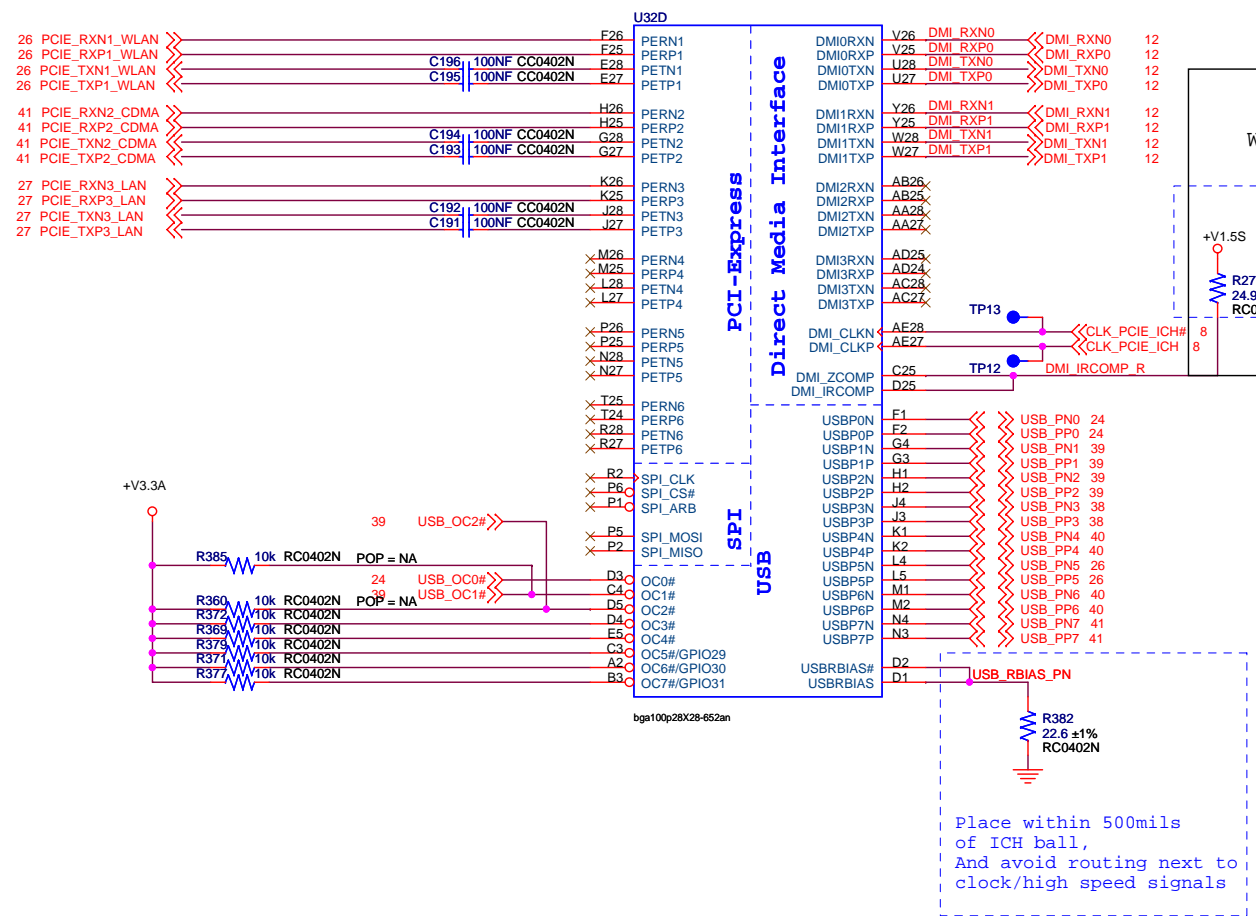
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	S101	
Page Name CRT	Rev V2.0	
Date: Wednesday, March 11, 2009	Sheet 19 of 43	



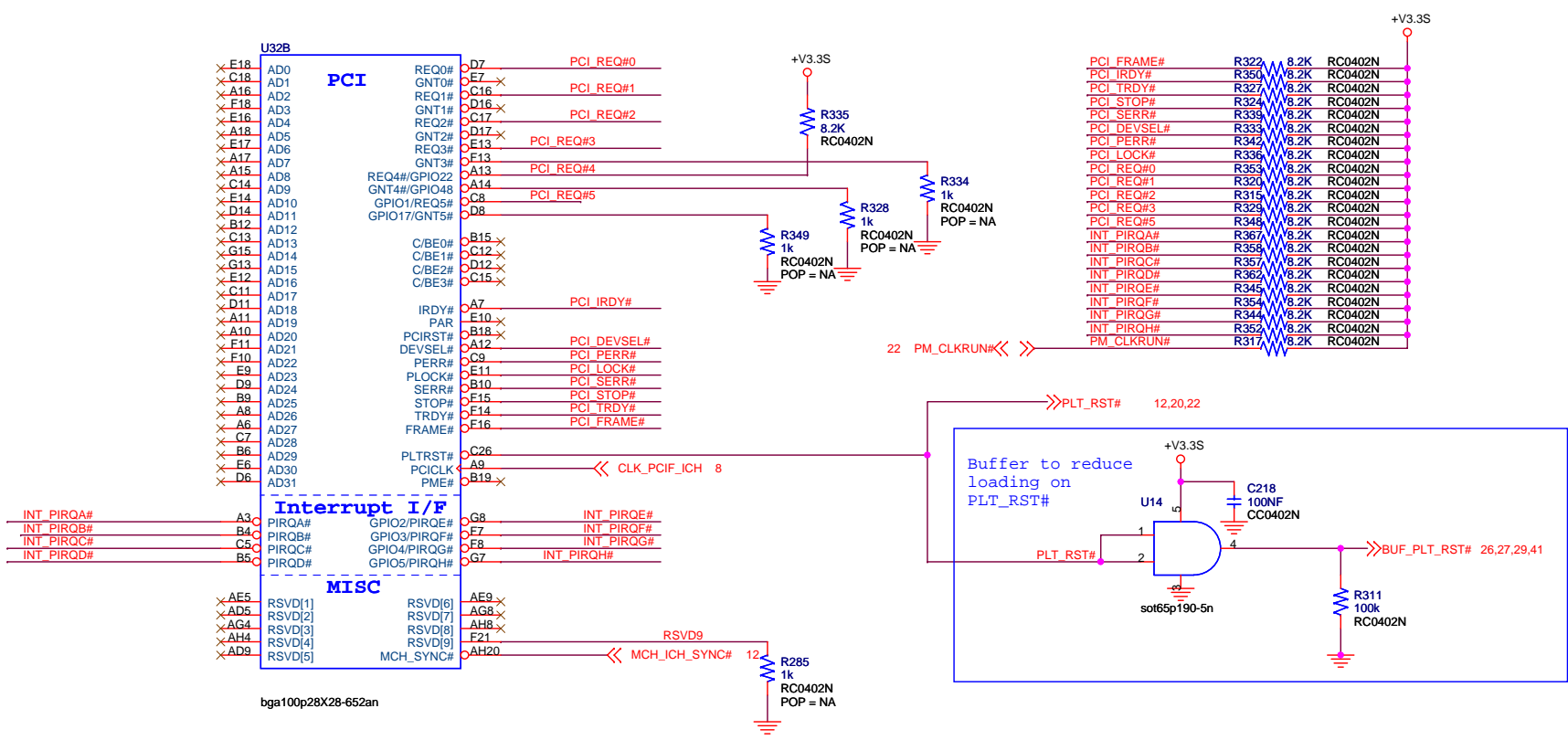
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	Mobile Name S101	
Page Name ICH7M1	Rev V2.0	
Date: Wednesday, March 11, 2009	Sheet 20 of 43	



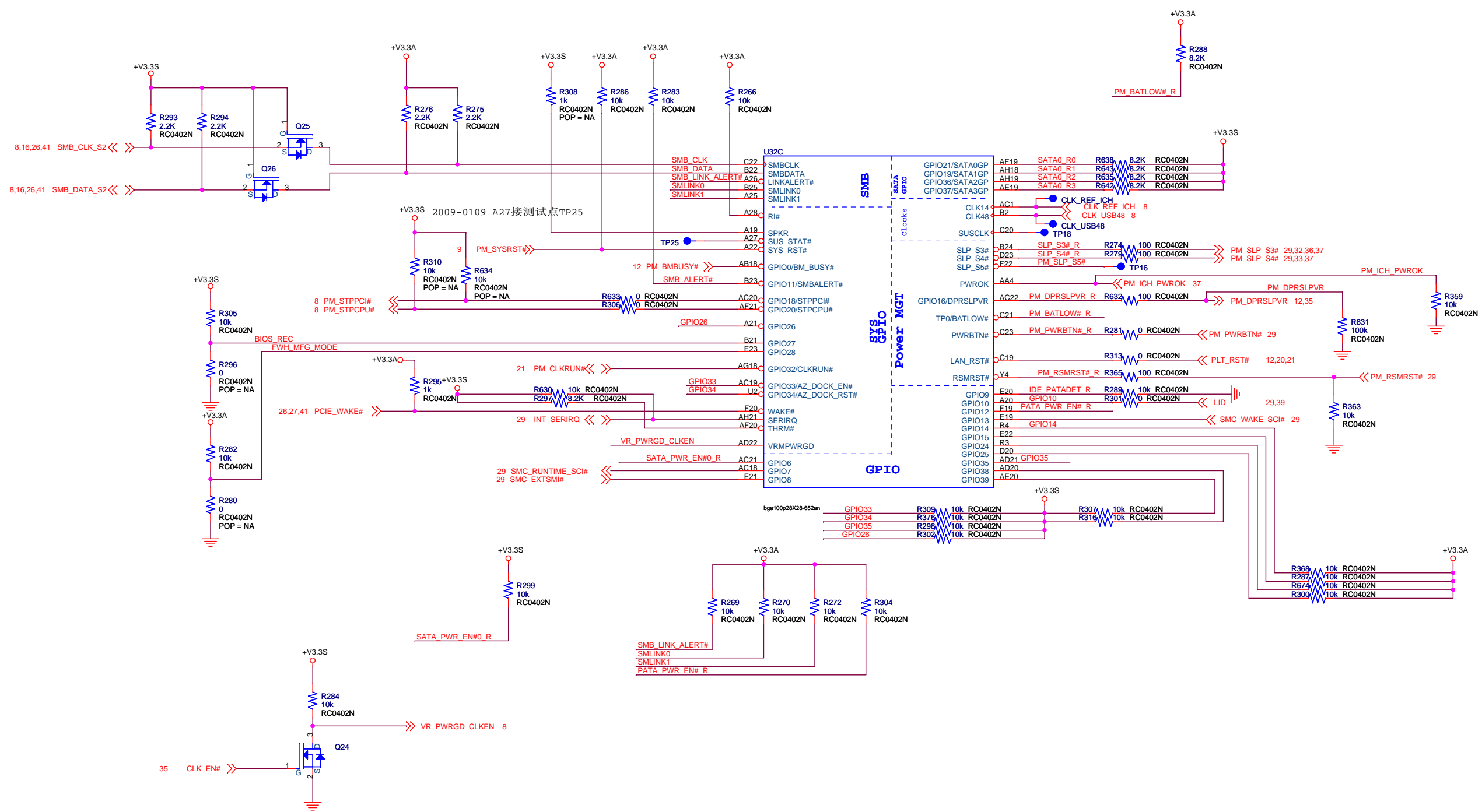
USN PORT	USED FOR
USB0	Outside USB PORT1
USB1	Outside USB PORT2
USB2	Outside USB PORT3
USB3	Card Reader
USB4	Bluetooth
USB5	WLAN
USB6	Camera
USB7	CDMA

PCI Pullups



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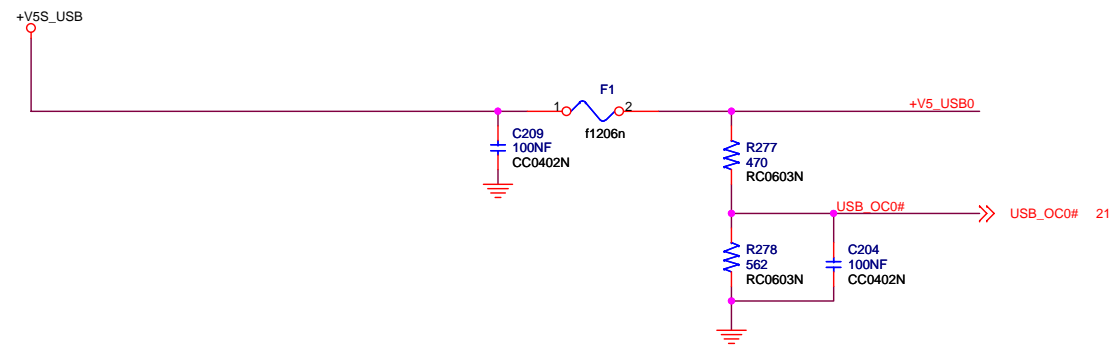
	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	ICH7M2	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 21 of 43	



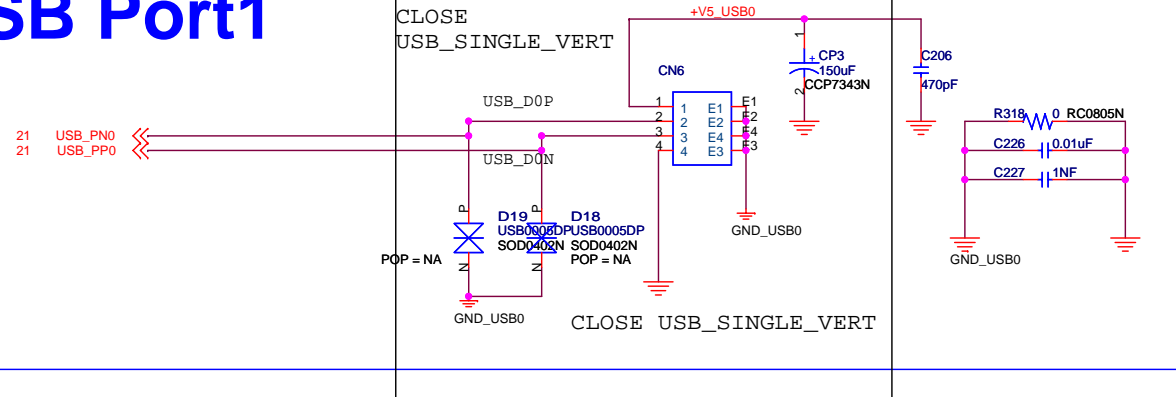
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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	ICH7M3	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 22 of 43	

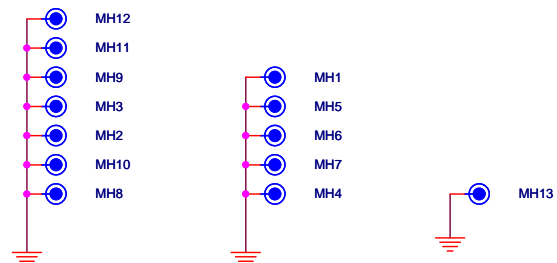
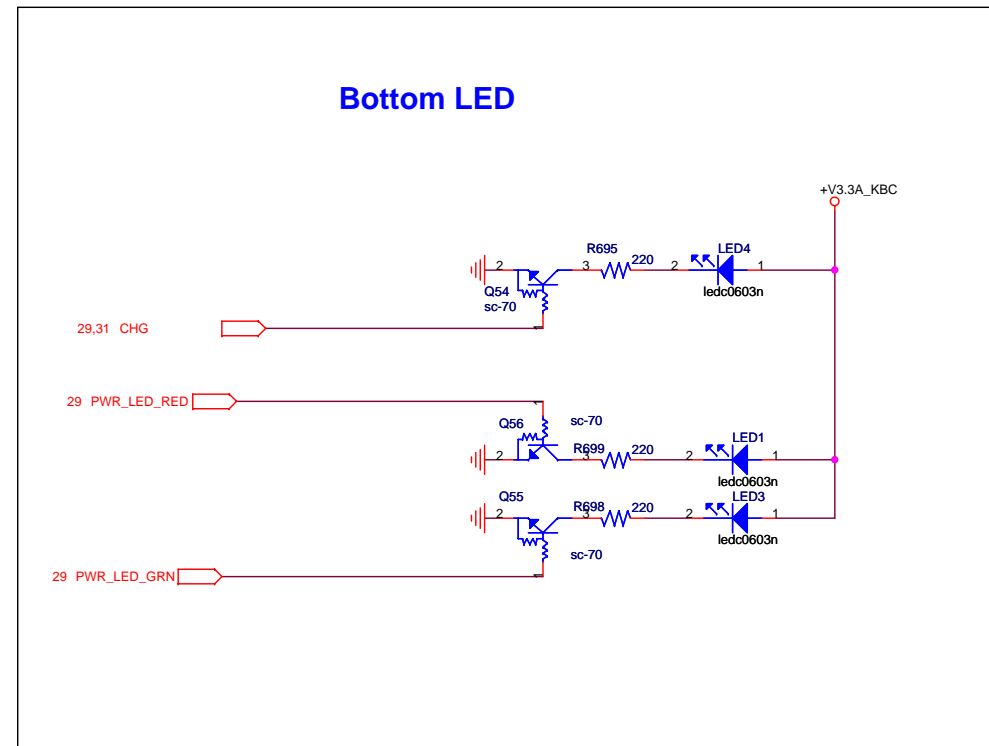
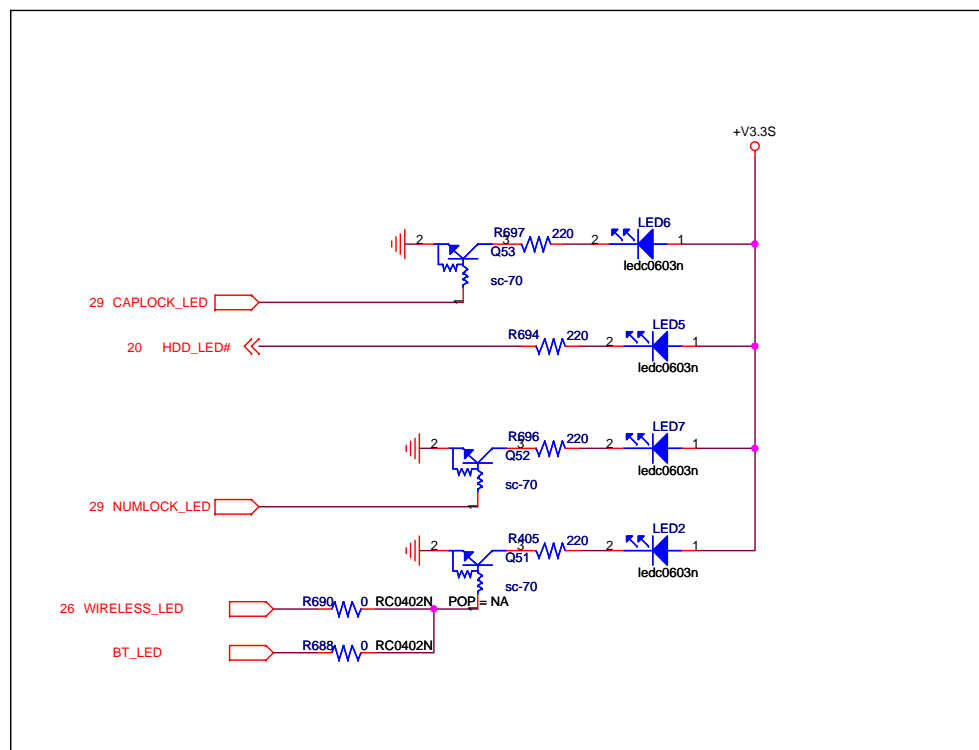
USB



USB Port1

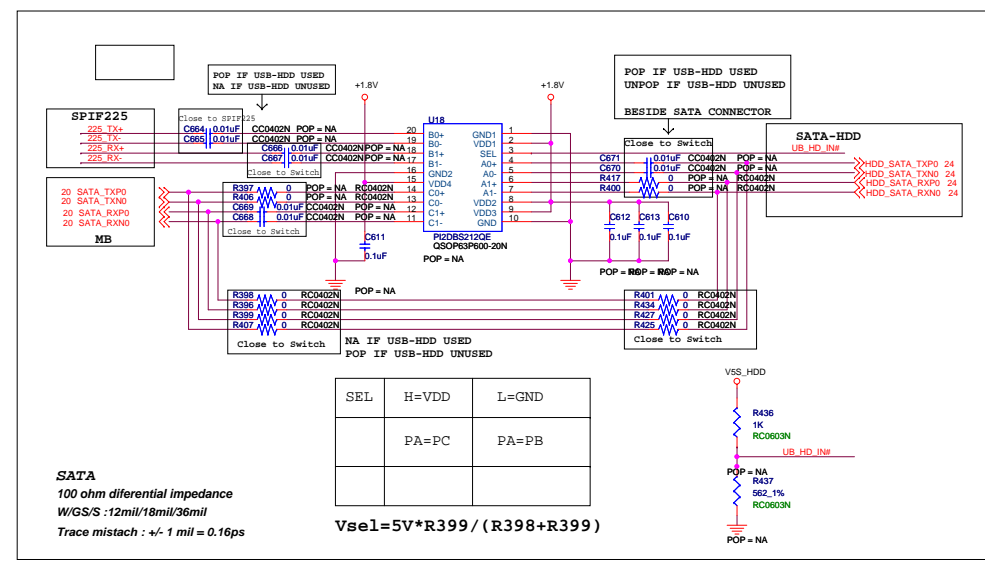
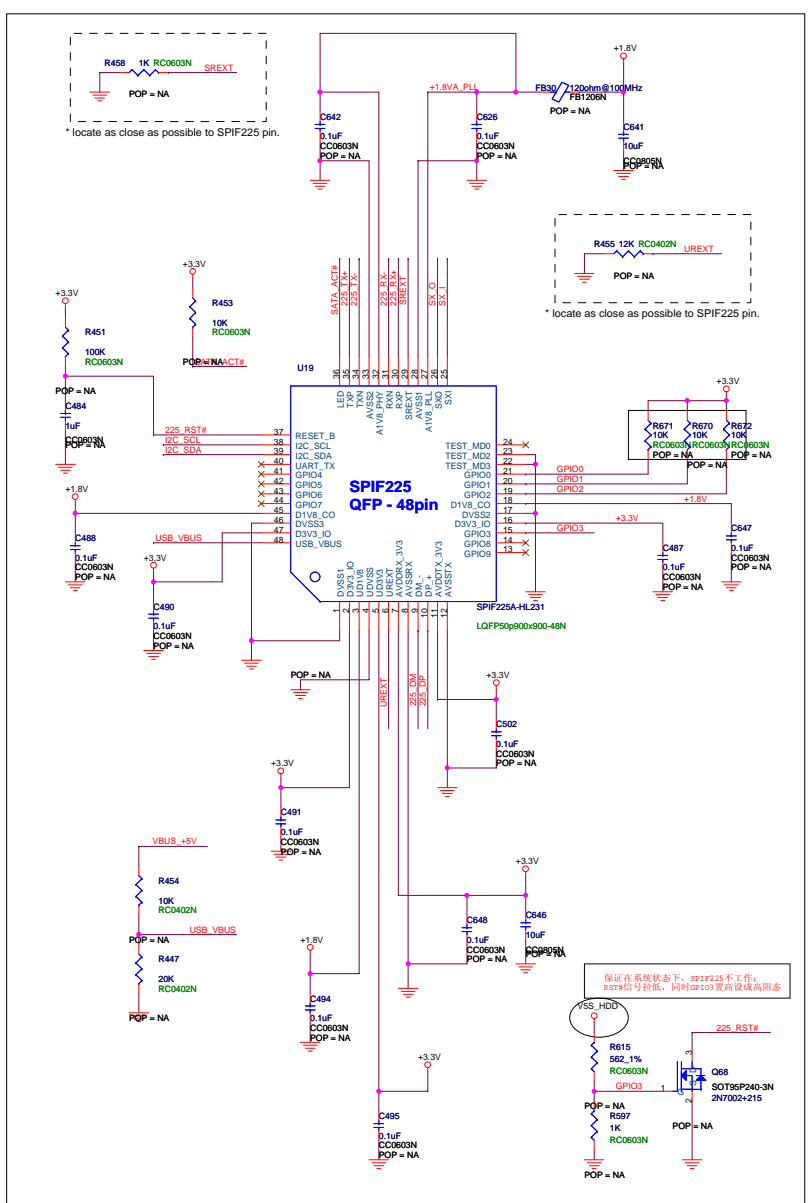
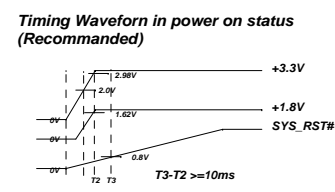
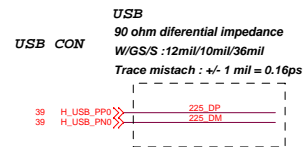


LED



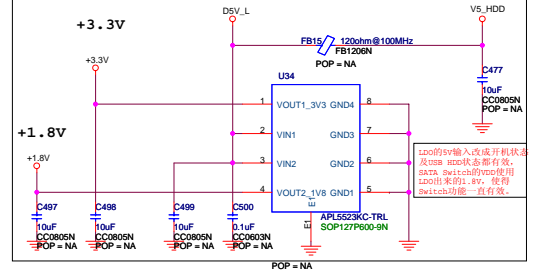
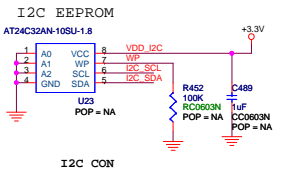
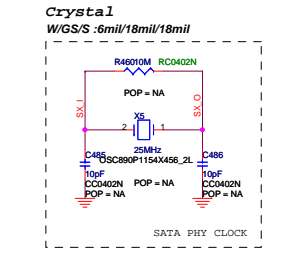
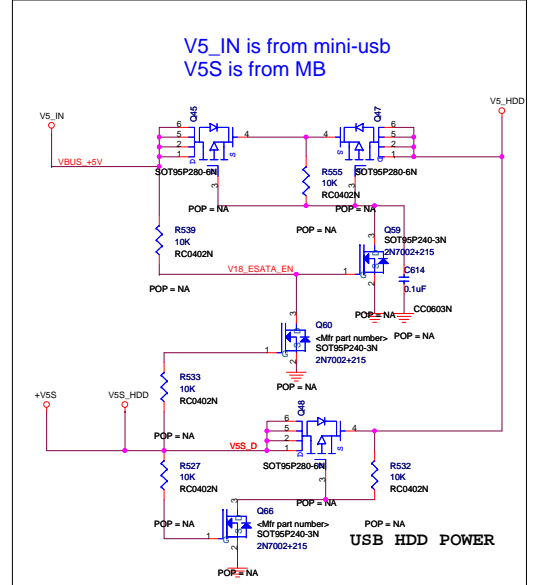
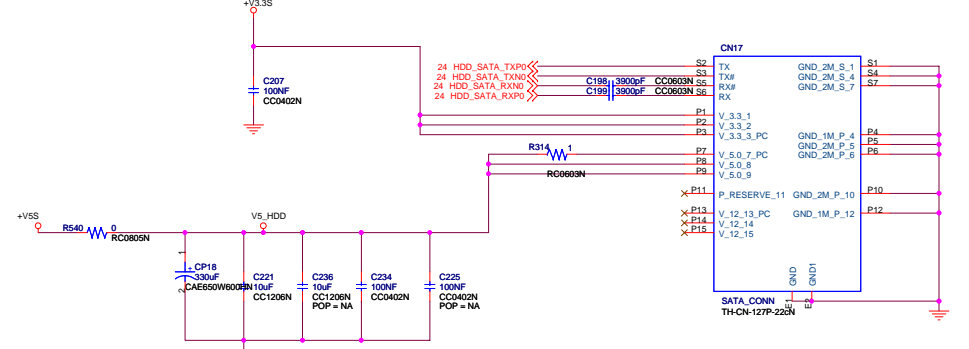
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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	USB_PORT&LED	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 24 of 43	



SEL	H=VDD	L=GND
PA=PC	PA=PB	

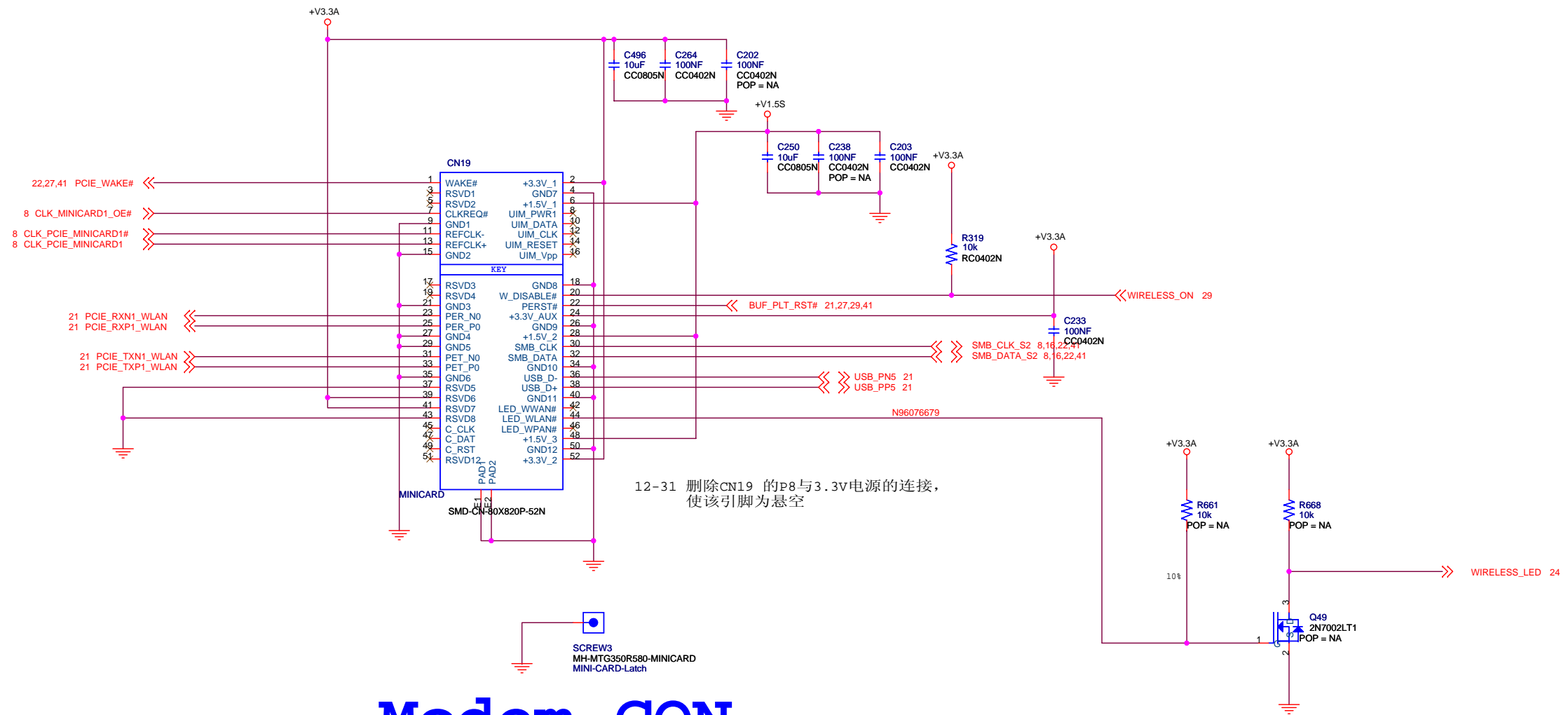
$V_{sel} = 5V * R399 / (R398 + R399)$



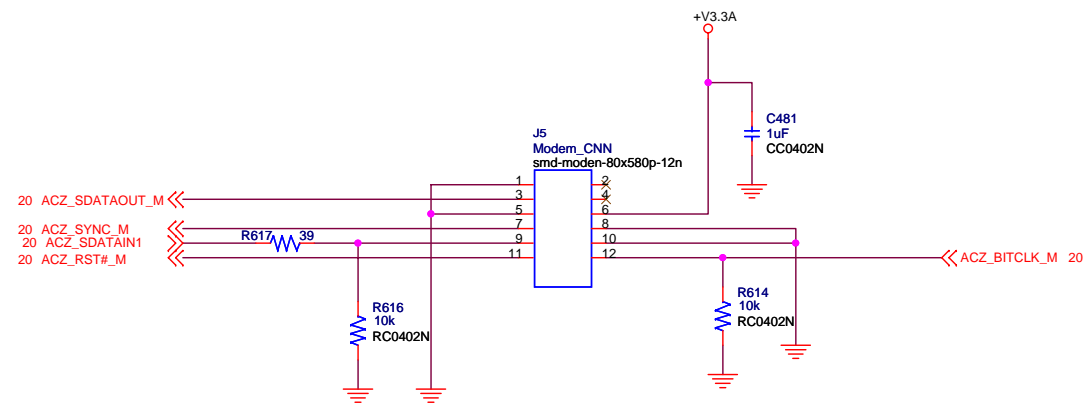
12-16 增加USB HDD相关元器件

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Minicard



Modem CON

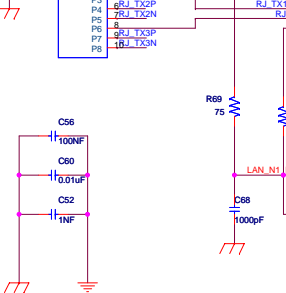
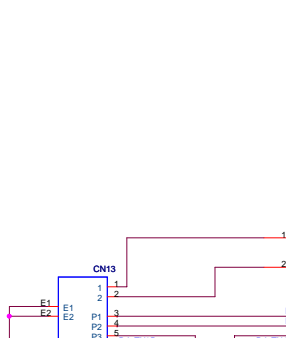


MH11, MH12为MODEN的螺柱

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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	MiniCard-WLAN&Modem	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 26	of 43

位置	0/100M	100/1000M
C181, C163, C220, C166, C183, C180, C511, C245, C267, C241, C263, C272	0.1uF	NA
C111, C270	22uF	NA
C276	0.1uF	1uF
L1	4.7uH	NA
R140, R135, R109	OR	NA
R144, R503	NA	OR
T2	H1601S	NA
C28	0.1uF	NA
R420, R423, R424, R619	OR	NA
U22	RTL8111C	RTL8102E
	-VC-GR	-VB-GR



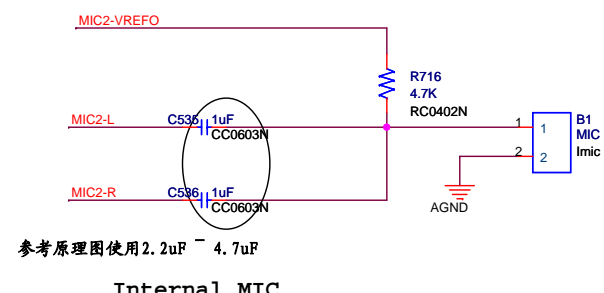
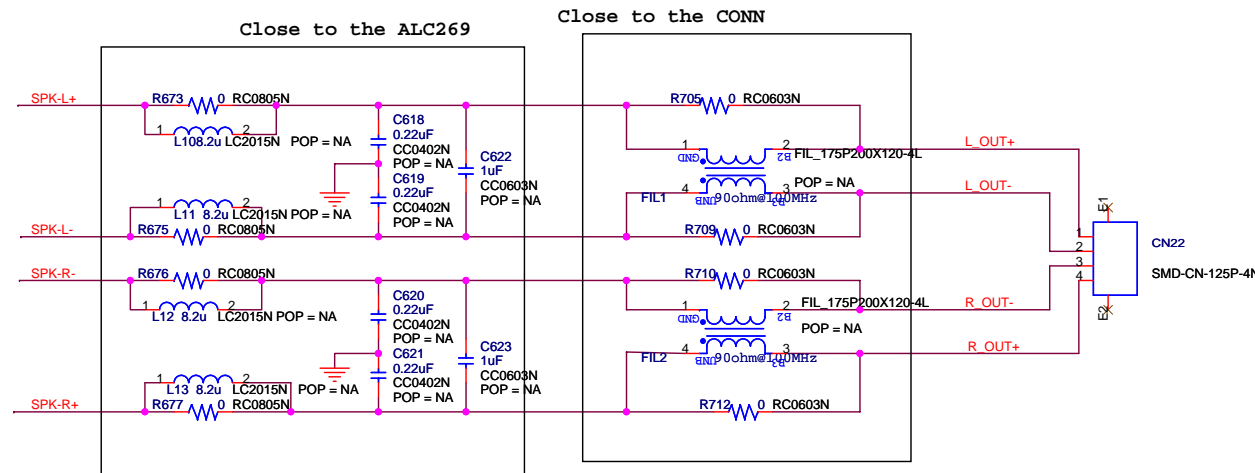
<<Attention>>
 Surges of PVDD >7V duration 0.1ms when class D amplifier is working may damage the amplifier, 10uF tantalum capacitors are required at PVDD1 and PVDD2 to suppress the surge.

根据实际测试结果进行调整

<<Attention>>

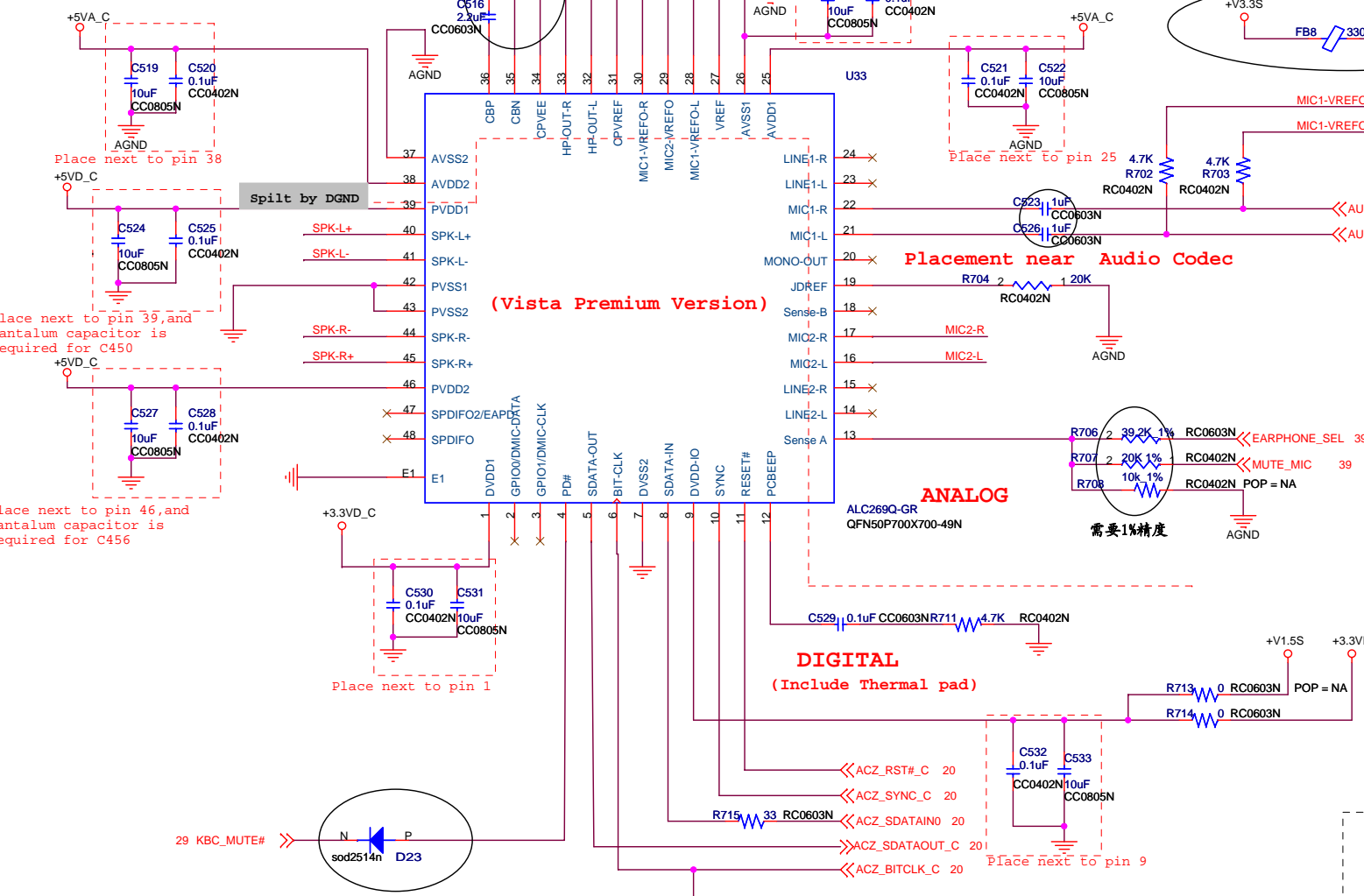
If you mount the LC filter(L1-L4;C4/C11;C2/C7/C10/C13),Please let them together and close to codec. If the PCB trace and Speaker wire length is less than 20cm, you don't need the LC filter(L1-L4;C4/C11) to eliminate the EMI,if L1,L2,L3,L4 are replaced by 0 ohm/1.6A resistor(please don't use general bead, because it may influence the THD+N quality), and C4,C11 should be NC.And,please make the trace length/ Speaker wire length of SPKL+/L-/R+/R- be the same as possible as you can. C2/C7/C10/C13 are reserved for EMI fine-tune; For EMI issue, please also refer our ALC269 Layout guide document

Demodulation Filter
 Placement near Audio Codec



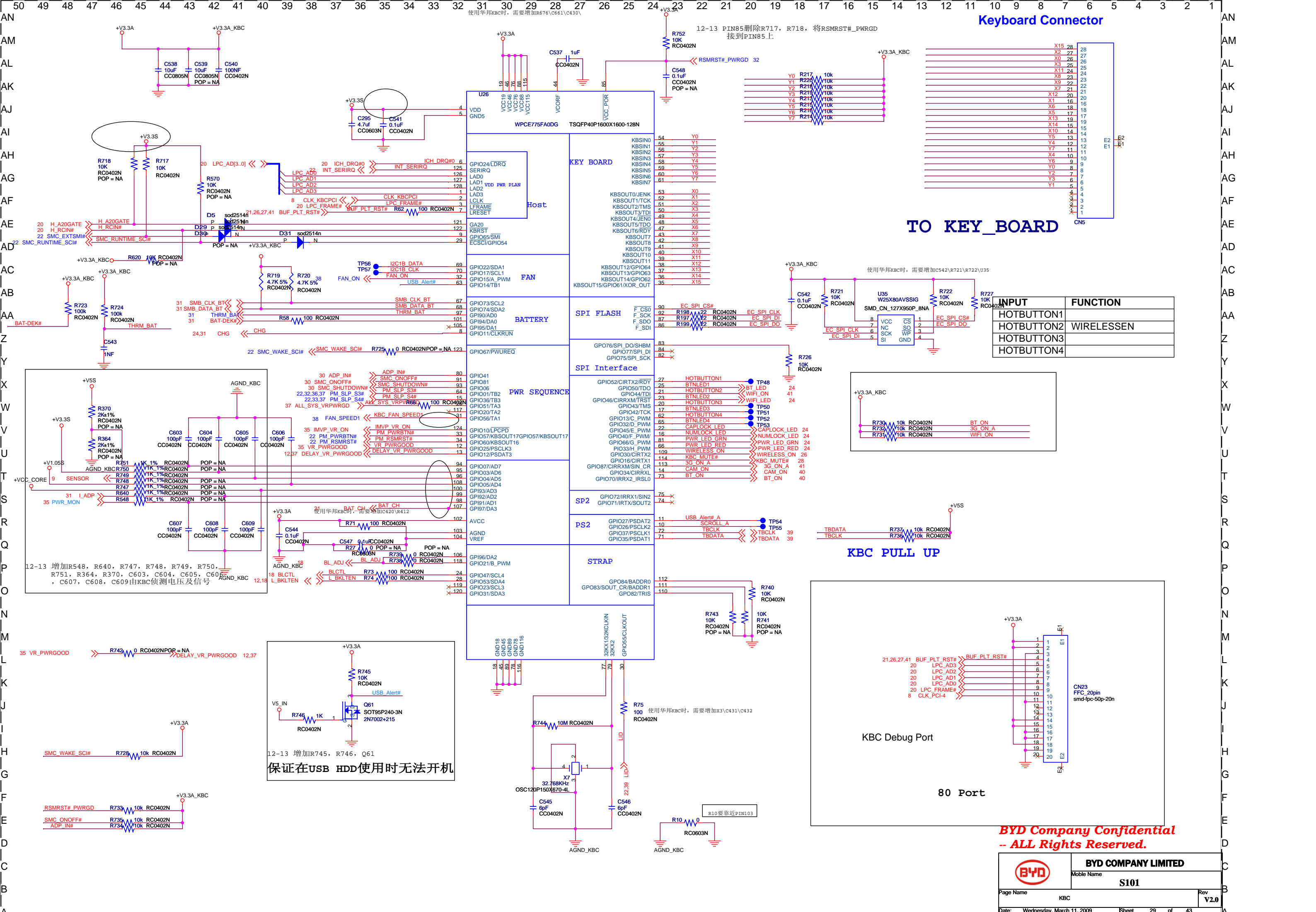
Configuration for ALC269 (Mobile system only)
 Internal Speaker + Internal Mic + PCBEEP +(SPDIF Out)
 HeadPhone + External Mic + SPDIF Out

Layout注意: 每组电源与地之间的电容需要放置在该组对应的两个PIN之间



<<Attention>>
 For power_on/off de-pop circuit and system booting warning signal: Please System BIOS Engineer Note :
 1. If you want the system make warning signal after power on, please let EC_MUTE# High first.
 2. When you want to exit your Bios Programming Code, please let the EC_MUTE# Low.(The programming is different from before .)

PD#=0V : Power down Class D SPK amplifier
 PD#=3.3V : Power up Class D SPK amplifier



Keyboard Connector

TO KEY_BOARD

INPUT	FUNCTION
HOTBUTTON1	
HOTBUTTON2	WIRELESSEN
HOTBUTTON3	
HOTBUTTON4	

KBC PULL UP

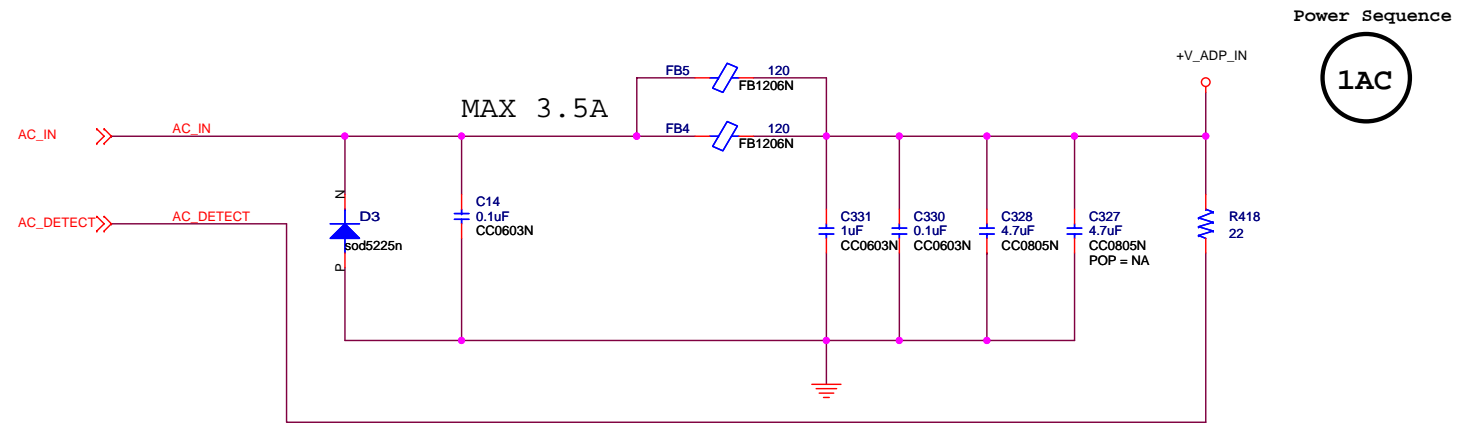
KBC Debug Port

80 Port

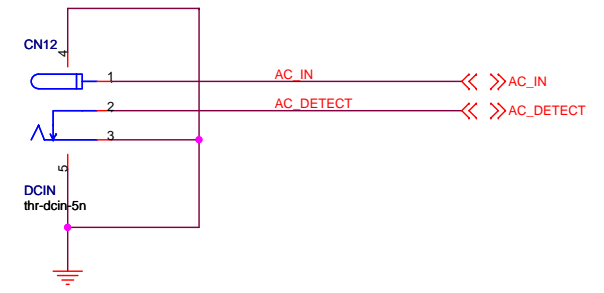
12-13 增加R745, R746, Q61
保证在USB HDD使用时无法开机

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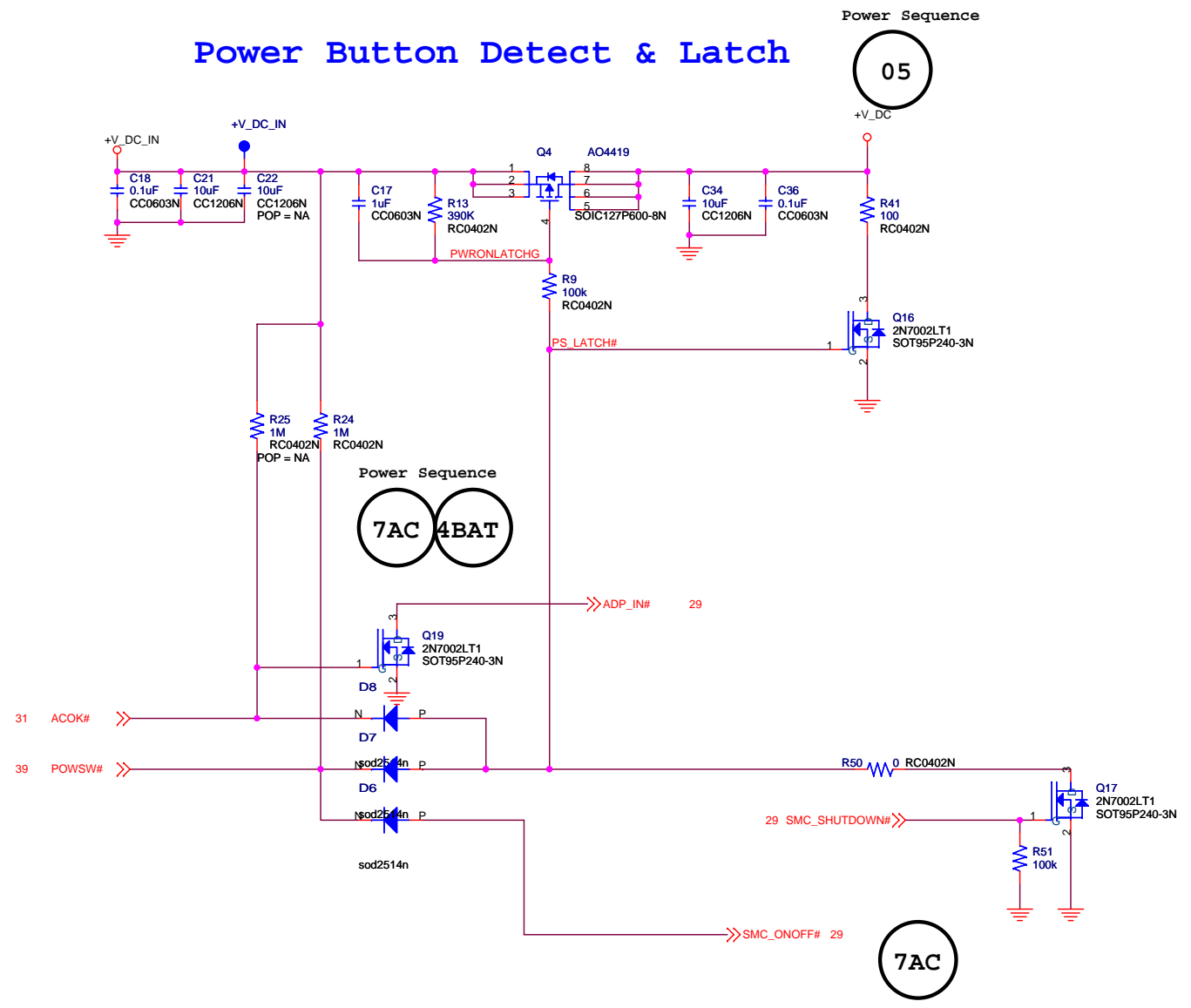
	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	KBC	Rev V2.0
Date:	Wednesday, March 11, 2009	Sheet 29 of 43



Adaptor In



Power Button Detect & Latch



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		BYD COMPANY LIMITED	
		Mobile Name S101	
Page Name ACIN & 5V_LDO	Rev V2.0		Date: Wednesday, March 11, 2009
Sheet 30 of 43			

7AC

12-17 修改R785, R789电阻值, 将充电电压保护值设定在15V

12-17 修改R790的阻值为68K, 设定充电保护电流为3.06A

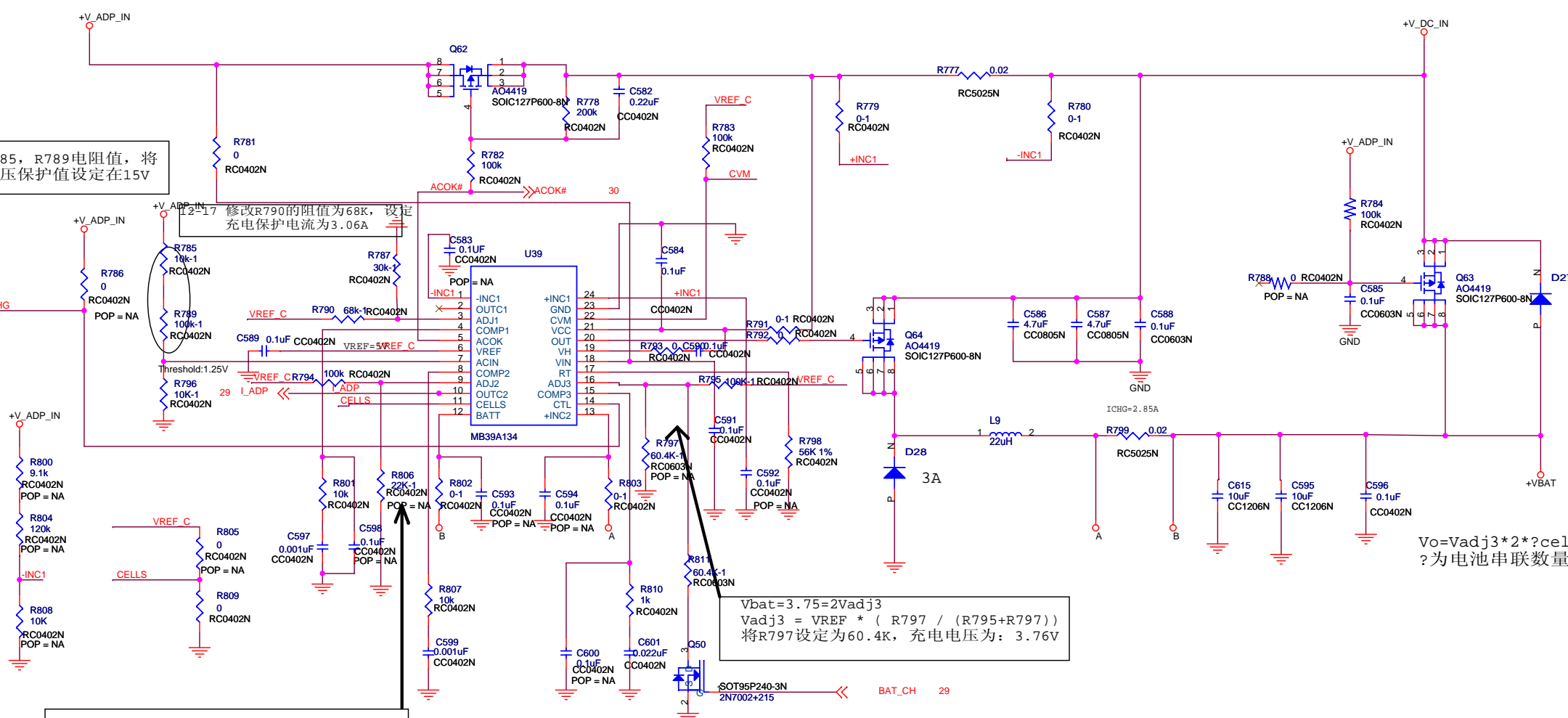
$V_{bat} = 3.75 = 2V_{adj3}$
 $V_{adj3} = VREF * (R797 / (R795 + R797))$
 将R797设定为60.4K, 充电电压为: 3.76V

$I_{bat} = 1.7A = 2(V_{adj2} - 0.075)$
 $V_{adj2} = VREF * (R806 / (R794 + R806))$
 将R806设定为24K, 充电电流为: 1.785A

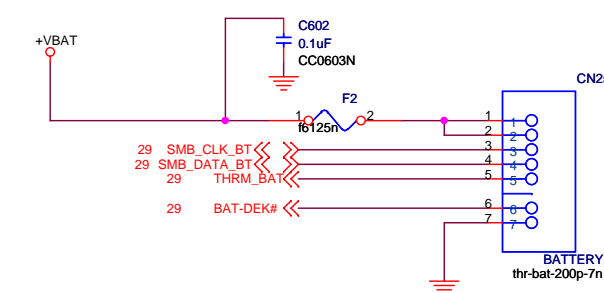
Place R12=0 for output 4.2V/cell
 Place R13=0 for output 4.1V/cell

Place R18=0 for 4cell operation
 Place R19=0 for 3cell operation
 Open R18 & R19 for 2cell operation

$V_o = V_{adj3} * 2 * \text{cell}$
 ?为电池串联数量



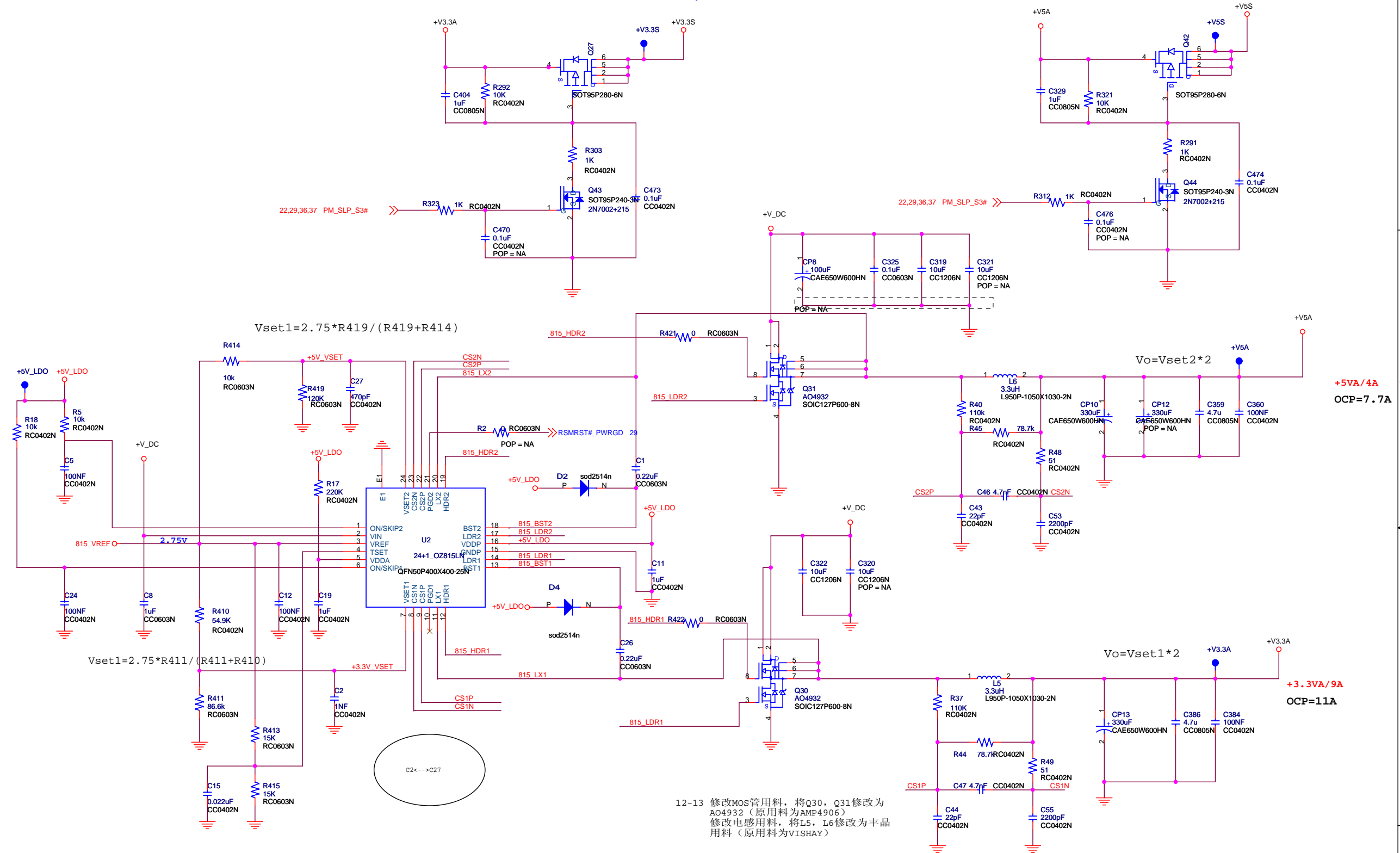
Battery Connector



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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	PWR_Charger & B conn	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 31 of 43	

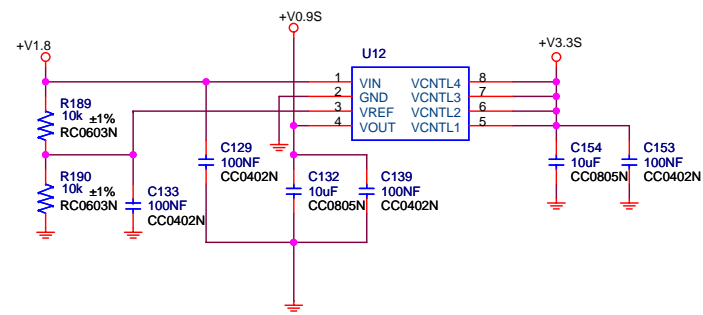
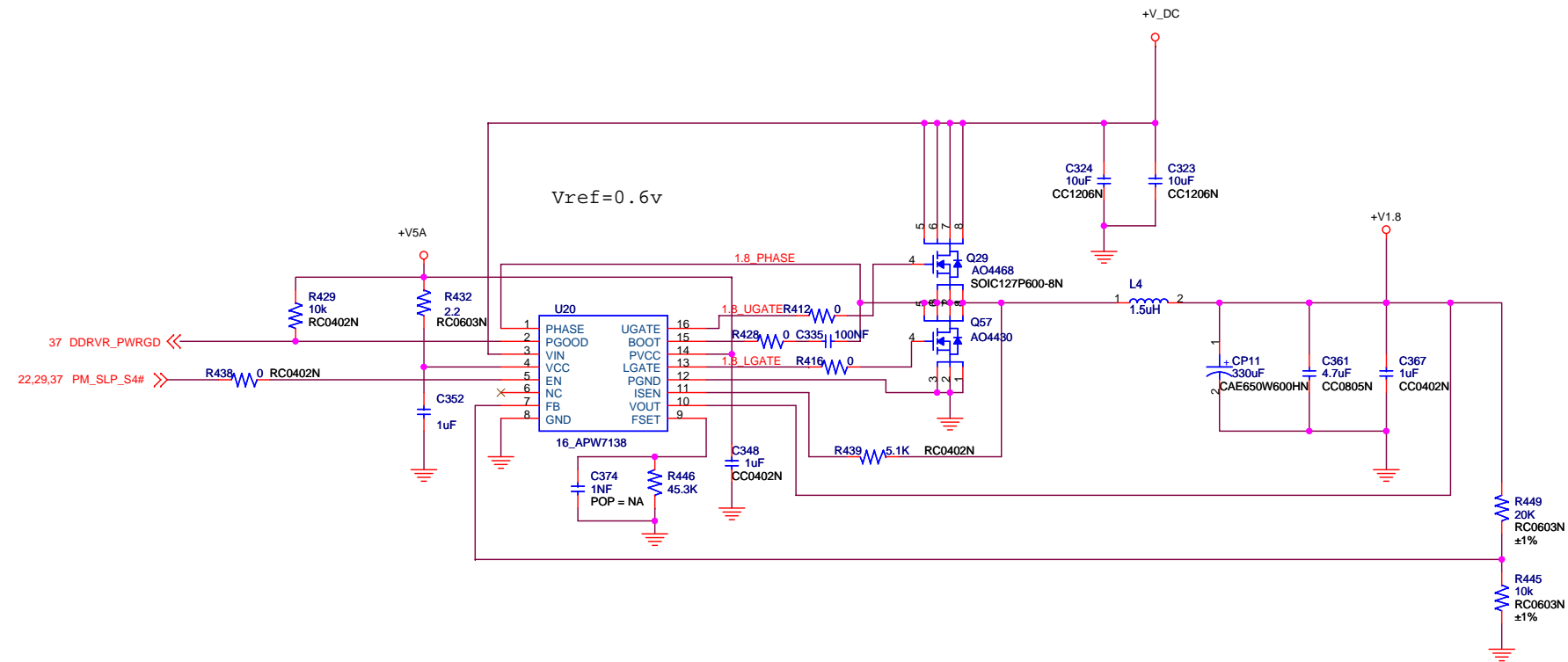
S3 /S4 Power control



12-13 修改MOS管用料, 将Q30, Q31修改为AO4932 (原用料为AMP4906)
 修改电感用料, 将L5, L6修改为丰晶用料 (原用料为VISHAY)

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		BYD COMPANY LIMITED	
		Mobile Name S101	
Page Name PWR_+V5A & +V3.3A	Date: Wednesday, March 11, 2009		Rev V2.0
Sheet 32 of 43			

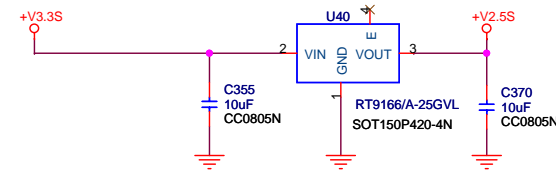
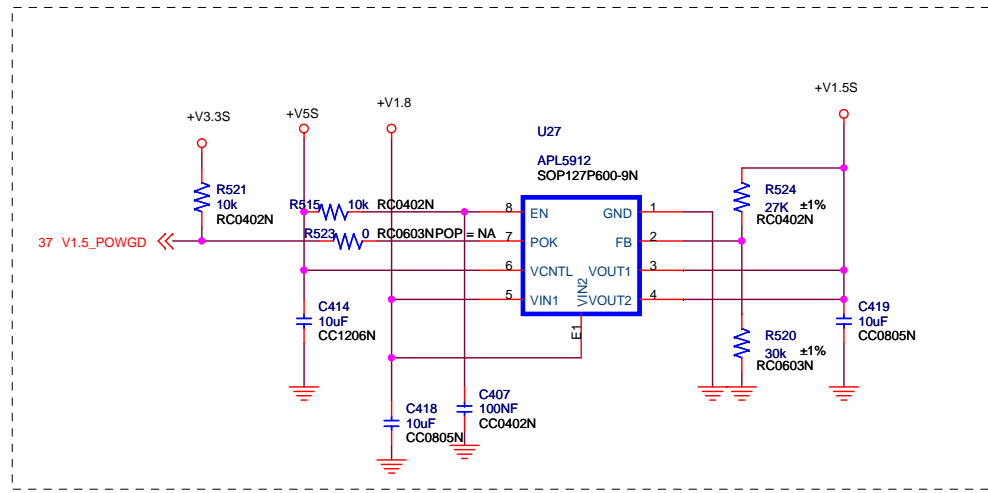


Vset	VDDQ	VTT&VTTREF	NOTE
GND	2.5	VDDQ/2	DDR
VDDA	1.8	VDDQ/2	DDR2
0.5-2.75	Vset	VDDQ/2	Adjustable

$$Vset = Vref * R4 / (R3 + R4)$$

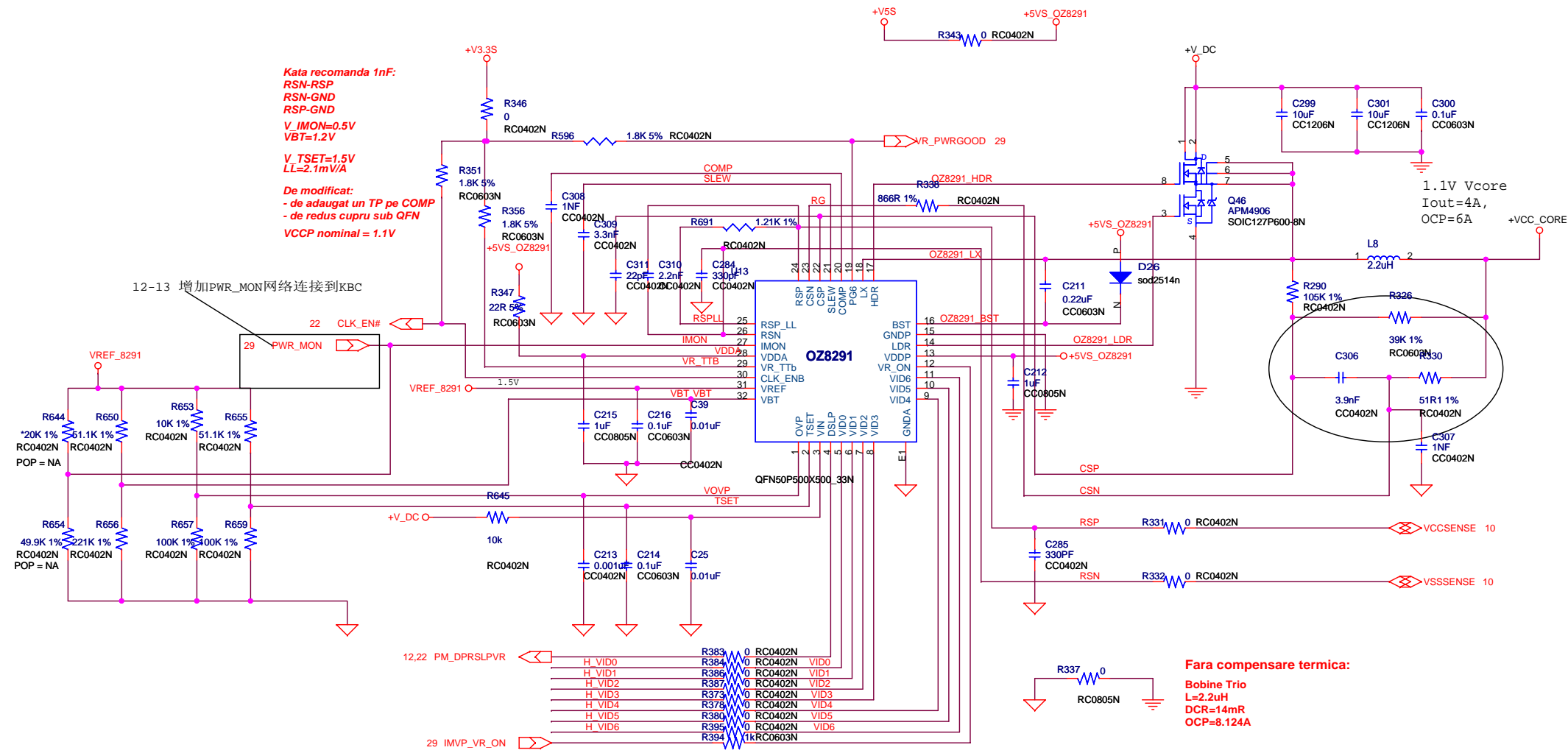
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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	PWR_+V1.8 & +V1.05S	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 33 of 43	



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	BYD COMPANY LIMITED	
	Mobile Name S101	
Page Name PWR_+V1.5S & +V2.5S	Rev V2.0	
Date: Wednesday, March 11, 2009	Sheet	34 of 43



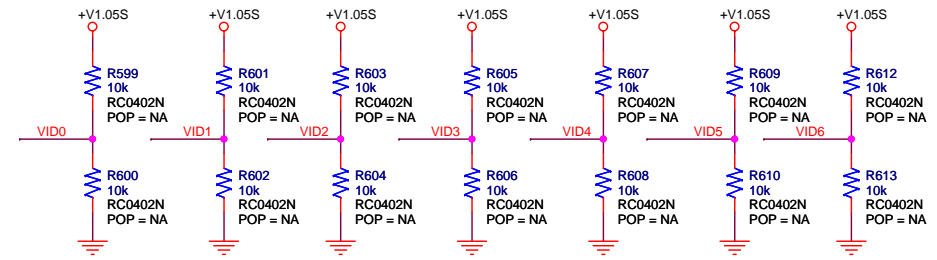
Kata recomandă 1nF:
 RSN-RSP
 RSN-GND
 RSP-GND
 V_{IMON}=0.5V
 V_{BT}=1.2V
 V_{TSET}=1.5V
 LL=2.1mVA
De modificat:
 - de adăugat un TP pe COMP
 - de redus cupru sub QFN
 VCCP nominal = 1.1V

12-13 增加PWR_MON网络连接到KBC

Fara compensare termica:
 Bobine Trio
 L=2.2uH
 DCR=14mR
 OCP=8.124A

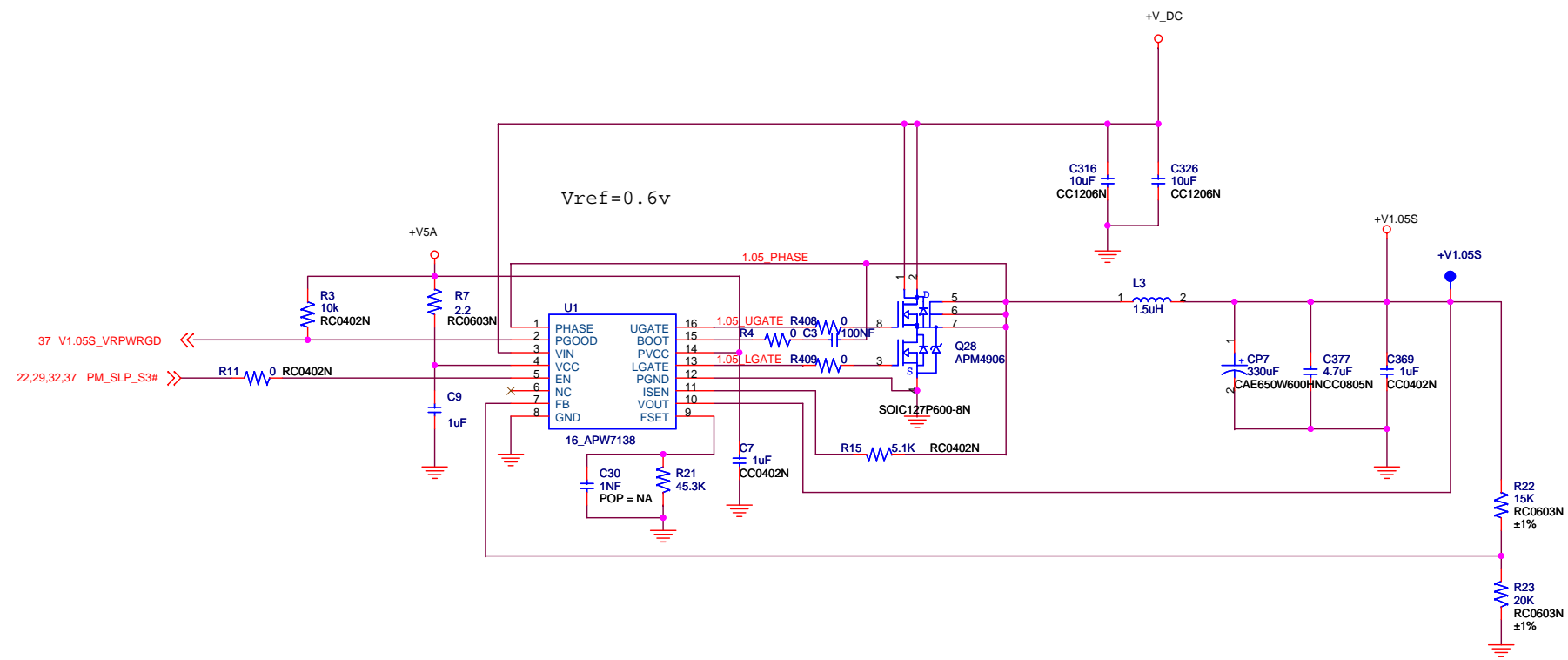
Vtset=1.0V, f=300~400KHz

10 H_VID[6..0] <<<



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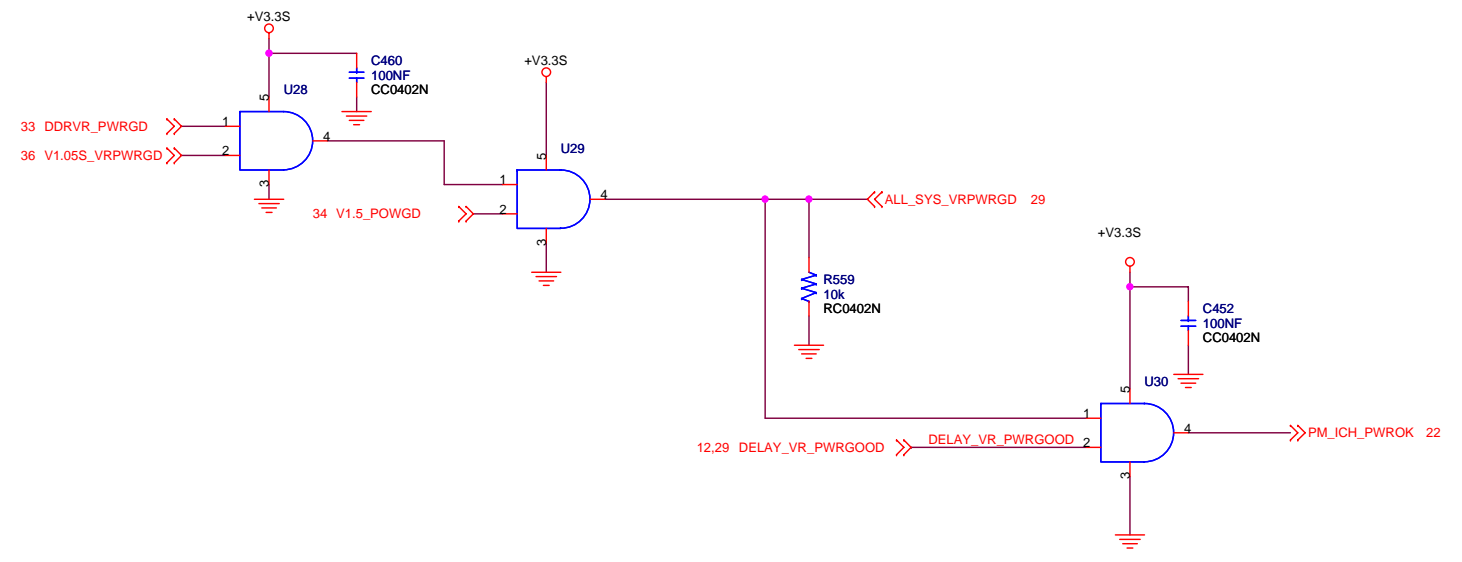
	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	V_CORE	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 35 of 43	



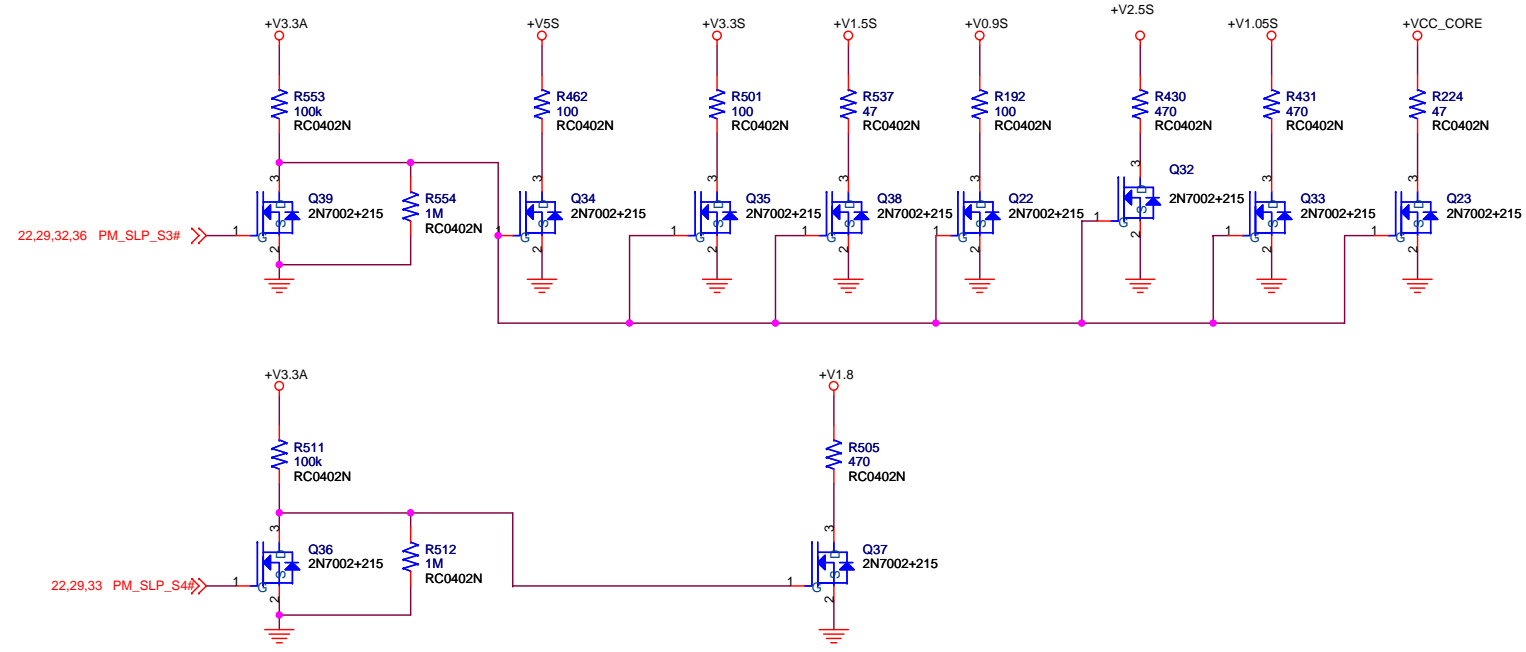
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	BYD COMPANY LIMITED	
	Module Name	S101
Page Name	USB HDD	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 36 of 43	

Power Good



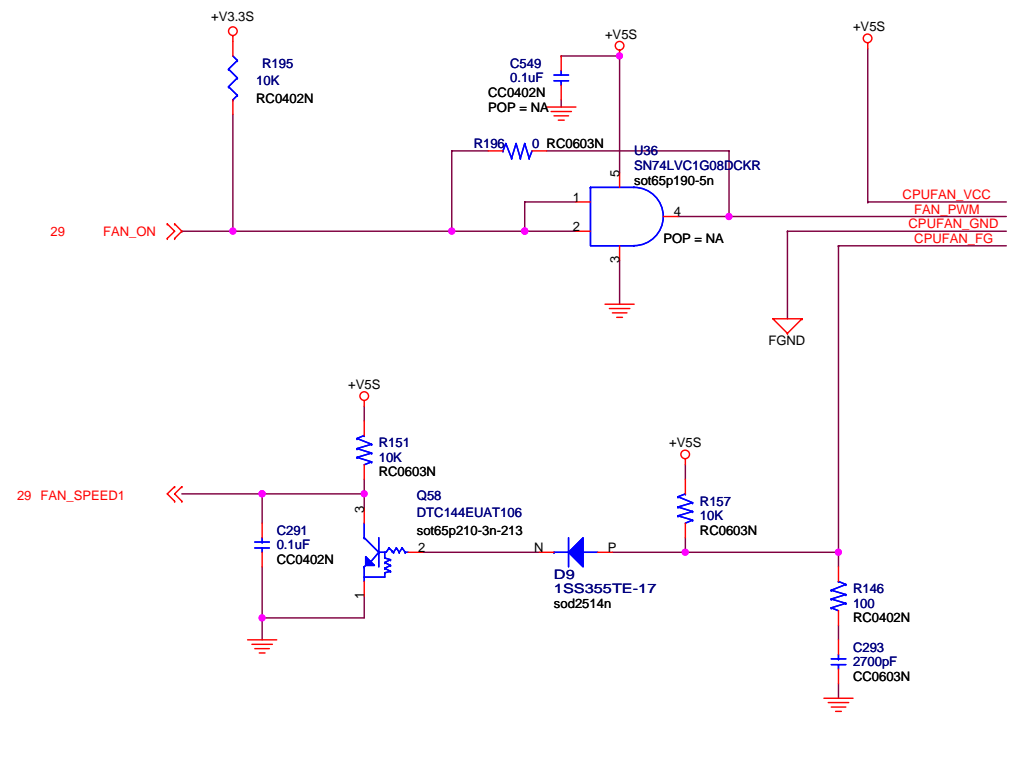
Power Discharge Circuit



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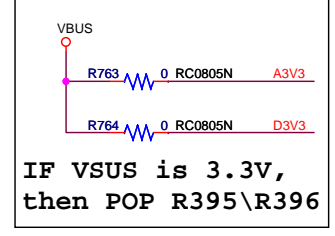
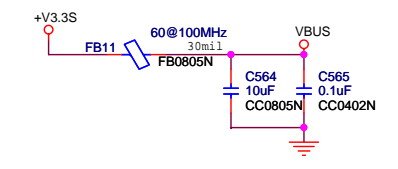
	BYD COMPANY LIMITED	
	Mobile Name	
S101		
Page Name	S3_S4_CNTR&Discharge	Rev
Date: Wednesday, March 11, 2009	Sheet 37 of 43	V2.0

FAN

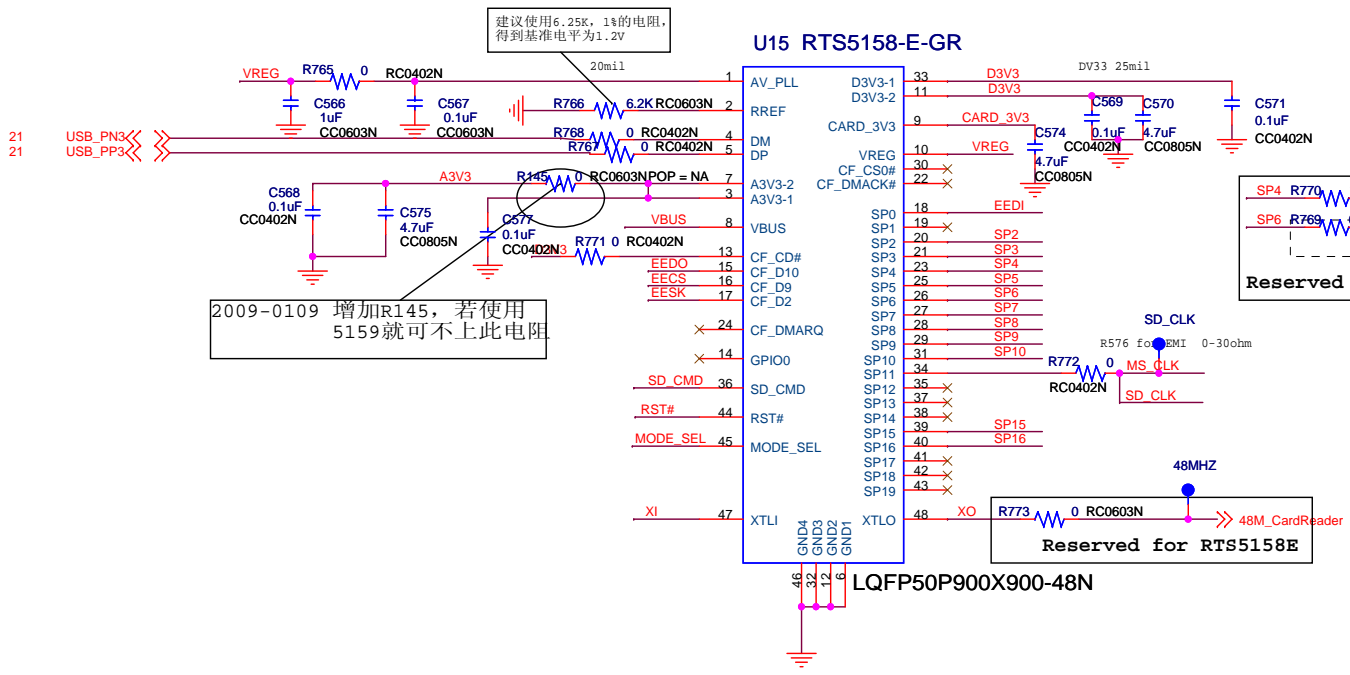
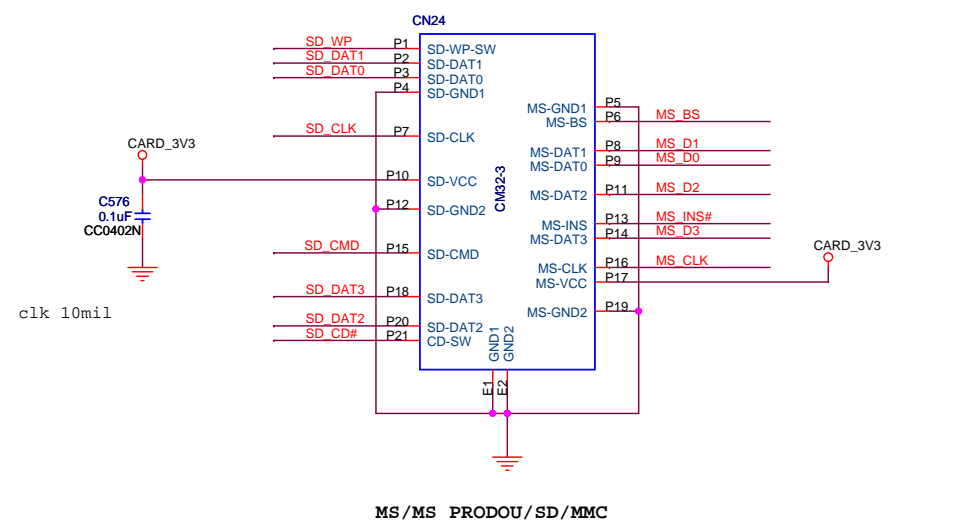


3-11 修改风扇连接器PIN定义

SP2	SD_WP
SP3	SD_CD#
SP5	MS_BS
SP6	MS_D1
SP7	SD_DAT0
SP8	MS_D2
SP9	MS_INS#
SP10	MS_D3
SP15	SD_DAT3
SP16	SD_DAT2



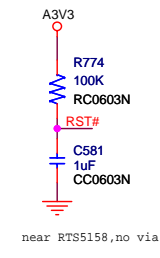
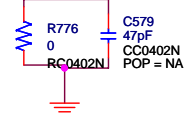
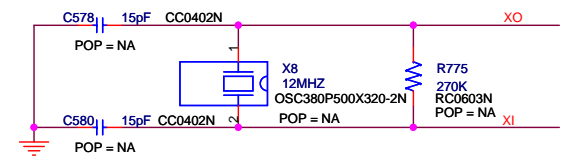
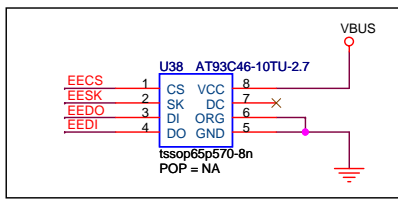
IF VSUS is 3.3V,
then POP R395\R396



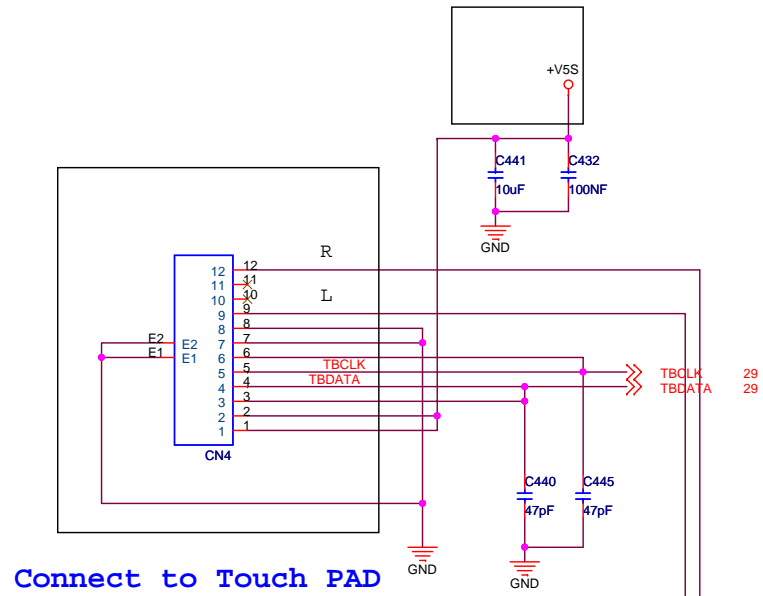
2009-0109 增加R145, 若使用
5159就可不上此电阻

Reserved for RTS5158E

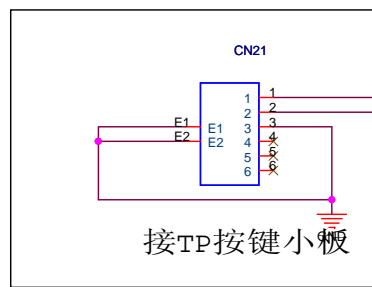
Reserved for RTS5158E



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Connect to Touch PAD

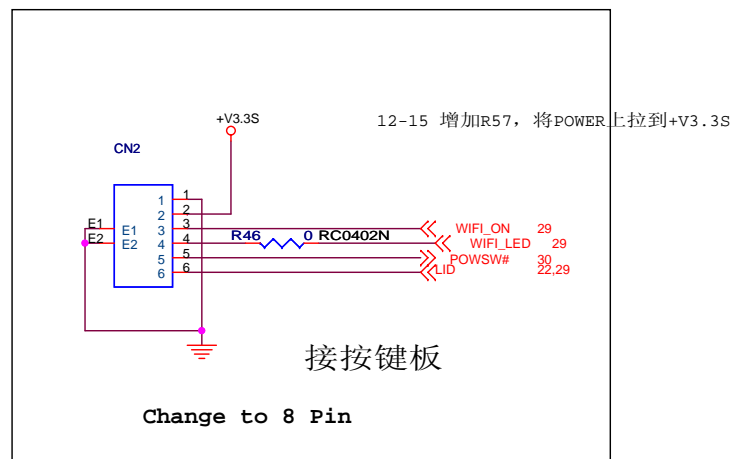


Connect to L R button

TO TP_BOARD

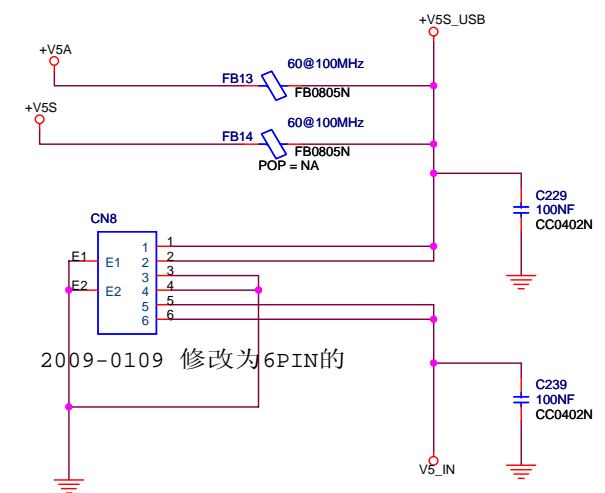
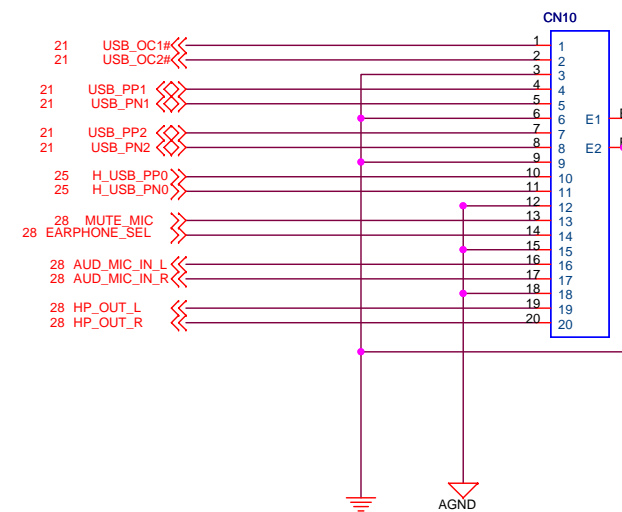
TO USB&Audio_BOARD

TO SW_BOARD



接按键板

Change to 8 Pin

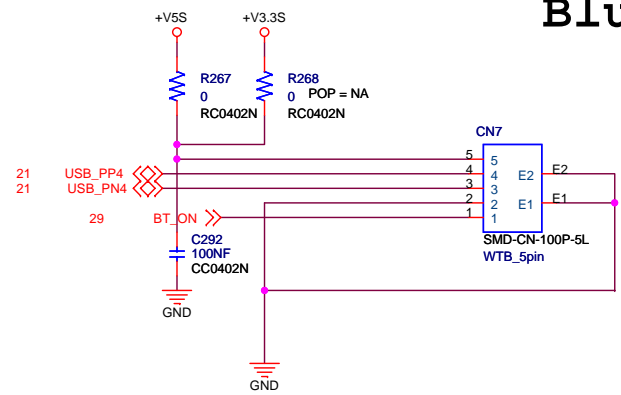


12-16 增加USB HDD电源网络

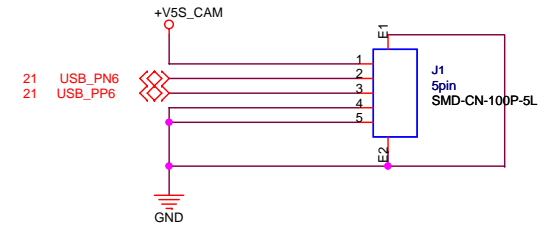
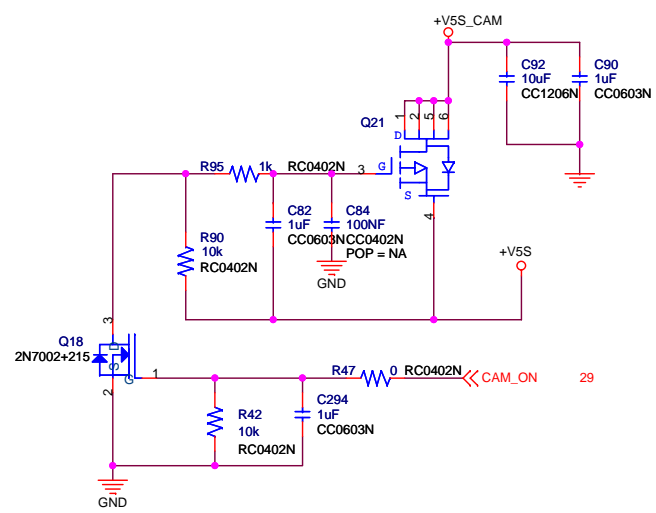
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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	Main_board CONN	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 39 of 43	


Bluetooth CONN



Camera CONN




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	BYD COMPANY LIMITED	
	Mobile Name S101	
Page Name 40 Option device:BT/Camera/TCM	Rev V2.0	
Date: Wednesday, March 11, 2009	Sheet 40 of 43	

Item	Page	Detail about what have been changed
1	42:43	删除了USB-HDD电路, ICH的SATA直接连接到HDD。SMC-ONOFF#直接连到KBC (USB_SMC_ONOFF#)
2	25	STXN0#等改成SATA_TXN0#等
3	29	USB_SMC_ONOFF#改为SMC_ONOFF#
4	33	1.8V输出MOS改成双MOS, 4468,4430
5	38	CardReader增加电源控制电路。
6	27	LAN 处E2PROM改成SOP8封装(4110 0400 0006)
7	39	KeyBoard连接器10Pin, 13Pin对换网络点
8	33:34	APL7138更改封装成: SOP63P600-16N
9	35	V-Core改成ISL6266
10	27	增加共模电感, 减小网卡的EMI辐射!
11		
12		
13		
14		
15		
16		

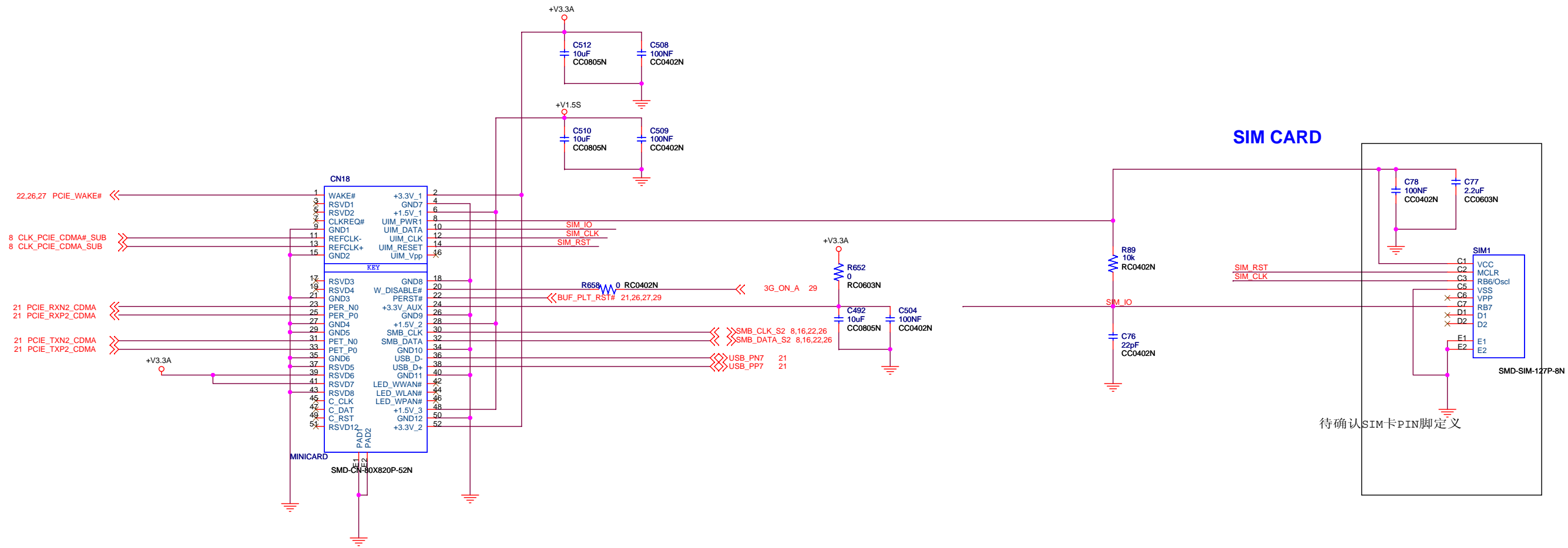
12-09 删除P20连接PATA连接器CN415的分页连接符
 删除P25的PATA连接器CN416的分页连接符,并将网络名前加"M_"与P20的网络名区分开
 将P25的PATA连接器周边的"GND"修改为"M-GND"
 将U7的PIN6的网络名设定为CLK_TPM, 即CN23的pin10连接到U7的pin6
 SENSOR NA
 H_CPURST# NA
 UB_HD_IN==WIRELESS_ON
 RSMRST#_PWRGD? ? ? ? ?
 PM_SUS_STAT# ? ? ? ? ?
 PM_SYSRST# ? ? ? ? ?

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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	3G_MINI_PCIE	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 41 of 43	

CDMA

SIM CARD



**Minicard ME Component,
Not bring to Layout**

SCREW1
MH-MTG350R580-MINICARD
MINI-CARD-Latch

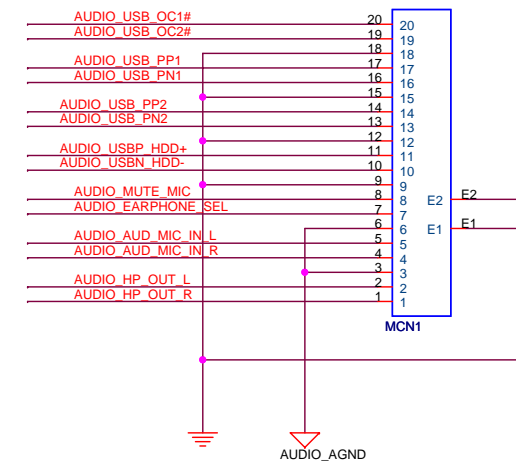
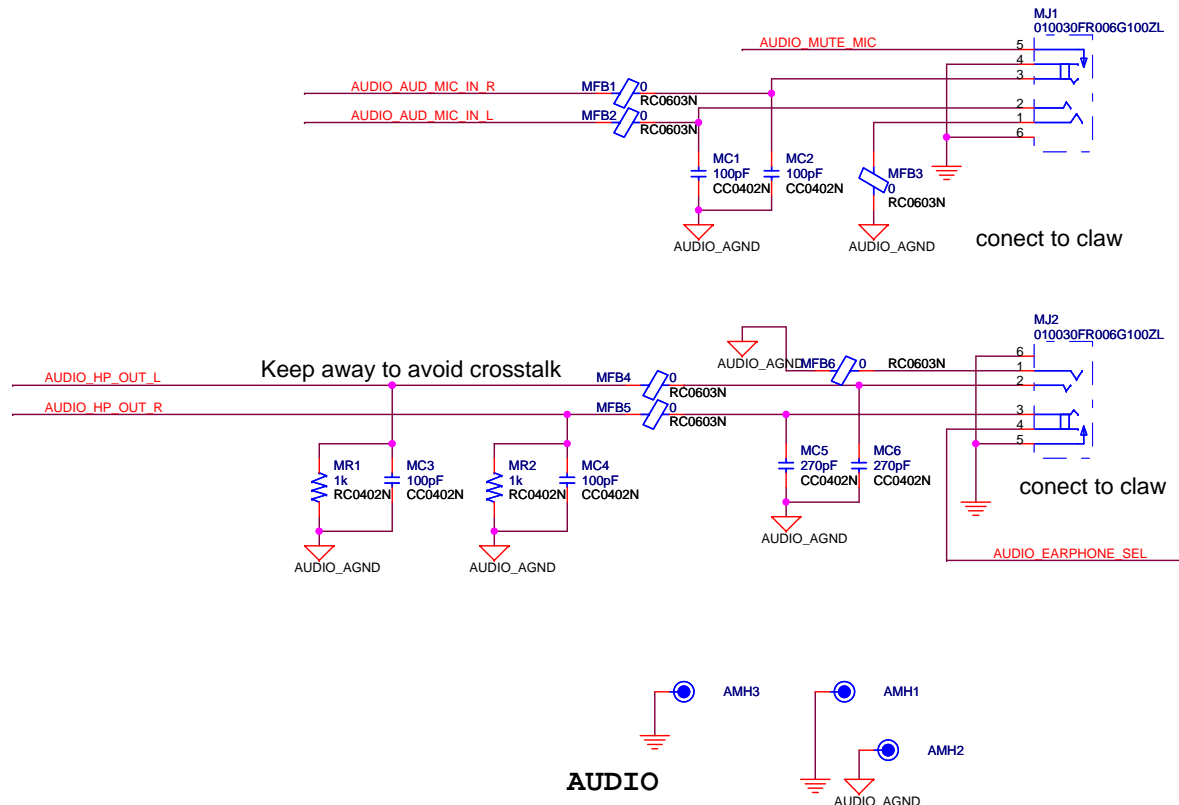
12-24 SCREW1, SCREW2 更换用料同M40

待确认SIM卡PIN脚定义

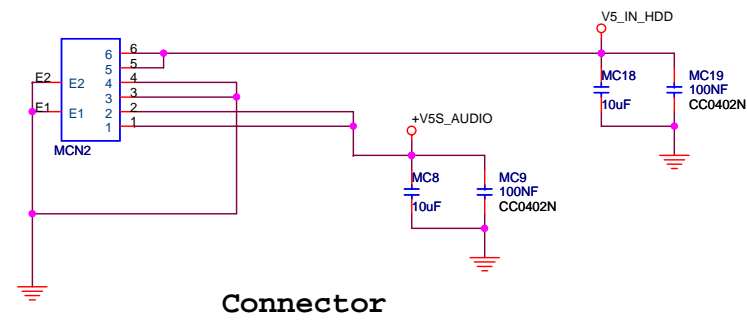
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	BYD COMPANY LIMITED	
	Mobile Name	S101
Page Name	ChangeList	Rev V2.0
Date: Wednesday, March 11, 2009	Sheet 42 of 43	

12-12 P43修改MIC和AUDIO 接口, 同M30用料

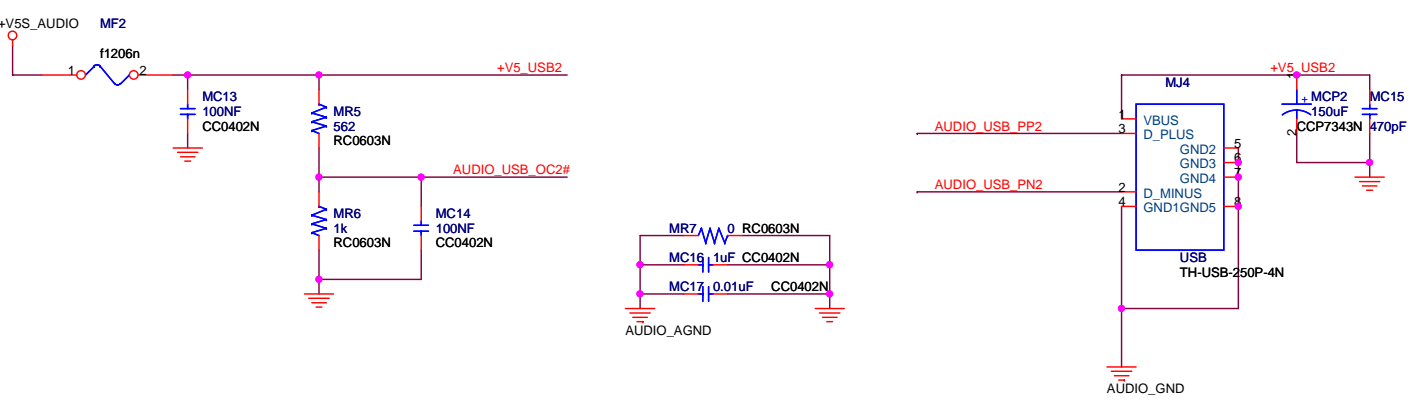
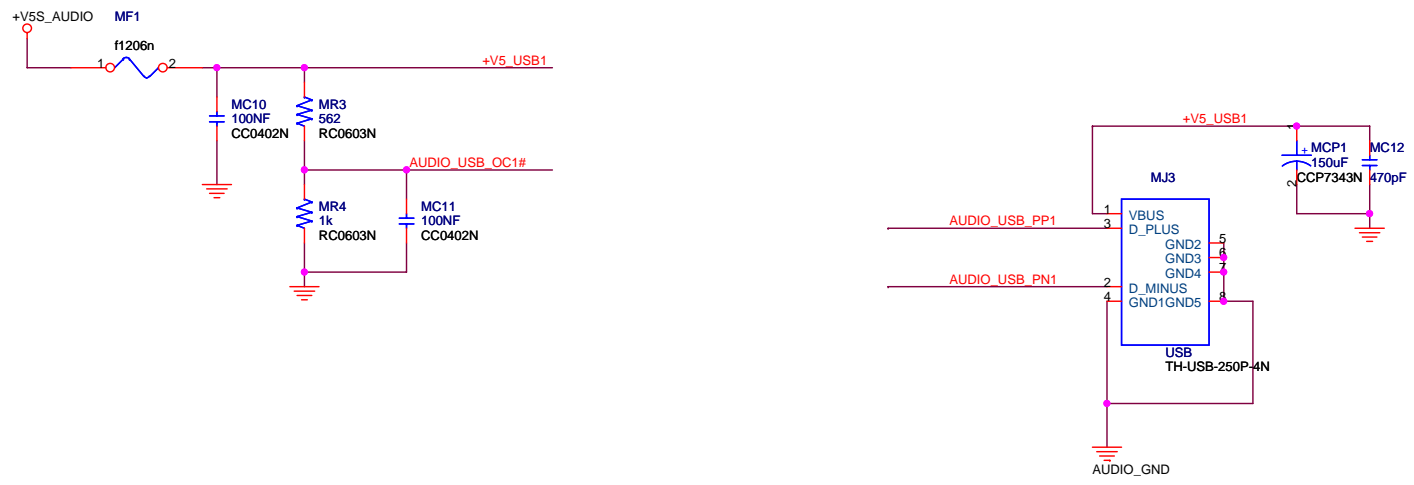


12-16 增加USB HDD电源网络



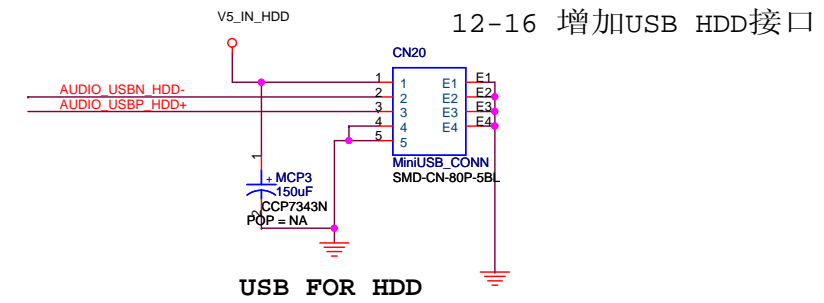
2009-0109 修改为6PIN的

Connector



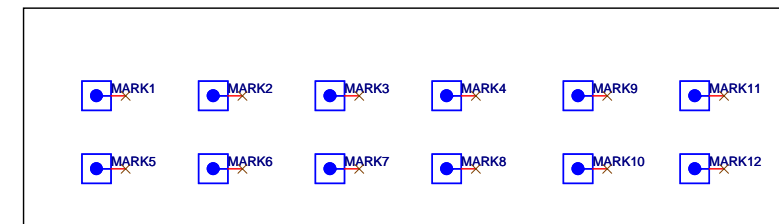
USB DJ

MiniUSB



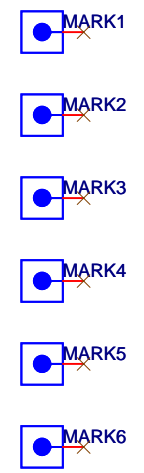
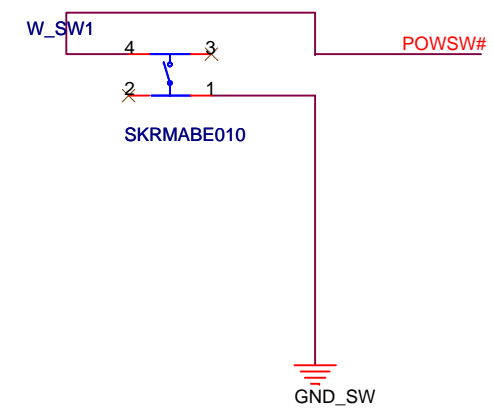
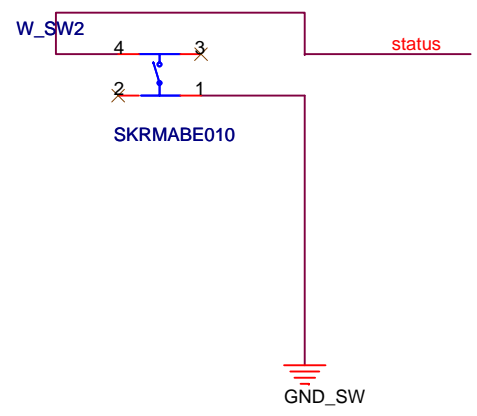
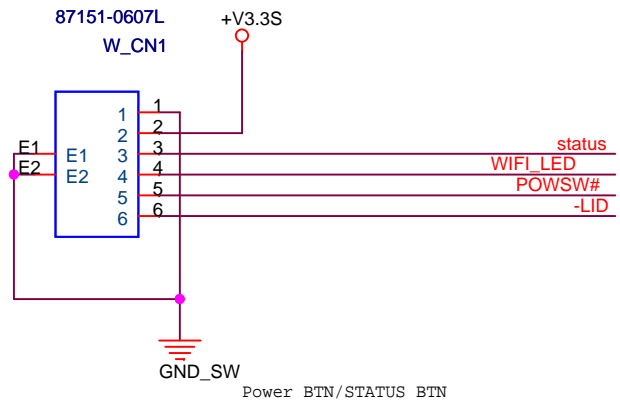
12-16 增加USB HDD接口

USB FOR HDD

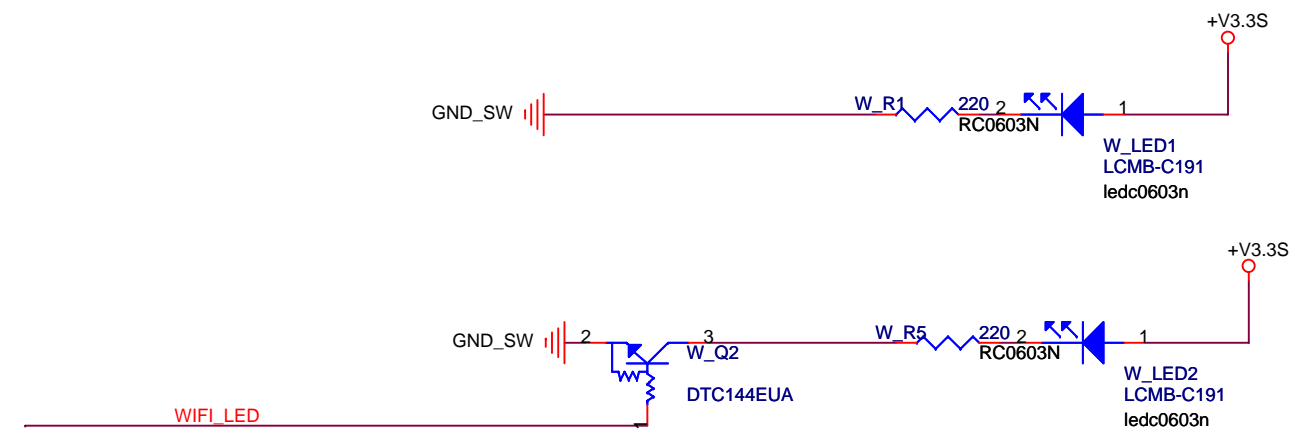
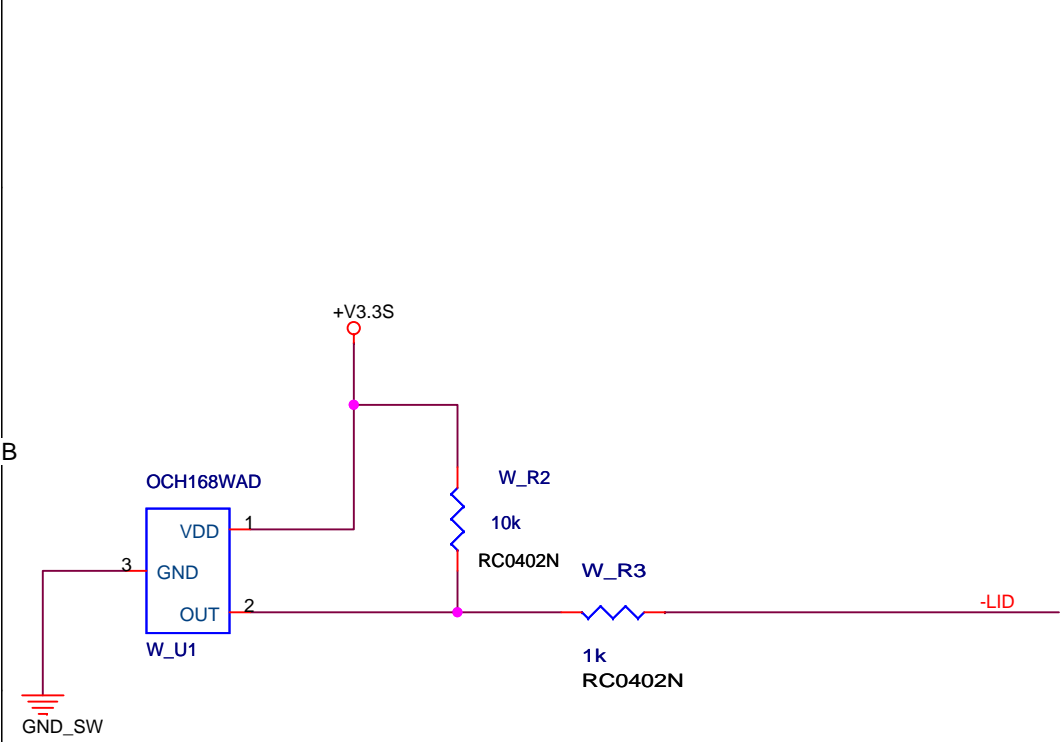


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		BYD COMPANY LIMITED	
		Mobile Name S101	
Page Name	AUDIO	Rev	V2.0
Date:	Wednesday, March 11, 2009	Sheet	43 of 43

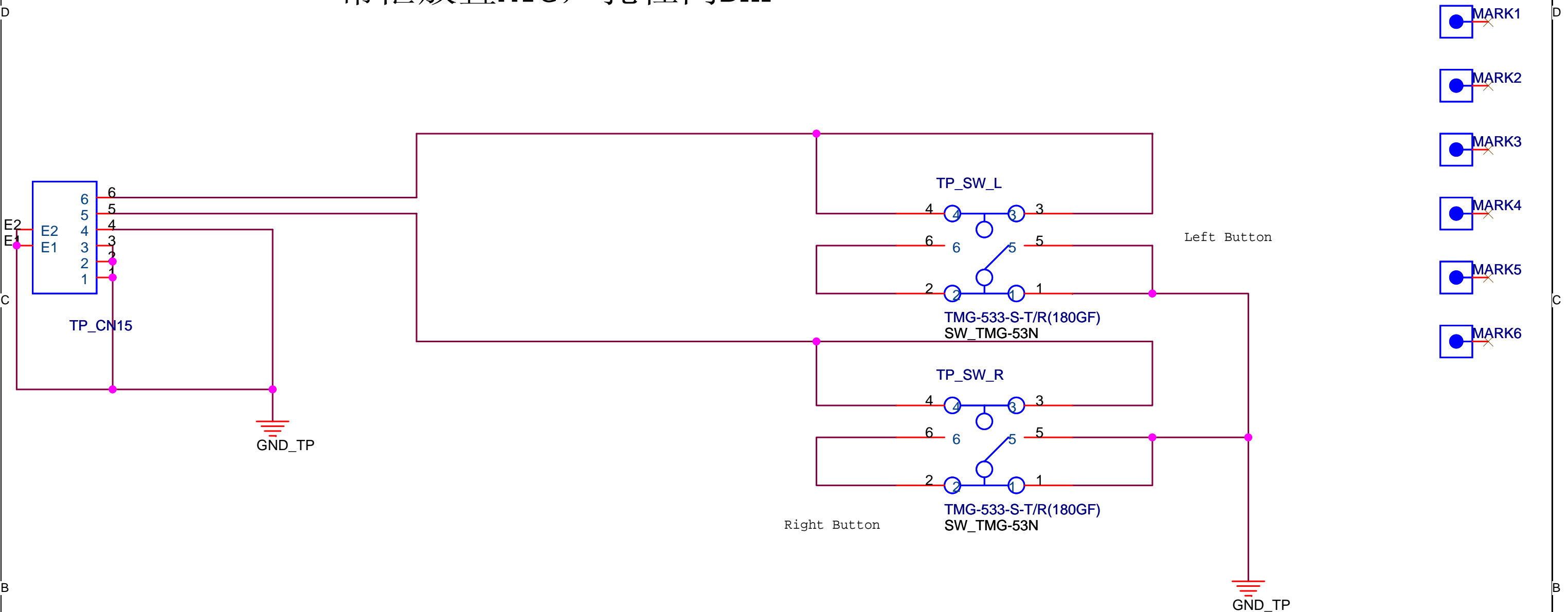


帮忙放置两MTG，孔径同DXF（5.1mm, 5.6mm），



Title		
SWITCH		
Size	Document Number	Rev
B	S101	2.0
Date:	Monday, March 09, 2009	Sheet 1 of 1

帮忙放置MTG, 孔径同DXF



Title		
TOUCH-PAD		
Size	Document Number	Rev
A	S101	2.0
Date:	Monday, March 09, 2009	Sheet 1 of 1