

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

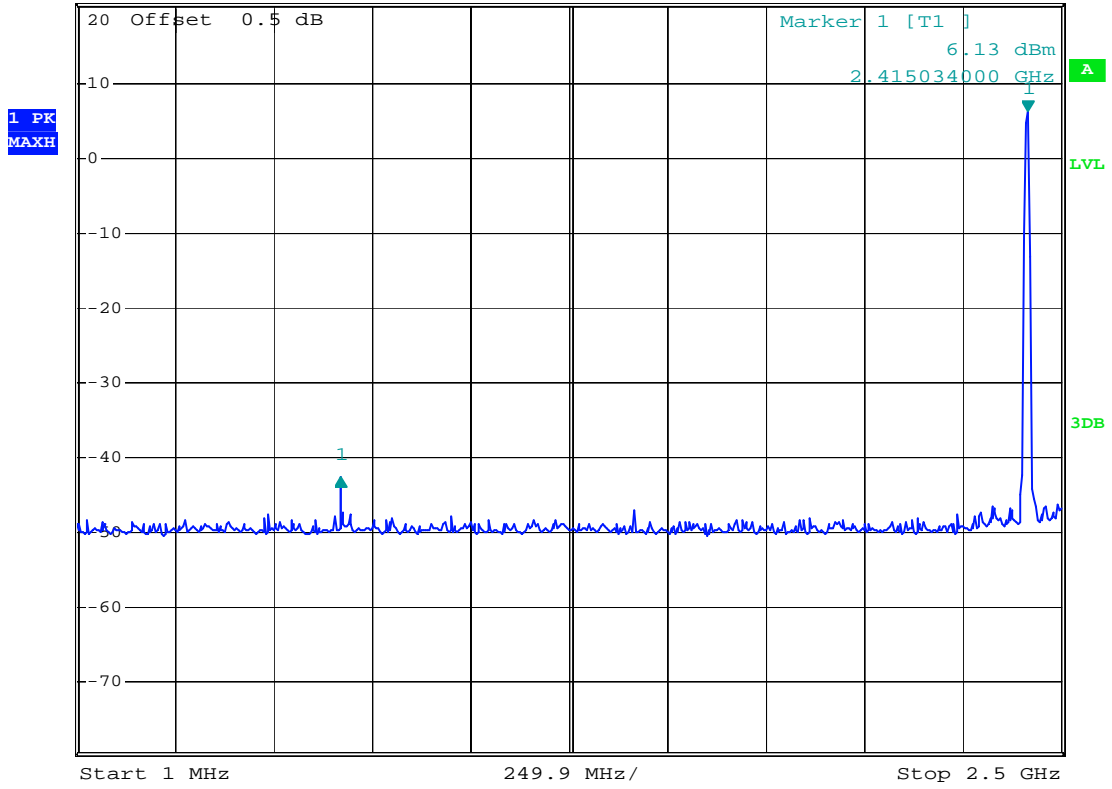
*VBW 300 kHz -48.67 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-1.744302000 GHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

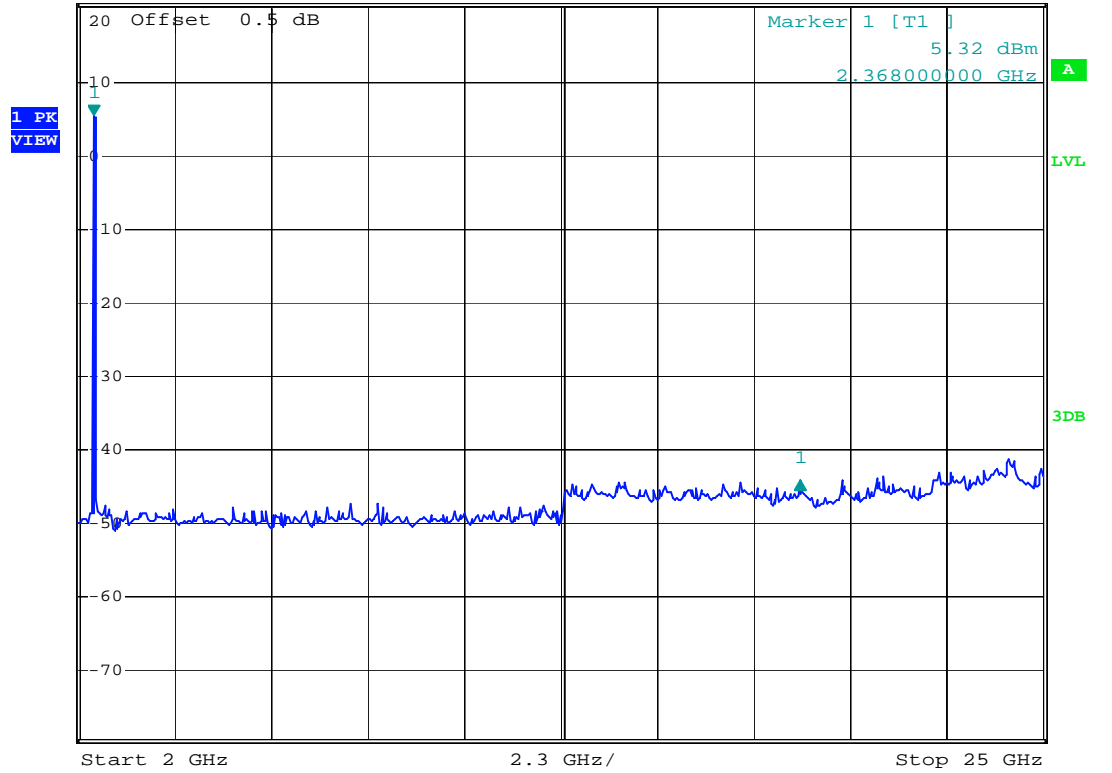
*VBW 300 kHz -49.45 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

16.836000000 GHz



Plot: B4A1 (1Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

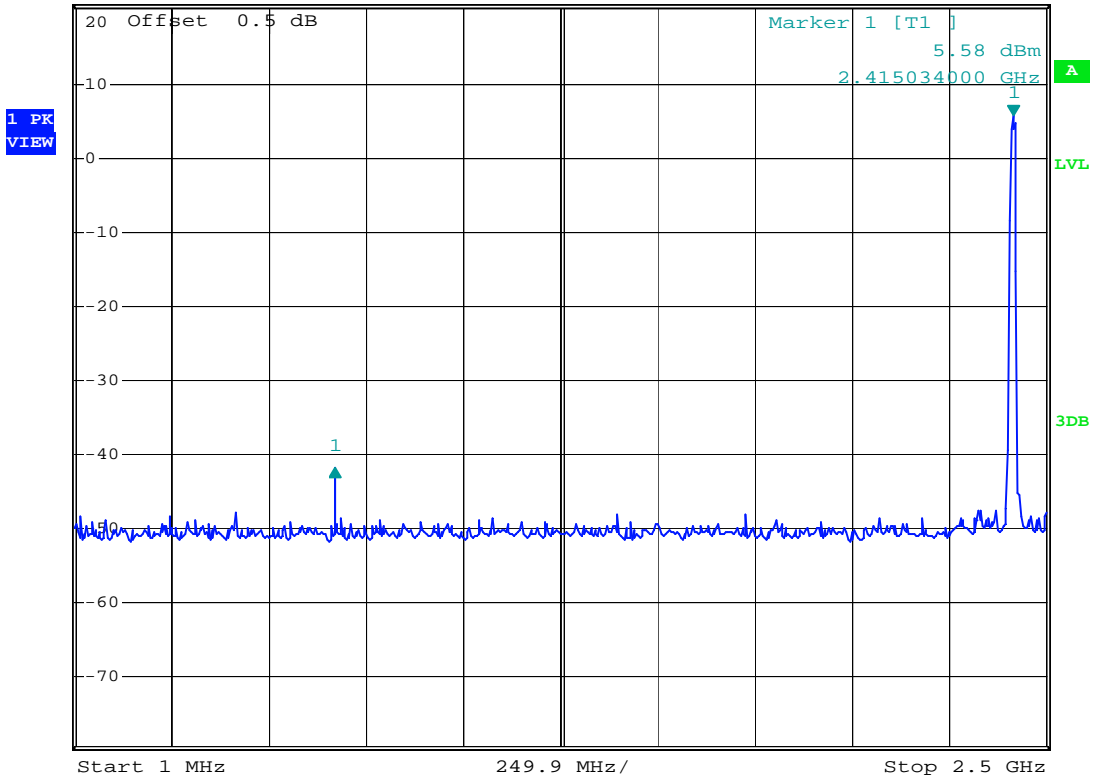
*VBW 300 kHz -47.32 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-1.744302000 GHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

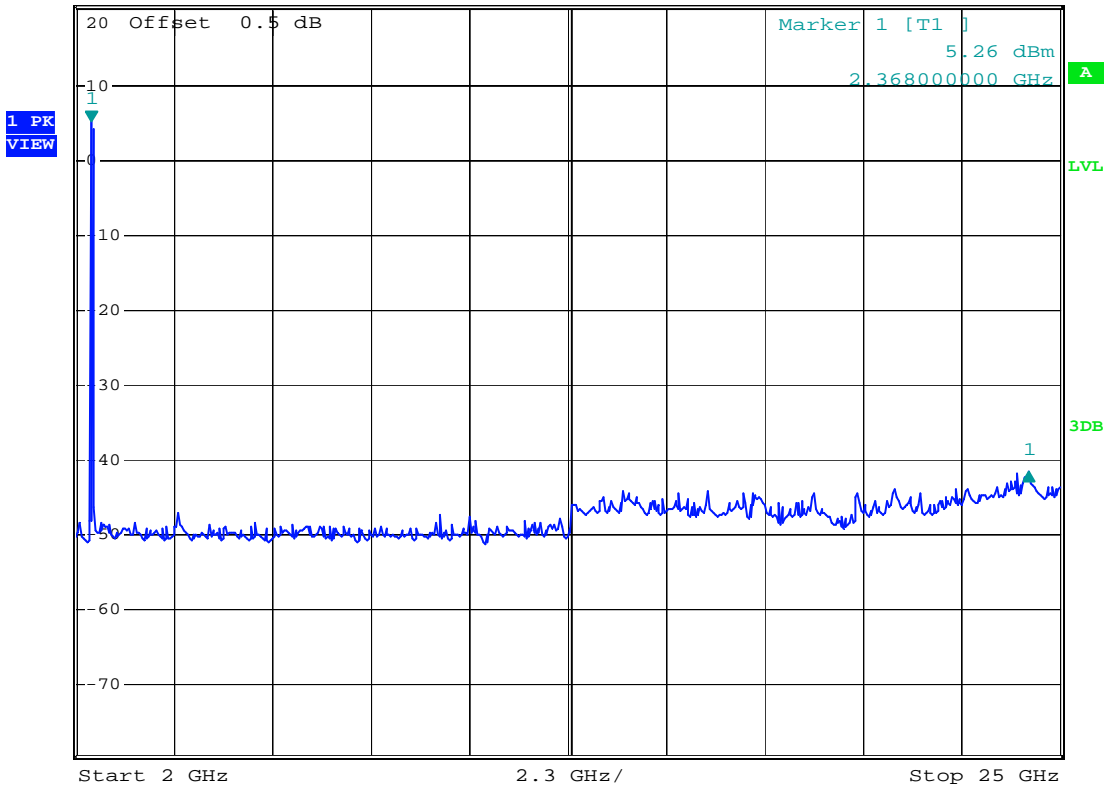
*VBW 300 kHz -46.77 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

21.896000000 GHz



Plot: B4A2 (11Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

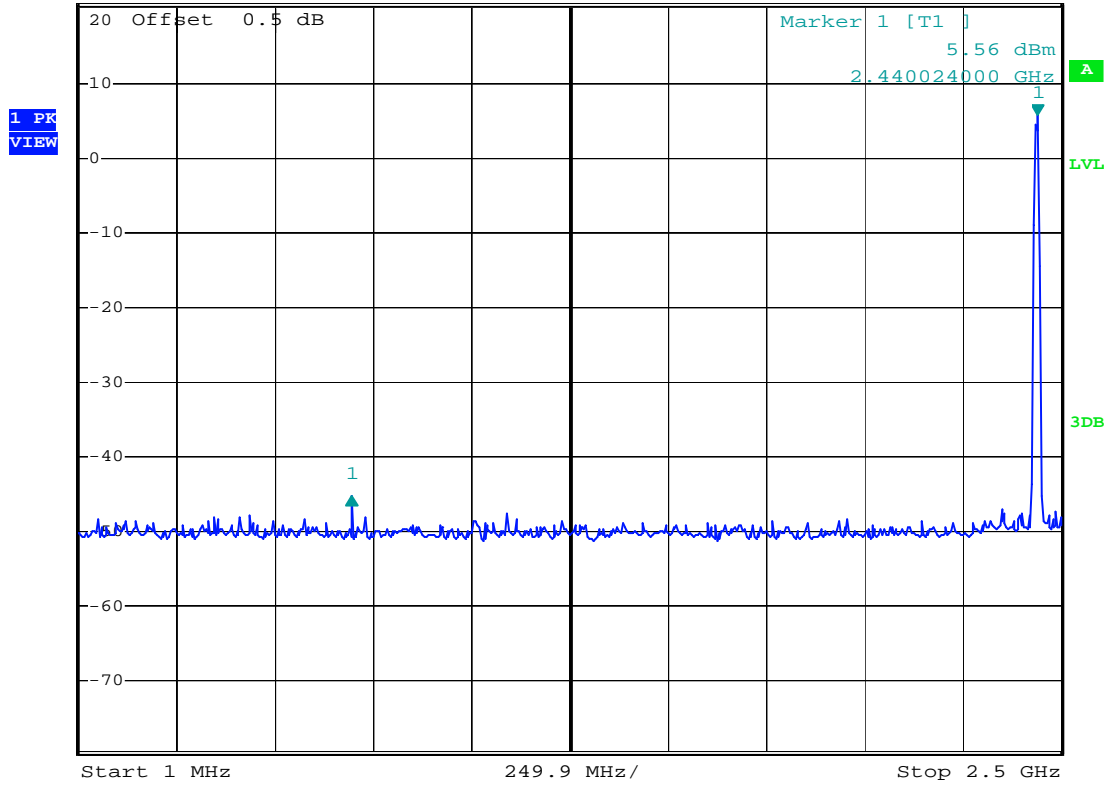
*VBW 300 kHz -50.69 dBm

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-1.744302000 GHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

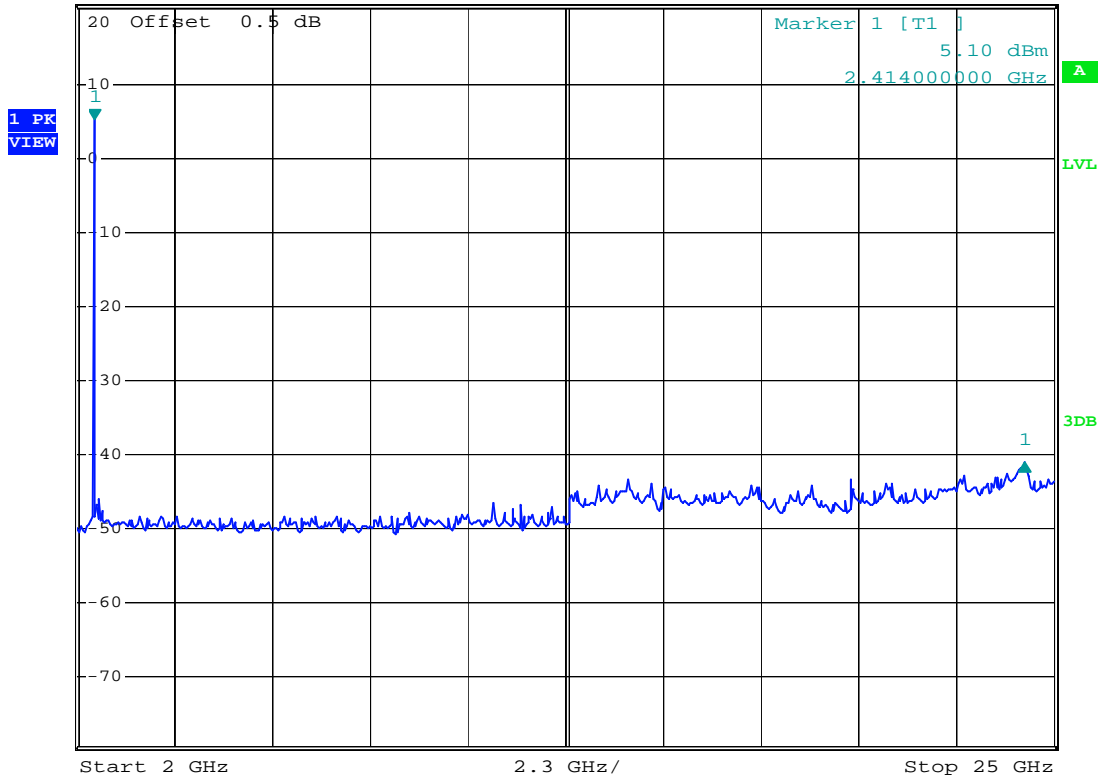
*VBW 300 kHz -46.07 dBm

Ref 20.5 dBm

*Att 30 dB

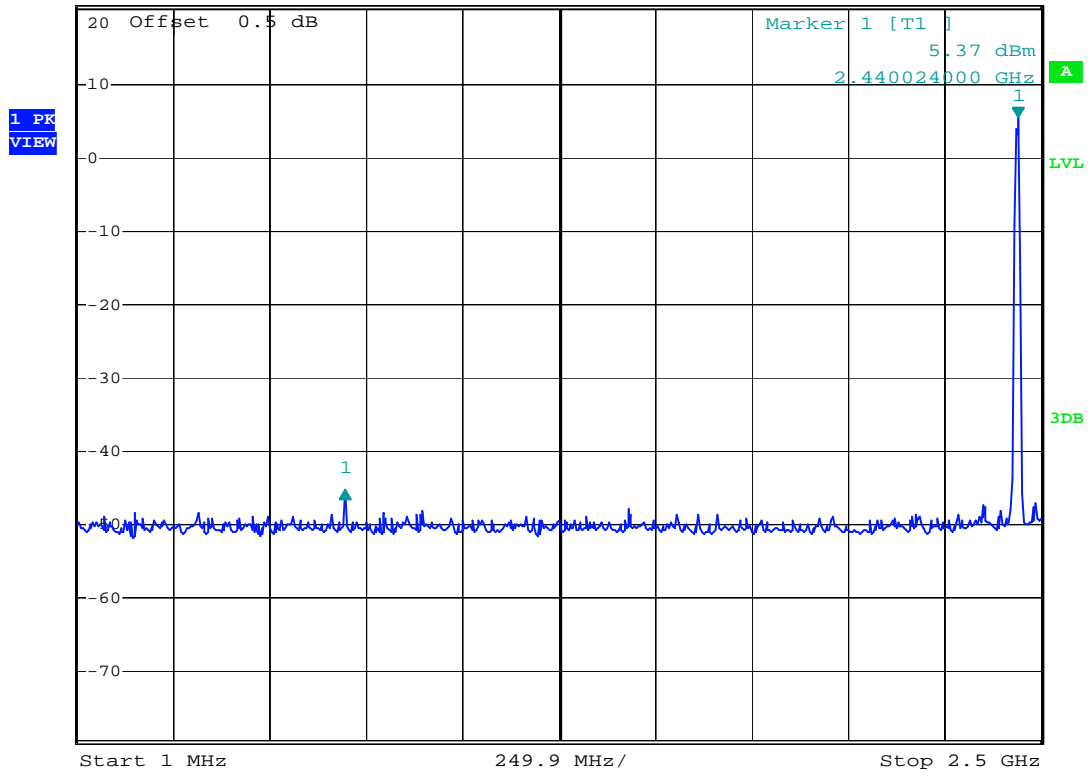
SWT 2.3 s

21.896000000 GHz

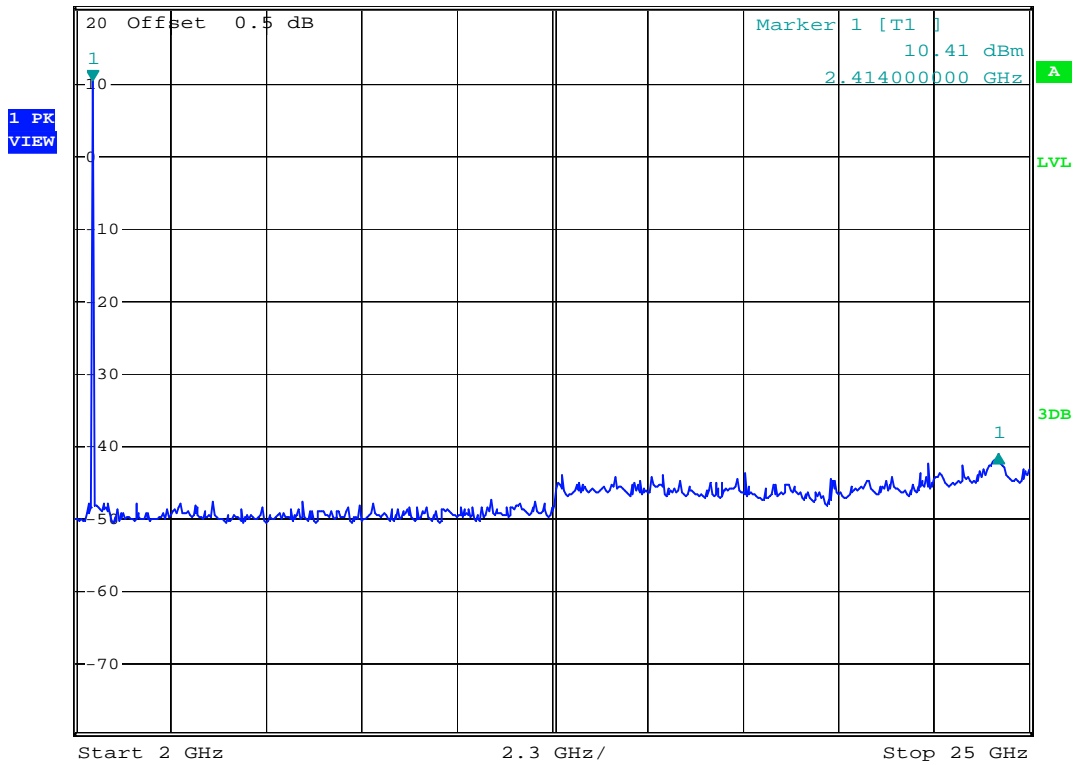


Plot: B4B1 (1Mbps)

Controlled by EMC32 *RBW 100 kHz Delta 1 [T1]
*VBW 300 kHz -50.50 dB
Ref 20.5 dBm *Att 30 dB SWT 250 ms -1.744302000 GHz



Controlled by EMC32 *RBW 100 kHz Delta 1 [T1]
*VBW 300 kHz -51.58 dB
Ref 20.5 dBm *Att 30 dB SWT 2.3 s 21.850000000 GHz



Plot: B4B2 (11Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

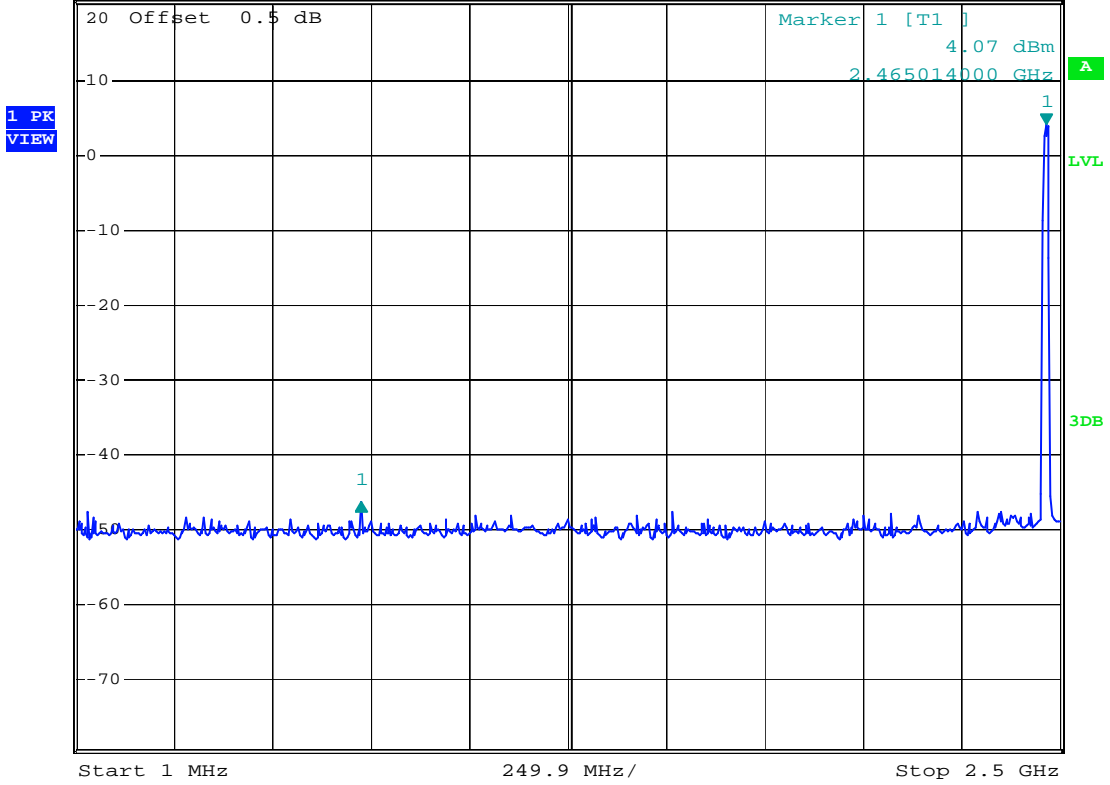
*VBW 300 kHz -50.37 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-1.739304000 GHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

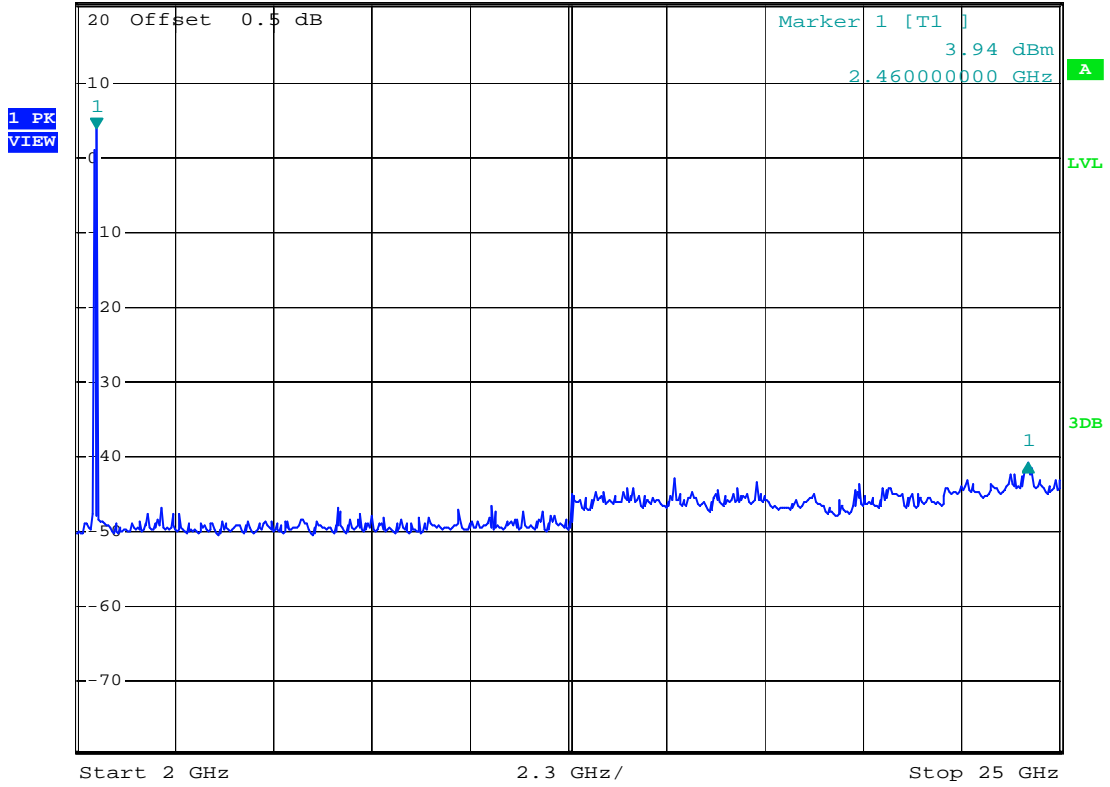
*VBW 300 kHz -44.69 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

21.804000000 GHz



Plot: B4C1 (1Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

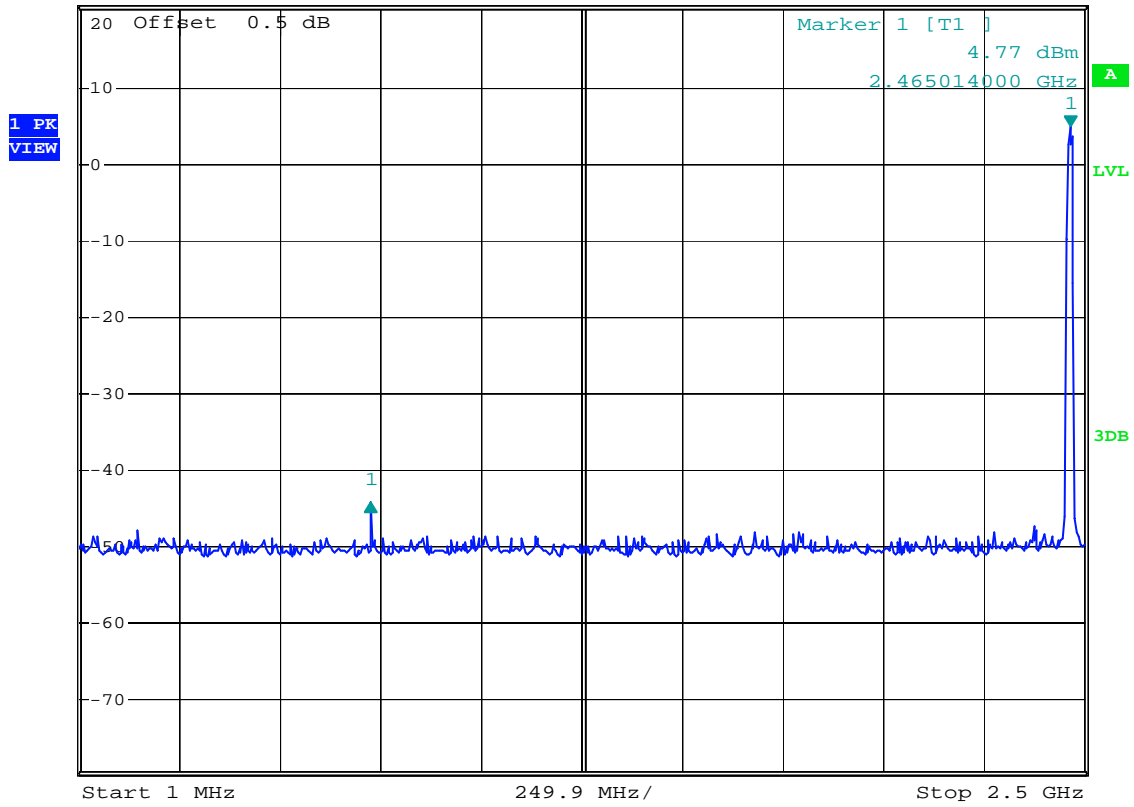
*VBW 300 kHz -48.97 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-1.739304000 GHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

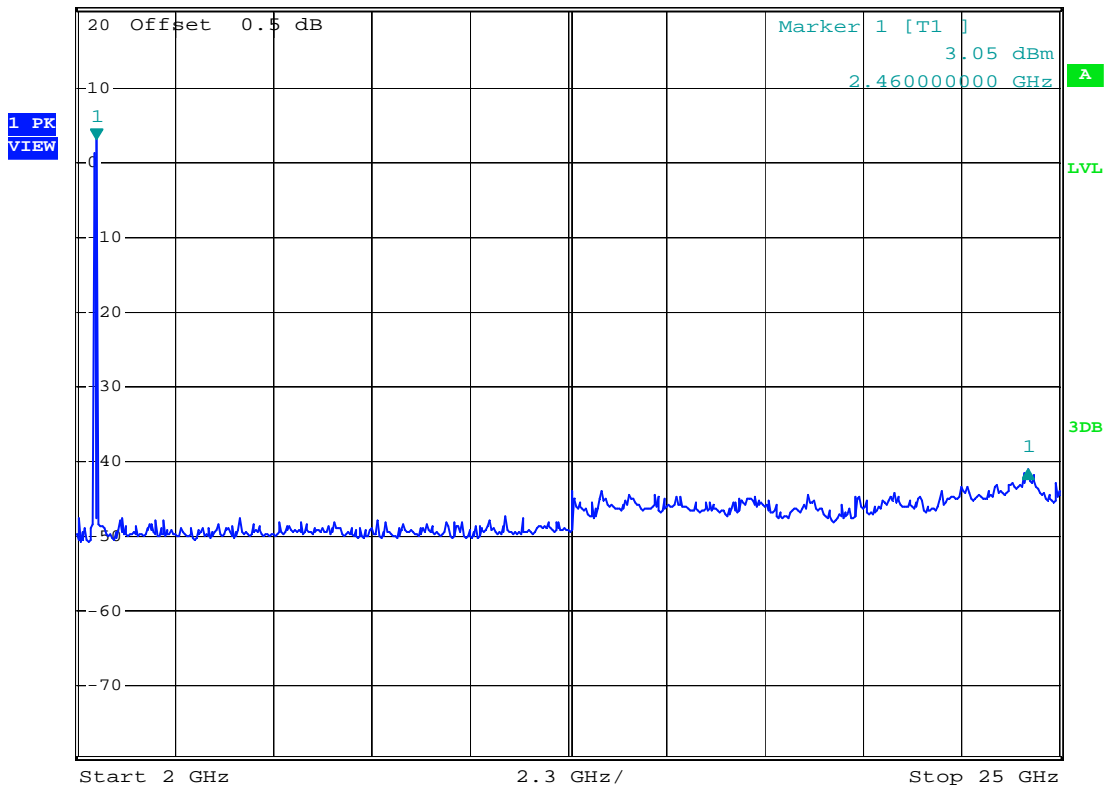
*VBW 300 kHz -44.21 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

21.804000000 GHz



Plot: B4C2 (11Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

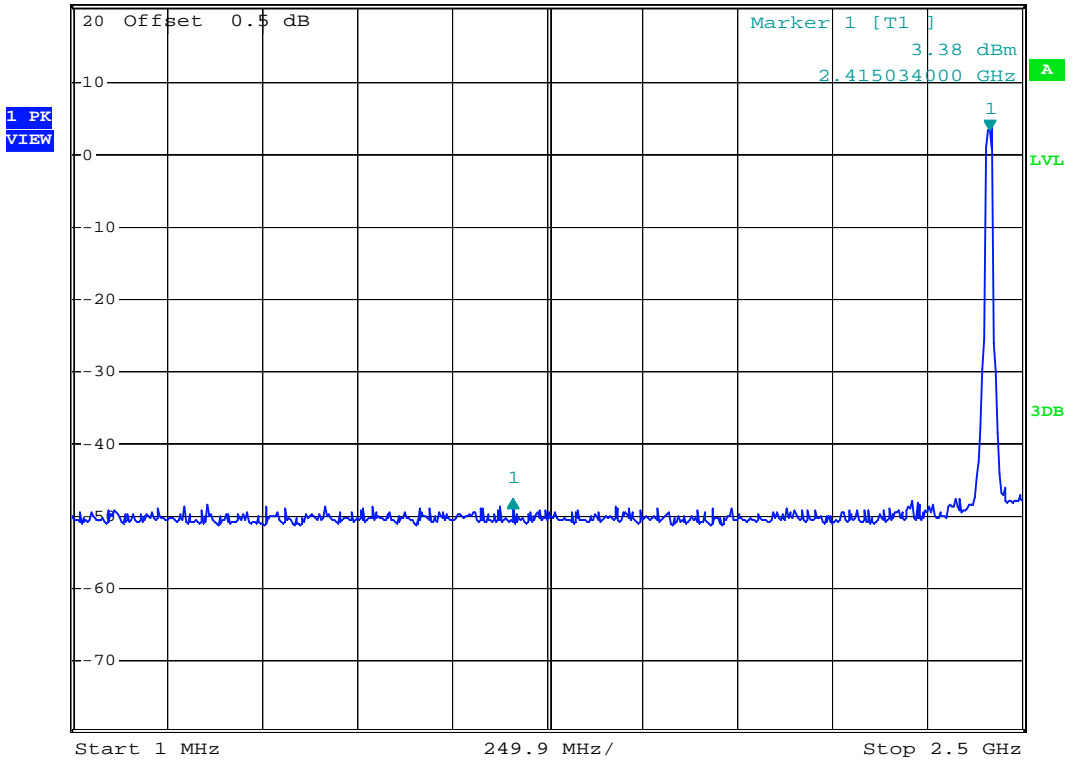
*VBW 300 kHz -51.06 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-1.254498000 GHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

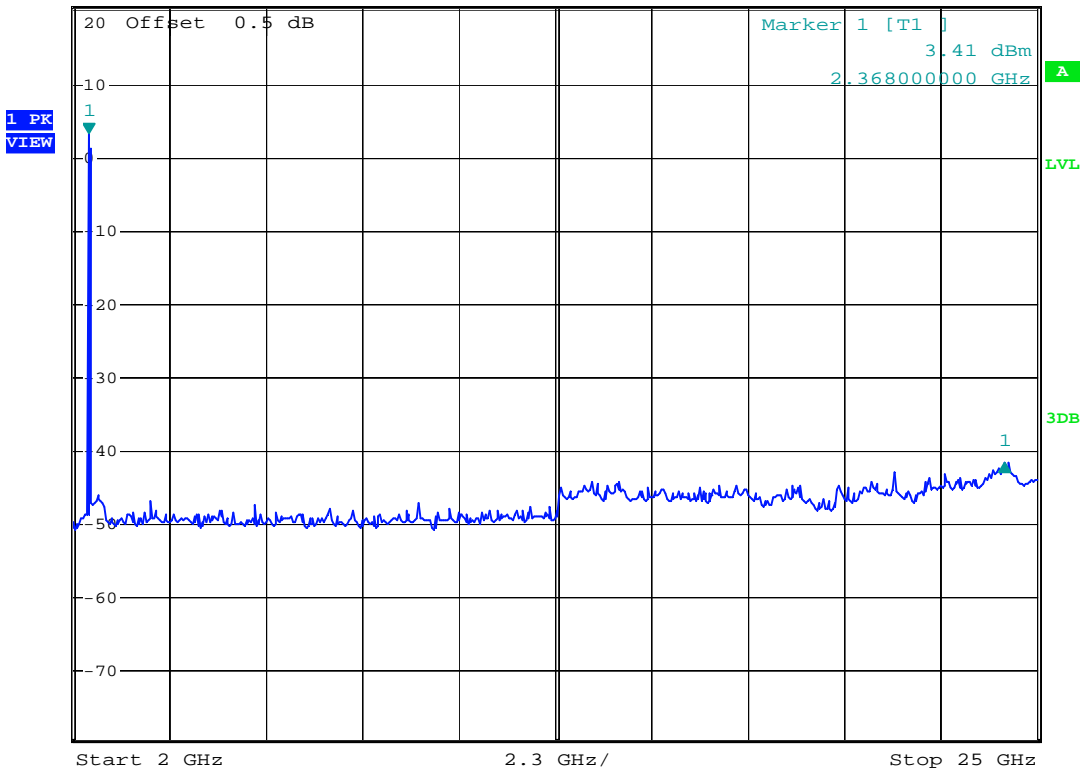
*VBW 300 kHz -45.01 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

21.850000000 GHz



Plot: G4A1 (6Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

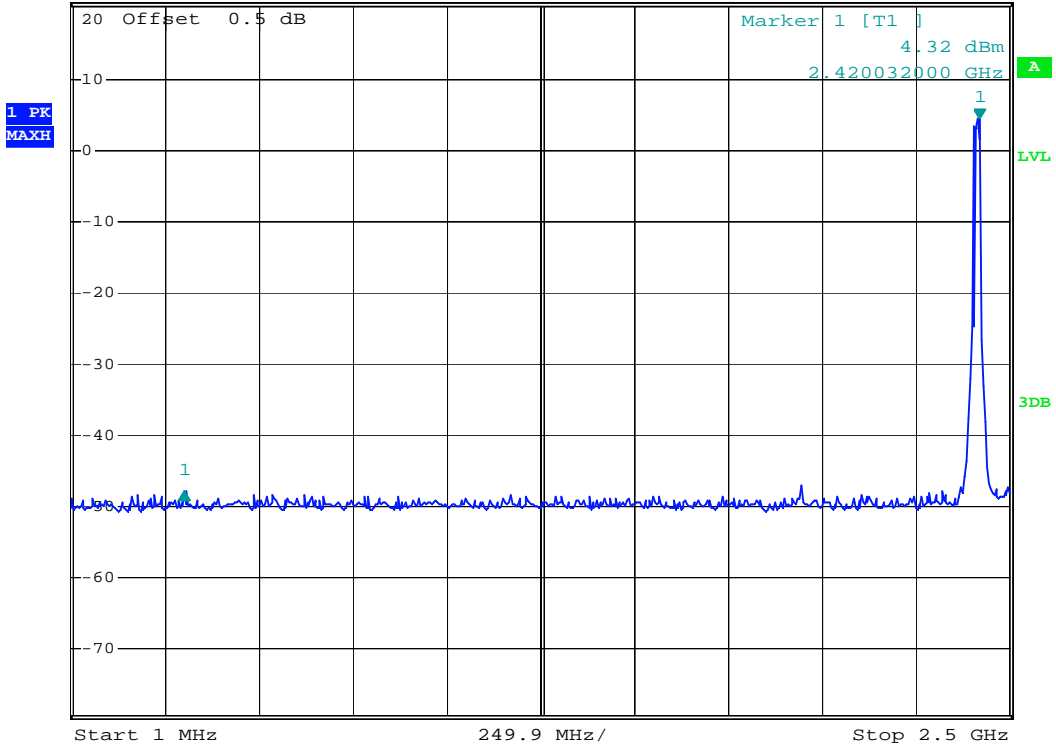
*VBW 300 kHz -52.29 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-2.119152000 GHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

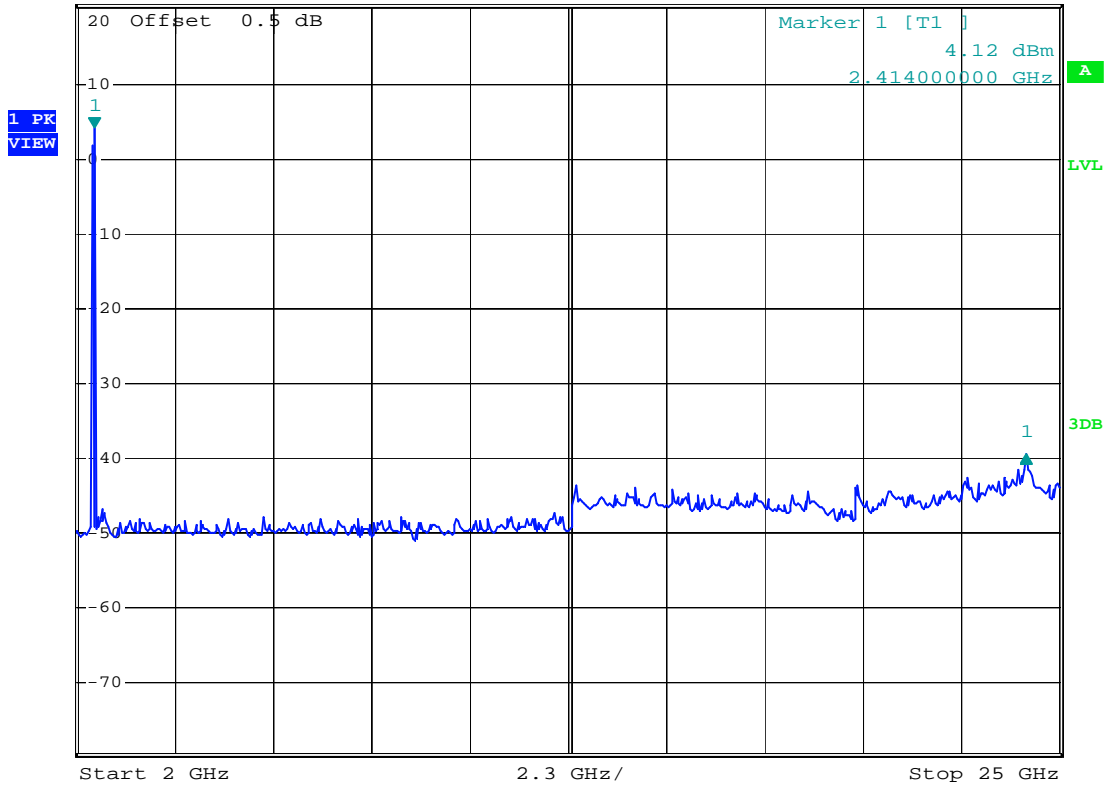
*VBW 300 kHz -43.71 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

21.804000000 GHz



Plot: G4A2 (54Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

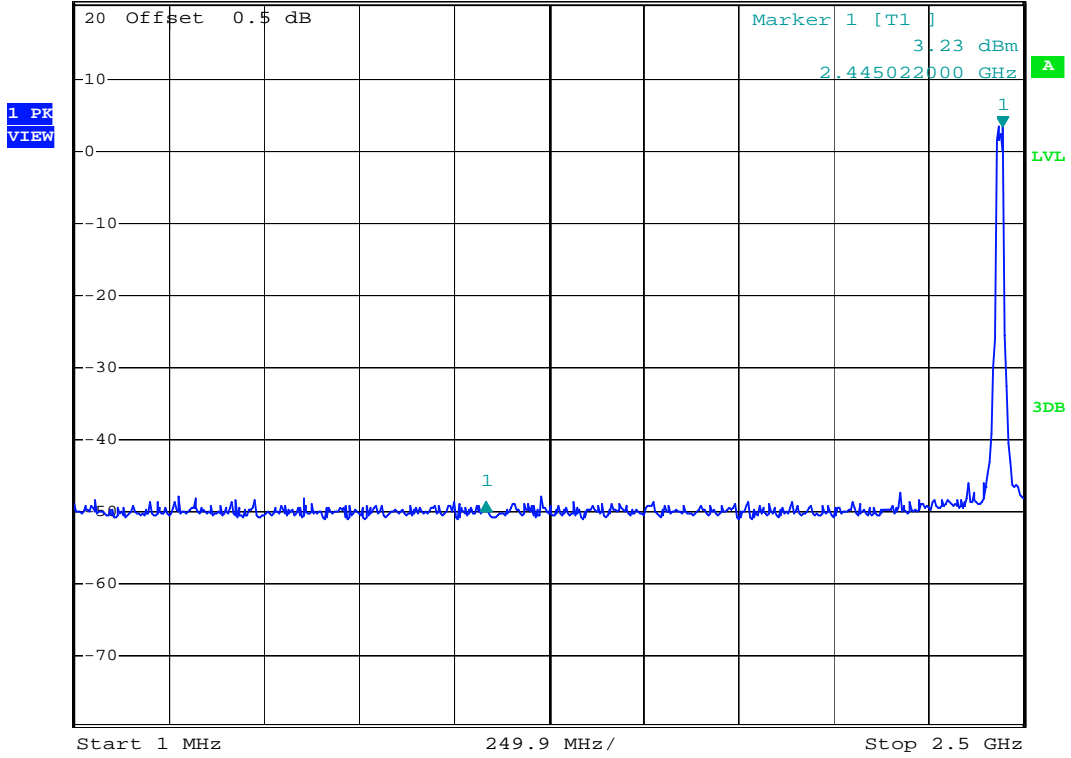
*VBW 300 kHz -51.81 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-1.359456000 GHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

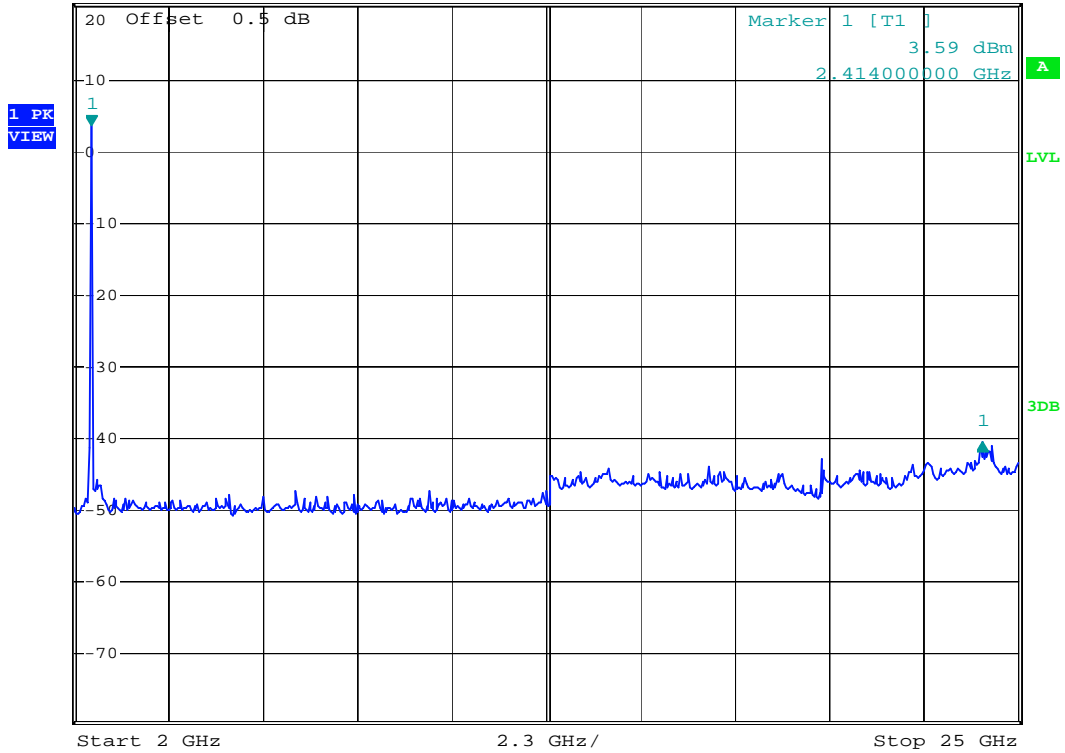
*VBW 300 kHz -44.16 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

21.712000000 GHz



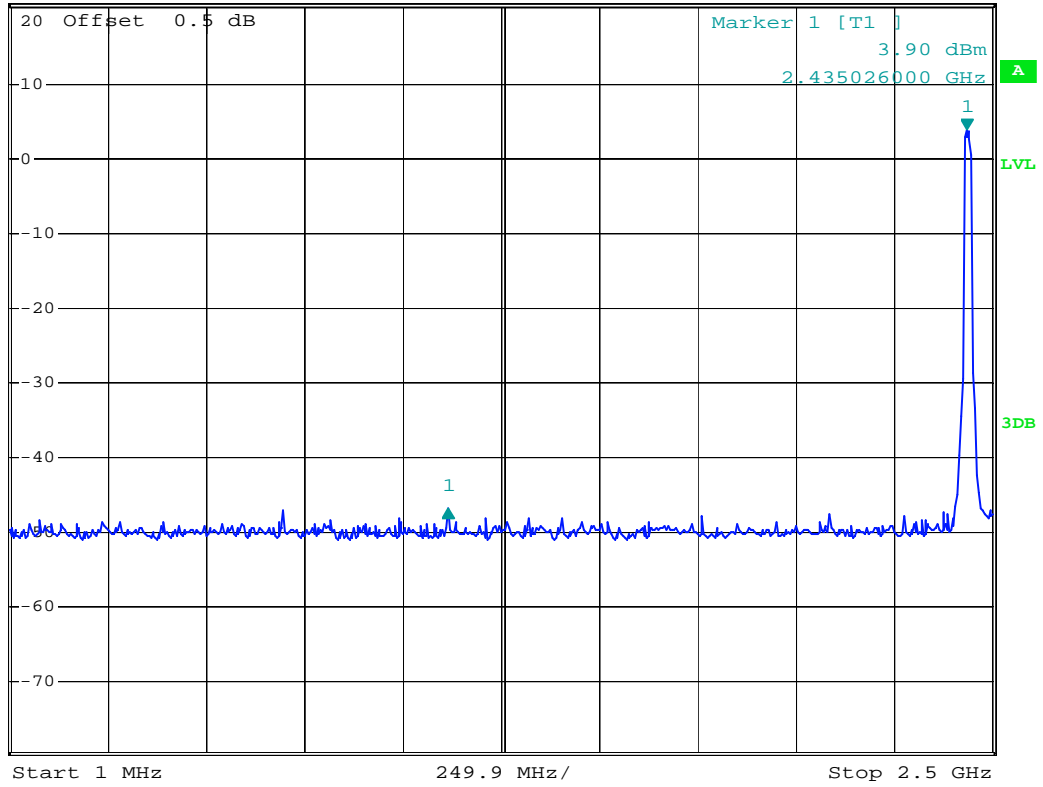
Plot: G4B1 (6Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]
*VBW 300 kHz -50.73 dB
SWT 250 ms -1.319472000 GHz

Ref 20.5 dBm *Att 30 dB

1 PK
VIEW

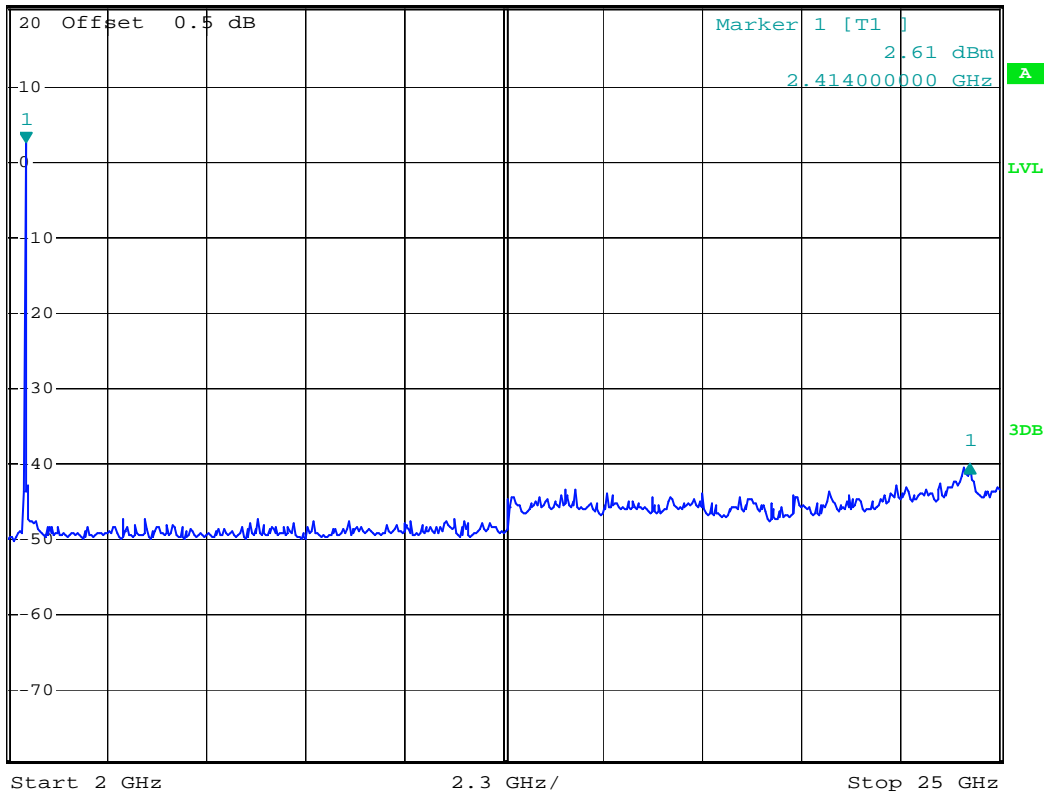


Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]
*VBW 300 kHz -42.72 dB
SWT 2.3 s 21.896000000 GHz

Ref 20.5 dBm *Att 30 dB

1 PK
VIEW



Plot: G4B2 (54Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

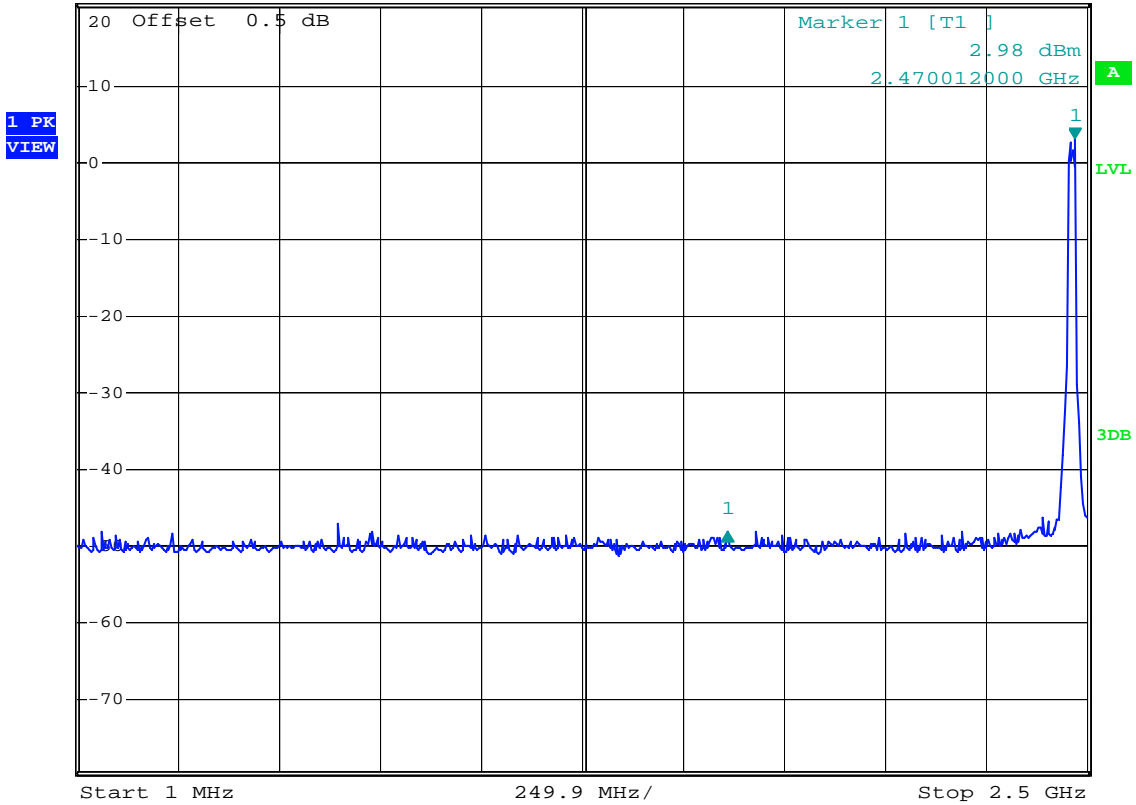
*VBW 300 kHz -51.16 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-859.656000000 MHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

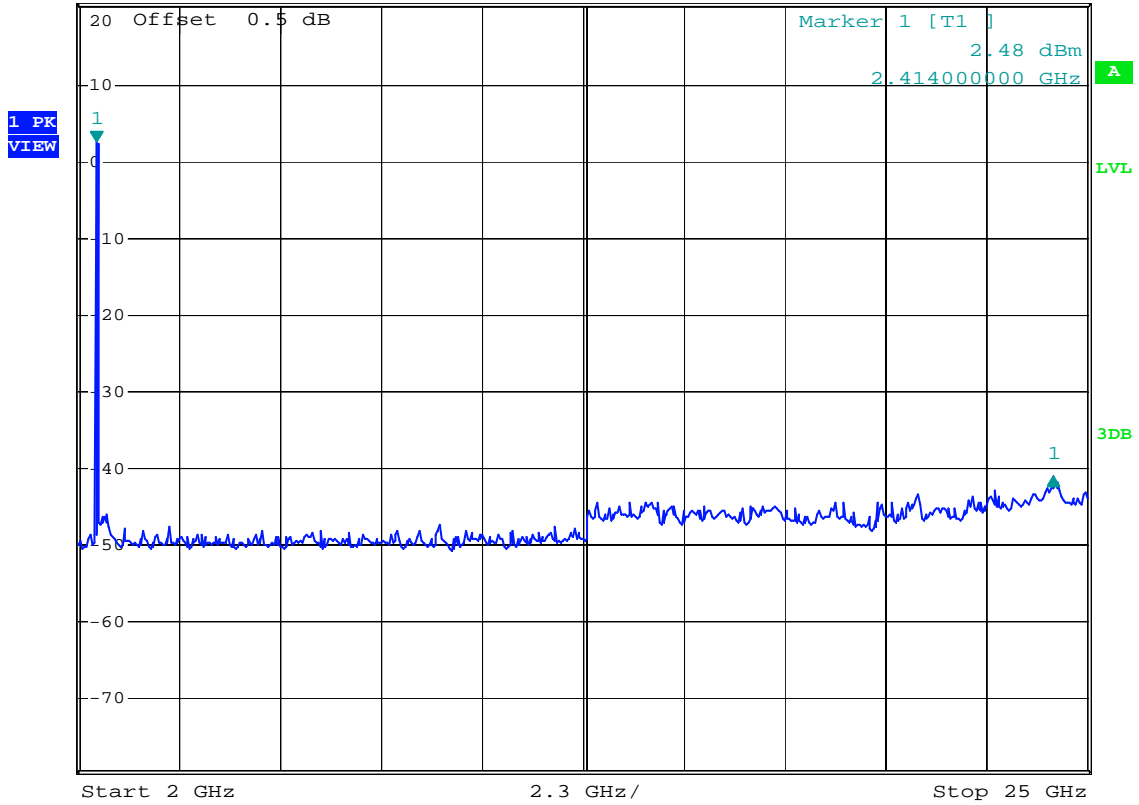
*VBW 300 kHz -43.49 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

21.804000000 GHz



Plot: G4C1 (6Mbps)

Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

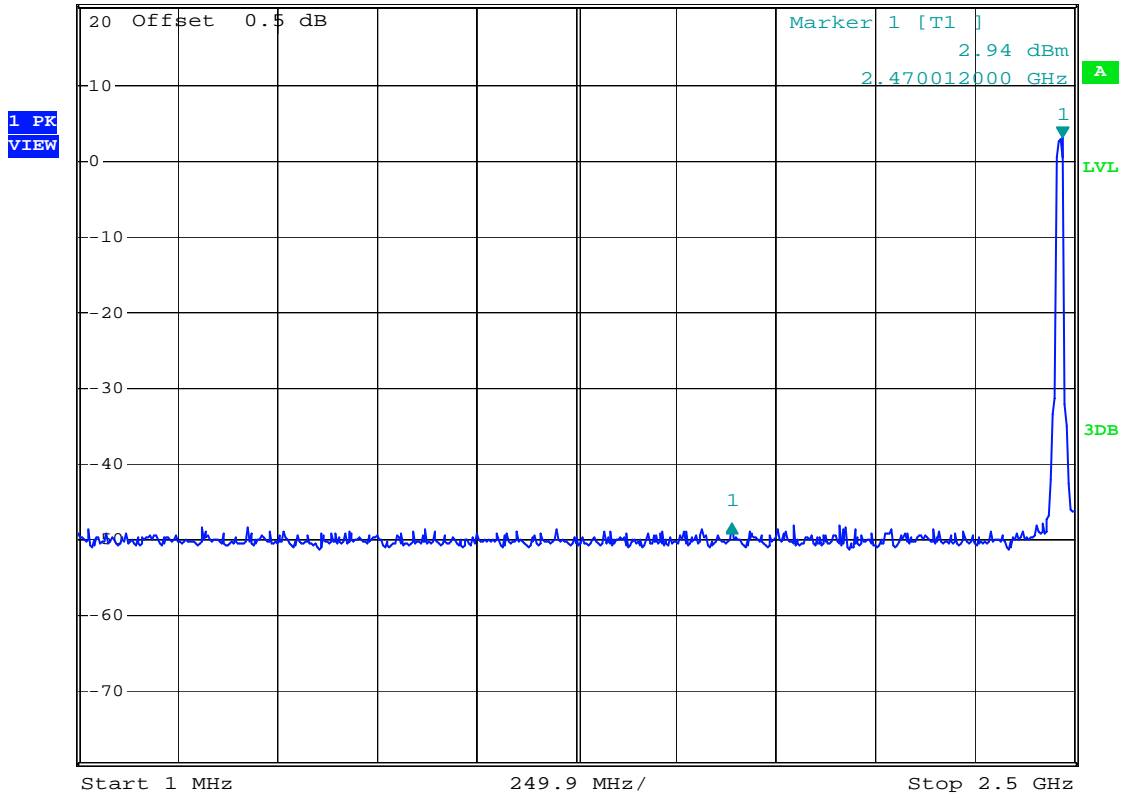
*VBW 300 kHz -50.80 dB

Ref 20.5 dBm

*Att 30 dB

SWT 250 ms

-829.668000000 MHz



Controlled by EMC32

*RBW 100 kHz Delta 1 [T1]

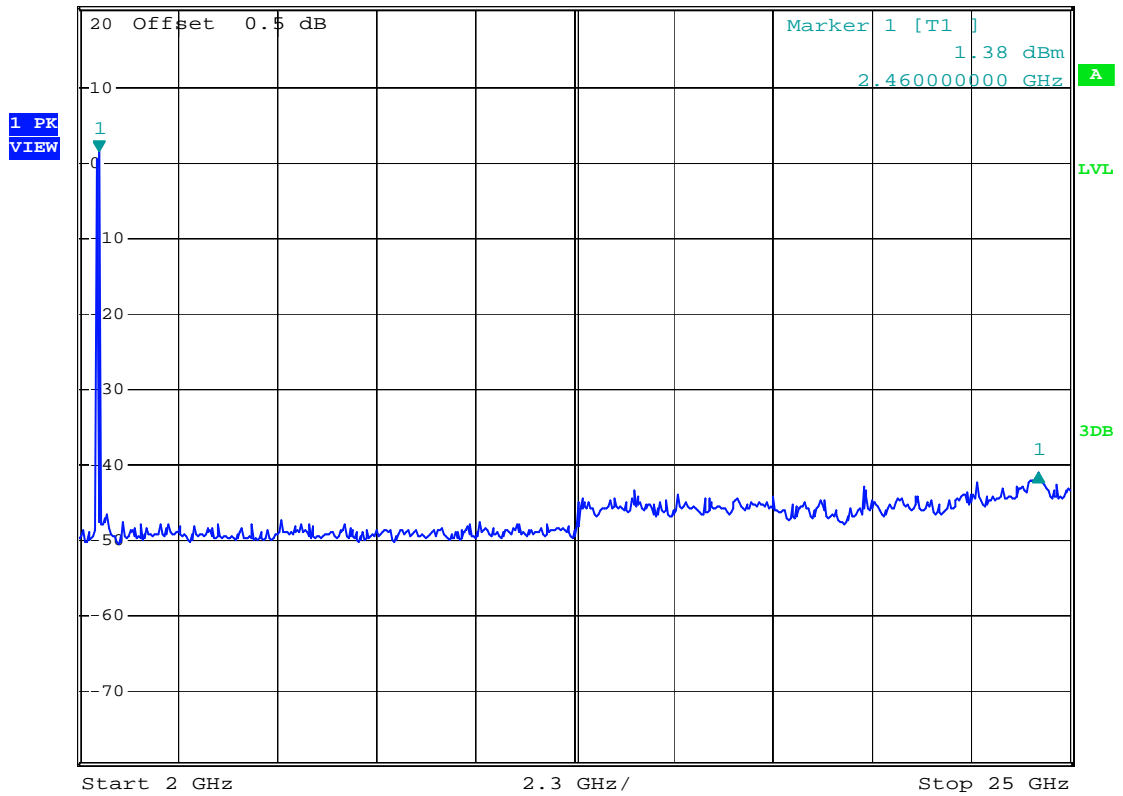
*VBW 300 kHz -42.48 dB

Ref 20.5 dBm

*Att 30 dB

SWT 2.3 s

21.804000000 GHz



Plot: G4C2 (54Mbps)