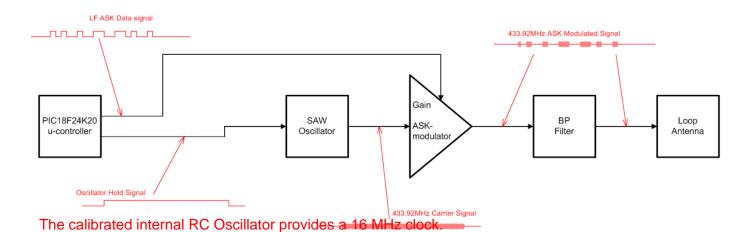


## **HBBRIO** function Block Diagram

Rev. A Date: 2013-05-29

Init: TOP



The PIC18F system clock can be divided by setting the OSCCON.

In this application, the OSCCON is set to 4, and the clock frequency of the CPU is 4MHz.

The frequency of the 'Watchdog' is 32kHz.

The PWM frequency for light management is 100KHz.