

Applicant: Dongguan Southstar Electronics Limited
FCC ID: X8CWS-2000

Operation Description

The transmitter is a universal SDR RF transmitter chip AX5051. The U1(AX5051) has an integrated RF frond end with modulator and demodulator, communication controller and communicates the microcontroller via a SIP buss. U4(MCP1700T-3302E/TT) is a power management chip and provides host with 3.3V power. U6(MC14521BDR2G) is the source of the clock of 1 second. U3(ATMEGA88PA-AU) is a High Performance, Low Power AVR 8-Bit Microcontroller used to interface with the radio frequency chip U1 on the SIP buss, read data from U2(PIC18F24J11-I/SS), and report data. Finally, all information is measured and reported to the receiver unit.

Antenna is formed by two copper wire configured in a dipole. Common grounding on PCB is not connected to real external ground. Power supply is DC 6V by four “AA” batteries.