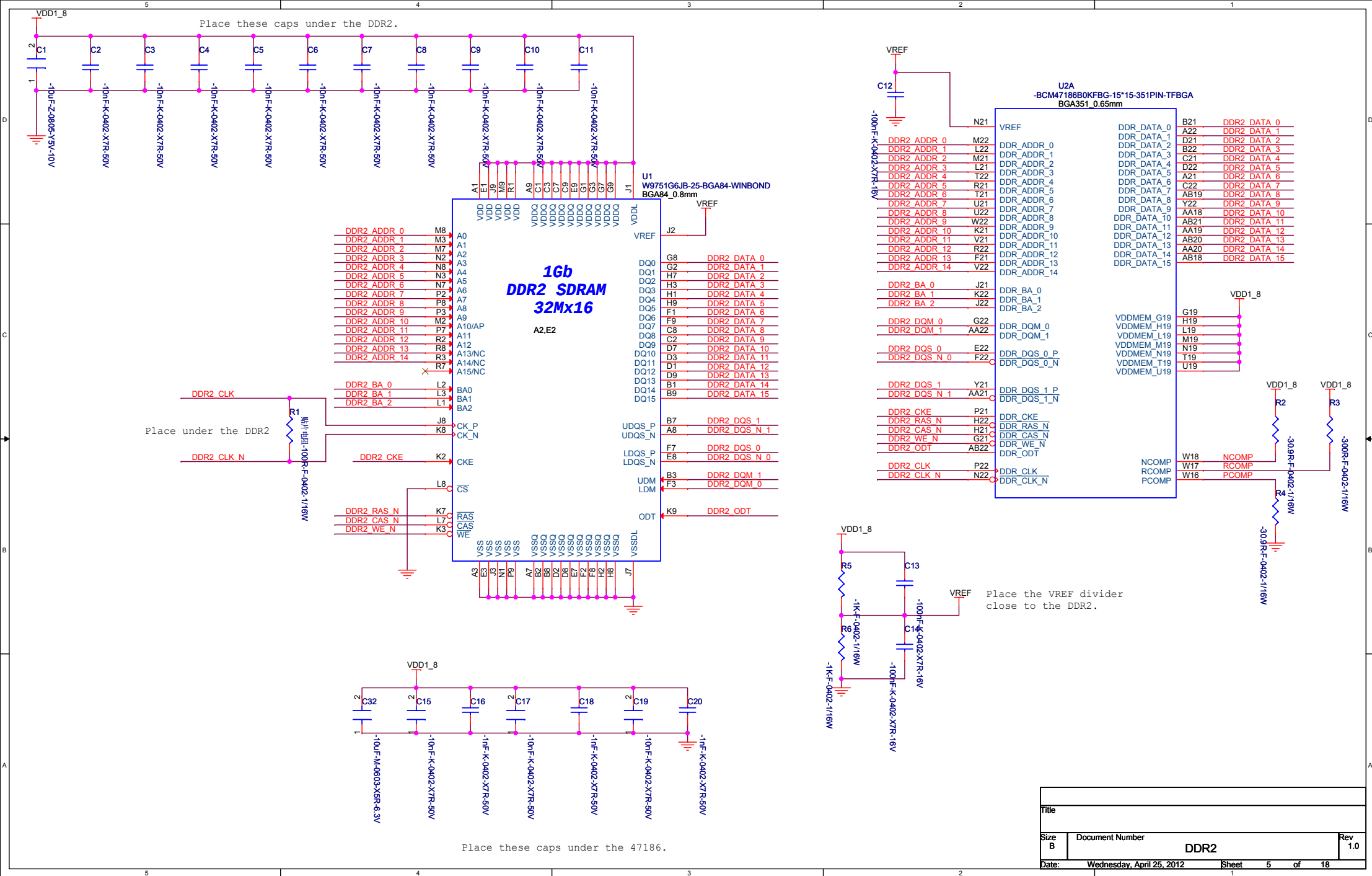
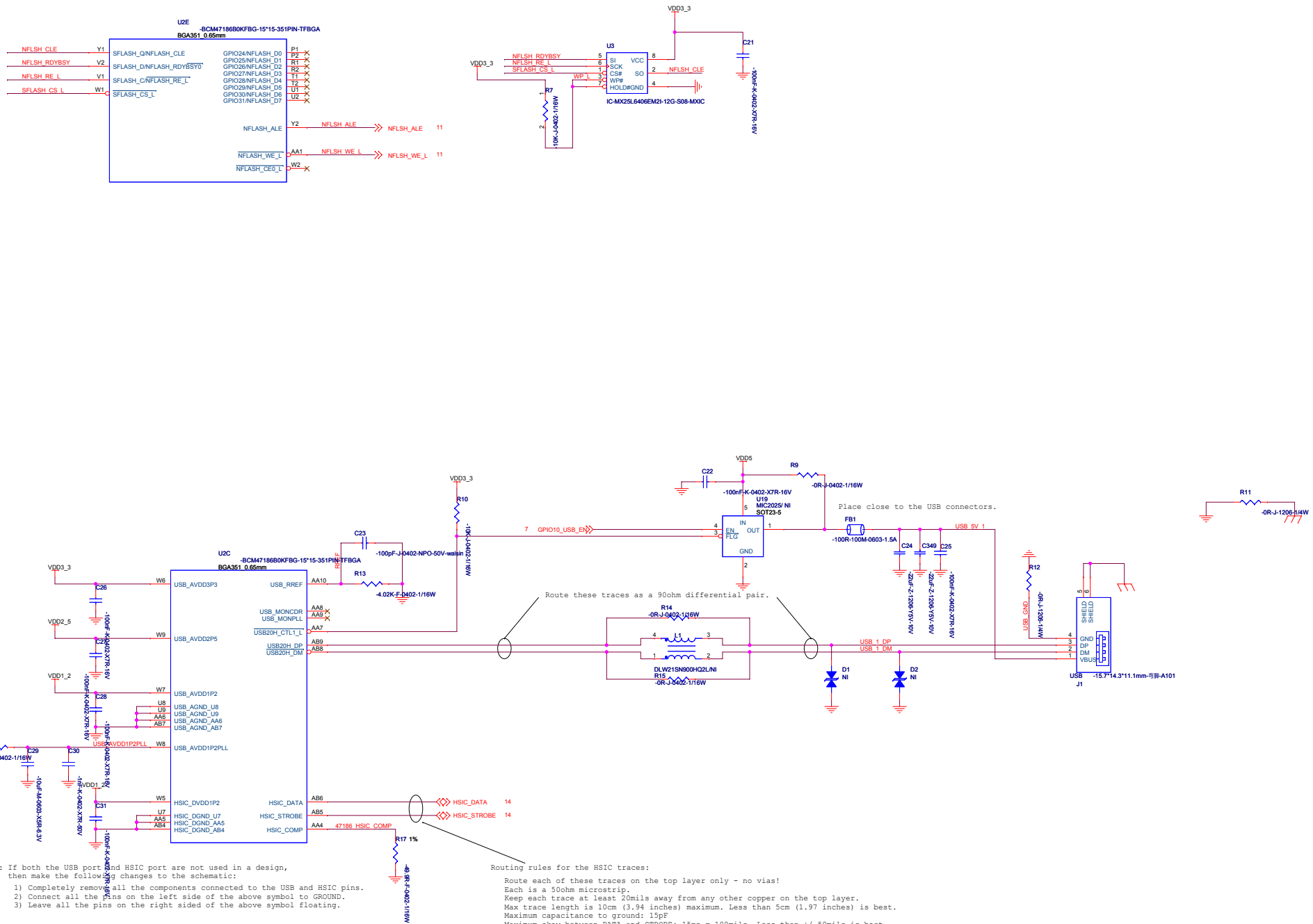
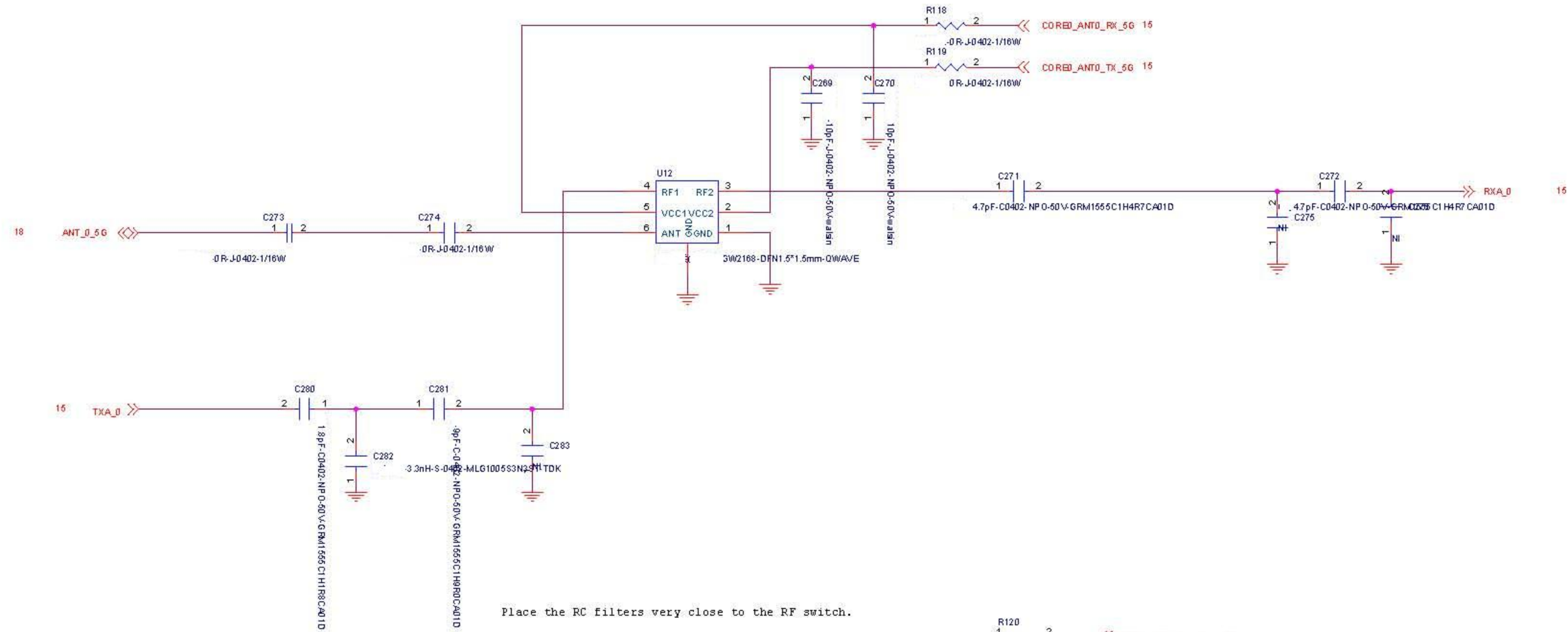


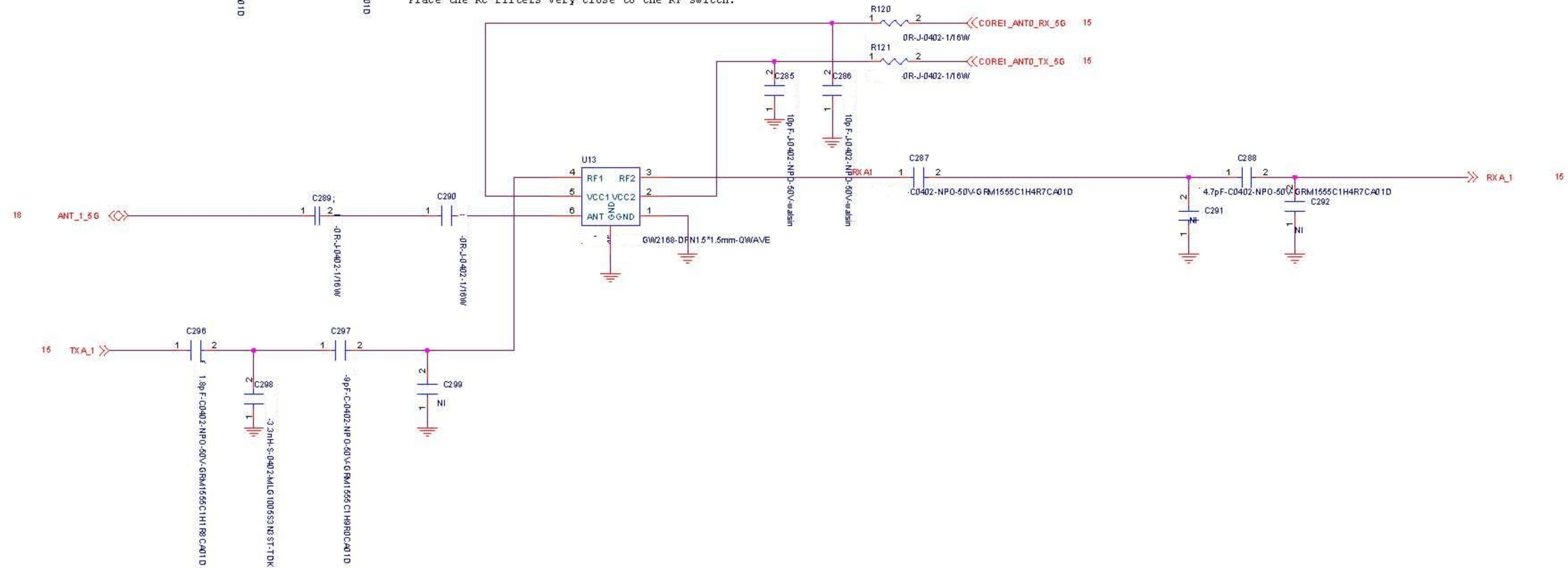


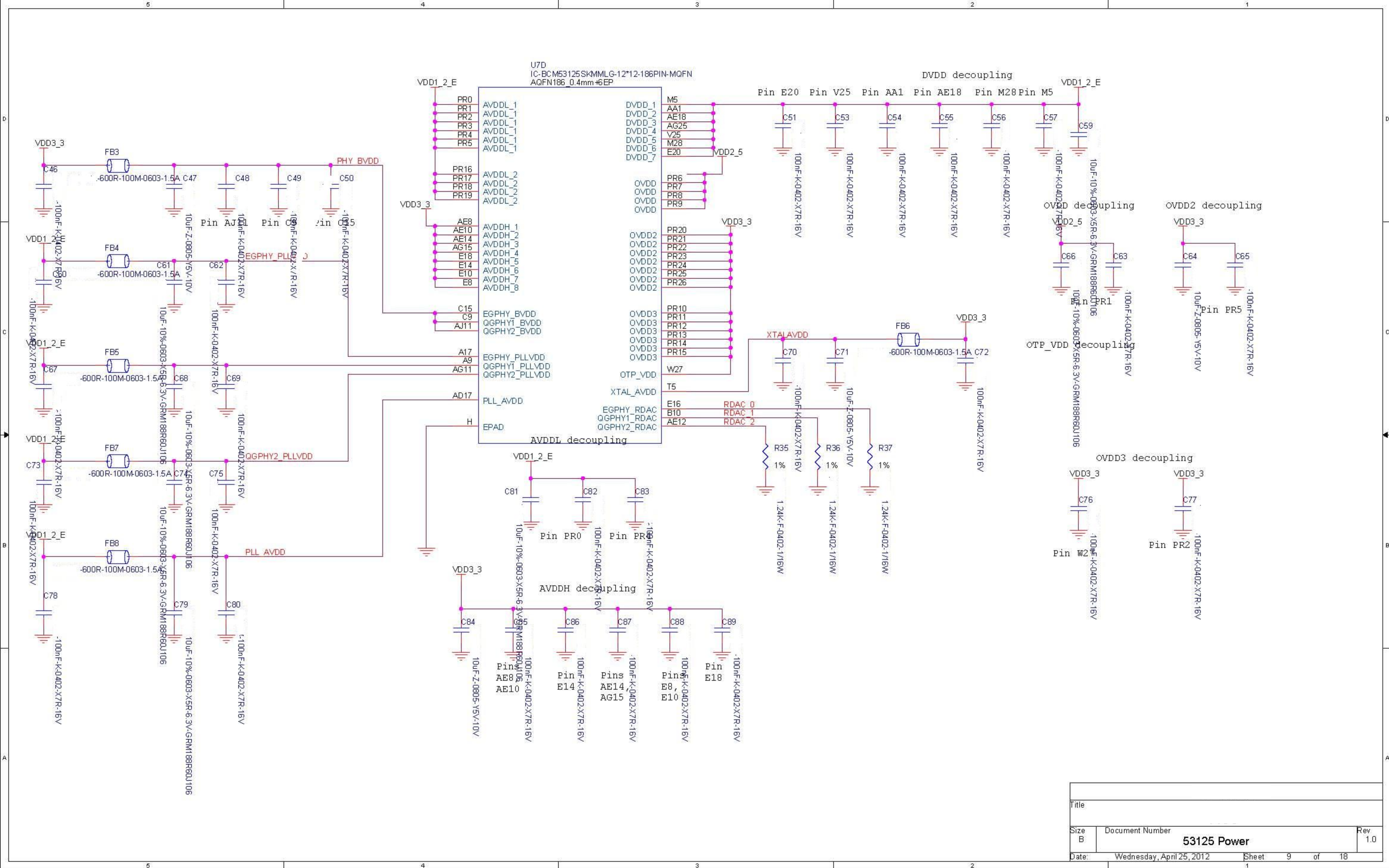


Place the RC filters very close to the RF switch.

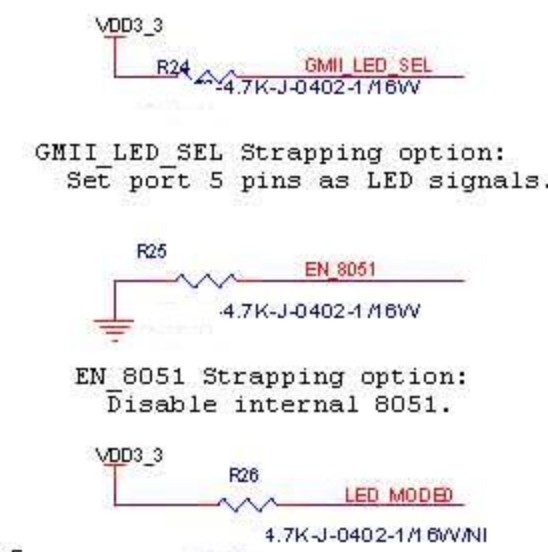


Place the RC filters very close to the RF switch.





NOTE: 53125 has an absolute maximum limit of 1.26V on it's 1.2V pins.

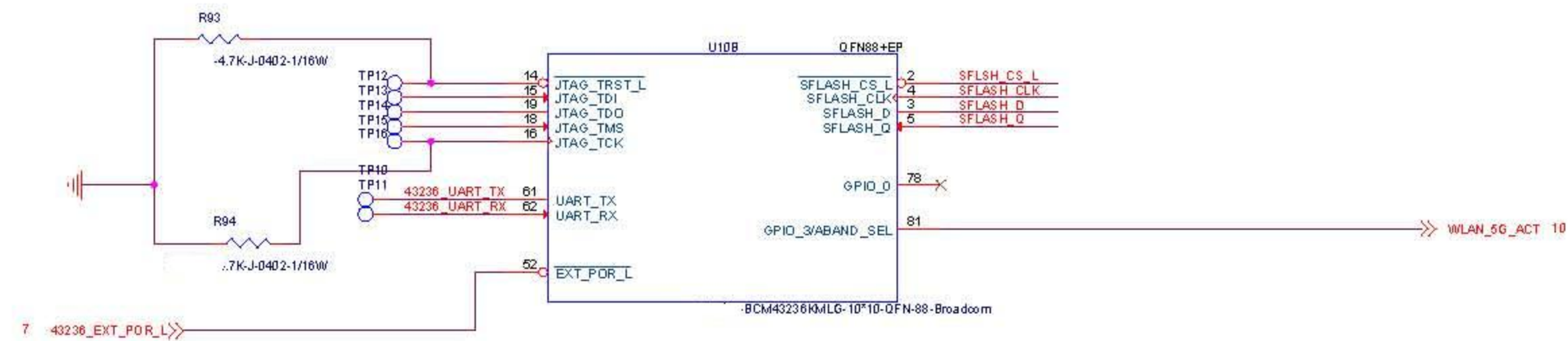
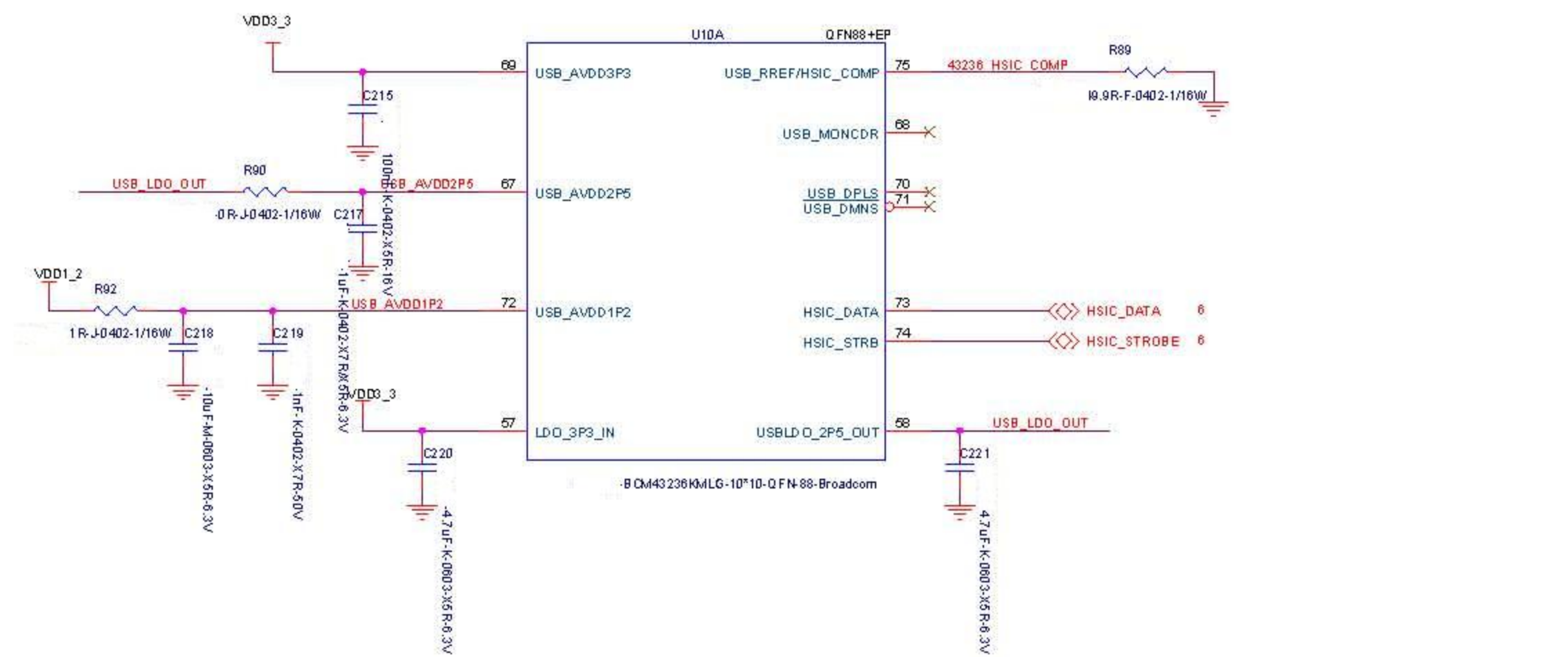


LED Functions
1G Link/Activity
100M Link/Activity
10M Link/Activity
Duplex (not used)

EN EEE Strapping option:
Disable Energy Efficient Ethernet
due to 53125 erratas.

[illegible]

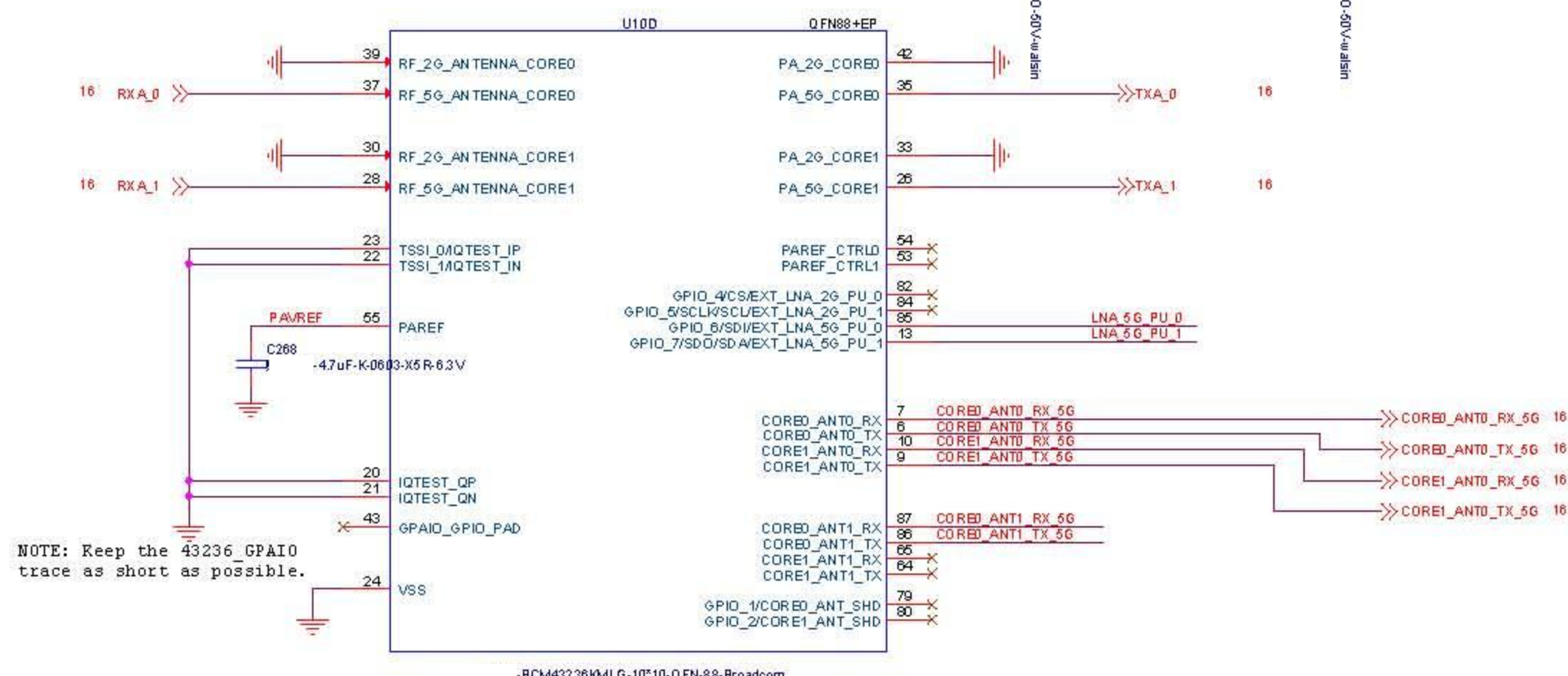
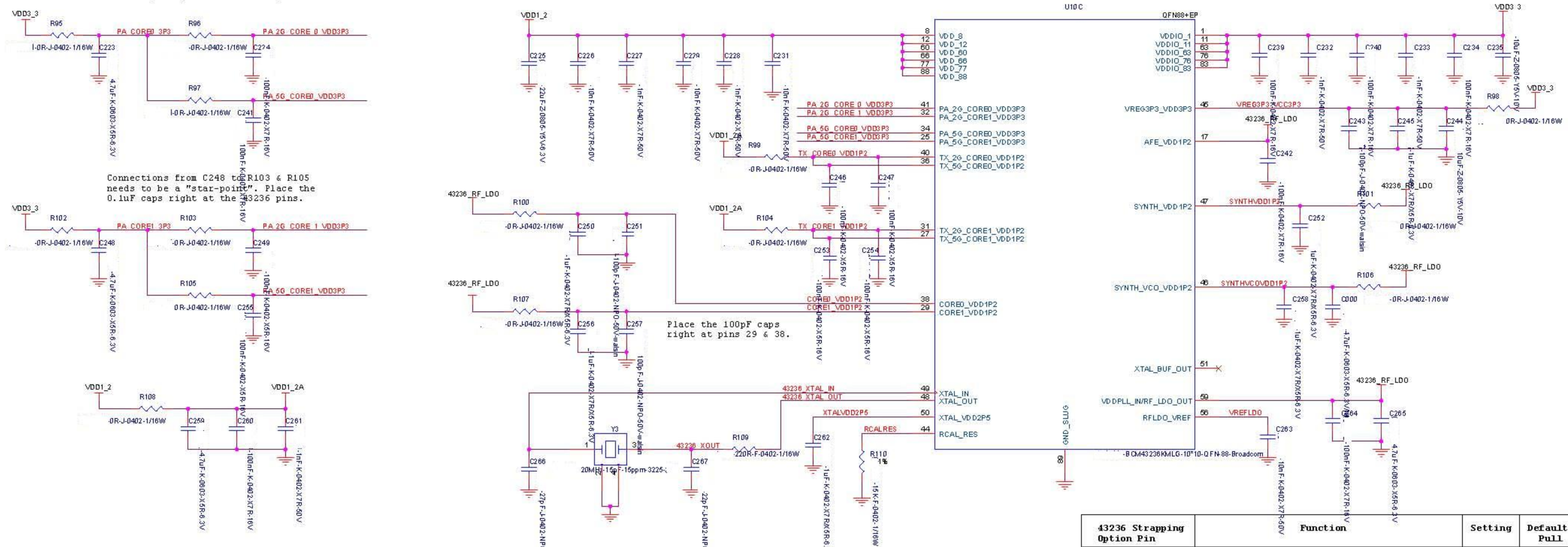
U7A			
IC: BCM53125 3MMxMLG-12*12-186PIN-MQFN			
AQFN186, 0.4mm-p6EP			
10	P4_T00P	AH12	T00_P_4
10	P4_T00N	AJ13	T00_N_4
10	P4_T01P	AH14	T01_P_4
10	P4_T01N	AG13	T01_N_4
10	P4_T02P	AJ15	T02_P_4
10	P4_T02N	AH16	T02_N_4
10	P4_T03P	AH18	T03_P_4
10	P4_T03N	AJ17	T03_N_4
10	P3_T00P	AH10	T00_P_3
10	P3_T00N	AJ9	T00_N_3
10	P3_T01P	AJ7	T01_P_3
10	P3_T01N	AH8	T01_N_3
10	P3_T02P	AG7	T02_P_3
10	P3_T02N	AH6	T02_N_3
10	P3_T03P	AH4	T03_P_3
10	P3_T03N	AJ6	T03_N_3
10	P2_T00P	B8	T00_P_2
10	P2_T00N	A7	T00_N_2
10	P2_T01P	B6	T01_P_2
10	P2_T01N	C7	T01_N_2
10	P2_T02P	B4	T02_P_2
10	P2_T02N	C3	T02_N_2
10	P2_T03P	C5	T03_P_2
10	P2_T03N	C6	T03_N_2
10	P1_T00P	C11	T00_P_1
10	P1_T00N	A11	T00_N_1
10	P1_T01P	C13	T01_P_1
10	P1_T01N	B12	T01_N_1
10	P1_T02P	A13	T02_P_1
10	P1_T02N	B14	T02_N_1
10	P1_T03P	B16	T03_P_1
10	P1_T03N	A15	T03_N_1
10	P0_T00P	B18	T00_P_0
10	P0_T00N	A19	T00_N_0
10	P0_T01P	B20	T01_P_0
10	P0_T01N	C19	T01_N_0
10	P0_T02P	A21	T02_P_0
10	P0_T02N	B22	T02_N_0
10	P0_T03P	B24	T03_P_0
10	P0_T03N	A23	T03_N_0
	GMII_LED_SEL	H2	GMII_LED_SEL
	RGMIIL_VOL_SEL	E3	RGMIIL_VOL_SEL
	GMII_VOL_REF		GMII_VOL_REF



Connections from C223 to R96 & R97 needs to be a "star-point". Place the 0.1uF caps right at the 43236 pins.

Connections from C248 to R103 & R105 needs to be a "star-point". Place the 0.1uF caps right at the 43236 pins.

Place the 100pF caps right at pins 29 & 38.



NOTE: Keep the 43236 GPAIO trace as short as possible.

43236 Strapping Option Pin	Function	Setting	Default Pull
CORE1_ANT0_TX	0 = Serial Flash NOT present [default] 1 = Serial Flash present	0	PD
CORE0_ANT0_RX	0 = Serial Flash Type is ST Micro [default] 1 = Serial Flash Type is Atmel	0*	PD
CORE0_ANT1_TX	0 = HSIC mode (board setting) 1 = USB 2.0 PHY mode [default]	0	PU
CORE0_ANT1_RX	0 = backplane at 96 (98.4) MHz 1 = backplane at 120 (123) MHz [default]	1	PU
CORE0_ANT0_TX	0 = Do not use OTP for configuration (setting) 1 = Use OTP for configuration [default]	0	PU
GPIO[7:6] LNA_5G_PU_1/ LNA_5G_PU_0	00 = boot from RAM, ARM held in reset 01 = boot from ROM [default] 10 = Reserved 11 = Reserved	01	none

* = Strapping option set at the default with no option to change.

