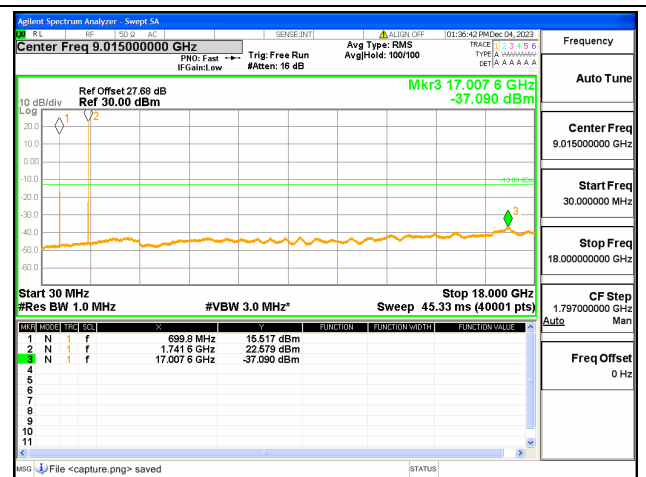
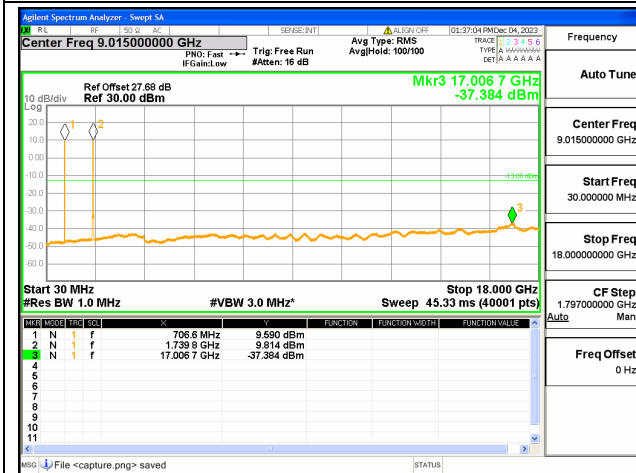


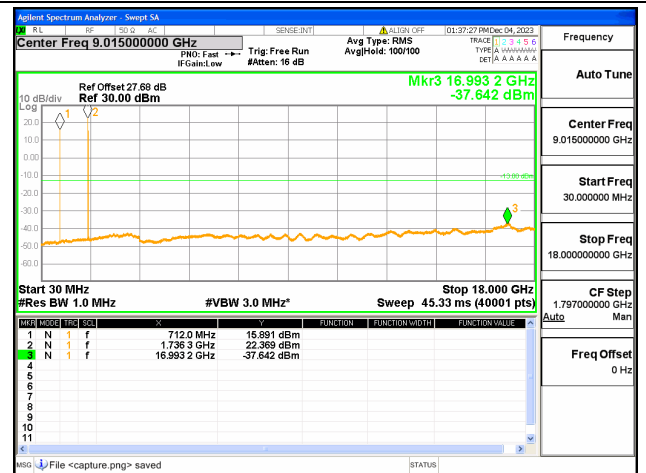
4A-12A / 20+10MHz / QPSK / Mid+Low CH / 1@0-1@49



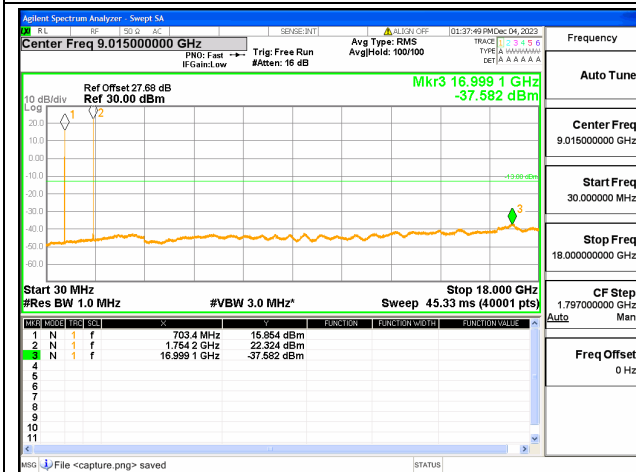
4A-12A / 20+10MHz / QPSK / Mid+Low CH / 1@99-1@0



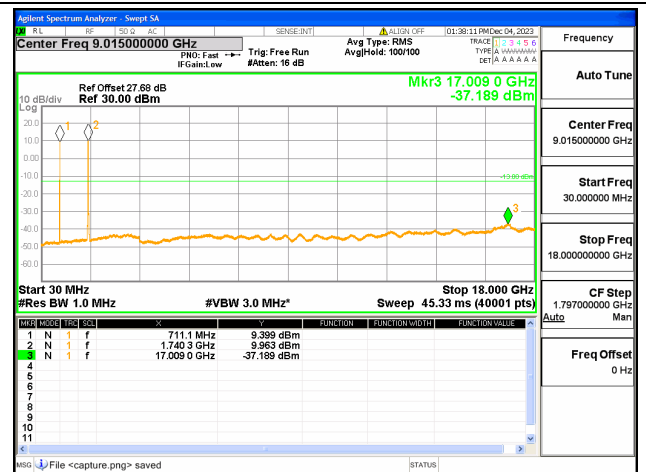
4A-12A / 20+10MHz / QPSK / Mid+Low CH / 100@0-50@0



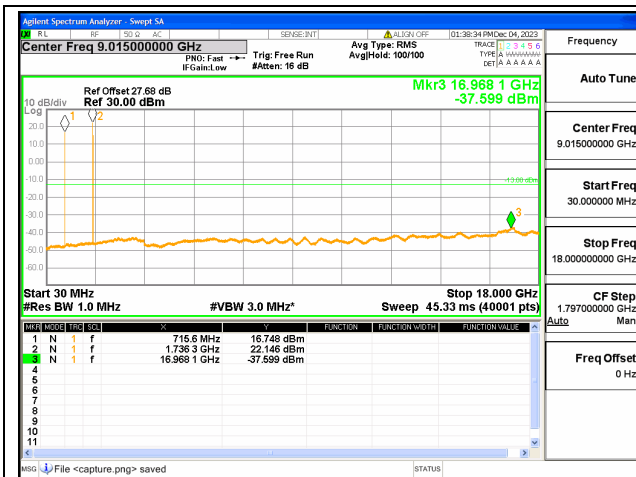
4A-12A / 20+10MHz / QPSK / High+Mid CH / 1@0-1@49



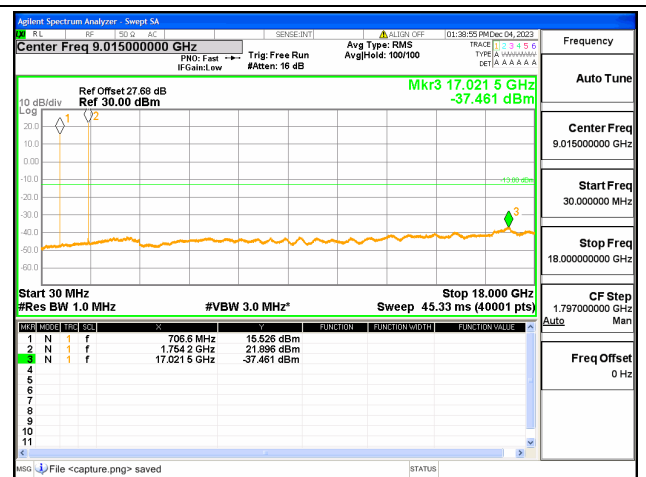
4A-12A / 20+10MHz / QPSK / High+Mid CH / 1@99-1@0



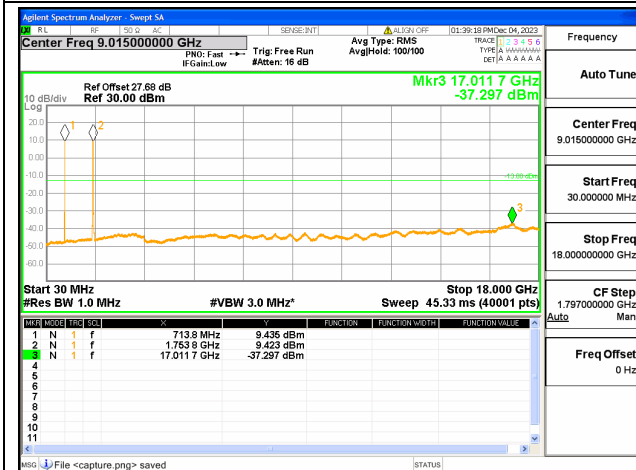
4A-12A / 20+10MHz / QPSK / High+Mid CH / 100@0-50@0



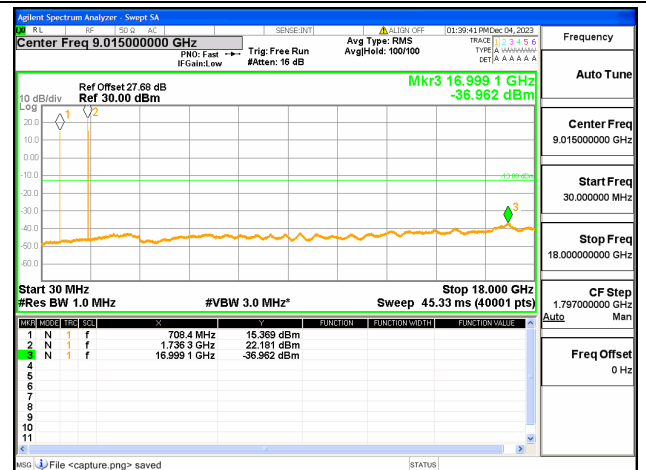
4A-12A / 20+10MHz / QPSK / High+High CH / 1@0-1@49



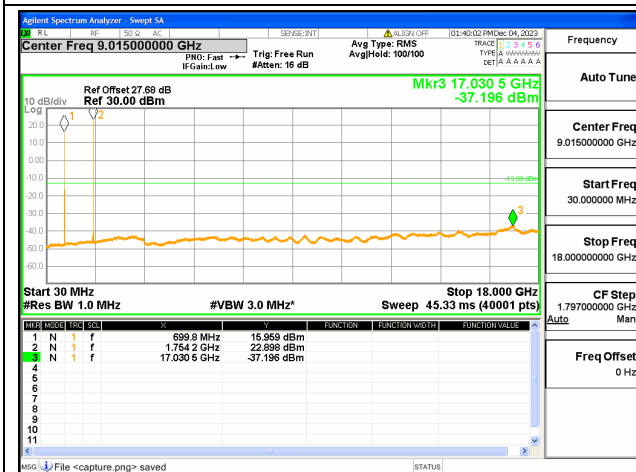
4A-12A / 20+10MHz / QPSK / High+High CH / 1@99-1@0



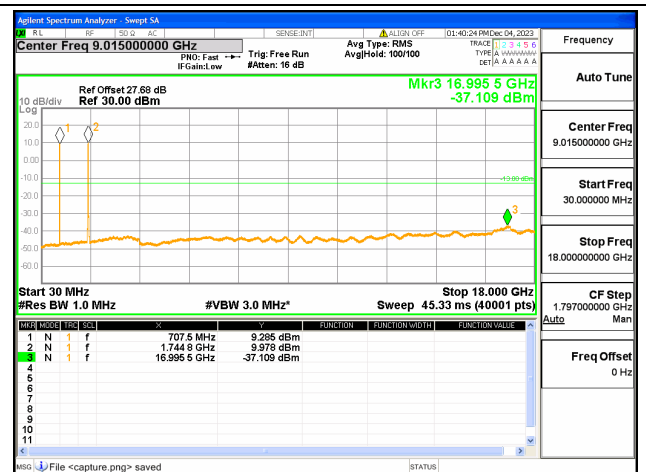
4A-12A / 20+10MHz / QPSK / High+High CH / 100@0-50@0



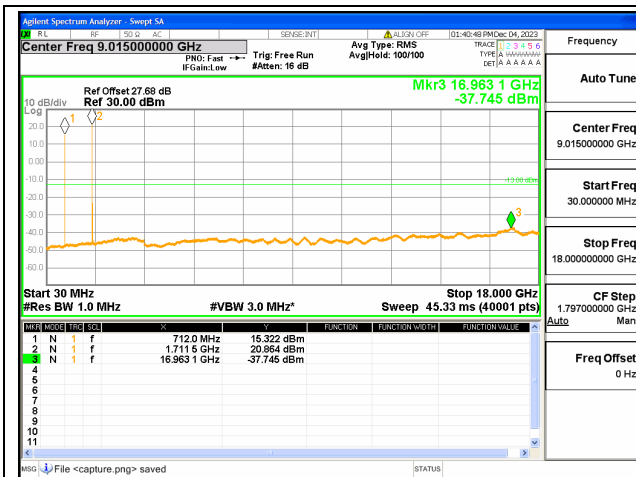
4A-12A / 20+10MHz / QPSK / High+Low CH / 1@0-1@49



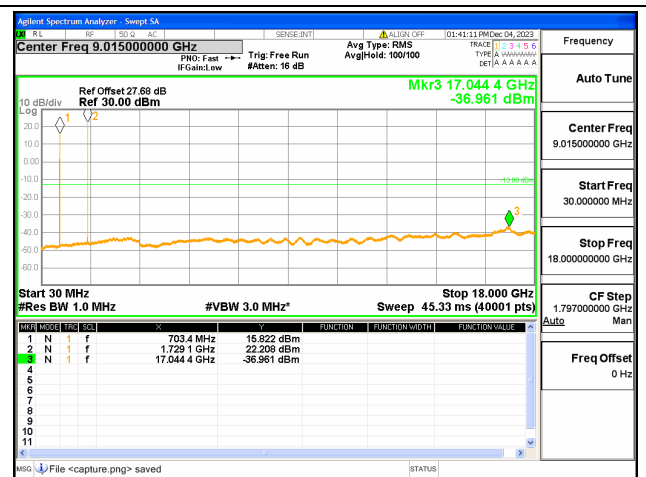
4A-12A / 20+10MHz / QPSK / High+Low CH / 1@99-1@0



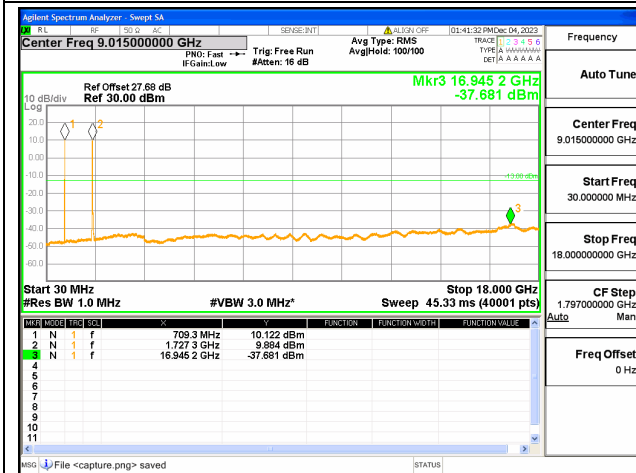
4A-12A / 20+10MHz / QPSK / High+Low CH / 100@0-50@0



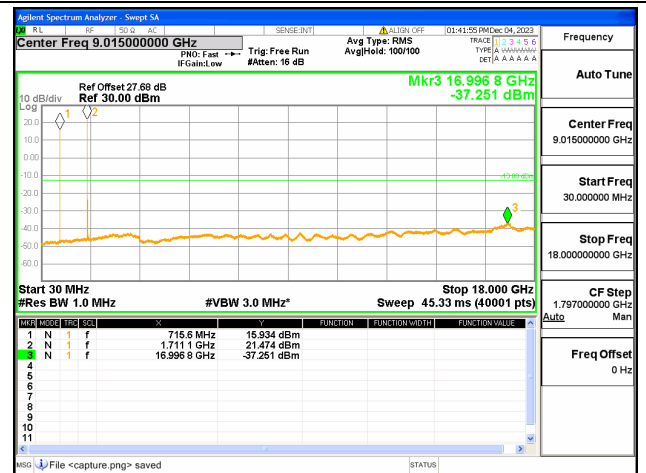
4A-12A / 20+10MHz / QPSK / Low+Mid CH / 1@0-1@49



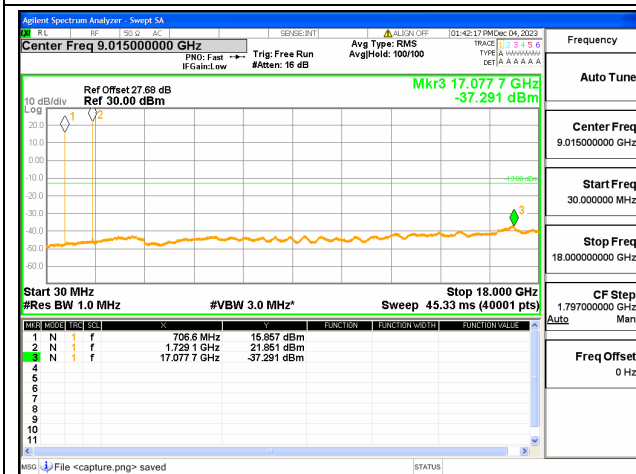
4A-12A / 20+10MHz / QPSK / Low+Mid CH / 1@99-1@0



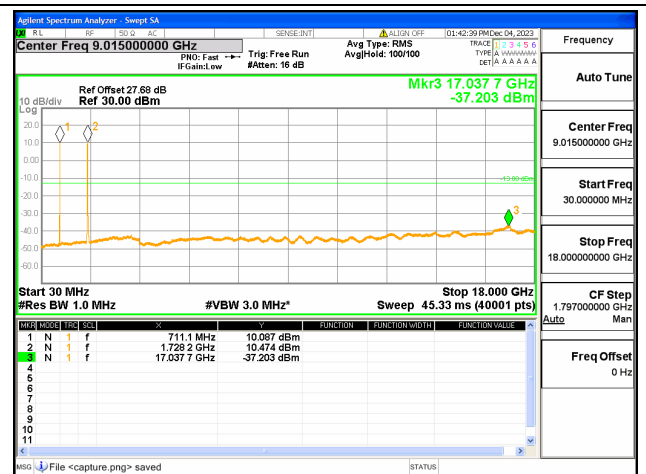
4A-12A / 20+10MHz / QPSK / Low+Mid CH / 100@0-50@0



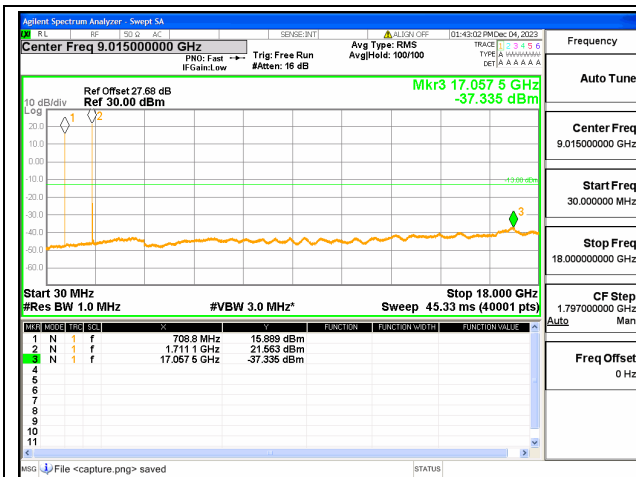
4A-12A / 20+10MHz / QPSK / Low+High CH / 1@0-1@49



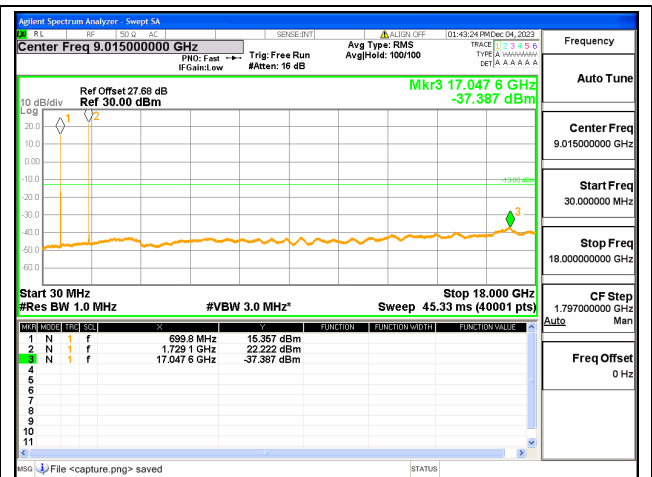
4A-12A / 20+10MHz / QPSK / Low+High CH / 1@99-1@0



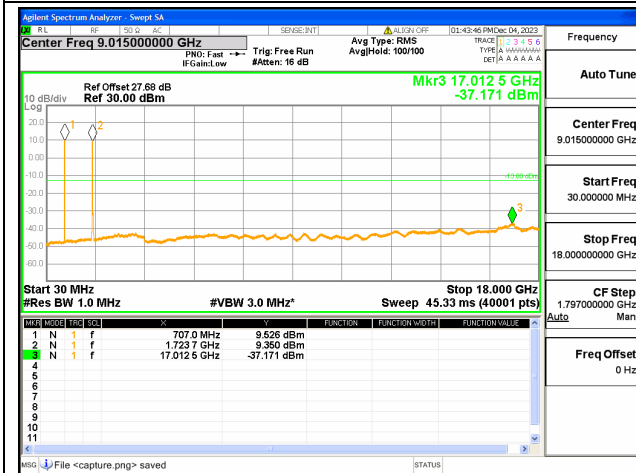
4A-12A / 20+10MHz / QPSK / Low+High CH / 100@0-50@0



4A-12A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@49

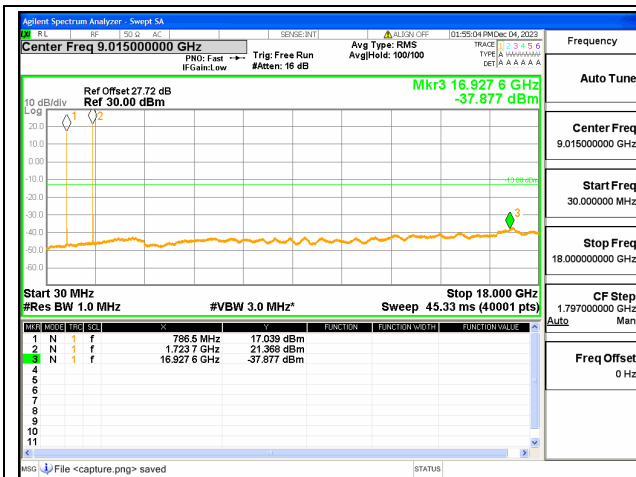


4A-12A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@0

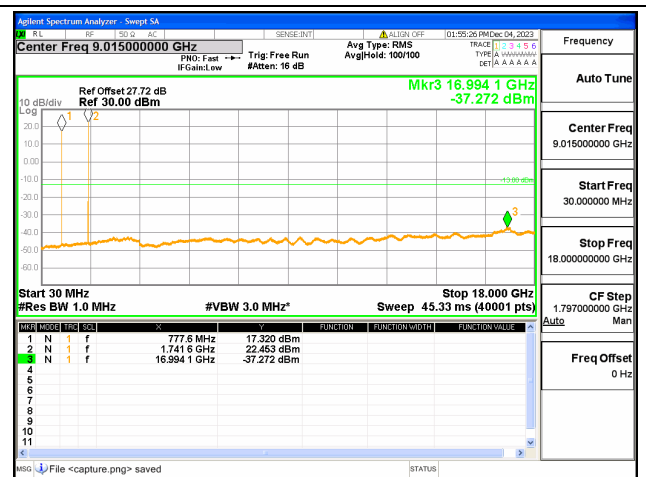


4A-12A / 20+10MHz / QPSK / Low+Low CH /
100@0-50@0

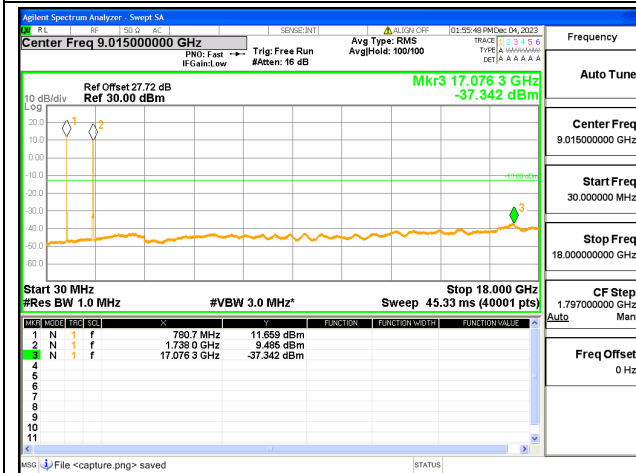
N/A



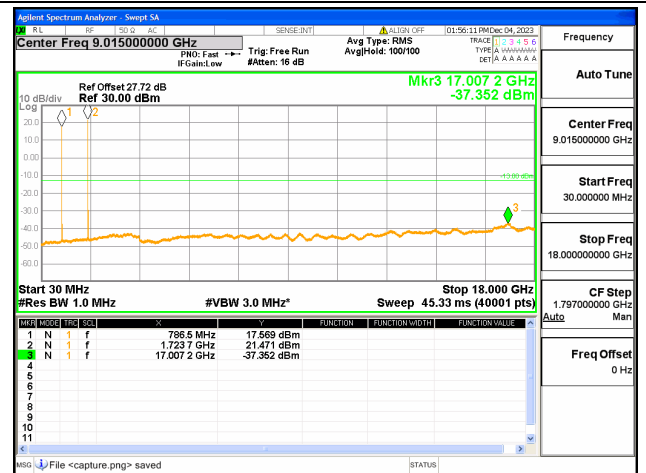
4A-13A / 20+10MHz / QPSK / Mid+Mid CH / 1@0-1@49



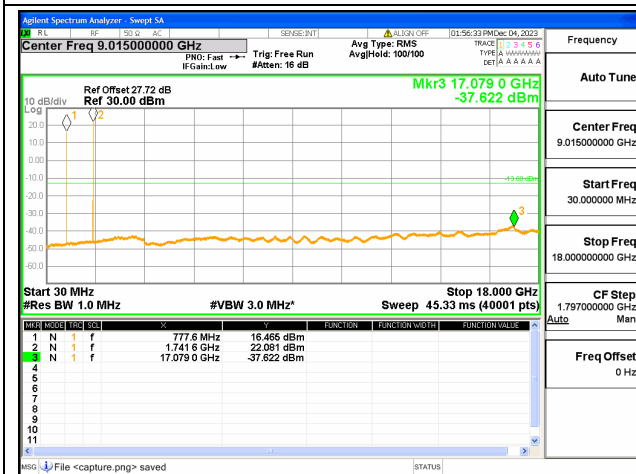
4A-13A / 20+10MHz / QPSK / Mid+Mid CH / 1@99-1@0



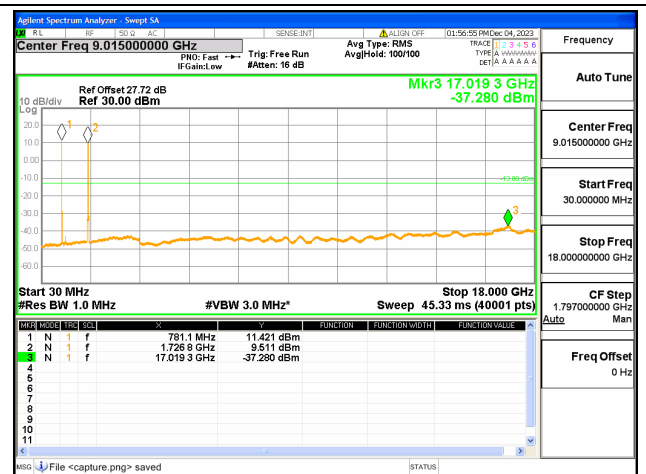
4A-13A / 20+10MHz / QPSK / Mid+Mid CH / 100@0-50@0



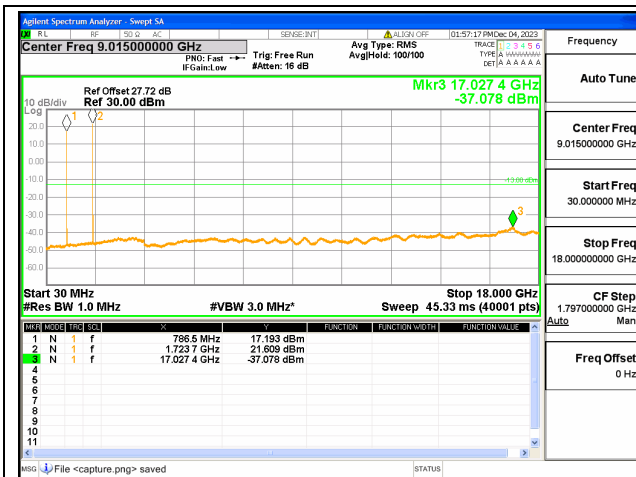
4A-13A / 20+10MHz / QPSK / Mid+High CH / 1@0-1@49



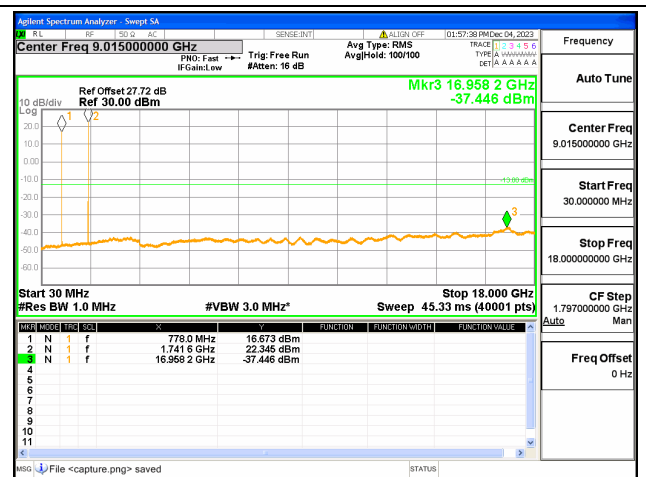
4A-13A / 20+10MHz / QPSK / Mid+High CH / 1@99-1@0



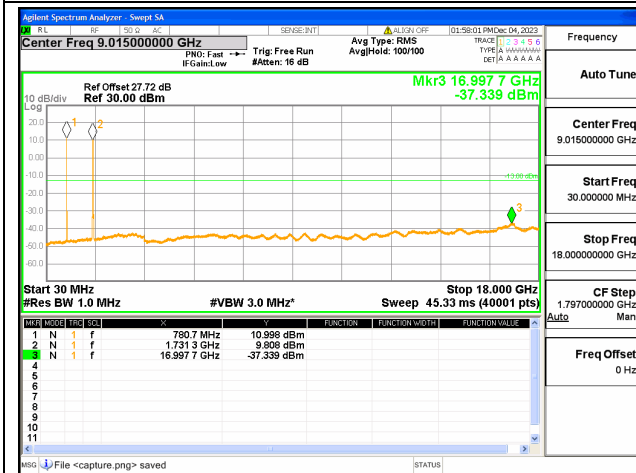
4A-13A / 20+10MHz / QPSK / Mid+High CH / 100@0-50@0



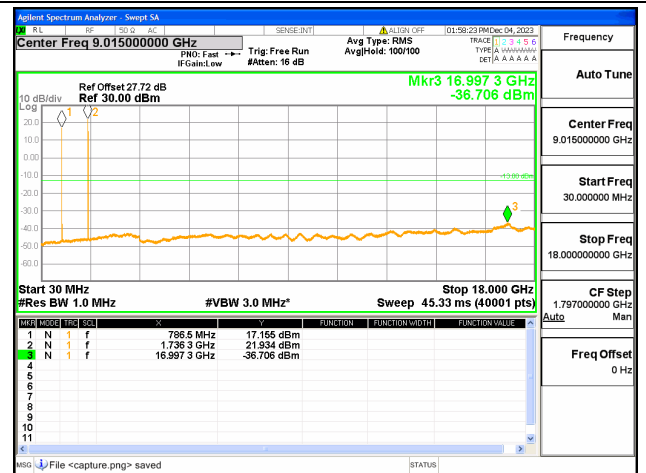
4A-13A / 20+10MHz / QPSK / Mid+Low CH / 1@0-1@49



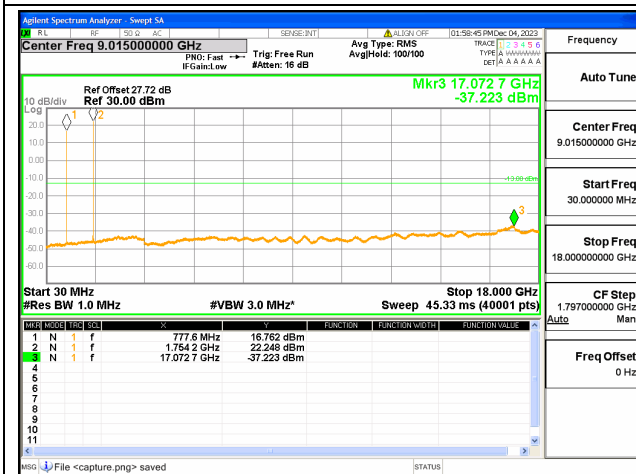
4A-13A / 20+10MHz / QPSK / Mid+Low CH / 1@99-1@0



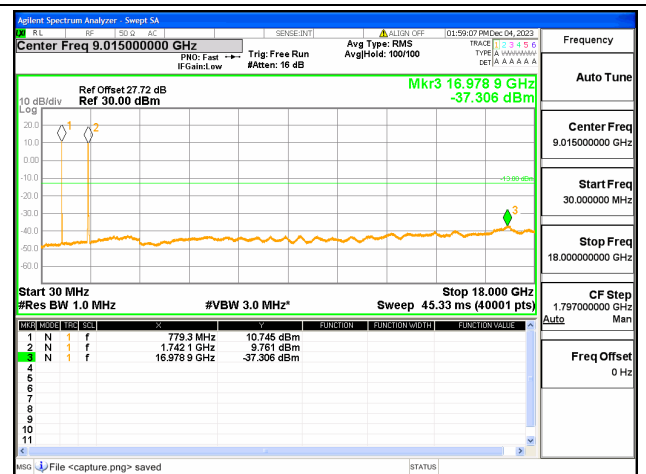
4A-13A / 20+10MHz / QPSK / Mid+Low CH / 100@0-50@0



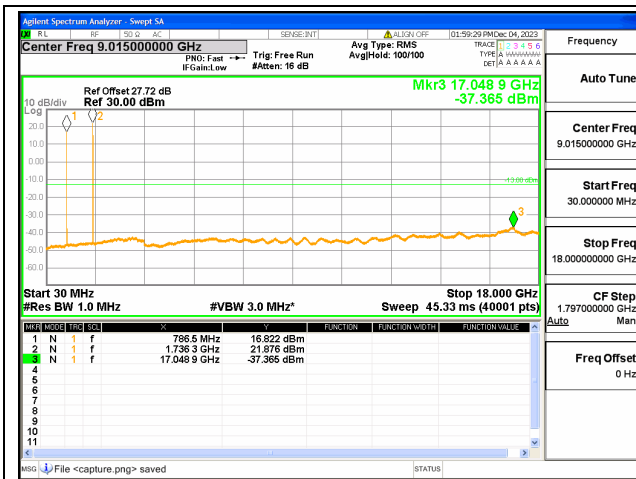
4A-13A / 20+10MHz / QPSK / High+Mid CH / 1@0-1@49



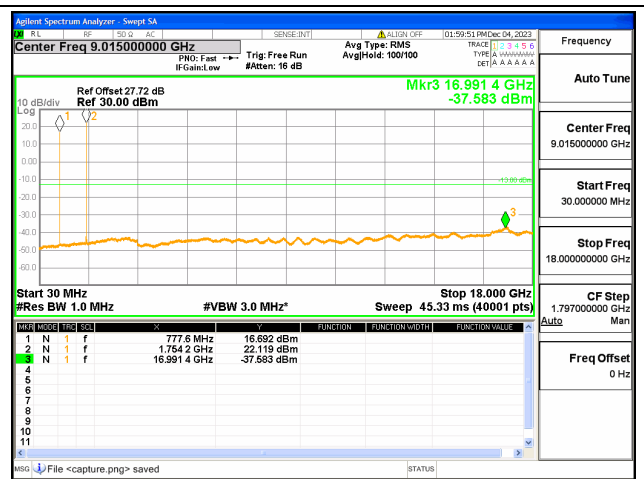
4A-13A / 20+10MHz / QPSK / High+Mid CH / 1@99-1@0



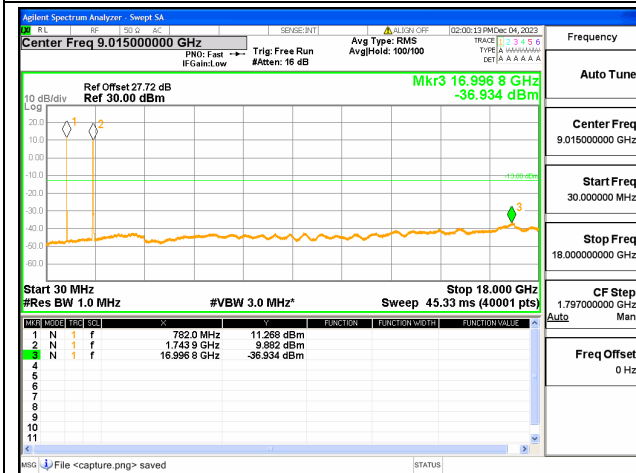
4A-13A / 20+10MHz / QPSK / High+Mid CH / 100@0-50@0



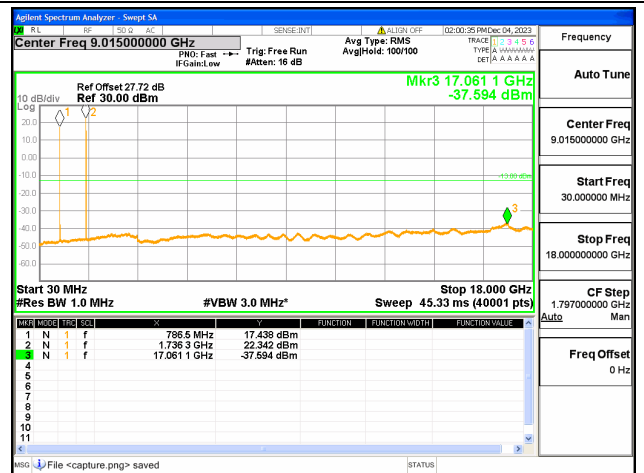
4A-13A / 20+10MHz / QPSK / High+High CH / 1@0-1@49



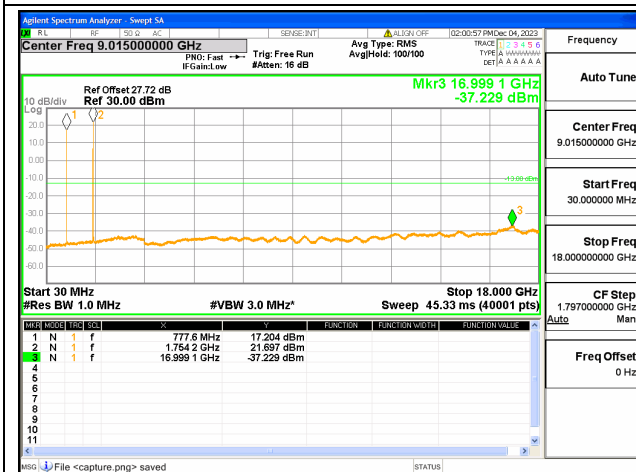
4A-13A / 20+10MHz / QPSK / High+High CH / 1@99-1@0



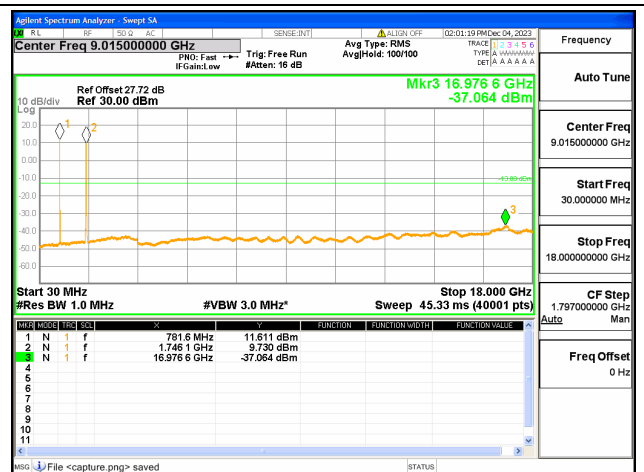
4A-13A / 20+10MHz / QPSK / High+High CH / 100@0-50@0



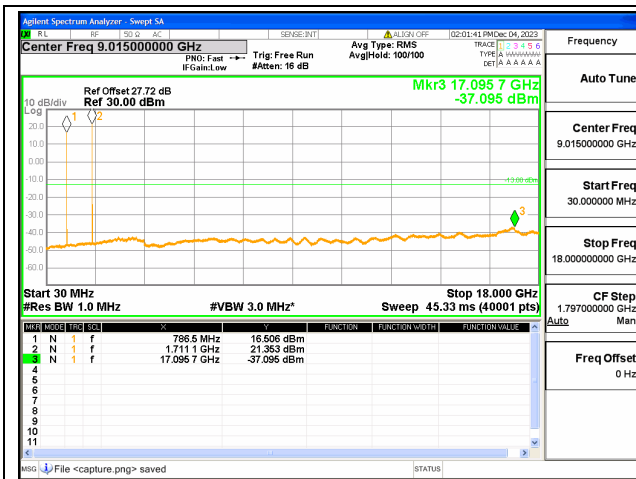
4A-13A / 20+10MHz / QPSK / High+Low CH / 1@0-1@49



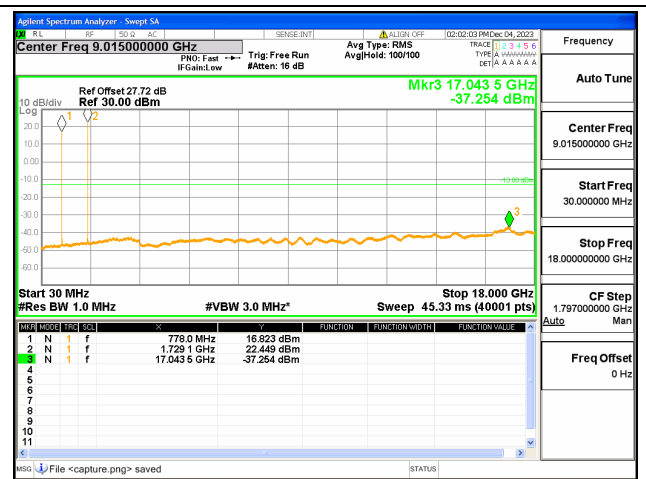
4A-13A / 20+10MHz / QPSK / High+Low CH / 1@99-1@0



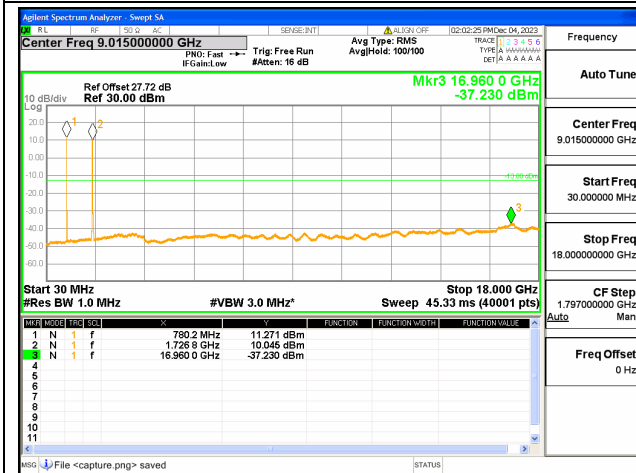
4A-13A / 20+10MHz / QPSK / High+Low CH / 100@0-50@0



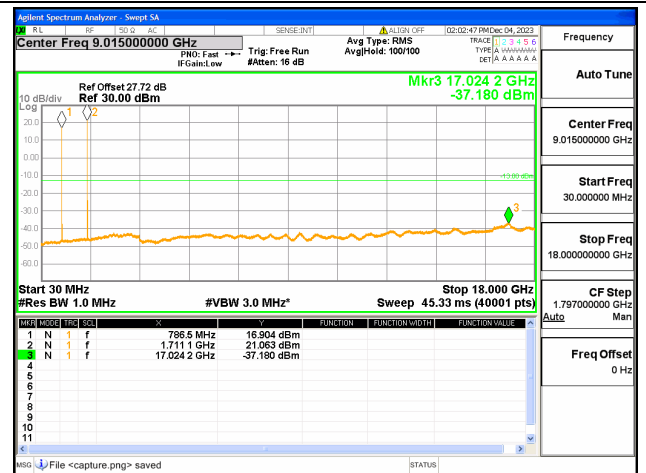
4A-13A / 20+10MHz / QPSK / Low+Mid CH / 1@0-1@49



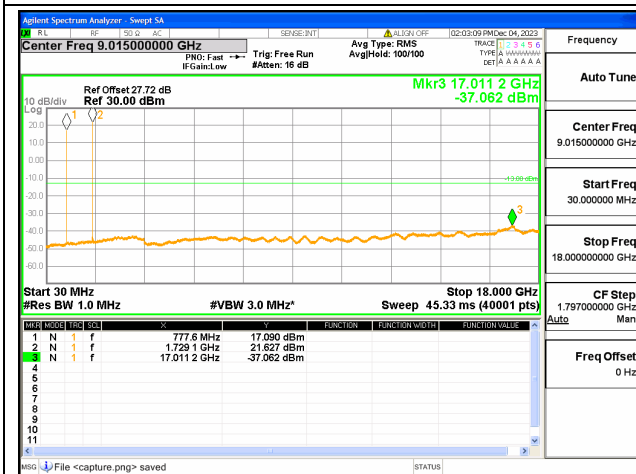
4A-13A / 20+10MHz / QPSK / Low+Mid CH / 1@99-1@0



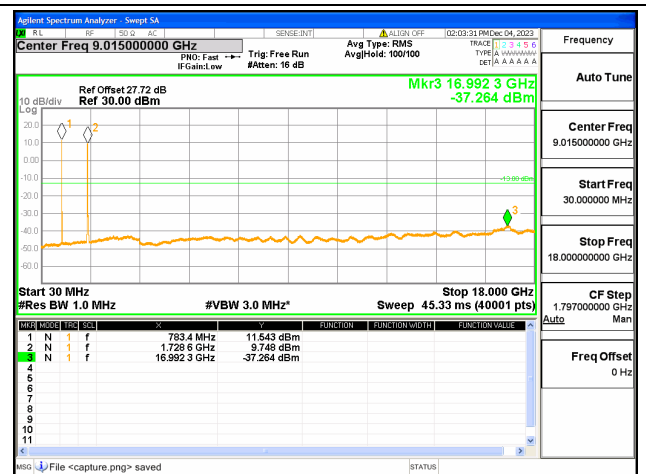
4A-13A / 20+10MHz / QPSK / Low+Mid CH / 100@0-50@0



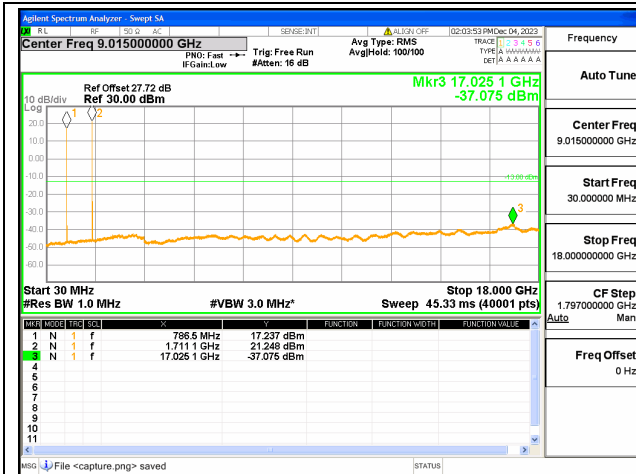
4A-13A / 20+10MHz / QPSK / Low+High CH / 1@0-1@49



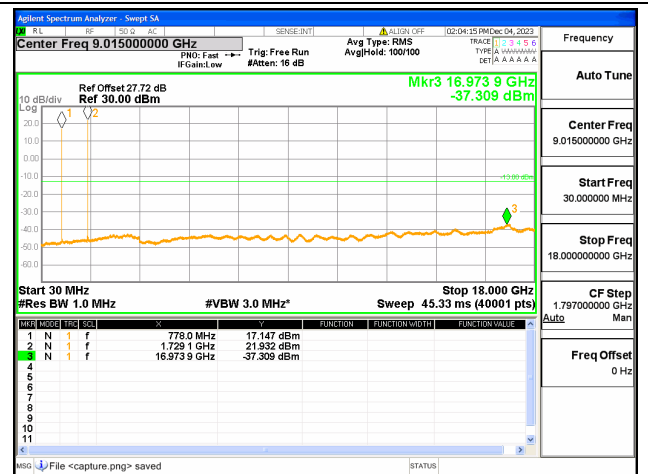
4A-13A / 20+10MHz / QPSK / Low+High CH / 1@99-1@0



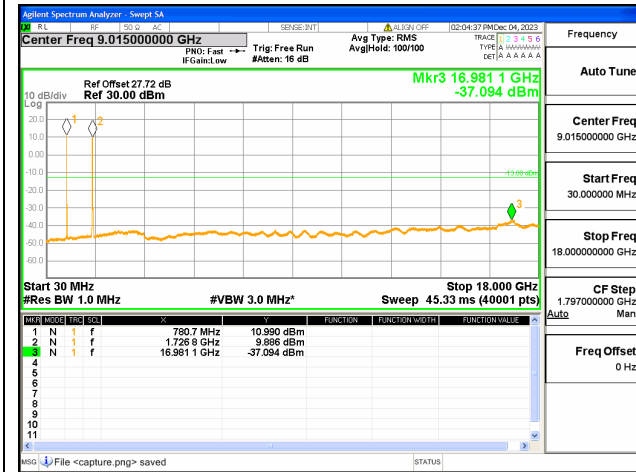
4A-13A / 20+10MHz / QPSK / Low+High CH / 100@0-50@0



4A-13A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@49

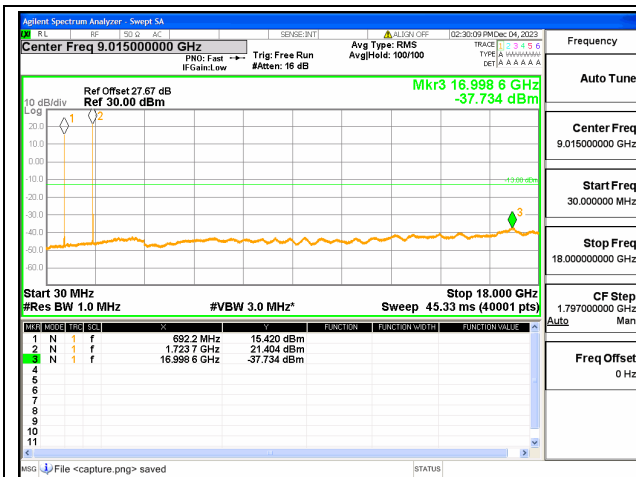


4A-13A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@0

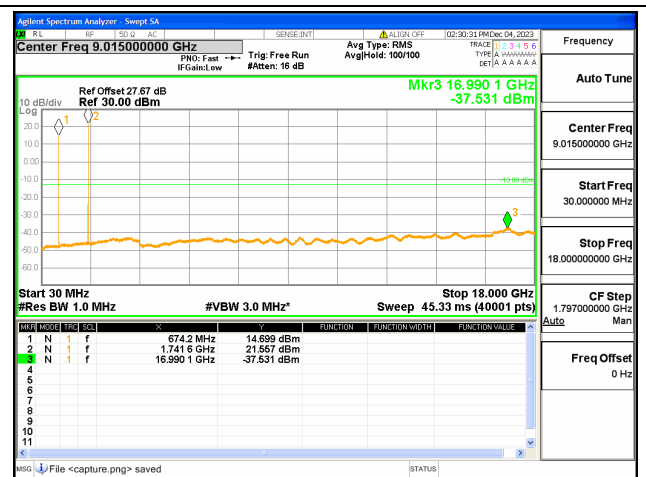


4A-13A / 20+10MHz / QPSK / Low+Low CH /
100@0-50@0

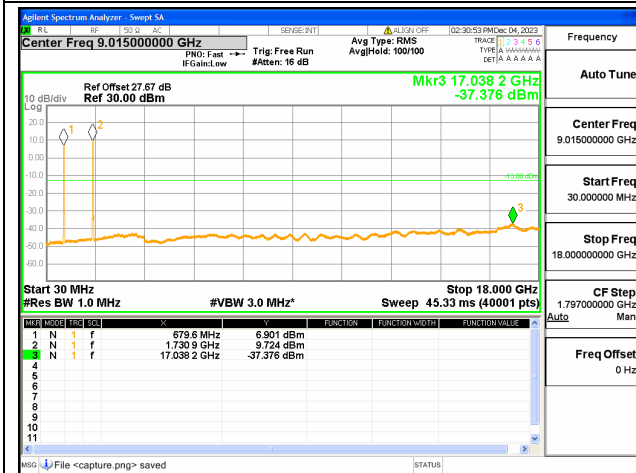
N/A



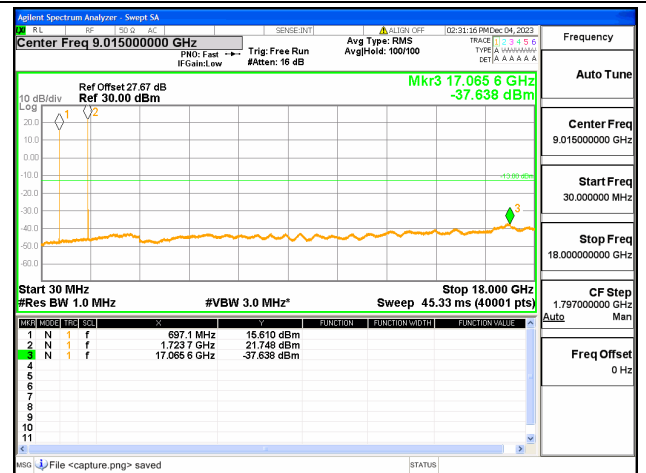
4A-71A / 20+20MHz / QPSK / Mid+Mid CH / 1@0-1@99



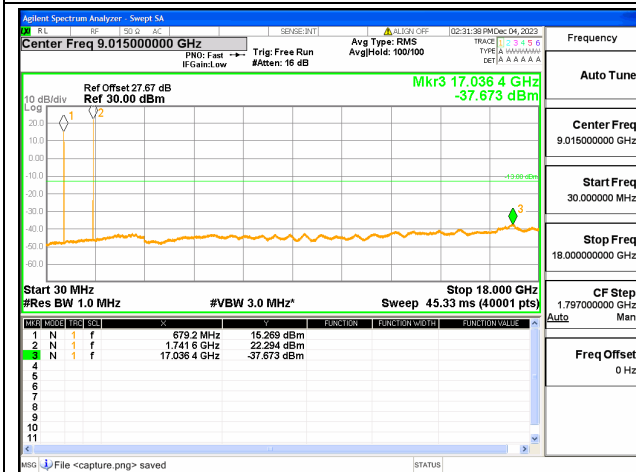
4A-71A / 20+20MHz / QPSK / Mid+Mid CH / 1@99-1@0



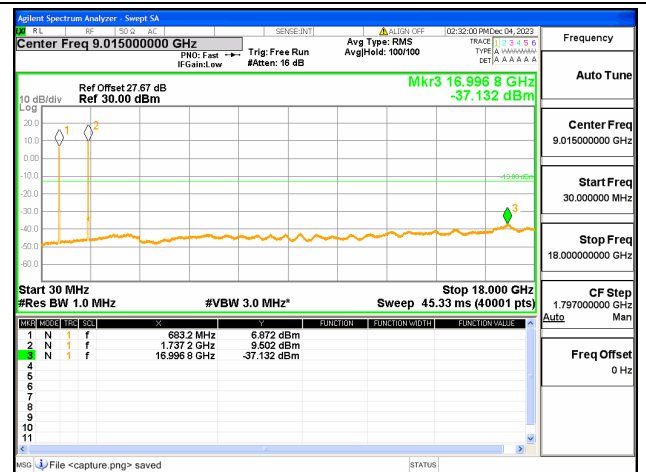
4A-71A / 20+20MHz / QPSK / Mid+Mid CH / 100@0-100@0



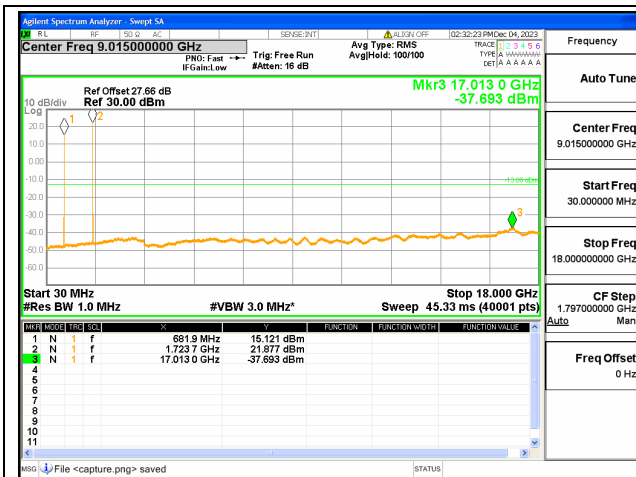
4A-71A / 20+20MHz / QPSK / Mid+High CH / 1@0-1@99



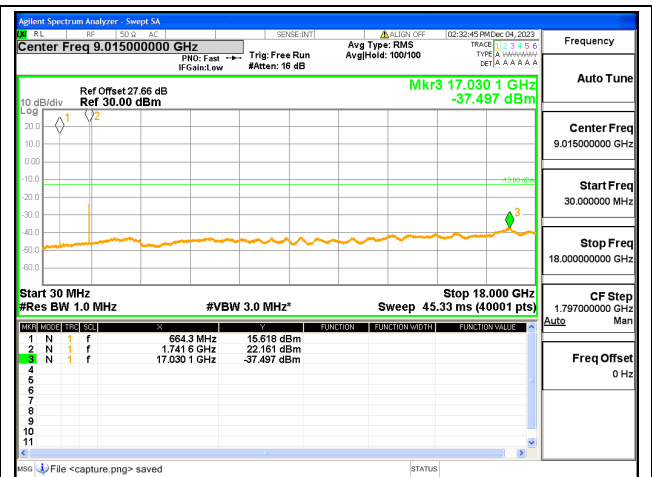
4A-71A / 20+20MHz / QPSK / Mid+High CH / 1@99-1@0



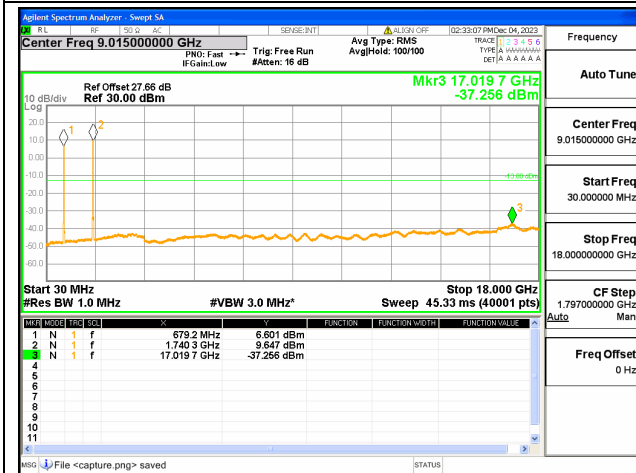
4A-71A / 20+20MHz / QPSK / Mid+High CH / 100@0-100@0



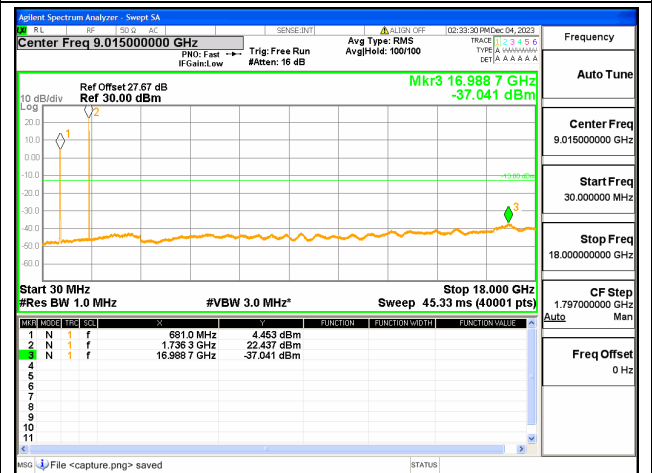
4A-71A / 20+20MHz / QPSK / Mid+Low CH / 1@0-1@99



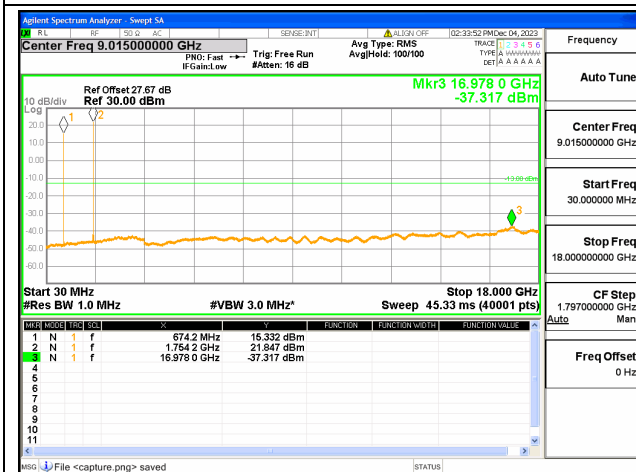
4A-71A / 20+20MHz / QPSK / Mid+Low CH / 1@99-1@0



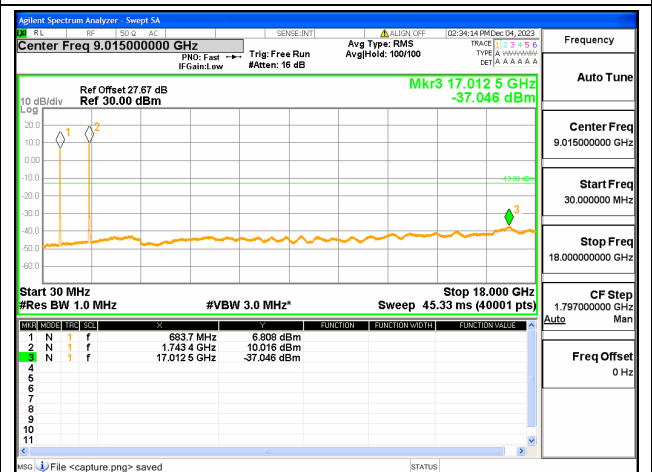
4A-71A / 20+20MHz / QPSK / Mid+Low CH / 100@0-100@0



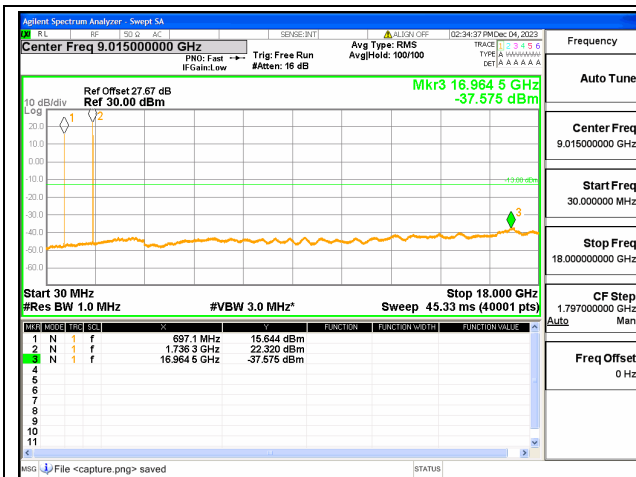
4A-71A / 20+20MHz / QPSK / High+Mid CH / 1@0-1@99



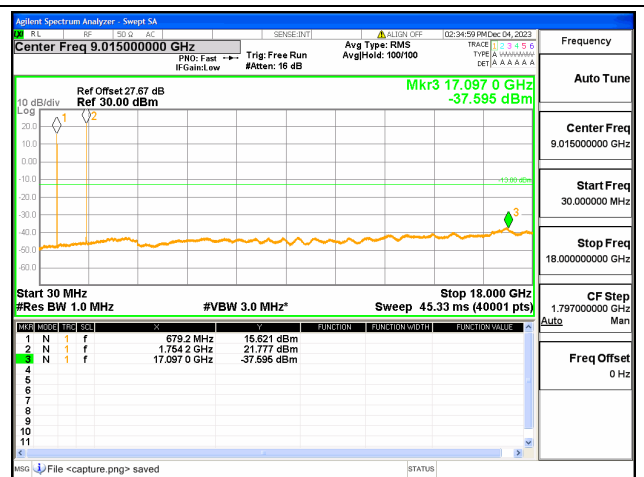
4A-71A / 20+20MHz / QPSK / High+Mid CH / 1@99-1@0



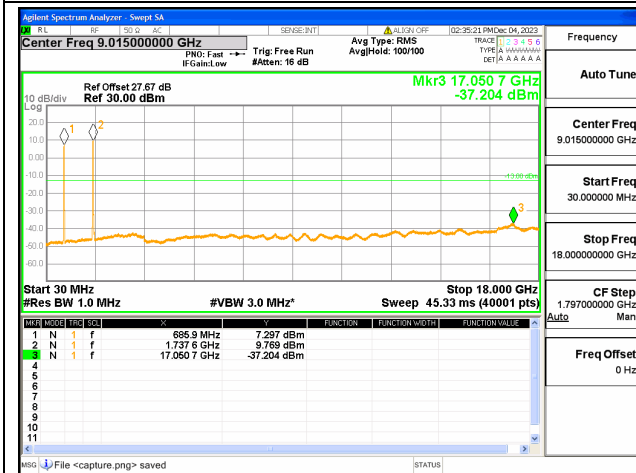
4A-71A / 20+20MHz / QPSK / High+Mid CH / 100@0-100@0



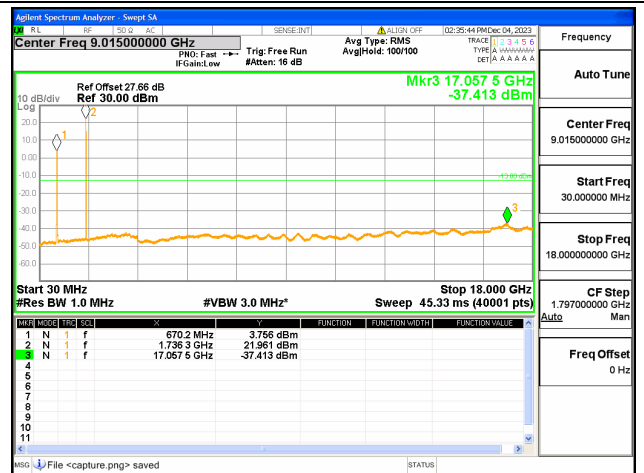
4A-71A / 20+20MHz / QPSK / High+High CH / 1@0-1@99



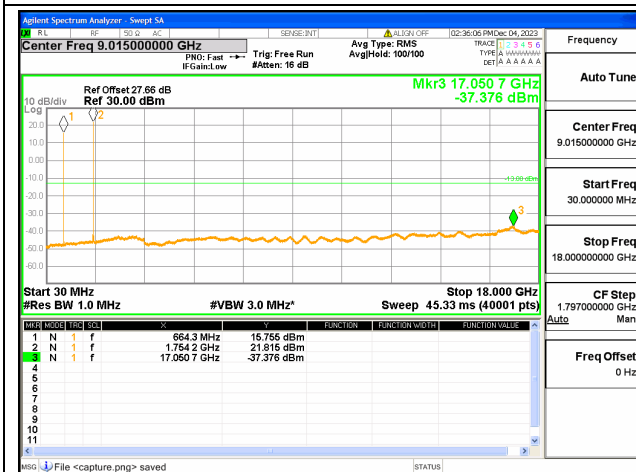
4A-71A / 20+20MHz / QPSK / High+High CH / 1@99-1@0



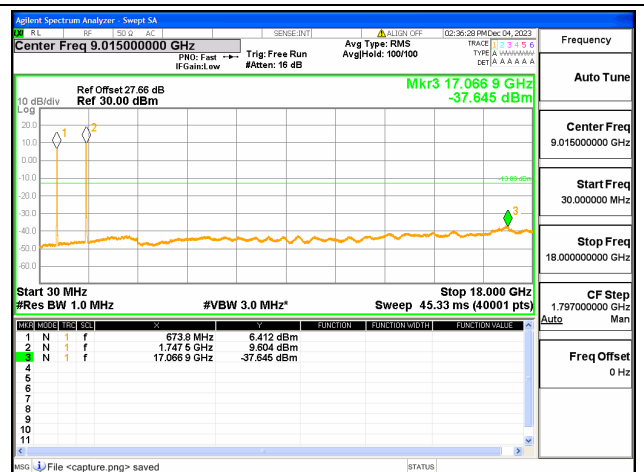
4A-71A / 20+20MHz / QPSK / High+High CH / 100@0-100@0



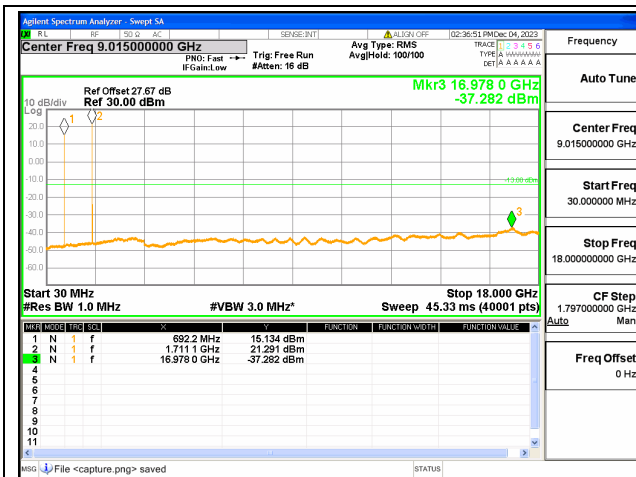
4A-71A / 20+20MHz / QPSK / High+Low CH / 1@0-1@99



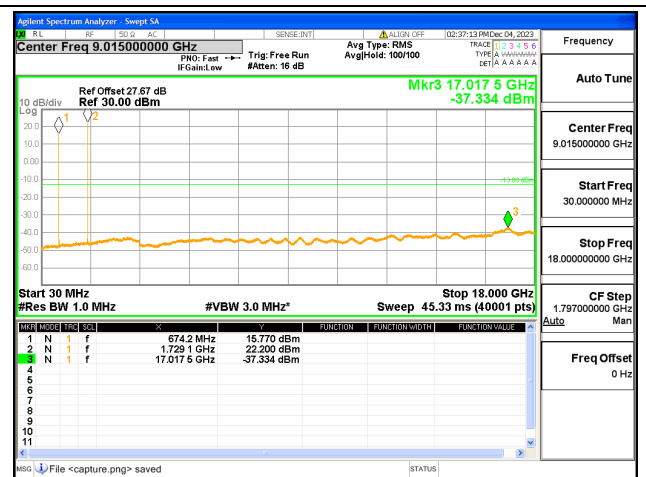
4A-71A / 20+20MHz / QPSK / High+Low CH / 1@99-1@0



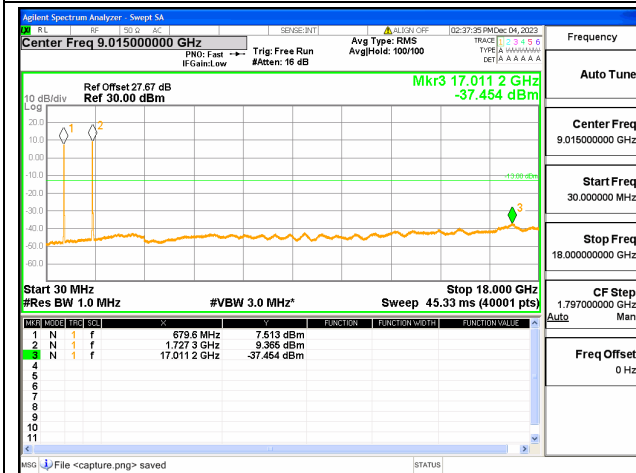
4A-71A / 20+20MHz / QPSK / High+Low CH / 100@0-100@0



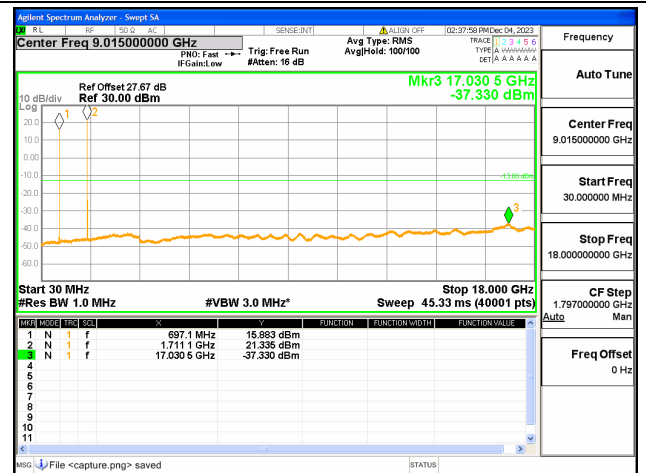
4A-71A / 20+20MHz / QPSK / Low+Mid CH / 1@0-1@99



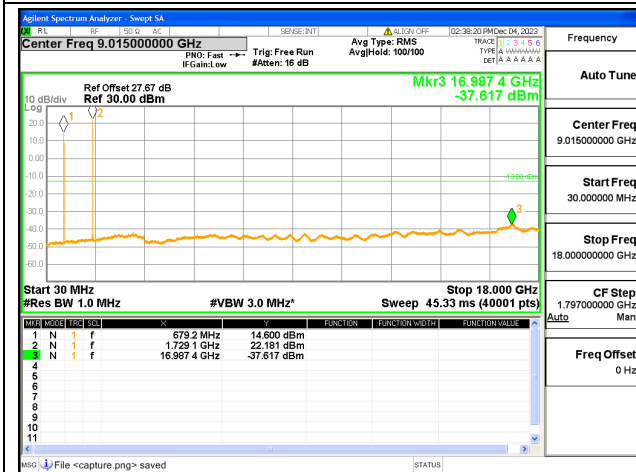
4A-71A / 20+20MHz / QPSK / Low+Mid CH / 1@99-1@0



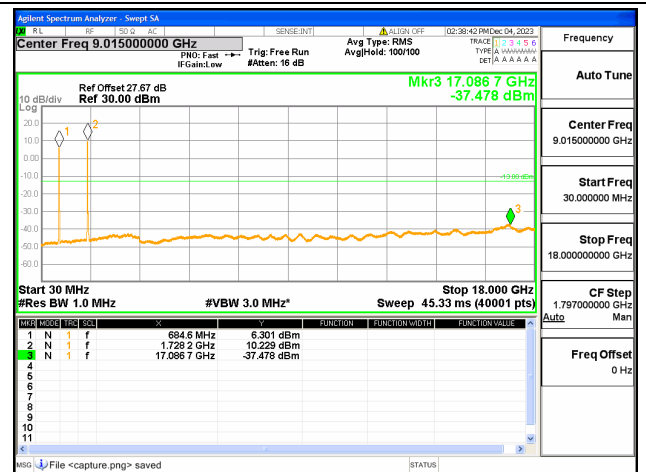
4A-71A / 20+20MHz / QPSK / Low+Mid CH / 100@0-100@0



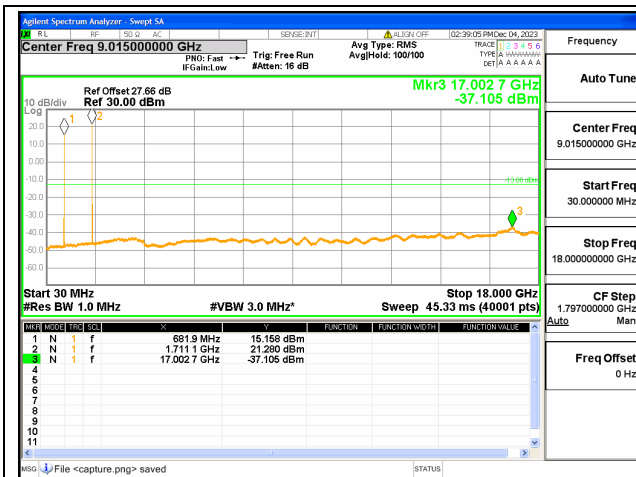
4A-71A / 20+20MHz / QPSK / Low+High CH / 1@0-1@99



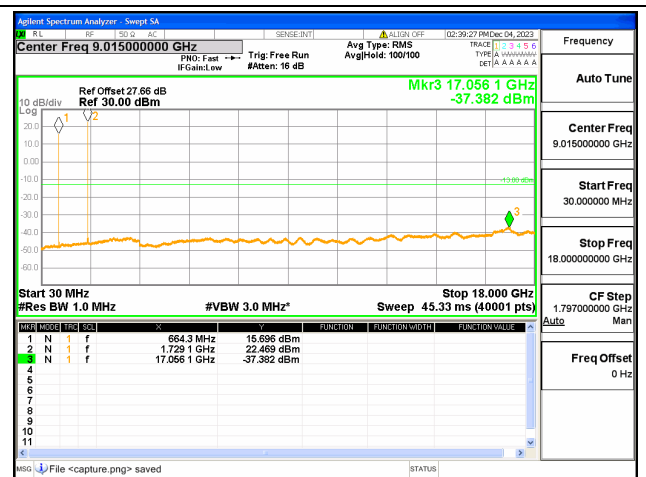
4A-71A / 20+20MHz / QPSK / Low+High CH / 1@99-1@0



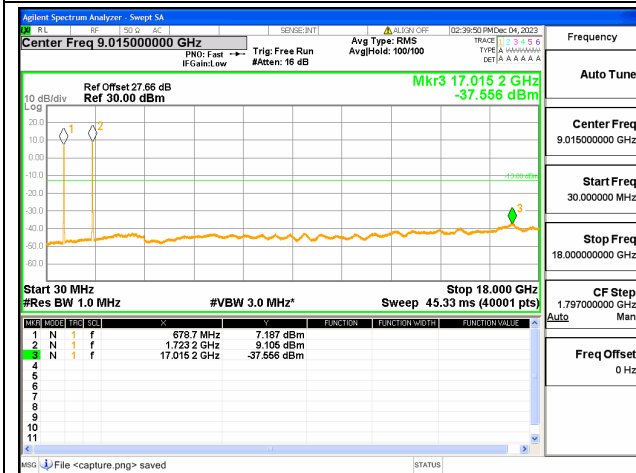
4A-71A / 20+20MHz / QPSK / Low+High CH / 100@0-100@0



4A-71A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@99

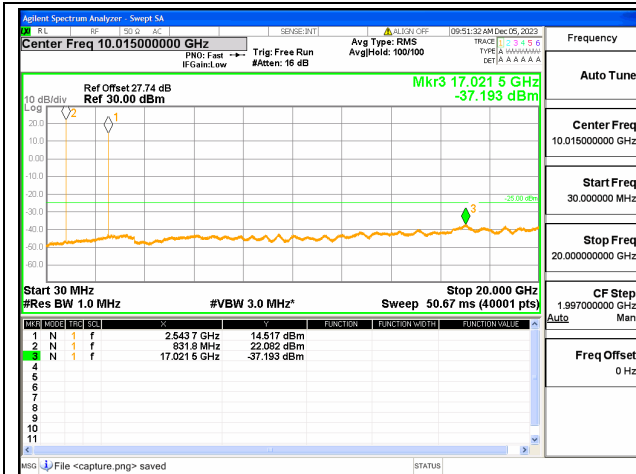


4A-71A / 20+20MHz / QPSK / Low+Low CH /
1@99-1@0

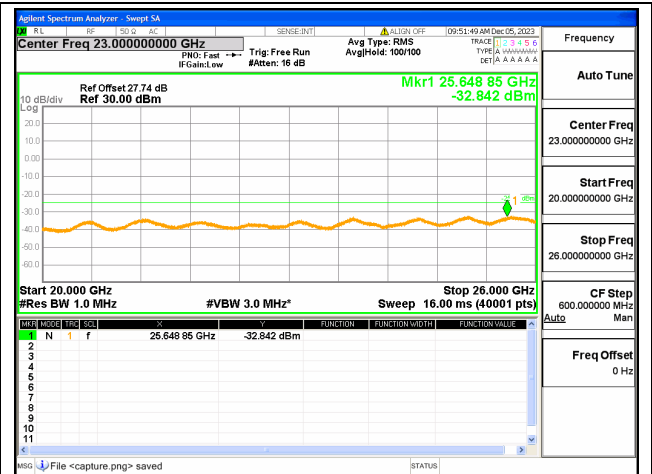


4A-71A / 20+20MHz / QPSK / Low+Low CH /
100@0-100@0

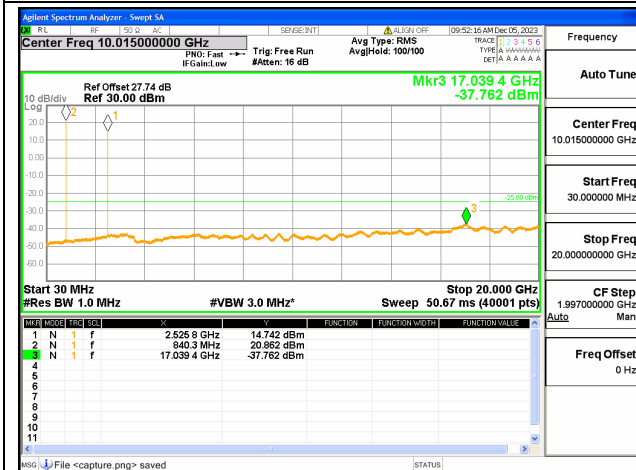
N/A



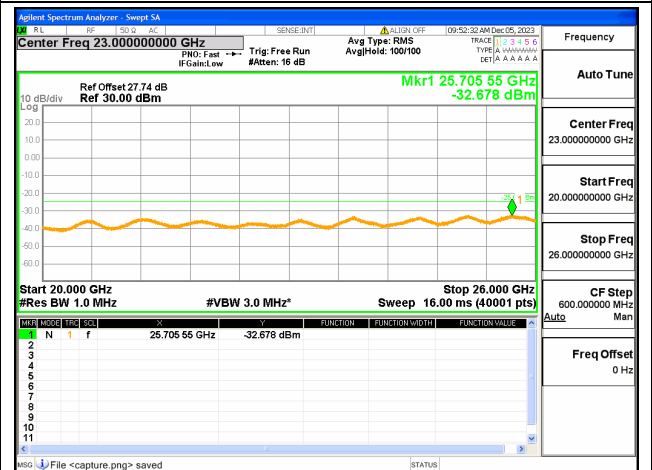
5A-7A-30M-20G / 10+20MHz / QPSK / Mid+Mid
CH / 1@0-1@99



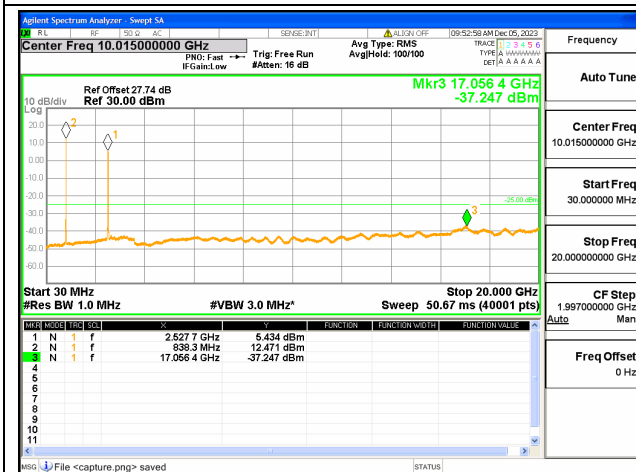
5A-7A-20G-26G / 10+20MHz / QPSK / Mid+Mid
CH / 1@0-1@99



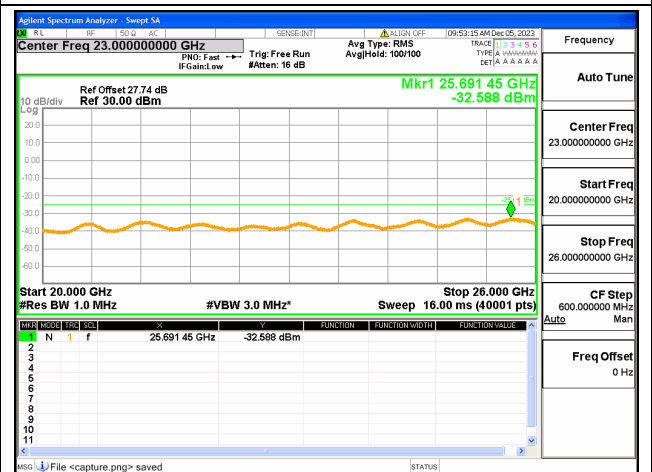
5A-7A-30M-20G / 10+20MHz / QPSK / Mid+Mid
CH / 1@49-1@0



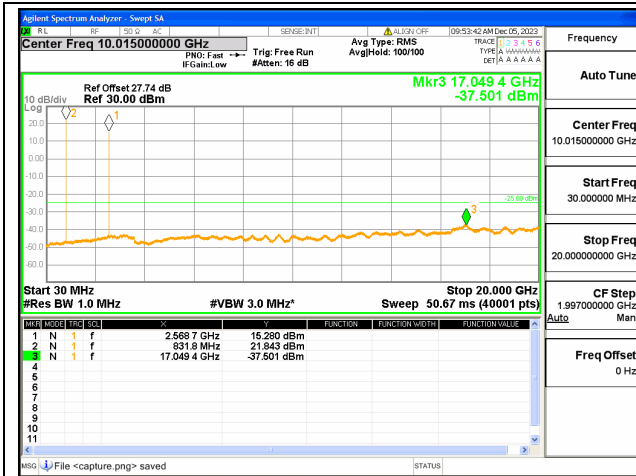
5A-7A-20G-26G / 10+20MHz / QPSK / Mid+Mid
CH / 1@49-1@0



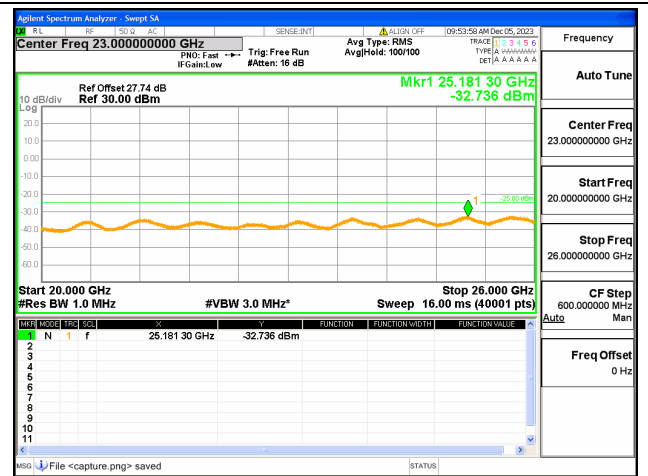
5A-7A-30M-20G / 10+20MHz / QPSK / Mid+Mid
CH / 50@0-100@0



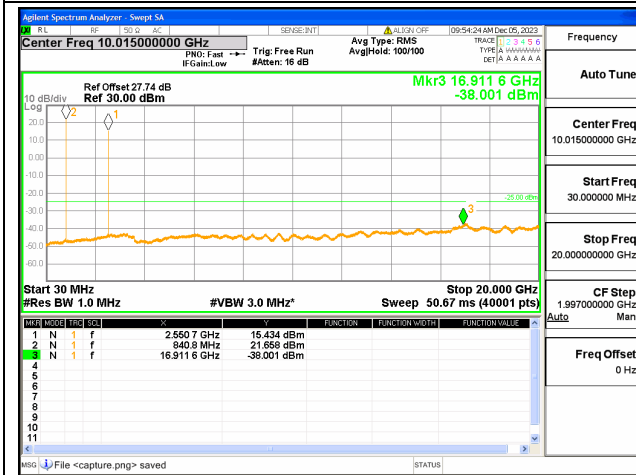
5A-7A-20G-26G / 10+20MHz / QPSK / Mid+Mid
CH / 50@0-100@0



5A-7A-30M-20G / 10+20MHz / QPSK /
Mid+High CH / 1@0-1@99



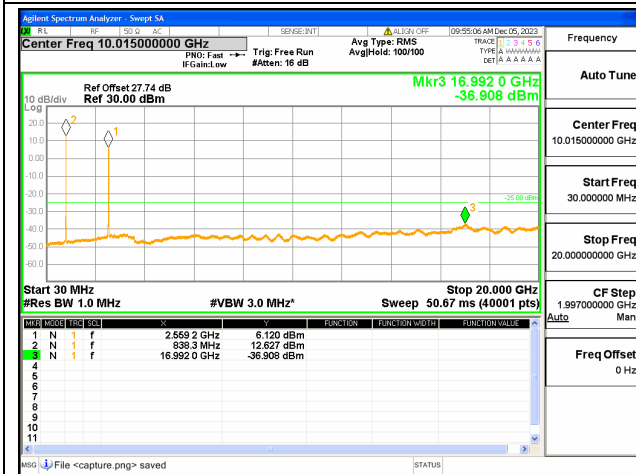
5A-7A-20G-26G / 10+20MHz / QPSK /
Mid+High CH / 1@0-1@99



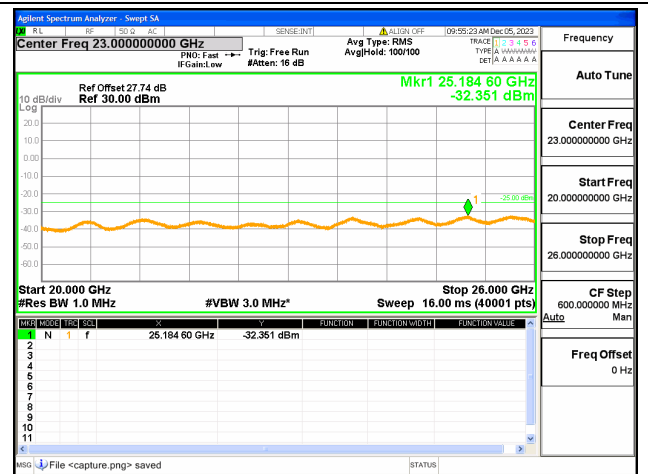
5A-7A-30M-20G / 10+20MHz / QPSK /
Mid+High CH / 1@49-1@0



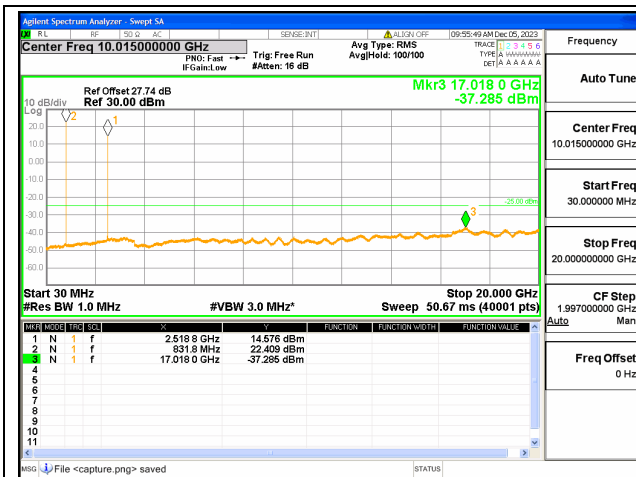
5A-7A-20G-26G / 10+20MHz / QPSK /
Mid+High CH / 1@49-1@0



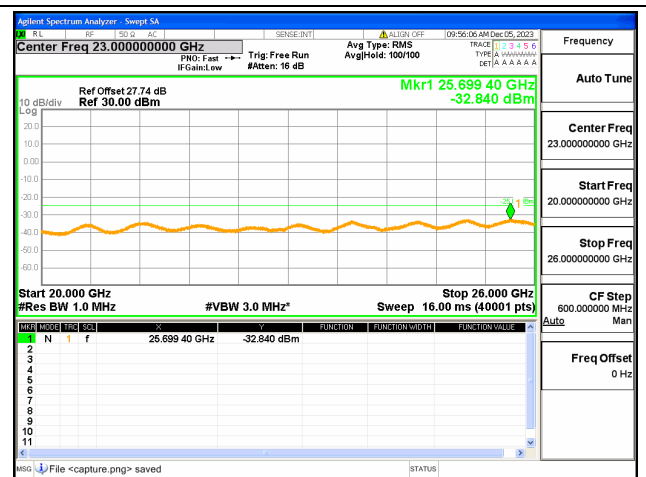
5A-7A-30M-20G / 10+20MHz / QPSK /
Mid+High CH / 50@0-100@0



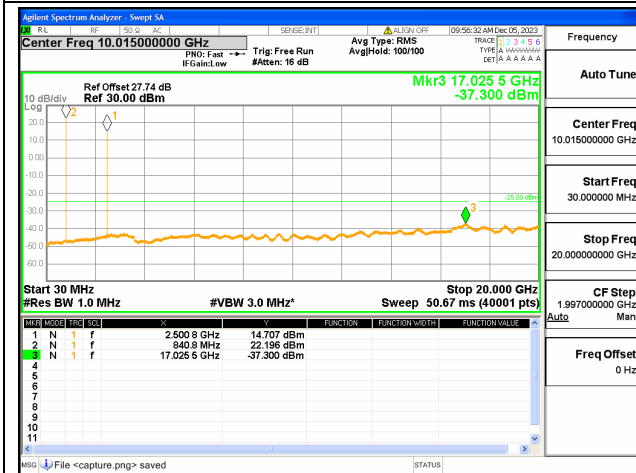
5A-7A-20G-26G / 10+20MHz / QPSK /
Mid+High CH / 50@0-100@0



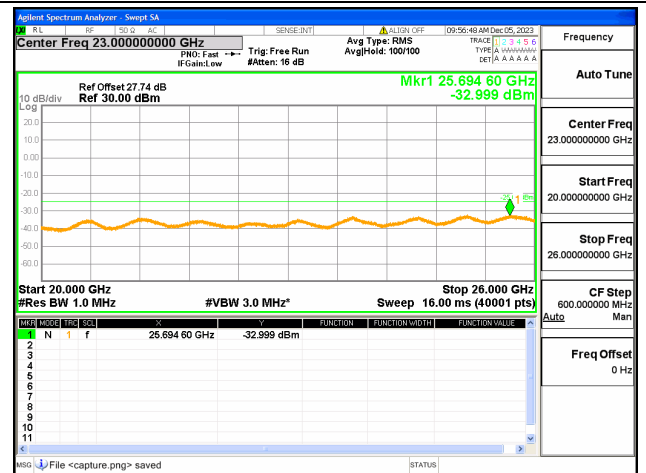
5A-7A-30M-20G / 10+20MHz / QPSK / Mid+Low CH / 1@0-1@99



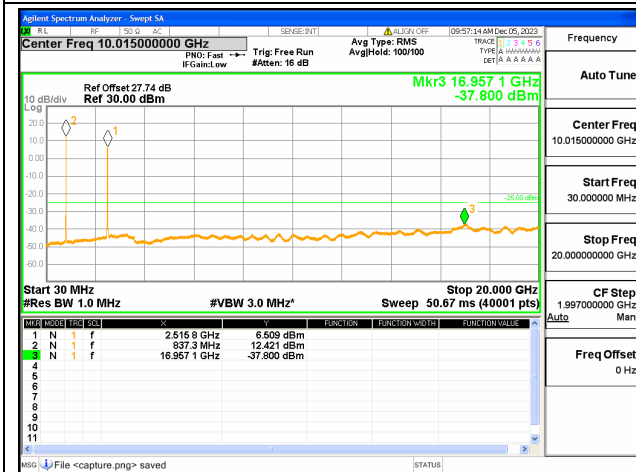
5A-7A-20G-26G / 10+20MHz / QPSK / Mid+Low CH / 1@0-1@99



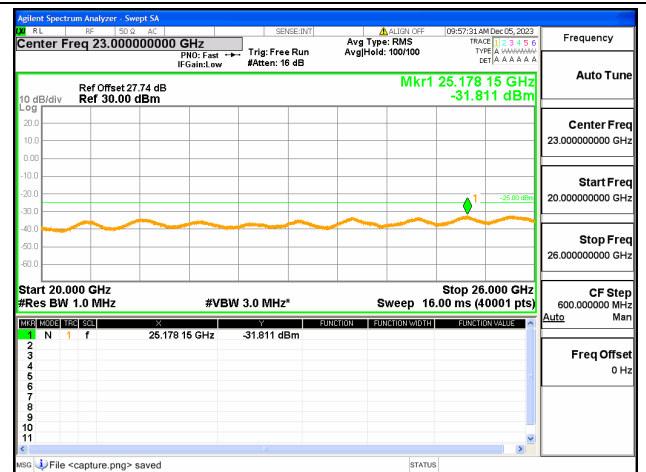
5A-7A-30M-20G / 10+20MHz / QPSK / Mid+Low CH / 1@49-1@0



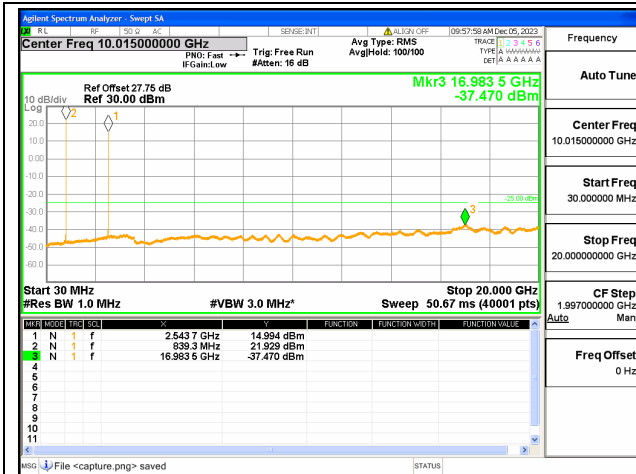
5A-7A-20G-26G / 10+20MHz / QPSK / Mid+Low CH / 1@49-1@0



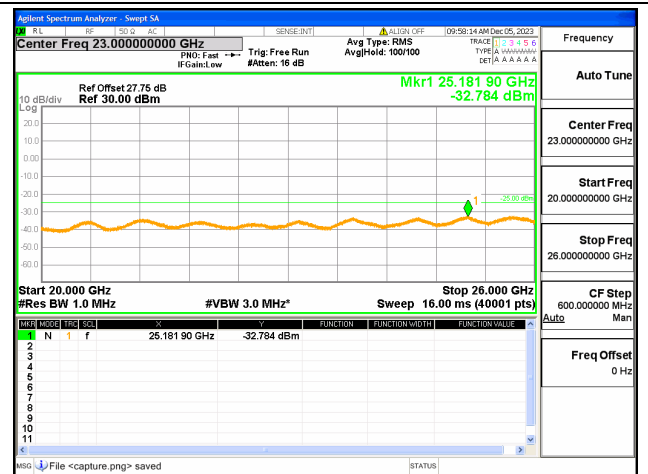
5A-7A-30M-20G / 10+20MHz / QPSK / Mid+Low CH / 50@0-100@0



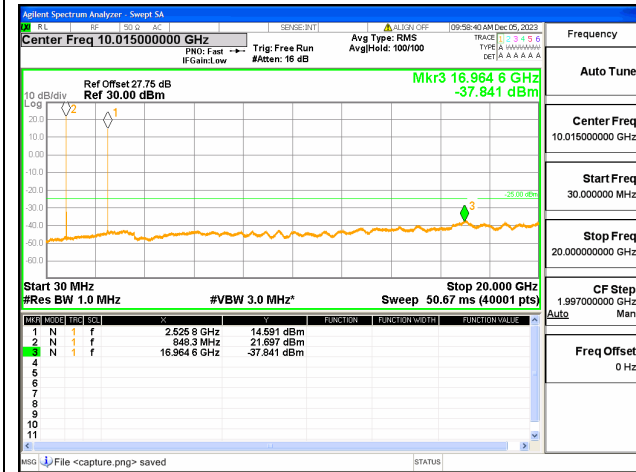
5A-7A-20G-26G / 10+20MHz / QPSK / Mid+Low CH / 50@0-100@0



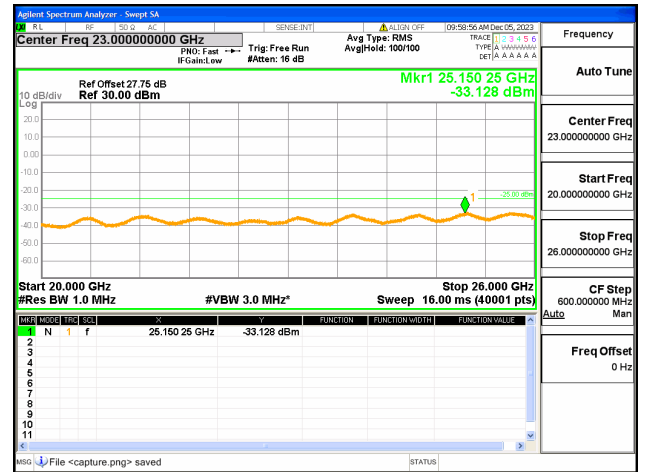
5A-7A-30M-20G / 10+20MHz / QPSK /
High+Mid CH / 1@0-1@99



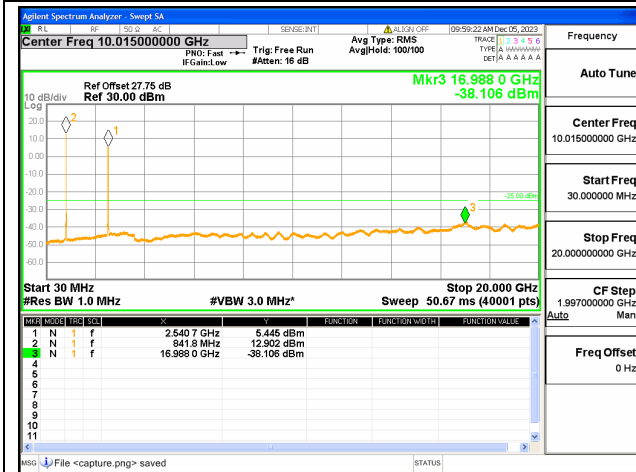
5A-7A-20G-26G / 10+20MHz / QPSK /
High+Mid CH / 1@0-1@99



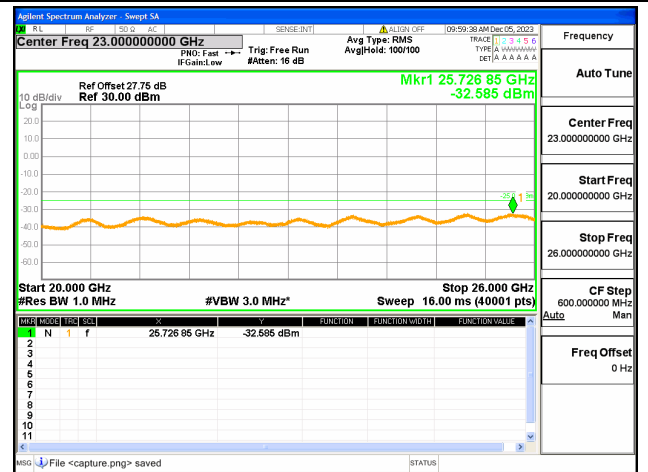
5A-7A-30M-20G / 10+20MHz / QPSK /
High+Mid CH / 1@49-1@0



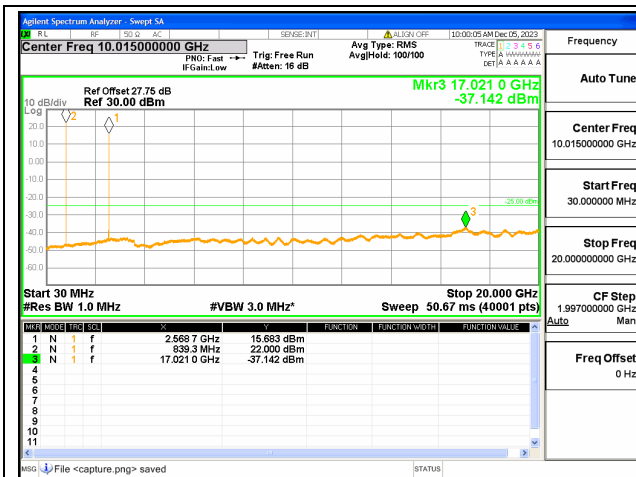
5A-7A-20G-26G / 10+20MHz / QPSK /
High+Mid CH / 1@49-1@0



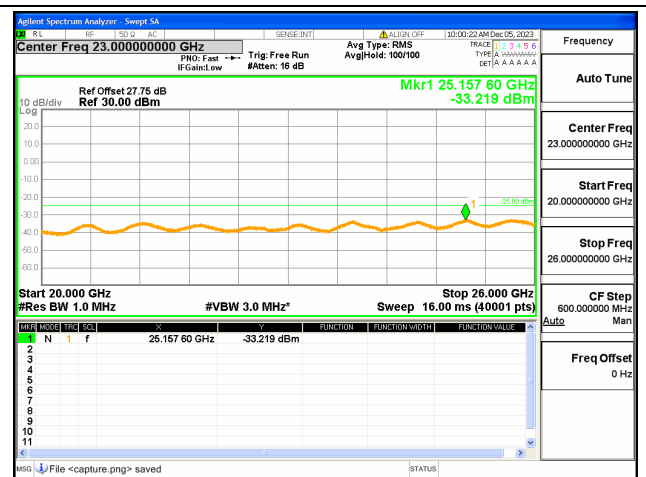
5A-7A-30M-20G / 10+20MHz / QPSK /
High+Mid CH / 50@0-100@0



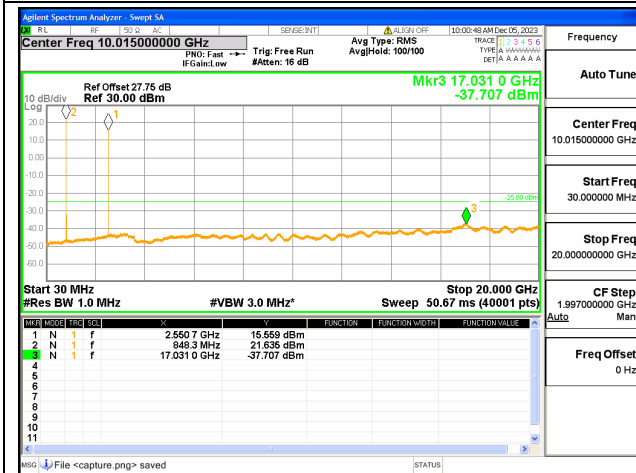
5A-7A-20G-26G / 10+20MHz / QPSK /
High+Mid CH / 50@0-100@0



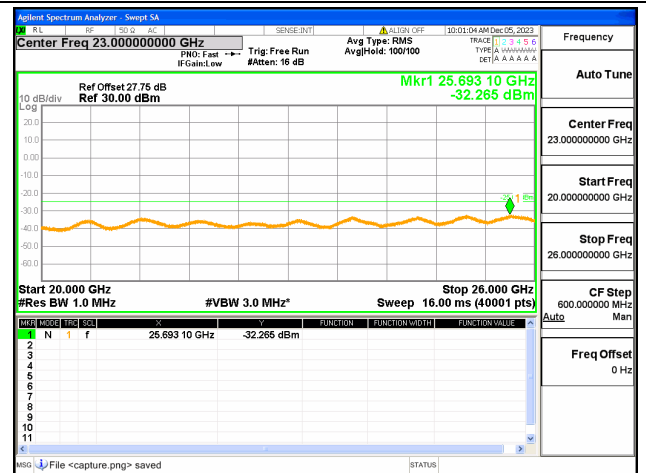
5A-7A-30M-20G / 10+20MHz / QPSK / High+High CH / 1@0-1@99



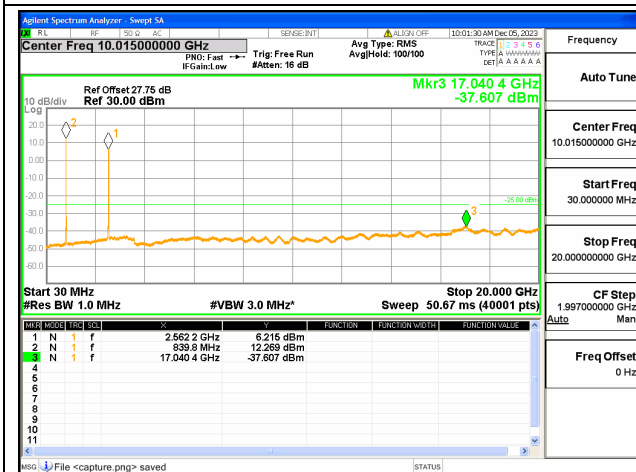
5A-7A-20G-26G / 10+20MHz / QPSK / High+High CH / 1@0-1@99



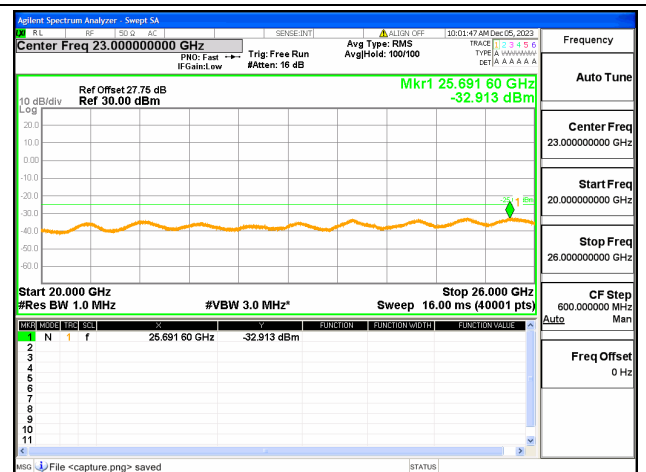
5A-7A-30M-20G / 10+20MHz / QPSK / High+High CH / 1@49-1@0



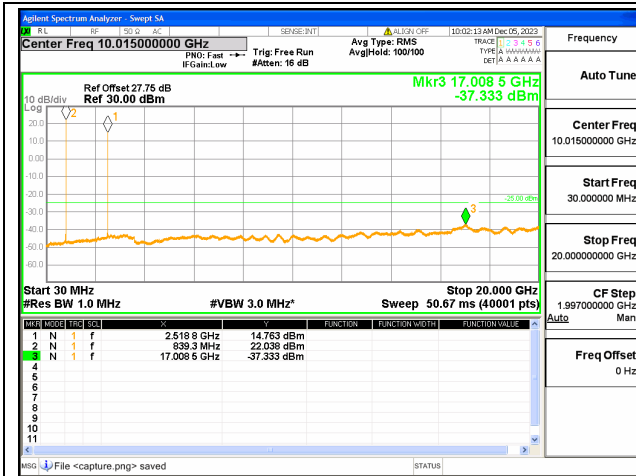
5A-7A-20G-26G / 10+20MHz / QPSK / High+High CH / 1@49-1@0



5A-7A-30M-20G / 10+20MHz / QPSK / High+High CH / 50@0-100@0



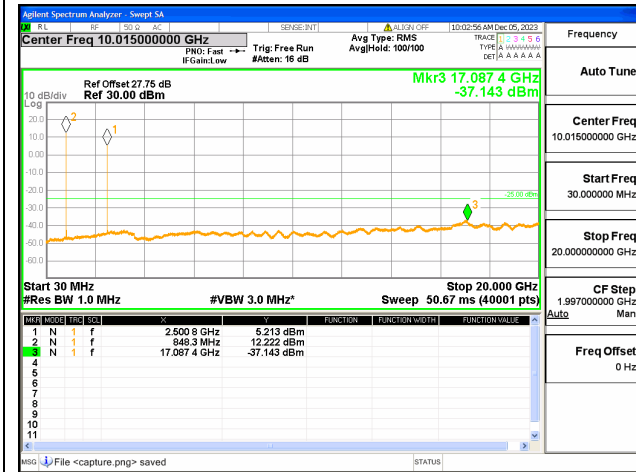
5A-7A-20G-26G / 10+20MHz / QPSK / High+High CH / 50@0-100@0



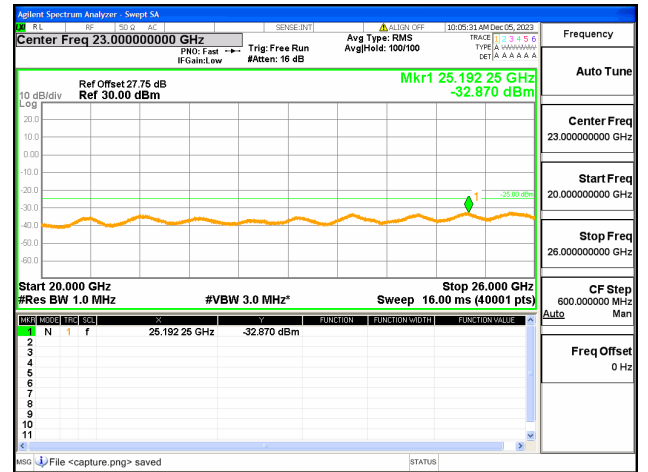
5A-7A-30M-20G / 10+20MHz / QPSK / High+Low CH / 1@0-1@99



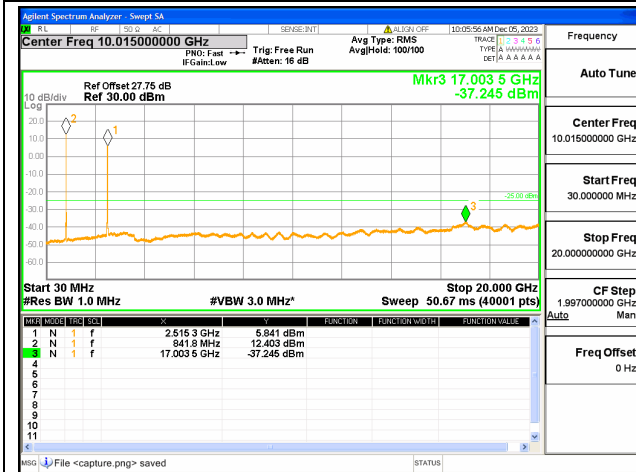
5A-7A-20G-26G / 10+20MHz / QPSK / High+Low CH / 1@0-1@99



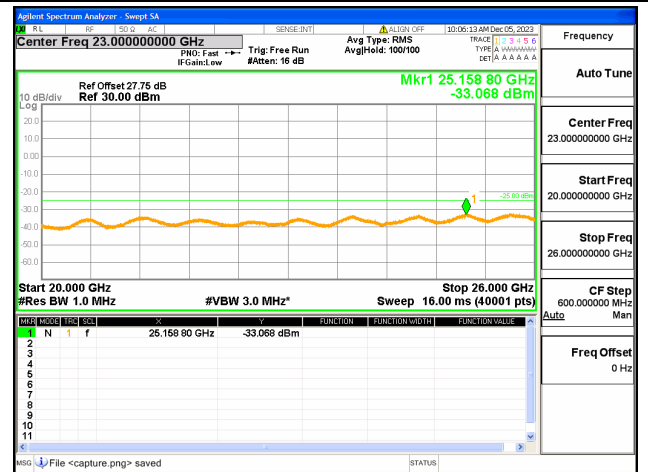
5A-7A-30M-20G / 10+20MHz / QPSK / High+Low CH / 1@49-1@0



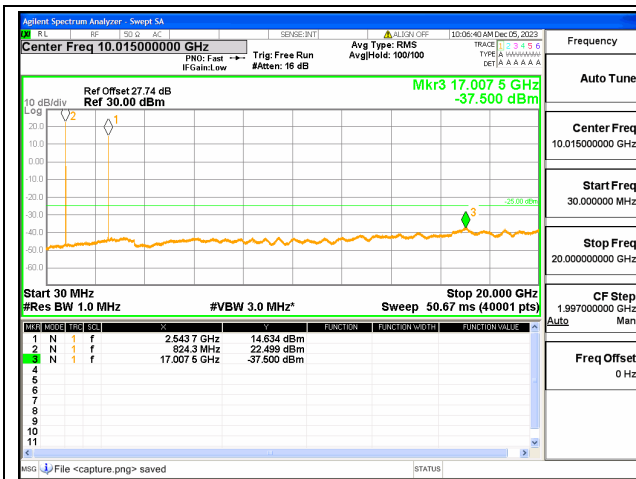
5A-7A-20G-26G / 10+20MHz / QPSK / High+Low CH / 1@49-1@0



5A-7A-30M-20G / 10+20MHz / QPSK / High+Low CH / 50@0-100@



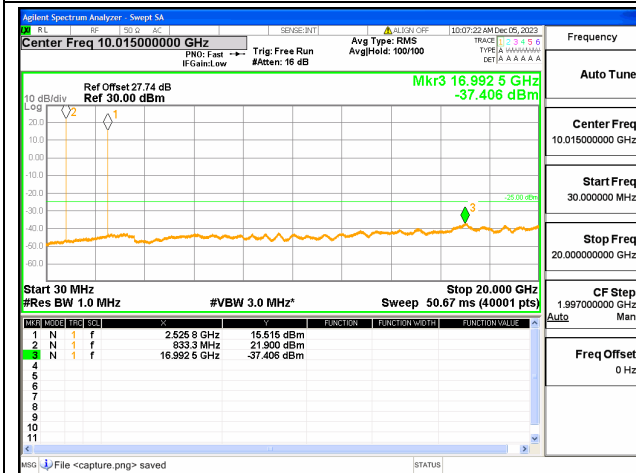
5A-7A-20G-26G / 10+20MHz / QPSK / High+Low CH / 50@0-100@



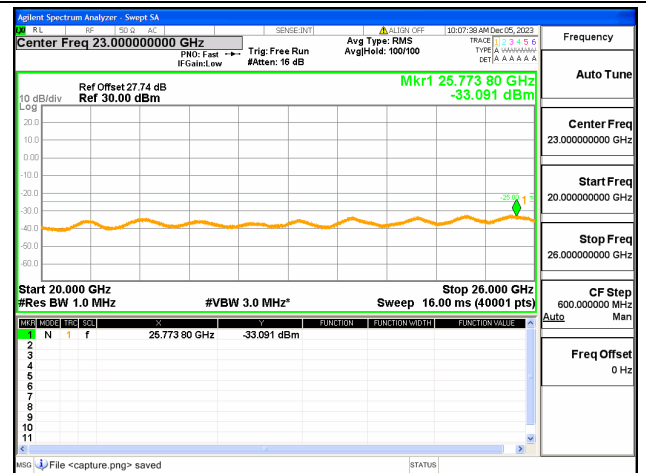
5A-7A-30M-20G / 10+20MHz / QPSK /
Low+Mid CH / 1@0-1@99



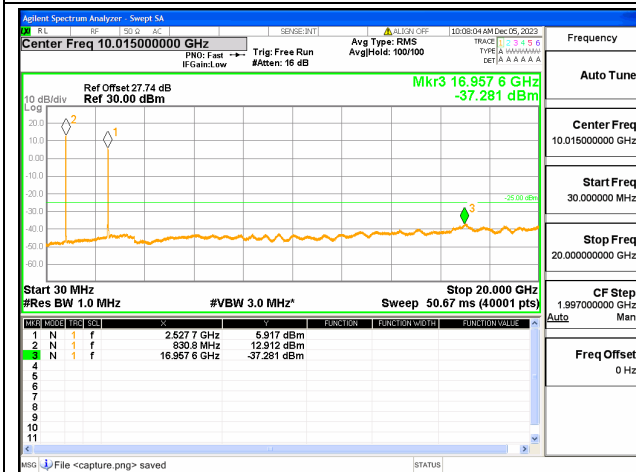
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+Mid CH / 1@0-1@99



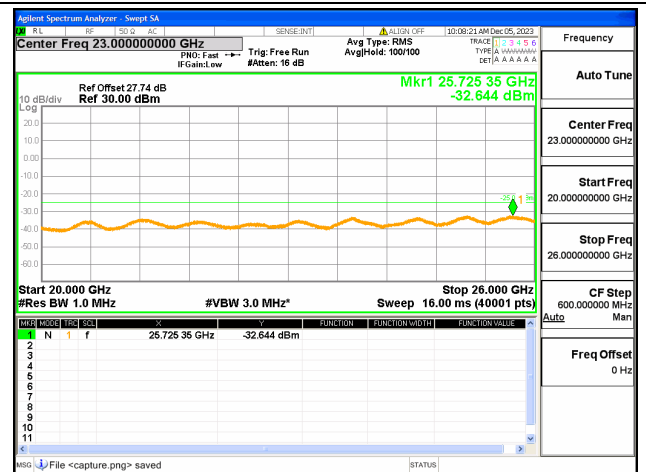
5A-7A-30M-20G / 10+20MHz / QPSK /
Low+Mid CH / 1@49-1@0



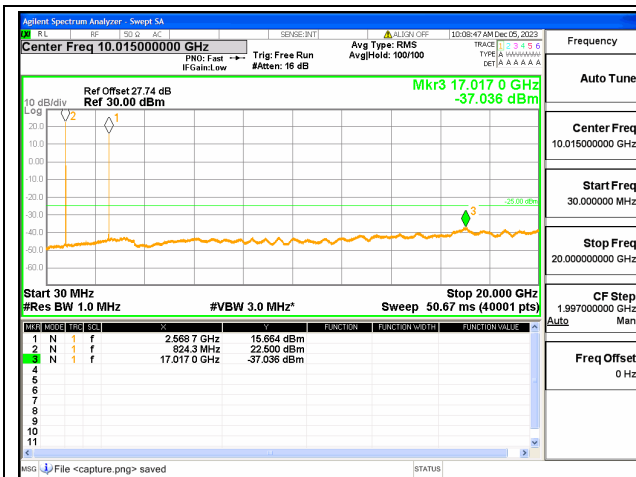
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+Mid CH / 1@49-1@0



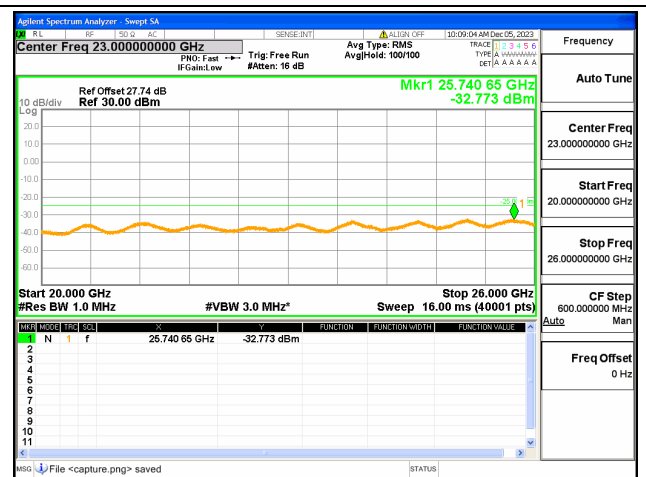
5A-7A-30M-20G / 10+20MHz / QPSK /
Low+Mid CH / 50@0-100@0



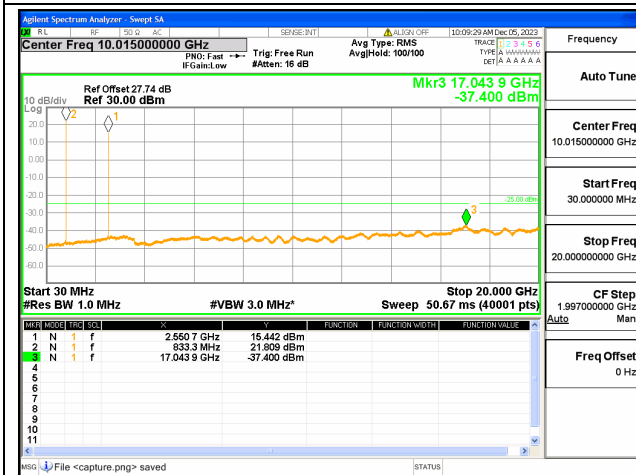
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+Mid CH / 50@0-100@0



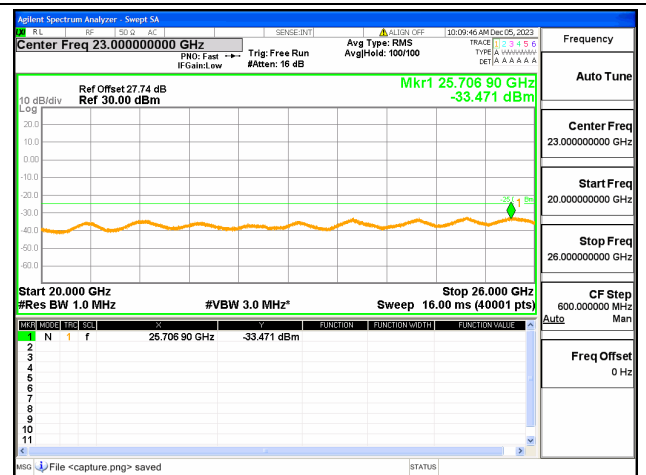
5A-7A-30M-20G / 10+20MHz / QPSK /
Low+High CH / 1@0-1@99



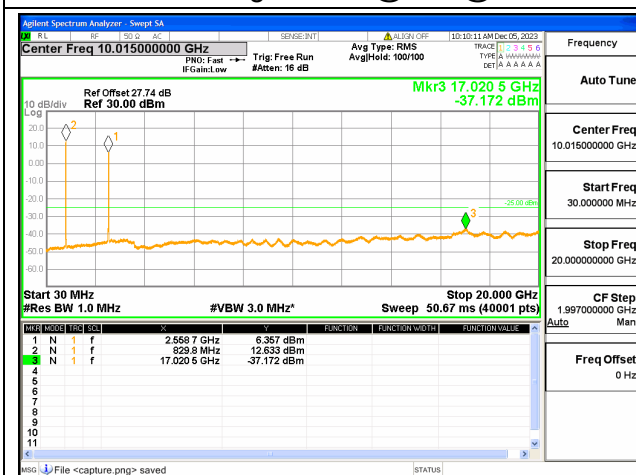
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+High CH / 1@0-1@99



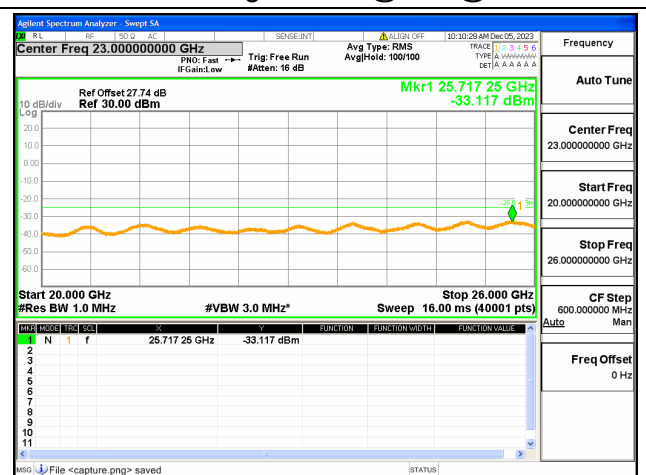
5A-7A-30M-20G / 10+20MHz / QPSK /
Low+High CH / 1@49-1@0



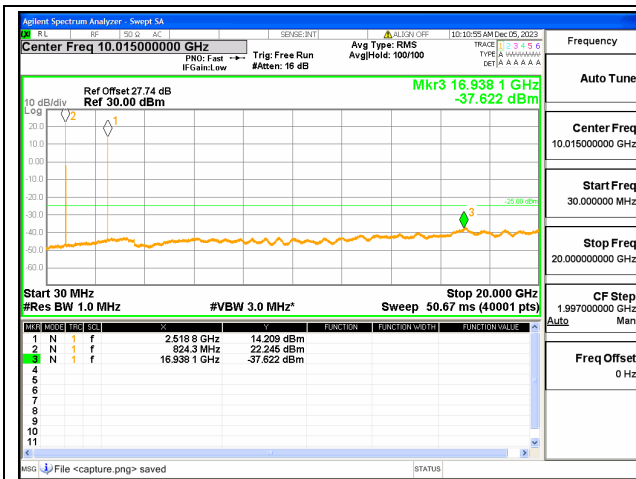
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+High CH / 1@49-1@0



5A-7A-30M-20G / 10+20MHz / QPSK /
Low+High CH / 50@0-100@



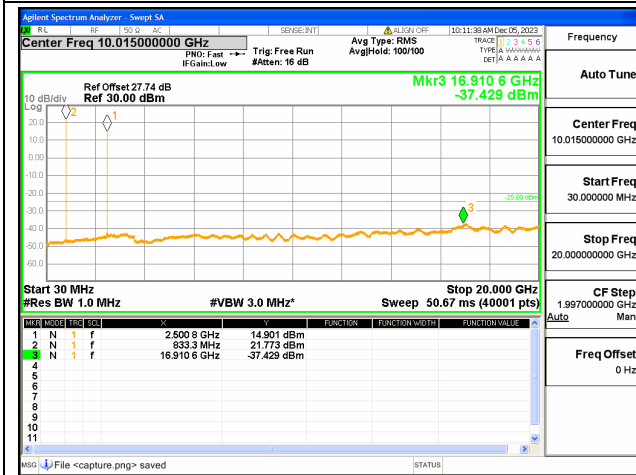
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+High CH / 50@0-100@



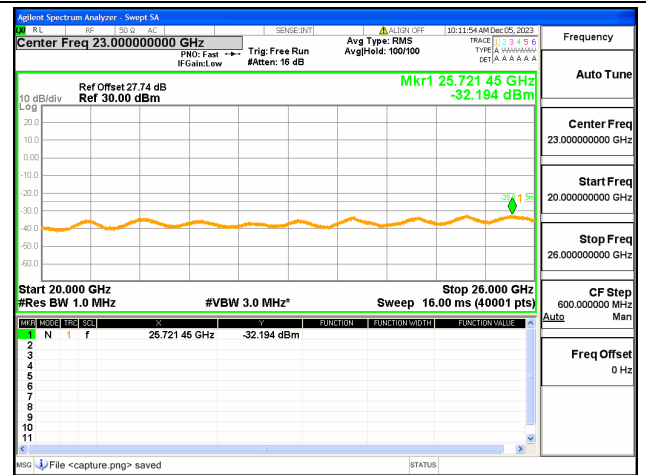
5A-7A-30M-20G / 10+20MHz / QPSK /
Low+Low CH / 1@0-1@99



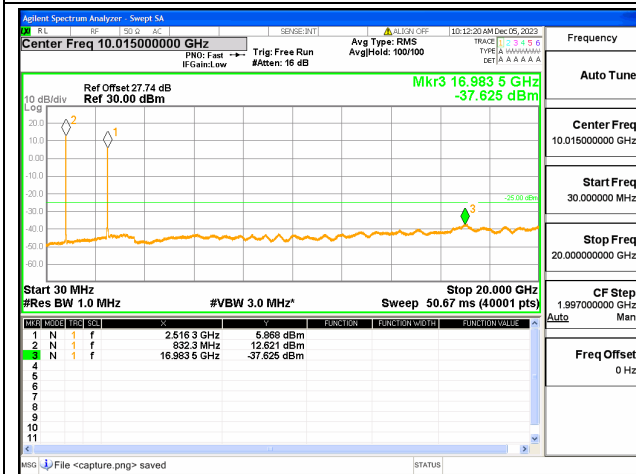
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+Low CH / 1@0-1@99



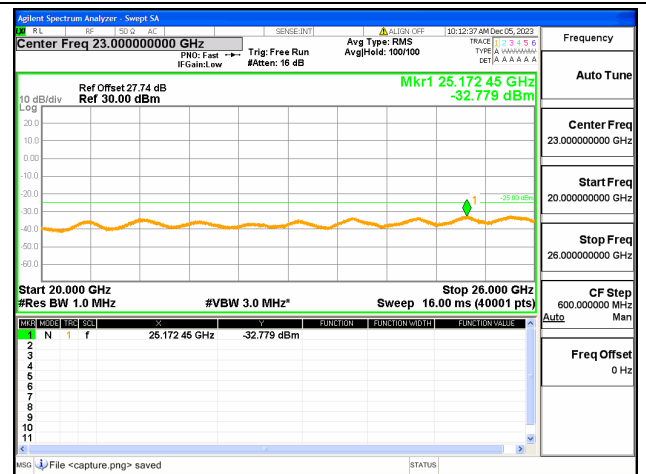
5A-7A-30M-20G / 10+20MHz / QPSK /
Low+Low CH / 1@49-1@0



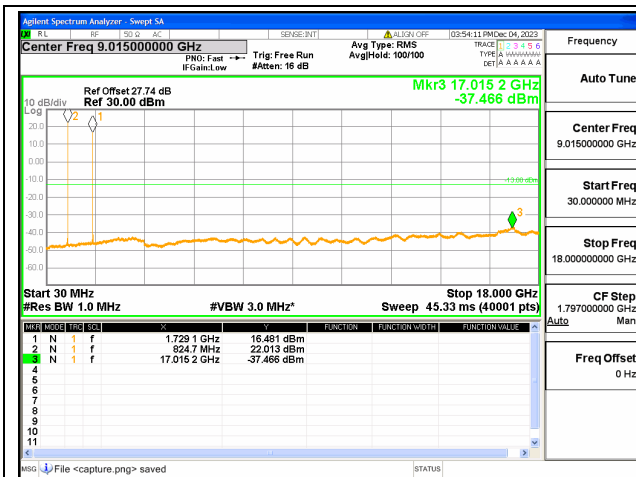
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+Low CH / 1@49-1@0



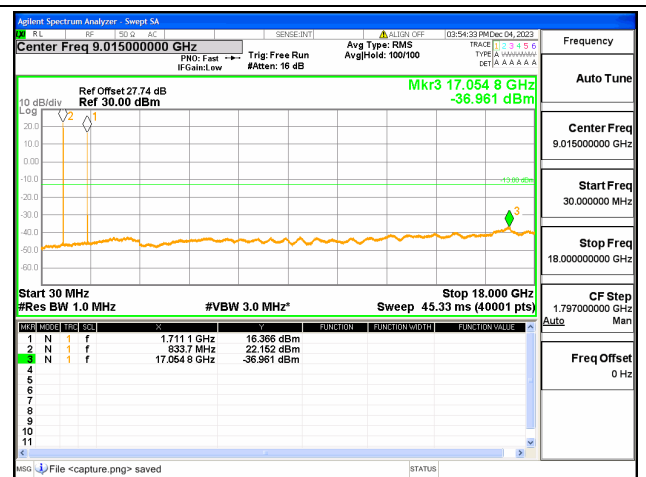
5A-7A-30M-20G / 10+20MHz / QPSK /
Low+Low CH / 50@0-100@0



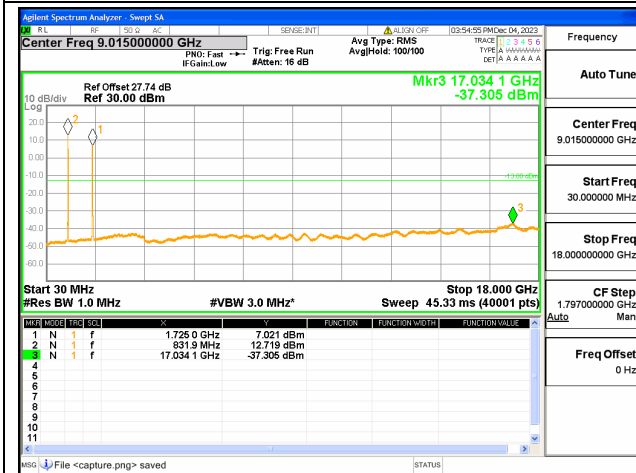
5A-7A-20G-26G / 10+20MHz / QPSK /
Low+Low CH / 50@0-100@0



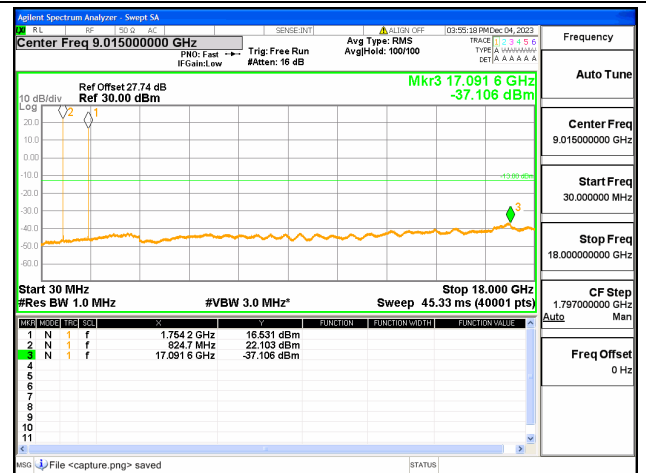
5A-66A / 10+20MHz / QPSK / Low+Low CH / 1@0-1@99



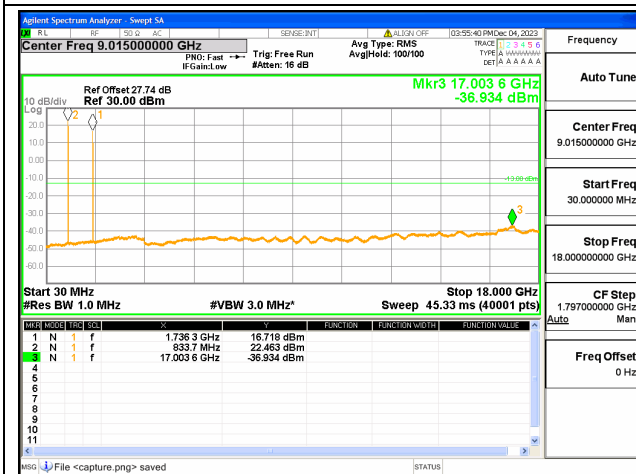
5A-66A / 10+20MHz / QPSK / Low+Low CH / 1@49-1@0



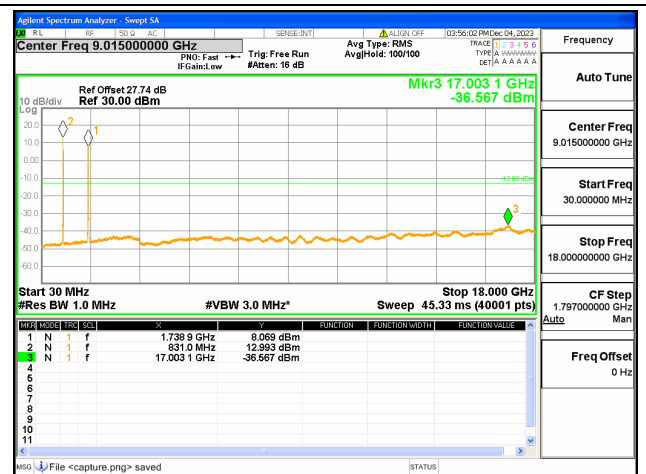
5A-66A / 10+20MHz / QPSK / Low+Low CH / 50@0-100@0



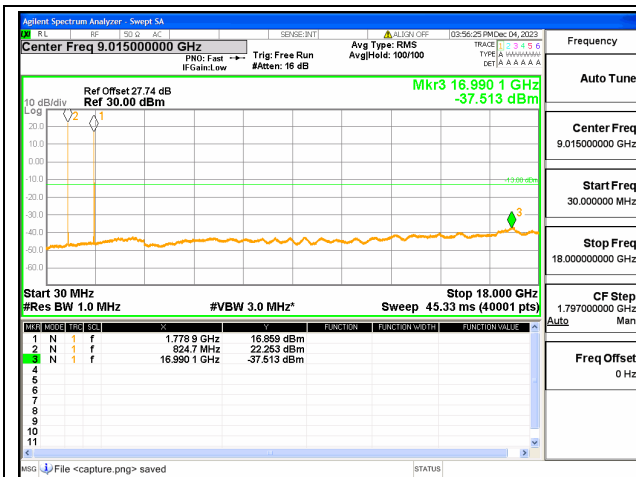
5A-66A / 10+20MHz / QPSK / Low+Mid CH / 1@0-1@99



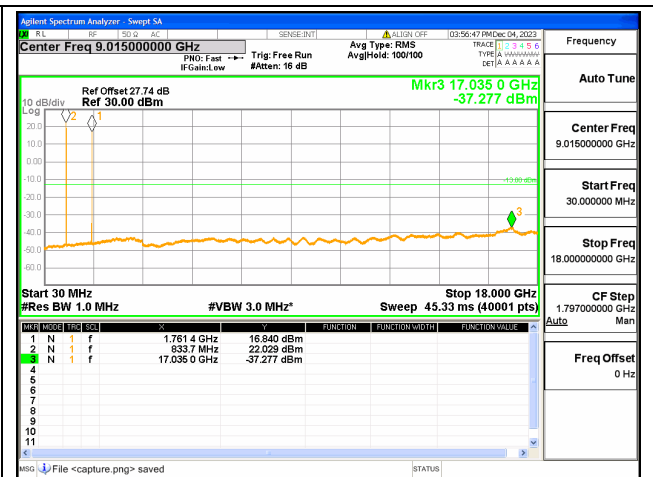
5A-66A / 10+20MHz / QPSK / Low+Mid CH / 1@49-1@0



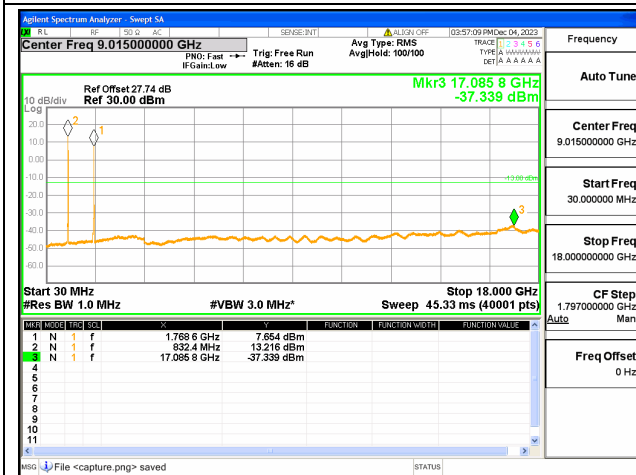
5A-66A / 10+20MHz / QPSK / Low+Mid CH / 50@0-100@0



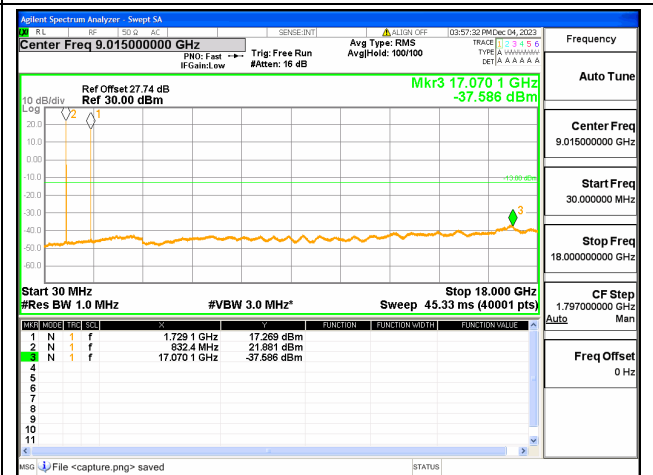
5A-66A / 10+20MHz / QPSK / Low+High CH / 1@0-1@99



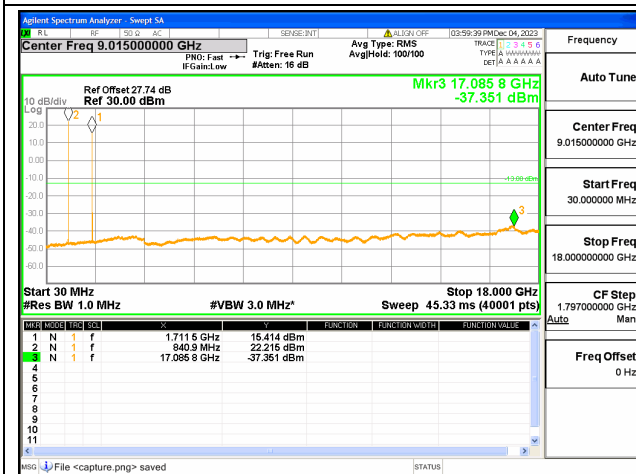
5A-66A / 10+20MHz / QPSK / Low+High CH / 1@49-1@0



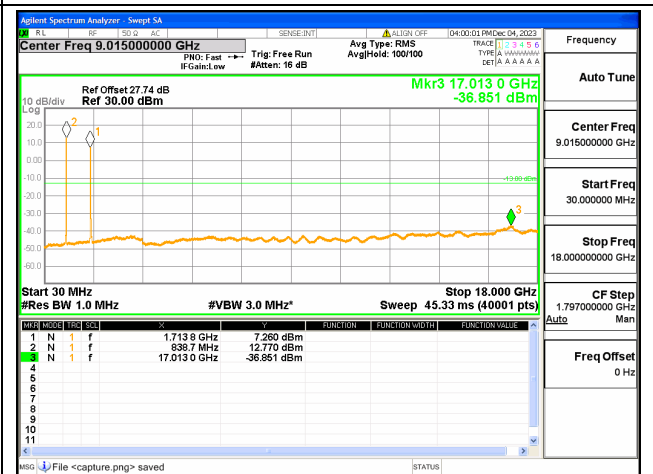
5A-66A / 10+20MHz / QPSK / Low+High CH / 50@0-100@0



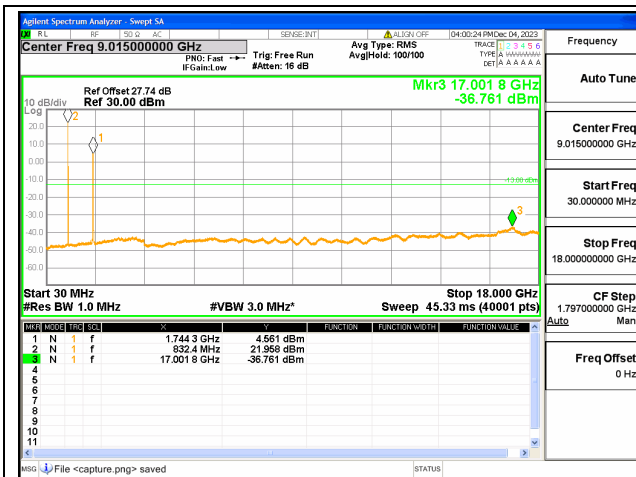
5A-66A / 10+20MHz / QPSK / Mid+Low CH / 1@0-1@99



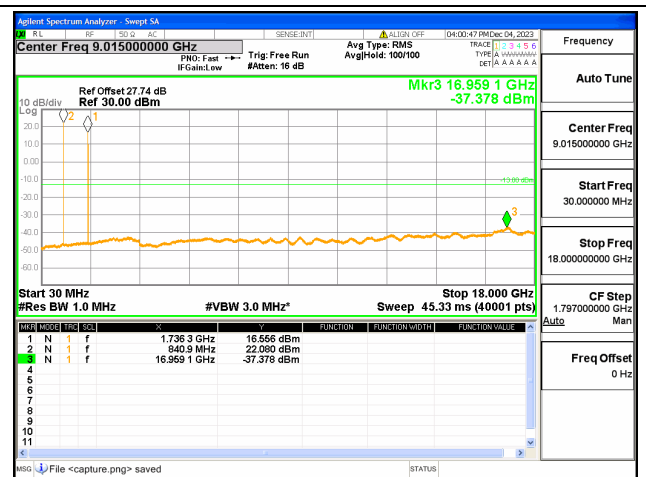
5A-66A / 10+20MHz / QPSK / Mid+Low CH / 1@49-1@0



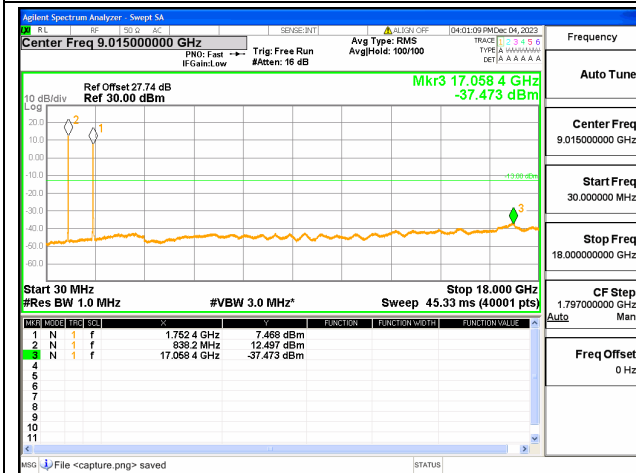
5A-66A / 10+20MHz / QPSK / Mid+Low CH / 50@0-100@0



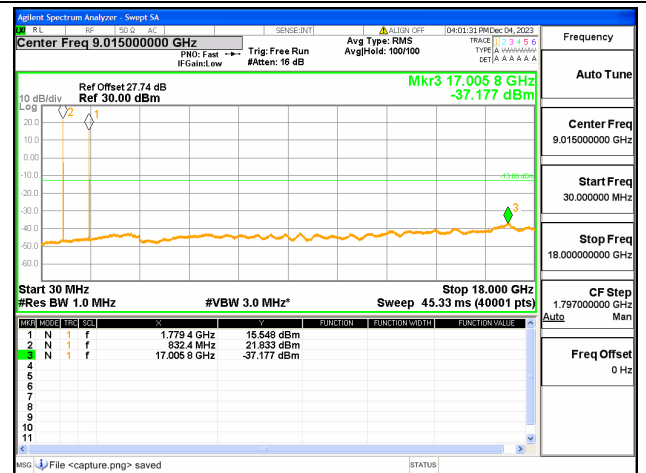
5A-66A / 10+20MHz / QPSK / Mid+Mid CH / 1@0-1@99



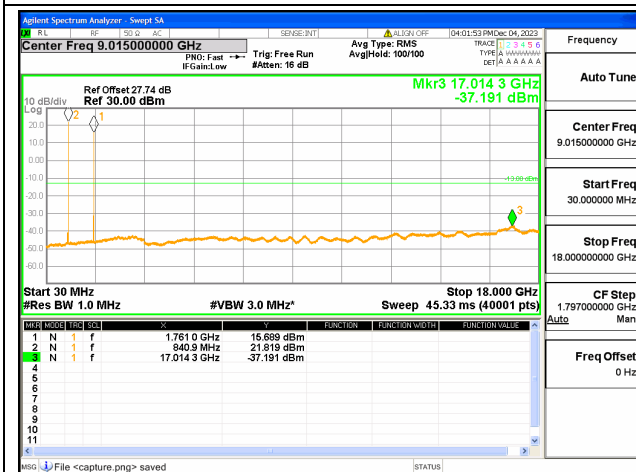
5A-66A / 10+20MHz / QPSK / Mid+Mid CH / 1@49-1@0



5A-66A / 10+20MHz / QPSK / Mid+Mid CH / 50@0-100@0



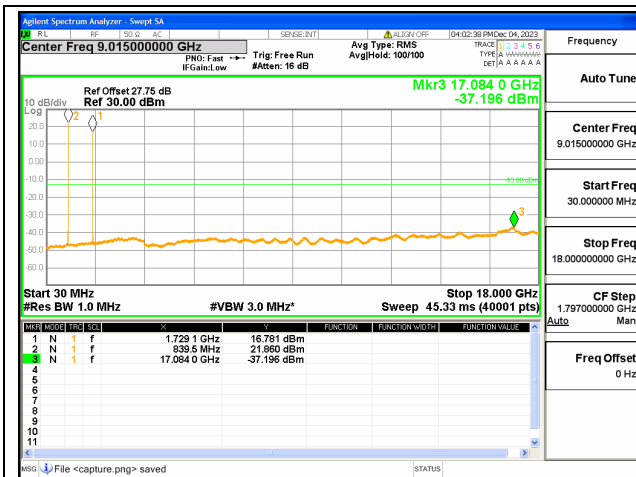
5A-66A / 10+20MHz / QPSK / Mid+High CH / 1@0-1@99



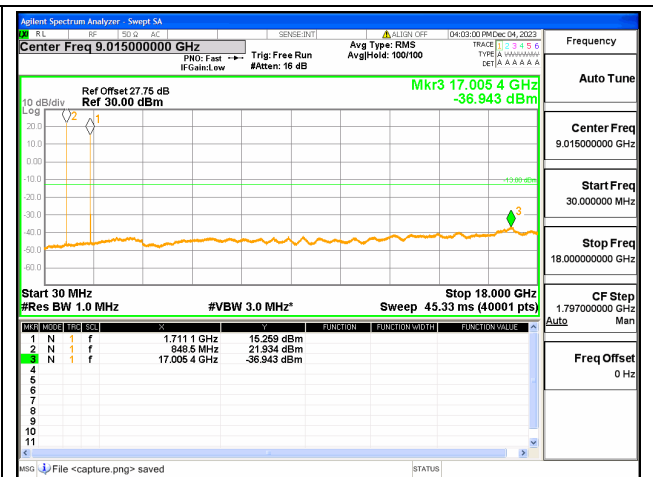
5A-66A / 10+20MHz / QPSK / Mid+High CH / 1@49-1@0



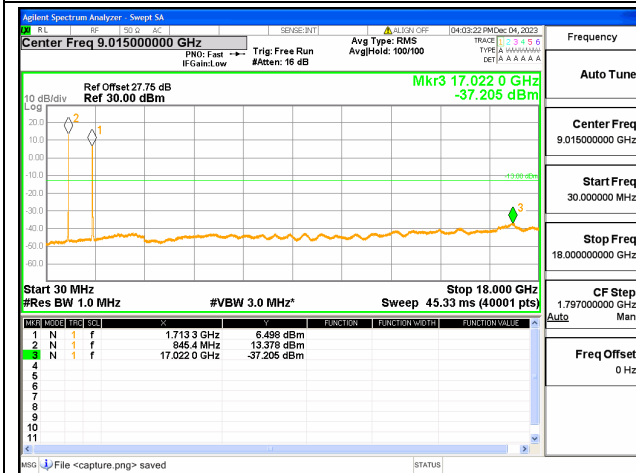
5A-66A / 10+20MHz / QPSK / Mid+High CH / 50@0-100@0



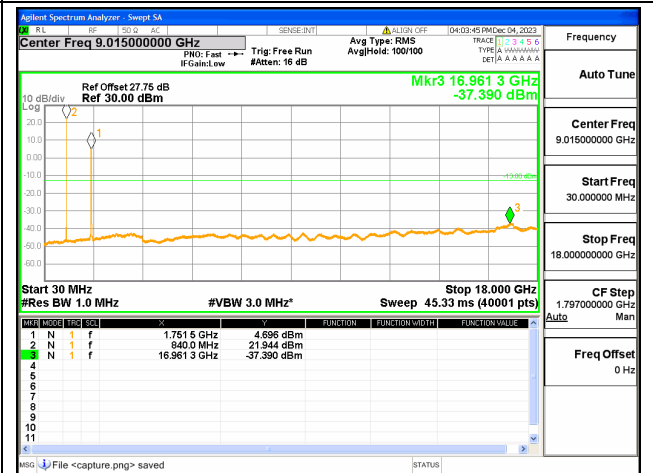
5A-66A / 10+20MHz / QPSK / High+Low CH / 1@0-1@99



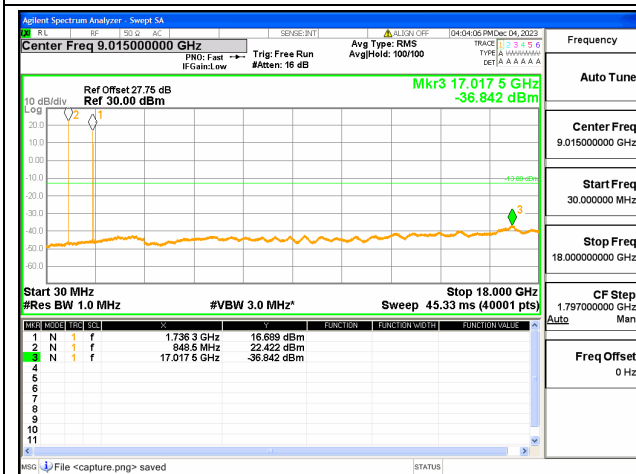
5A-66A / 10+20MHz / QPSK / High+Low CH / 1@49-1@0



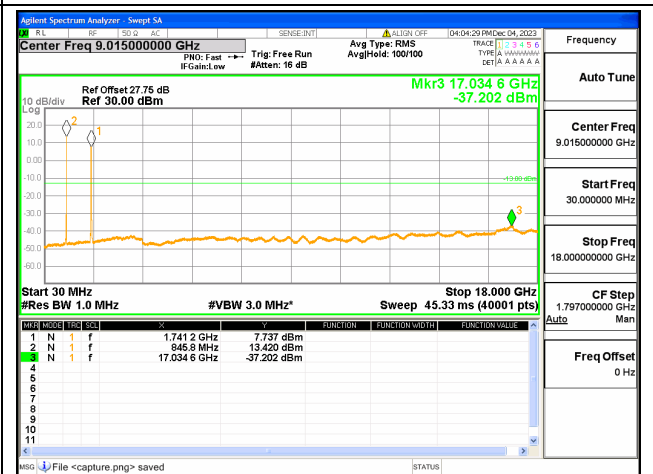
5A-66A / 10+20MHz / QPSK / High+Low CH / 50@0-100@0



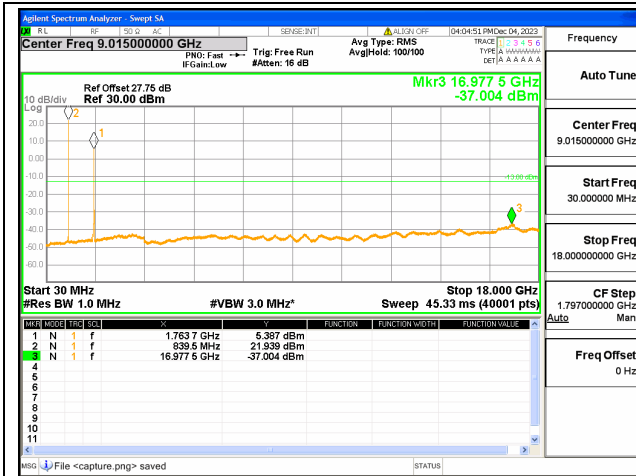
5A-66A / 10+20MHz / QPSK / High+Mid CH / 1@0-1@99



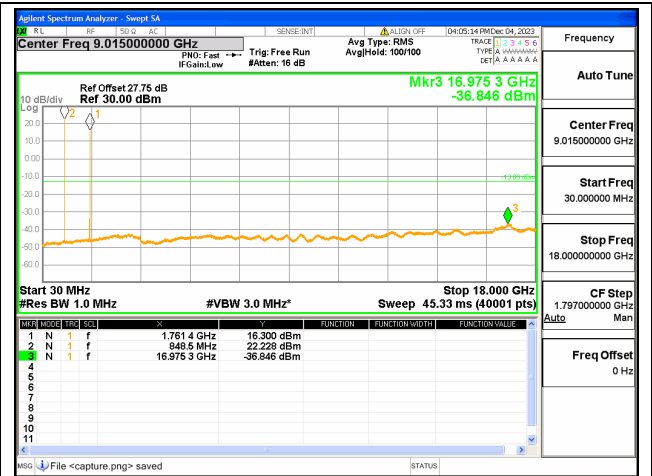
5A-66A / 10+20MHz / QPSK / High+Mid CH / 1@49-1@0



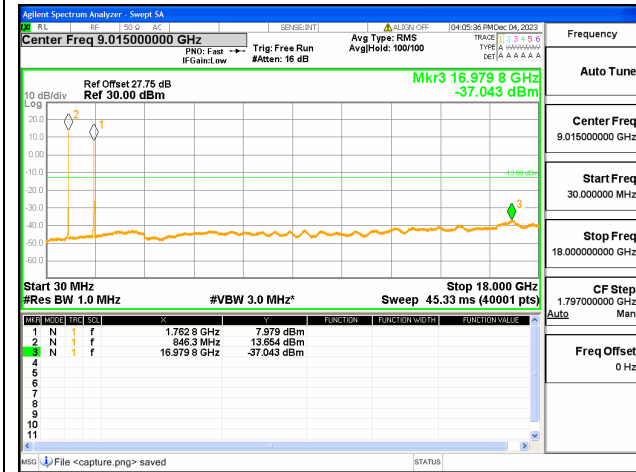
5A-66A / 10+20MHz / QPSK / High+Mid CH / 50@0-100@0



5A-66A / 10+20MHz / QPSK / High+High CH /
1@0-1@99

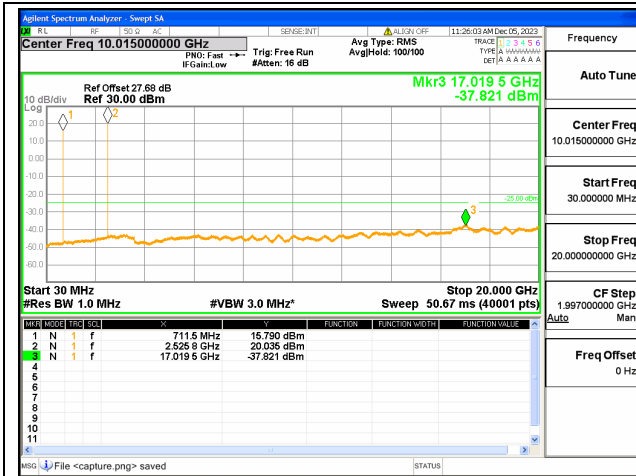


5A-66A / 10+20MHz / QPSK / High+High CH /
1@49-1@0

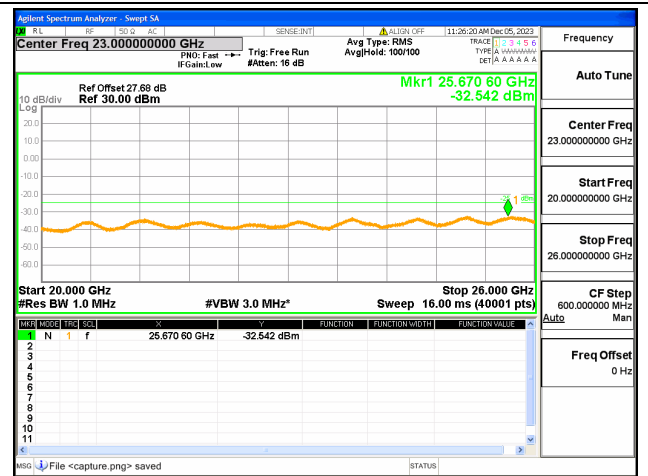


5A-66A / 10+20MHz / QPSK / High+High CH /
50@0-100@0

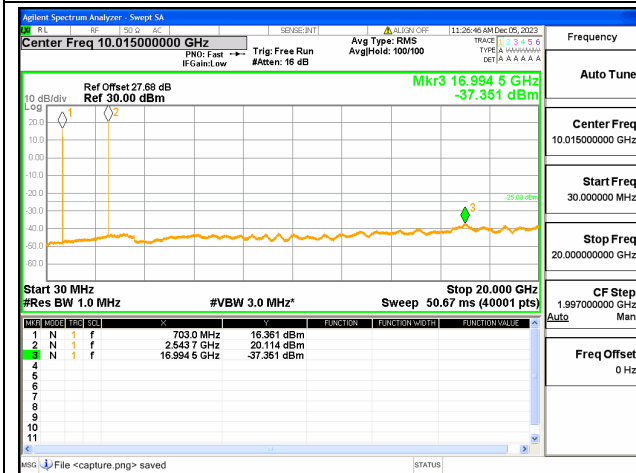
N/A



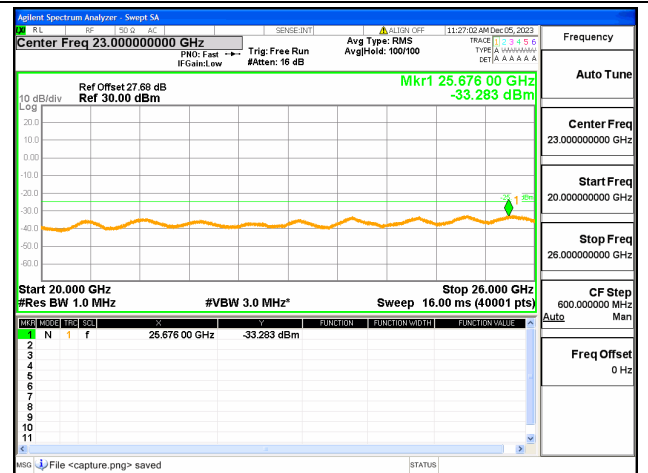
7A-12A-30M-20G / 20+10MHz / QPSK /
Mid+Mid CH / 1@0-1@49



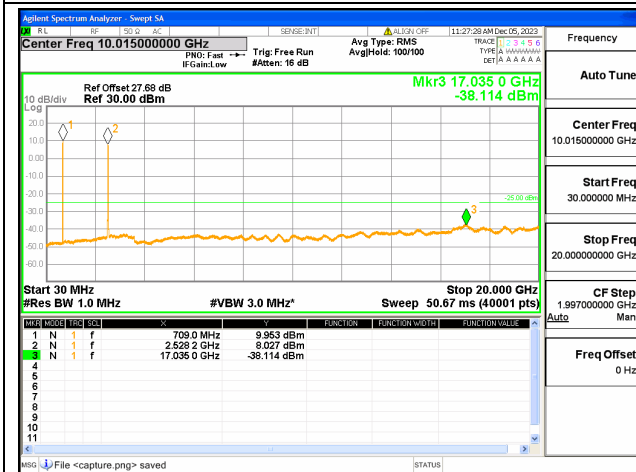
7A-12A-20G-26G / 20+10MHz / QPSK /
Mid+Mid CH / 1@0-1@49



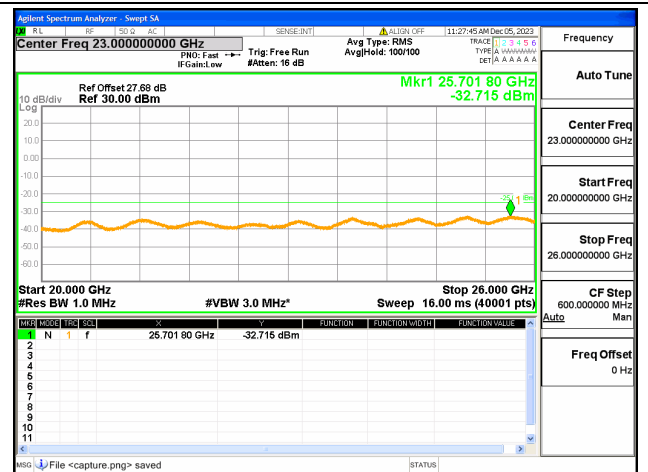
7A-12A-30M-20G / 20+10MHz / QPSK /
Mid+Mid CH / 1@99-1@0



7A-12A-20G-26G / 20+10MHz / QPSK /
Mid+Mid CH / 1@99-1@0



7A-12A-30M-20G / 20+10MHz / QPSK /
Mid+Mid CH / 100@0-50@0



7A-12A-20G-26G / 20+10MHz / QPSK /
Mid+Mid CH / 100@0-50@0